

# Listening Post software for the Commodore 64! 

Build our simple audio signal generator

Microwave oven leak detector to build

## Intro to the $\mathbf{8 0 8 8}$ micro

- Follow-up an the Listening Post project - FORTH - the programmer's power tool - Amateurs! - hulld a Morse 'memory keyer'
- A 'difierent' Xmas IIghts dispiay - Completing the Tilbrook preamp



## And It Gives You So Much More!

It's unbelievable! The amazing VZ-300 colour computer is at its lowest price ever!! Whether you're a computer expert or a first-timer, the VZ-300 has plenty to offer.
Now, for under $\$ 100$ there's a computer that will expand to meet your needs. With 18 K RAM memory - expandable to an amazing 32 K , high resolution graphics, and optional disk drive, printer and a host of extras available: the VZ- 300 is unbeatable value!
Because it uses the most popular computer language in the world, 'Microsoft Basic', there's an incredible range of software available; games, business/management, education . . . there's dozens to choose from!
Look at these fantastic specifications!:
CPU. - Z80A running at 3.5 MHz
Memory. - 16K Basic ROM
RAM - 18 K expandable to 32 K
Keyboard - 46 key full stroke with automatic repeat key


Graphics -32 columns $\times 16$ lines. $128 \times 64 \operatorname{dot}(8$ colour) $/ 64 \times$ 32 dots ( 9 colour) selectable colours.
With an unbelievable range of add-ons available your VZ-300 will give you years of service! When good value isn't good enough - DSE makes it even better! Cat X-7300

## Dick Smith Electronics Pty Ltd

 seen by many governments to be necessary tools for the maintenance of strategic and commercial interests, and as primary sources of modern technological innovation that invigorates existing industries. They are of particular value in the management of large, sparsely populated countries such as Australia.
"Spacecraft are becoming an integral part of national and international systems for communications, meteorology, resources management, surveillance, remote sensing, navigation, search-andrescue, and national security. Continued financial commitment by Australia to such systems is inevitable."

So says the opening paragraphs of the Executive Summary of the CSIRO's report from their 1984 Space Science and Technology Study Group. And they're prophetic words. Either Australia develops an industry group exploiting 'space-related technologies', or we join the ranks of the technological Third World.
One of the significant things this study group identified was that a space-related technology industry needs a 'kick-start' from the government in order to become established. As part of a programme to institute that kick-start, the CSIRO has setup the Office of Space Science and Applications (COSSA), headed by Dr Ken McCracken.
That action was one recommendation of eight made in the study group report. I'm glad to see it was so quickly implemented. Another I'd like to see rapidly implemented arises from Recommendation 3 which, in part seeks to encourage ". . . the development of a series of relatively small satellites which will establish system integration competence in Australian industry . . . and in cooperation with industry, tertiary education institutions, and government departments and instrumentalities." The italics are theirs, but I'd like to emphasis that, too.
The USSR has had just such a programme operating for some years. Their ISKRA (translates as "spark") series of open-access communications satellites are designed and built by students from some 27 countries studying at the Moscow Aviation Institute. And the Russians aren't the only ones. The UOSAT series of satellites come from Surrey University in Britain.
If we are to train engineers in space-related technologies, who will enter the workforce over the next 5-10 years, then we must start with the students currently passing through our tertiary institutes. I propose that we establish a similar scheme here. There is a precedent. A group of students at Melbourne University produced the OSCAR 5 amateur radio satellite in the late '60s. Can we not re-kindle that technologically adventurous spirit once more?

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## PROJECTS TO BUILD



## AEM4600 Dual-Speed Modem

Our feature project is simple to build, simple to operate and low in cost.

## AEM6010 Ultra-Fidelity

 Preamp - Part 3And this is where the story really finishes! Final wiring and assembly details plus power-up testing completes the preamp.

## AEM5502 Microwave

 Oven Leak Detector44Concerned about leakage from your microwave oven? This inexpensive project is simple to build, but effective. With it you can put your mind to rest - or not.


AEM2500 Simple Sine/Square Audio Signal Generator

Here's one of the fundamental instruments required in every electronics lab. or hobbyist's workshop.


## AEM Data Sheet

The '7910 FSK modem IC features this month 'heart' of our Dual-Speed Modem feature project.

## Benchbook

$\qquad$
Christmas tree lights with a difference! Here's an ingenious circuit to jazz up that set of boring old on/off/on/off Christmas tree lights you've been using for years.

## 'FOR STARTERS'

- our popular series returns next month. You really should take a break over Christmas, you know!


## PRACTICAL COMPUTING

Listening Post Software 69
Disk or tape software for the Microbee and Commodore 64.


The Ins \& Outs of RS232 - Part 2

Here we get to the hookups, troubleshooting and your check list. In a mess with the old RS?
Here's how to sort it out.


SoftTalk -
the good ol' 88
. . . 90
Bill Thomas gets into the Intel 8088 microprocessor - heart of the IBM PC, its many 'compatibles' and 'clones'

## Set Forth

Here's a plain language introduction to what is described as "the programmer's power tool''.

## BeeBuzz

- Tom Moffat, after a marathon with the Listening Post this month, returns in January.


## COMMUNICATIONS

 SCENE

Follow-Up to the AEM3500 Listening Post

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Here's what you've been
waiting for! -
Commodore 64 software
and interface details, plus
hints and tips on
receivers, etc.
A Variable Speed CW Memory

Here's a great little weekend project that finds that elusive DX for you. VKЗAMK gives away his 'secret'.


## CONTEST

Win a Yaesu Scanner from Dick Smith!

Here's your chance to own a top-line scanner, courtesy of Dick Smith Electronics.

Radio Communicators Guide to the lonosphere - Part 4 has been held over due to lack of space.

## FEATURE



## CONSUMER ELECTRONICS

Review of Nakamichi's OMS-7 Compact Disc Player

Now, was the test disc testing the player or the player testing the test disc? Employing fourtimes oversampling, the OMS-7 delivers stunning results. But we have a few questions to resolve.


CONTEST
Win A Robot!

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Enter this great contest and you could win a great little robot that walks and talks!

Getting the Best TV Reception

Cables, their haunts and
habits. There's more to
them than meets the eye.

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## Here's a remarkably

sophisticated product with a special appeal.

## News Review

## History demolished!

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## NEXT MONTH!



TILBROOK'S 3-WAY
LOUDSPEAKERS
Here comes the second in our series of superlative 'digital era' projects. Again employing Vifa drivers, this bass reflex 3 -way design has impeccable performance unmatched at the price you can build it

HONESTY, FALLIBILITY AND POLICE RADAR
This article examines how police radars work, what they can and can't tell in any given situation. In layman's terms, it describes how the radars are used and what can go wrong. In addition it tells what to do if you feel you've been unfairly caught. The author discusses how the situation might be improved and what research is being done towards this.

## LCD SCREEN FOR YOUR MICROBEE

This project shows you how to convert your 16 K or 32 K 'Bee into a 'porta-Bee' using a locally-available liquid crystal display and a rechargeable 12 V battery.

POWER AMPS, MOSFETS
AND RATINGS
Prompted by a reader's letter, David Tilbrook discusses just how much output you can squeeze from MOSFET power amp circuits and the conditions under which they fail.

# he Great business offer from Microbee 



Microbee Small Business Computers are already providing invaluable help to thousands of Businesses around Australia, indeed around the World. It would seem that there are few professions or areas of commercial endeavour that cannot be streamlined or made to be more "accountable" with a Microbee Computer.

## Butchers, Bakers, etc.

Users range from publishers to pathologists, even car yards are finding the Microbee Small Business System the cost effective technology tool that keeps their records straight their correspondence in order, and keeps them in touch with the fast moving world of Data Communications and Videotext Services.

$$
\begin{aligned}
& \text { SPECIAL OFFER } \\
& \text { 'Living Letters Package' }
\end{aligned}
$$



## The Software You Need

With the Microbee's now famous Bundled Software and CP/M operating system most routine computer functions are catered for without spending another cent, but it is highly likely that it is in the area of specialist applications software that Microbee scores most points. With so many third party software supplies able to provide specific solutions at realistic costs that don't in themselves create problems (check the prices of software to run on so called Compatibles).

## User Friendly Interface.

Every Microbee Small Business System has its own user friendly ' B -Shell" which allows the easy choice of software by simple one finger selection of self explanatory ICONS. A comprehensive Help system is supplied and 'housekeeping' functions are simplified.

## Australian Guaranteed

Built to exacting control standards and World class quality the Microbee System is particularly robust remember the Microbee was first developed for use in schools, and in fact the same machine is in extensive use in schools, both in Australia and overseas.

## The Complete Business Package

The Microbee Small Business System comprises:
Microbee 128 K Computer
Dual 400K 5.25" Disk Drives
High Resolution Monitor
DP100 Dot Matrix Printer
Cables and full set of manuals plus

## Bundled Software

worth hundreds of dollars includingWordStar/Mailmerge 3.3, Microsoft Basic, Microsoft Multiplan, MicroWorld Basic, Telcom Communications Package. Full range of support utilities, Comprehensive Training Guides and Tutorials, A complete library of manuals so you can easily and quickly gain the maximum benefit from your system is also included.

## The Price

For the complete Small Business System only $\$ 2395$ including Sales Tax

As many of the Microbee Systems out there are used extensively for Word Processing with little need for Microsoft Multiplan, the new 'Living Letters Package' has no Multiplan or Microsoft BASIC. But it does have The Complete WordStar Package to bring life to your writing and considerable savings to your pocket

## EXCITING NEW OPTIONS

As part of Microbee's Product Innovation Program, new releases which will shortly be announced include:
The DP100 NLQ or Near Letter Quality Printer. The MB 7030 High Resolution ( 0.38 pitch) RGB Colour Monitor.
The MB 3010 Green Screen Monitor. The ESE Economy RGB Colour Monitor.

## Microbee Technology Centres

| New South Wales | Victoria |
| :---: | :---: |
| 1 Patison A venue. Waitara. N S.W. 2077 Phone (02) 4872711 | 50.52 Whitehorse Road. Deepdene, Vic. 3103 Phone (03) 8171371 |
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## The thrilling new C-Series from KEF

## Now the famed bestselling British speakers attain new standards of exceptional performance.

KEF'S C-Series speakers have been bestsellers for more than two decades, winning the loyalty and delight of audiophiles the world over. Now the elaborate research tools developed by this trailblazing British manufacturer have produced numerous performances and aesthetic improvements in KEF'S remarkable range of six new C-Series models for 1985.

New cabinet design, new drive units, and radical new crossover networks for lighter amplifier load are just some of the features. And the innovative use of polypropylene as the revolutionary new cone material has brought the major benefits of reduced colouration and substantially improved efficiency. From the tiny but amazing C10 bookshelf speaker right through to the elegantly tall C80 with its exceptionally high sensitivity, these latest speakers by KEF are masterpieces for the money.

## Historic electronics store demolished!

## - DSE Gore Hill goes under the hammer

Mike Wilson, Managing Director of Dick Smith Electronics, struck the first blow for the demolition of the firm's historic Gore Hill store, which opened for business 13 years ago in December 1972, amid heckles from a 'rent-a-crowd' of protesters led by a slight, energetic, darkhaired man in horn-rimmed glasses whom nobody could identify.

Carrying banners declaiming "Don't desecrate Dick's" and "Free Norm" (who? - Ed.), and chanting "shame, shame, you're to blame" ${ }^{\text {, the protesters were }}$ herded away from the barriers by Police (I thought they were a pop band - Ed.) before the dignitaries (some bloke called Chuck - but who was the spunky lady with him?) arrived.
So much for the fun, here's the real story.

The Gore Hill store is being demolished to make way for a multi-storey development on the site. During its construction. DSE will have a retail store located virtually next door so that those customers who've found the location convenient can still shop there
Upon completion of the development, a new retail store will open on the ground floor of the new building, so DSE will 'return' to the original site.
The Gore hill site was Dick Smith Electronics' first 'proper' shop, having previously traded from the front bedroom of a small house in nearby Aitcheson Street, Crows Nest.
The original Gore Hill store was on the ground floor of a two-storey building, with offices upstairs. It was from this location that Dick Smith built the chain of stores that spans Australia and New Zealand. Later, the whole building became offices and warehouse and a 'self-serve' retail store was opened next door.
The now-famous annual DSE catalogues were 'launched' from the Gore Hill headquarters, the first appearing in 1973. These catalogues became the cornerstone of what was then a unique mail order service in electronics, and have become an 'institution'.

Rapid company expansion
brought on 'bursting-at-theseams' disease and headquarters moved to Carlotta Street, Artarmon four years later, in 1977.

Despite swallowing up surrounding building space there, 'bursting-at-the-seams' broke out again just two years later in 1979. necessitating a 'clean start'. A new headquarters building was designed from the ground up and installed, in 1979, on a site at North Ryde industrial park. where the company headquarters is still located (though somewhat enlarged). The largest Australian flag in existence flies from a flagpole out the front, on the corner of Lane Cove and Waterloo Roads, North Ryde.

## Anniversaries

Just to prompt a few memories, and to record appropriate things in this column, we'd like to remind you of a few anniversaries.

1985 marks the 75th anniversary of the Wireless Institute of Australia (WIA), the oldest radio society in the world! The various state divisions of the WIA held anniversary celebrations, the NSW Division (the 'founding' state) undertaking a number of activities, including a celebratory dinner and the assembly of a 'time capsule' with contributions from local amateurs. Australia Post joined in with the issue of a special stamp and envelope and the Department of Communications permitted the use of special callsign prefixes and special WIA callsigns. The Federal body of the WIA held a grand dinner in Melbourne, attended by a prestigious group of overseas visitors from Britain, Japan, Indonesia, Hong Kong,


New Zealand, Pakistan, Korea, Finland, the USA. The Peoples Republic of China, the International Telecommunications Union, the International Amateur Radio Union and our own Minister for Communications.
This year also celebrates the 50th anniversary of the birth of radio direction and ranging radar. In 1935, experiments in Britain proved the feasibility of detecting aircraft and ships at a distance by the reflection of radio waves. A prominent Australian, Sir Robert Watson-Watt, was a principal in the development of radar during WW II.
The development of the magnetron, now widely used in domestic microwave ovens, was one device fundamental to the technological progress of radar.

## Bell me blues

Are we the only people having telephone problems? Our impression is that the network is gradually becoming overloaded. perhaps not surprising with the proliferation of modem users, bulletin boards. and other electronic information services like Telecom's Viatel, or E-Post etc. It seems
unlikely that Telecom expected or provided for the colossal increase in the use of the network.
In busy periods (mid-morning and late afternoon) it is quite normal to get the engaged tone although the called party later confirms categorically that his phone was not in use at the time.
Recently, the whole of Sydney's telephone service appeared to become unworkable. Apparently, the reason was a series of radio announcements that tickets for the latest Dire Straits concert were available at booking agencies. Telecom say that the flood of calls resulting from these announcements severely overtaxed the metropoli$\tan$ network.
Whilst we can sympathise with Telecom planners at their failure to predict telephonic hysteria, it does seem possible that the margins for general growth built into network planning were thin to say the least. It really isn't going to be very impressive reading Telecom's self-congratulatory pieces about moving into the electronic age with Viatel, E-Post and the like if the ordinary telephone fails to work.

## JAYCAR - APOLOGY <br> OOPS!

In the November issue of AEM, page 31, we advertised the Avtek MultiModems at the incorrect price. The correct price for these modems is as follows:
Standard MultiModem, cat. no. XC4820 - \$365
Autoanswer modem cat. no. XC4822 - \$399
We apologise for this oversight.


## That'll learn 'em

In response to the growing interactive commercial video disc market. Pioneer Australia has enlarged its operations with an expanded Video LaserDisc Division. additional customer service, and plans to introduce new product lines over the months ahead.

Pioneer's recently appointed Commercial Video LaserDisc

Manager, Doug Bell, describes the potential for the high quality audio visual medium in Australia as "enormous", and says "We have barely uncovered the tip of the iceberg. The true potential of the Laserlisc as a totally programmable and completely interactive learning or information bank is limited only by the imagination"
The company's expansion coincides with the introduction

## PARAMETERS MOVES IN SYDNEY

Parameters, Sydney, moved to larger modern premises at North Ryde in November.

Managing Director Bruce McCarthy said that the move will allow continued expansion of the Company's activities in the fields of industrial process control and industrial weighing as well as its test and measurement activities.

The new building will incorporate a lecture theatre for customer instruction and a large demonstration area for the Company's range of computer-based instrumentation.

The new address is: 25-27 Paul Street North, PO Box 261, North Ryde NSW 2113 (02) 8888777.

## Bi-annual trade fair for Munich

From October 1986. Munich's unique interlinking electronics fair concept will include a new international trade fair. SYSTEC 86, International Trade Fair and Congress for Computer Integration in L.ogistics. Development, Design. Production and Quality Assurance, will have its premiere on October 17th to 30th 1986, at the Munich Trade Fair Centre.
SYSTEC is being organised by the Munich Trade Fair Corporation (MMG) in co-operation with the SYSTEC Advisory Board, and will be held every two years.
The Advisory Board includes representatives of Digital Equipment. IBM, Matra Datavision, Nixdorf. PCS. Softlab and Sie-
mens. The Chairman of the SYSTEC Advisory Board is Eberhard Farber of PCS Periphere Computer Systems.
The target groups the MMG has in mind for SYSTEC include development. design. production and system engineers in small. medium-sized and large companies.

The exhibition range will comprise information systems. logistics. CAD, CAM. CAE. CAT, etc, software. networking. testing systems, peripherals and variable automation systems.
The 1986 Congress, which is being sponsored by the Association of German Engineers (V'DI). will be dealing with two general subjects. "Computer Application in Planning and Development and its System Aspects" and "Flexible Automation"
of two new PAL products, giving Pioneer the capability to provide hardware and discs to suit either the PAL or NTSC format.
The new PAL player is the LD-V 4000 which. like the NTSC model 4000 and 6000 , is a third generation player with a number of unique benefits over earlier models. This series uses a solid state laser which is less than one fifth of the size of the gas laser used in earlier products, allowing the players to be smaller and lighter. All Pioneer's current range are 420 mm wide. stackable and front loading, making them much more versatile and easy to use.
A PAL microcomputer is the other new product. It is the MSX-standard PX-7 and is custom built for controlling and programming video disc players. Some of its special features are the ability to overlay computer images over disc images. and sound synthesising in stereo. Memory capacity is 48 K . and a 16 K EPROM cartridge or $3.5^{\prime \prime}$ floppy disc can be used as a storage medium.
Future products suitable for
commercial applications under development by Pioneer, include an automatic videodisc changer for extended presenations, and a portable video laserdisc player which is carried about like a briefcase and can broadcast directly to a television receiver.
For further information contact Doug Bell, Pioneer LaserDisc Division, on (03) 5809911.

## Australian solar lighting for Fiji

Amtex Electronics, a division of TLE Electrical Pty Ltd, inform us that they have scored an export first with the winning of an order for 10 solar systems to Fiji.
The 10 systems, each comprising a one metre by one-half metre wide Australian made solar panel, fluorescent lighting, cable and deep cycle batteries will be used to provide internal lighting in several Fijian homes.
Amtex is a supplier of solar systems to Australian government and industry, and is confident of developing new markets in the Pacific for the system.


## TEN-GRAND OF TASCAM FROM TEAC FOR TECH

A donation of TASCAM audio equipment valued at around $\$ 10000$ was recently made by Teac Australia Pty Lid, to the School of Applied Electronics and Telecommunications at the Royal Melbourne Institute of Technology.
Tascam national product manager, Travers Falkiner (right), officially handed over the equipment to the Head of School at RMIT, Mr Kevin Alsop (left).
Mr. Alsop said the additional Tascam equipment would provide valuable hands-on experience for technicians and apprentices in broadcasting and audio courses at the School of Applied Electronics and Telecommunications.

# Ifit wasn't Philips you might think it was a trick 

Philips' new high-performance dual-trace PM 3206 oscilloscope won't make your money do a disappearing act.
But its remarkable specification might make you think it had been pulled from a magician's hat.
In fact, the only trick Philips has up its sleeve is the ability to conjure up modern design and production techniques to keep costs down without compromising quality.

The formula of features for the 15 MHz PM 3206 includes:

- Easy-to-use. reliable triggering. even on complex waveforms
- Wide sensitivity range 5 mV -20V.
- Bright CRT with excellent display
- Robust construction tested to withstand the harshest environments.

Philips Scientific \& Industrial
SYDNEY: Box 119
North Ryde 2113
Tel. (02) 8888222
(Toll free (008) 22 6661)
MELBOURNE: Locked bag No. 5
Oakleigh South 3167
Tel. (03) 5423600
(Toll free (008) 33 5295)

A NEW MONSTER CABLE


So what could you possibly do to a speaker cable to make it sound better?

As you know Monster cable has been the reference standard for high performance speaker cables since it first appeared on the market. Since then we here at the Monster have furthered our research into the electro-magnetic behaviour of audio signals as they travel through wire. the results have been significant
Cable construcition is the most important factor in speaker cable performance. Even small changes affect the sound, and not always for the better. But our knowledge of how to control complex current fields has allowed us to make sonic improvements to the original Monster at virtually no increase in cost. Brain power is a lot cheaper than copper! So, we've reconstructed the winding and stranding configuration of Monster Cable to conform to our latest research and the result is the new Monster Cable The most obvious sonic differences are tighter bass, better inaging, (the ability to locate instruments across a speaker soundstage), and clearer, more open sound in the midrange. But don't take our word for it, take a listen for yourself.

Happy listening.

## FULFILLING THE PROMISE OF DIGITAL. INTERLINK CD.

## Super high resolution compact disc interconnecting cable. Fulfilling the promise of digital.

Conventional cables run out of breath when trying to cope with today's digital sound. The incredible sonic capabilities of the compact disc player pushes all your components to their limit ... including your connecting cables. The incredible resolution, awesome dynamic range, and powerful bottom end available from your compact disc player, is lost when you use ordinary interconnect cables.

## Why a special cable for Compact Disc?

In addition to the high sonic performance that effortlessly and accurately transfers everything you player dishes out, Interlink CD is specially designed to compensate for the transient and phase distortions of the digital process as well as minimizing some of the "harshness" found in some CD program material. Using our revolutionary "Bandwidth Balanced ${ }^{\text {rw }}$ ", technology we can control high frequency phase shift to produce a more enjoyable, thoroughly musical sound. Bass is tighter, transients are quicker, cleaner, and less smeared. The sound is more coherent with richer harmonic overtones and a 3 dimensional depth and "liveness'' to the music. Interlink CD fulfills the promise of digital with music reproduction that brings you closer to the original performance.

## Video today and tomorrow - the view from Japan

## Malcolm Goldfinch

It is not easy to get people in Japan concerned with the video industry to speak out frankly about the Japanese view of mistakes, problems and directions past, present, and future, of this burgeoning industry.
Malcolm Goldfinch was able to do this on a recent visit to Japan. The views expressed are in response to questions he put to a number of well-informed Japanese executives, in both the consumer electronics and camera industries, which are now becoming integrated, and cross-manufacturing their specialties for each other.
Q. Where do you see the video industry at this moment? A. Without exaggeration, it is in the throws of a revolution. Now VHS has won the half inch format battle, the next conflict will be within the 8 mm format; but the greatest impact will come later from the move to digital technology. It has already started in the audio section of the V8 format.
Q. Why did VHS win the half inch video format war when Sony started with a big lead for Beta?
A. Video formats are like languages, and each area of use can only support one tongue. Professional video recognised this problem at an early stage, as once tape went into cassette it travelled easily and had to playback instantly with proper sync and colour balance over a network.
Home video was less fortunate, with so many formats around the world, and was further complicated by TV standards: NTSC, PAL with variations, SECAMs. Universality of VCR video standards was possible even if the TV standards were not.
The major market for home video was in the NTSC areas of Japan and the Americas, which was why Philips V-2000 format never really got going; having a home market split by PAL and SECAM TV.

This left only two contenders for a world standard, the original US leader, Sony Beta, and JVC-Matsushita (National) VHS. We give credit here to JVC for being front-runner in organising a powerful group of VHS manufacturers who benefited from the superior technology developed, largely by JVC.
It is significant that Sanyo, NEC and Toshiba, who were original Beta suppliers, have switched to making VHS to retain market share. As a result, Japan has the responsibility of undisputed leadership in the world of video.
Q. Why didn't Sony make a VHS model?
A. Pride is a very very important factor in the quality of Japanese goods; sometimes it is not so good for commercial decisions.
Q. Has sales of portable video, for making home movies,
kept pace with VCR used for timeshift and library video? A. No, but this is because growth of home VCR use has been beyond anyone's imagination five years ago. Keeping up with demand for VCR has been a problem and portable video requires a lot of sales education effort with slow results. But, this is all changing now.

In consumer electronics, video is expected to remain the front runner, although it is approaching saturation levels for home VCR. Sales in units have been: 1983-3.7 million, 1984 $-4.3 \mathrm{~m}, 1985$ estimated 4 m approximately.
The market has stopped growing for the first time, since last September, but the camcorder boom, with added impetus from the V8 format, is expected to make up losses.
The time for light, portable video, camcorders, has arrived and is expected to be a totally new market; perhaps not quite as big as the timeshift VCR proved to be.
$Q$. How do you expect this to affect chemical photography? A. The photographic and camera industry will suffer; for home users it will largely become part of their video. Sony and Canon already have good colour print hard copiers; as well as other film companies. Film processors will make hard copies from video cassette (VC) if you do not own a copier. The consumer will accept a drop in definition because the choice of shot copied will always be the best pose.
$Q$. Will the new V8 format dominate the camcorder market? A. A good question. Originally, V8 was promoted for this purpose, but no-one quite knows. Betamovie first established the one-piece camcorder but it required a Beta VCR as well to see the picture.

For VCR owners in any format, the camcorder will always be an accessory. But it will allow very simple edits to an "album tape" in the home VCR. This opens a whole new field of home entertainment.
$Q$. Is there likely to be any future format battles now there is only one 8 mm format?
A. I would like to say - no conflict. Sony is currently out in front with a whole range of V8 products within the for-
mat and filling in many of the technical grey areas left. As Sony is technically very competent it is good that someone has taken a lead. Other manufacturers would be advised to see their offerings are fully compatible.

There is no secret about the disputes amongst the V8 consortium members that delayed the final format agreement. Not all these differences have been resolved they and centre on the fact that the original object of V8 was to produce a camcorder of better than 2 kg and 1 kg lighter than the smallest VHS/Beta camcorder, but only Sony's just-released CCD-M8 has achieved this objective.
But, before the 8 mm agreement was reached, JVC had, by brilliant new technology, reduced their half inch head-scan drum to a size similar to the proposed V8 standard and marketed a 2 kg camcorder, including EVF and TV playback. It used a compact half inch VHS compatible VC that fitted all VHS machines, with a small adaptor. This was only slightly thicker than the V8 VC.
It also became common knowledge that Matsushita was about to use the same technology to release a full VHS camcorder with playback, yet only about half a kg more in weight. It was then the question of a new video format being necessary at all was widely canvassed by JVC.
A factor saving 8 mm was Kodak's involvement. And Pioneer, a long time video disc promoter, decided to join the magnetic tape, video world by way of V8, offering a badgeengineered camcorder, at the same time promoting a dual roll for the V8 format as a DAT (digital audio tape recorder) with 6 PCM stereo tracks, each lasting 4 hrs on long play, sampling 8 bits at 31.5 kHz for a $\mathrm{S} / \mathrm{N}$ of $80+\mathrm{dB}$ and response from $20 \mathrm{~Hz}-15 \mathrm{kHz}$.

Although the DAT is not fully ratified by the V8 conference, Pioneer and Sony have such a dual purpose VCR to V8 format, incorporating both TV and FM tuner, timing, on demonstration, and for imminent sale, JVC, Matsushita and Hitachi are reported to be on the way; yet they are not showing signs of releasing a camcorder in V8.
The whole scene is very confusing. Audio, video and digital are in a soup pot! With the digital audio capability, and perhaps a later digital video potential, there is a valid reason for a V8 format VC, at least.
Q. Is light weight and compactness the most important
objective in camcorders?
A. No. I think many consumers will make compromises to get all the features they want in one package. Playback direct from the camcorder to any TV, and EVF (TV viewfinder), and even the use of a VHS standard VC, may prove to be as important as reduction in size and weight. Consumers will make this decision when they learn the options; also, deciding the importance of the extra cost and bulk for auto-focus.

Considering the Matsushita M1 full-VHS camcorder is only 2.5 kg , and the Sony CCD-V8, 2 kg , both without batteries, the extra half kilogram in VHS can look very attractive for the added advantage of being able to go to any library and hire a top film on VHS and play it on the hotel TV, just by plugging into the aerial inlet.

AIso, who has not wanted to copy a VC? Akihabara dealers in Tokyo report good sales of full format VHS camcorders. The VHS group say they are winners, why confuse your customer with a new product. Toyota buyers mostly buy Toyotas, they say, don't they?

Hitachi, who announced a prototype V8 camcorder some time ago, with their VHS CCD camcorder selling well, now appears to have a similar outlook.
Q. Well, where does the VHS group stand in relation to V8? A. Firstly JVC, the leaders, find their go-it-alone with the compact VHS camcorder GRC-1 VideoMovie is gaining great consumer acceptance. Bad experiences with failed formats in records, hi-fi and tape recorders have made consumers wary.

They prefer to keep to one well-established format and VHS has, what, say at a guess, 10 million VCR worldwide? A compact and compatible little VC in VHS will have a lot of appeal if they decide to add a camcorder.

JVC are reported doubling their current 40000 per annum VideoMovie production rate to meet demand, and other VHS suppliers are reported considering production of a VHS-C camcorder.
Matsushita, on the other hand, is the most advanced of all in V8, having been the supplier of Kodak's V8 two-piece, portable video with tuner/timer dock, for over a year. So they are well into V8. This allows them, along with Hitachi who has also released a full function VHS camcorder, to concentrate on selling fully compatible camcorders to the big ownership of home VHS, VCR, most of whom are satisfied customers and like the idea of a portable which allows compatability. The VC of a baseball match can be taken straight home and played on the home VCR, no connections required. This explains Betamovie's popularity.

## Q. Where does Beta and Betamovie go from here?

A. Perhaps Sony would like that answer too. Recent attempts to upgrade Beta have resulted in compatibility problems which always upset and frighten consumers, many of whom are fanatical Beta supporters. First, there was NTSC Beta HiFi which would not work under PAL TV standards, and the VHS system had to be adopted. Now there is a change in Beta video bandwidth. What they call high-band Beta has shown inherent problems; some call it "quality innovation".

For Beta Hi-Fi, the video carrier signal had to be shifted 400 kHz to make way for an adequate audio range, but FM audio with four carriers must be shifted by 600 kHz for sideband room, some 800 kHz in all. It was taken from FM audio, and although carriers were raised, the bandwidth was not and compatability was impossible.
So the new Beta format VCs, which are increasing in numbers at video libraries, when used on existing Beta VCR, give a noisy or dim replay. Sony has not claimed "compatibility" between the Beta formats, but "co-existence". But this only applies to owners of Sony high-band Beta VCRs. Sony is a great company, highly respected for its technical competence, but attempts to win a new market share by turning to past supporters is not a way to regain their confidence.
$Q$. Where does the camera industry stand in video now and in the future?
A. The time for photography to join with the consumer electronics revolution has already arrived. From the beginning of portable video, camera people have been involved and in so doing learnt about electronic controls for aperture, measuring light and focus. The lens technology has all come from the camera industry and now the flow is back to the camera manufacturer as they design and manufacture their own video systems. Canon is the undisputed leader in video as they have always been heavily involved in electronics, calculators, copying machines and now computers too. But, for the last decade, the camera has been moving from being a calculator with a lens and film, to a computer with lens and floppy disc.
The microchip with a window, we know as a CCD (charge coupled device), is now used in many camcorders instead of picture tubes, and is about to replace the film in still cameras.
There has been a great resentment to the inroads of electronics into the camera industry, mainly in Europe and to a degree in America. In Japan, camera and film manufacturers have recognised that chemical imaging will be replaced by electronics, so they helped with the video success of Japan electronics companies, while envying them. Now, with the camcorder, they join them on equal terms.

Sony's Mavica was premature, but the concept is sound.

Matsushita and Canon as well, and the major camera companies, are well down the road to electronic photography.
$Q$. What about the film companies, where do they go in electronic photography?
A. Film companies are already trying to catch up with lost opportunities in the magnetic tape market. They were geared to be the logical manufacturers of recording tape, but, except for Agfa and Fuji, they left tape to chemical companies. Kodak now recognises this mistake quite openly and is selling VCs made by TDK to obtain a market niche.

As world pioneer of the V8 format, Kodak is leading the film industry into the heart of Silicon Valley. Here, Fuji has been making tapes for broadcst stations for many years, and lately $1 / 2^{\prime \prime}$ VCs supplied to leading VCR manufacturers of both formats. Now they are also entering the VCR field through the V8 format and supplying high density VCs. Konica and Kyocera are on the same track. TDK, probably acknowledged world leader in tape technology, is keeping at arm's length from hardware for tape use and is on good terms with all the industry for this reason. Without TDK's know-how, 8 mm may never have succeeded.
Q. What does the digitising of video mean; you said at the beginning about video, ". . . but the greatest impact will come later from the move to digital technology. It has already started in the audio section of the V8 format.'"? A. Firstly, may I answer the PCM, or digital audio part; they both mean the same thing. This is where 8 mm compatibility is breaking down. With VHS and Beta you had two different VCs; if they fitted into a VCR they worked with just a little tracking adjustment; until high-band Beta. With V8 there are options in the format for either modulated FM (mono) or PCM (stereo) audio. Some equipment will use one or the other and these VCs will be incompatible. In camcorders, for space, weight and cost reasons, a dual system appears out of the question. Also, the sizes of some VCs are not the same. Some can play only audio, and on the specific type of PCM VCR they came from.
The current V8 format PCM sound will not suit some hi-fi fans, so the Pioneer DAT audio system is an option used for the 6 -track, 24 hour superior audio. Even this is not good enough for some, so a higher sampling frequency is also mooted with another sub-standard. No one seems to know for sure where these variations are leading and the VHS/Beta conflict could be repeated within the V8 format. Conference committees seldom reach a firm agreement. The success of Philips' compact cassette was due to their rigid and dictatorial control of one fully compatible standard.
The V8 video and audio specification may hold in analogue terms, but the audio variation in digital is very confusing at present. Only time can provide the answer.

## Q. And the digital video, when may we expect that?

A. The digital camcorder, or two-piece portable, even the home VCR, seems to be a long way in the future. The NTSC markets will probably determine when this PCM change happens; your PAL areas will have to wait. It is likely that the whole decision will be governed by a radical change from NTSC to a HDTV (high definition) MUSE TV, now on demonstration by Japan's National TV Corporation NHK, it is their standard. The change is overdue and the logic of a new TV standard for NTSC areas, as the whole TV goes digital, is beyond question.

As you know, TV sets are now part digital, yo-yo-ing from analogue to digital and back. Satellite standards for signals in chrominance and luminance sampling are well advanced and the idea of digital signals, perhaps direct from the CCD in the camera, all through the chain to an analogue converter at the TV picture tube or array, must be the ultimate objective.

Any improved TV standard may require a broader frequen-
cy band for transmission and in the VCR. Hitachi has produced a prototype PCM VCR for NTSC video and audio with a bandwidth of over 4 MHz compared to the normal $3+\mathrm{MHz}$, but recording time is about an hour. A MUSE HDTV bandwidth would be at least half as much again. Until these problems are overcome, PCM video will not be commercially possible. Only time can provide the answer.

I think I said at the beginning, the whole video industry is in a revolution; so is TV. The outcome can only be guessed at.
Q. Please make a guess?
A. Well, although the 8 mm format seems assured of a long term as a major format in magnetic recording, it may be mainly in the area of portable video, and in hi-fi. Unless the film makers are prepared to release hits on V8 at the same time as VHS, this format may never become a viable time-shift or pre-recorded, recorded-VC player.
Dubbing from camcorders into existing home VCR will be the first major use of V8 format. Buying a V8 format, home VCR will mainly be for the first-time buyer. As the VCR market is now approaching saturation in many countries, this may not be a big market.
As the V8 format is at its top extremity to record analogue video, there seems little chance of it accomodating digital video in current NTSC; HDTV would be well beyond reach. This leaves the $1 / 2^{\prime \prime}$ VHS format as first possibility for such a digital VCR for home use. It may even be PCM HDTV.
Just consider the same technology used to make V8 viable, now applied to VHS, and add the ability to double record in cross-azimuth depth, using the VHS hi-fi mode; not possible in V8. Using digital logic filters there would be little chance of crosstalk and the bandwidth is doubled. In depth recording, the second track may exclude the use of V8 $7 \mu \mathrm{~m}$ tape technology, but new high density coatings could allow much depth recording and tighter bit density; aided by new record/playback heads used in V8, or even thin-film head technolgy. As I am guessing, such logical solutions are probably being tested under tight security in many R\&D labs.

On the other hand I am not saying we must not discount a maverick solution to the digital problem. Stillvideo cameras tend towards a magnetic disc storage. If such a disc could have an adequate storage for some hours of replay, they could be the trump for digital video. Such a disc, using CD (compact disc) physicals, is well advanced at TDK and known as a CD-ROM (read only memory). Existing laser technology allows a once-only record function and unlimited playback. The disadvantage of a new disc for every recording is somewhat offset by instant programme location. The cost per disc will be the crucial factor, and as it is reputed to be a dye-based system in plastic, hopes are high for wide economic use of this CD-ROM; perhaps in video too.
$Q$. It seems to me, that the consumer electronics, camera, and to some extent the computer industries, are all involved in this TV/video revolution. As Japan has what must be the largest single market share in the world of these products together; considering a near monopoly in video, you have a great responsibility to see that the revolution is an orderly one; both for Japan's interests, and the rest of the world? A. Agreed, and this is why the proposed V8 format was so widely circulated for approval. As a result, of the participating companies, 25 are outside Japan, while the Japanese companies have most of their trade offshore and regard themselves as truly international. This is the pattern you may expect for all new standards and formats; where Japan has an obvious initiative.
> Q. Perhaps we can review these questions and answers with what has happened in a year's time? A. Yes, that would be interesting; I hope, not too embarrassing.

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A2
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A3
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A4
Q5: How many LEDs are there on top of Elami Jr's head? A5

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## Getting the best TV reception



CHOOSING THE RIGHT CABLE for a TV antenna installation is an important step towards having an efficient system. In fact, if a certain well known motor mechanic had been brought up in the TV antenna business, he would probably insist, "cables ain't cables". This applies especially to feeders used for television and other radio frequency (RF) systems, where electrical currents behave quite differently from thos in mains power wiring and electrical wiring in cars. Also, the relevant characteristics of a particular cable are likely to vary considerably between VHF and UHF television installations. Even on relatively short lengths of antenna feeder wire, the RF current at different points along the cable at any instant might be flowing in opposite directions. This phenomenon is not normally experienced on regular electrical wiring and I have had difficulty over the years in convincing some of my electrician friends that this condition not only occurs but is an everyday event in RF circuits. Also, RF energy picked up by the antenna may become trapped in the cable and bounce aimlessly backwards and forwards between the set and the antenna. When energy reflects in this way it sets up standing waves (VSWR) which can adversely affect reception, even on a good antenna in a strong signal area.

But just having the right feeder or cable is not the whole story, either. To get optimum results from the cable and antenna, the system must be carefully installed. Simple things that can go wrong during the installation, or bad planning where more than one outlet is to be provided in the system, can quickly knock dollars off the value of even the best antennas and feeders.

Many practices which are acceptable in general electrical wiring become quite unacceptable in RF systems such as TV installations. One of these is the seemingly innocent practice of connecting multiple points together, in parallel. Sometimes it works but more often than not it may cause snow or ghosting or both, on one or more channels. The sound, too, may be affected.

## Performance - the main factors

Perhaps the two main factors which govern the performance of antenna feeders, whether they be ribbon or coaxial cable (coax), are the cable impedance and attenuation jor loss. Even faulty installation techniques and careless handling of the fittings ultimately affects either the circuit impedance or attenutation or both. These, in turn, affect the quality of reception.


The impedance of a cable is affected mainly by:
(a) the diameters of the wire conductors.
(b) the distance between the wires, and
(c) the material between the wires, usually air or some form of insulation. This is called the dielectric.
The mathematical relationship between these variables determines what is called the charocteristic impedance of the cable.

Whenever the cable is manhandled, cut, bent. placed under clamps, wet or stretched, one or mo e of its vital statistics is likely to vary and alter the cable mpedance at that point. Accordingly, a simple understanding of : uth factors is the best guide to selecting and installing cables for 'TV reception. Standard values for TV installations are 750 hms . for coax, and 300 ohms, for ribbon.

For the signal to be efficiently transferred from one part of the system to another the impedances of the two parts should be equal, or matched. This means that the signal energy cannot be effectively transferred from the antenna to the feeder unless the impedances of the antenna and the feeder are equal.

Likewise, signal energy cannot be effectively transferred from the feeder to the TV receiver unless the impedance of the feeder, or line, in the distribution system is equal to the impedance of the receiver antenna connection.
Assuming then the antenna picks up a strong enough signal, everything should be fine, if:
(a) the feeder and fittings such as splitters or dividers, do not attenuate the signal beyond acceptable limits, and
(b) correct impedance matching takes place wherever any two parts of the system are joined together, including feeder-to-feeder(s) in multiple point installations.
Where it is necessary to connect together circuits of different impedances an impedance matching device such as a balun must be used. The small box-like adaptor used for
connecting 300 ohm ribbon to the 75 ohm socket on a receiver is a balun. Physically different, weatherproofed baluns are fitted to some antennas so that they may be used with either coax or ribbon feeders.
Attenuation is an important cable specification usually available in catalogues and technical literature published by the manufacturer. Where detailed figures are not available the cable may be described simply as either a regular grade or a low-loss type for a particular application.
Typically, attenuation is expressed in decibels per 100 metres, or per 100 feet, depending on the country of origin.
For example, the nominal attenuation of a particular cable might be ten decibels per one hundred metres, or

$$
\text { Nom. Attn. }=10 \mathrm{~dB} / 100 \mathrm{~m} .
$$

Fifty metres of this cable would have an attenuation of five decibels, 5 dB . Two hundred metres of the same cable would attenuate the signal by 20 dB . A linear relationship exists between the cable length and its dB attenuation. Accordingly, an average household run of, say, ten metres of cable having an attenuation of 10 dB per 100 metres would have a nominal attenuation of around 1 dB , a fairly acceptable figure.
Of course, the lower the decibel loss the better the cable, from an attenuation point of view. A three decibel loss in any system is a loss of half the available power. usually, losses of less than three decibels are tolerable but not always, especially in fringe areas where every decibel counts towards reducing snow and improving colour.
It is worth noting, too, that when the energy from the antenna meets a two-way splitter in the line, half the energy goes each way through the splitter. This is a 3 dB loss as far as each extension is concerned.

A ten decibel loss leaves only one-tenth of the original power, and a twenty decibel loss leaves only a hundredth. The relationship is logarithmic and each ten decibels raises the power ratio by a factor of ten.

The attenuation of most feeders, however, is not constant and increases significantly at higher frequencies. It is usual, therefore, to find attenuation specified for various frequencies throughout the VHF and UHF bands.
Television frequencies extend from around 50 megaherts ( MHz ) to 850 MHz , taking in the VHF and UHF bands. The upper VHF channels are in the region just above 200 MHz (see \#1 in this series). The attentuation of cables varies dramatically over this 800 MHz wide range.
The following figures were supplied for popular, economy grade coax cables for TV antenna systems:

| Nom. atiten. <br> at | dB <br> per 100m |  |
| :---: | :---: | :---: |
|  | CABLE A | CABLE B |
| 50 MHz | 6.2 | 6.81 |
| 100 MHz | 8.7 | 9.72 |
| 200 MHz | 11.8 | 13.89 |
| 500 MHz | 19.0 | 22.36 |
| 650 MHz | 22.0 | 25.67 |
| 850 MHz | 25.0 | 29.57 |

These figures emphasise the need to be fairly critical of cable required for UHF installations. Cable A has almost 20 dB extra loss at 850 MHz , the top of the UHF TV band compared with the lower channels in the VHF band. In terms of signal power, the loss at UHF is 100 times that incurred at lower VHF channels, such as Channel 0 and Channel 2.
Cable B has marginally higher losses at low frequencies but the losses are even greater at UHF, compared with cable A's performance.
The figures also highlight the need to keep antenna feeder in UHF systems as short as practicable to minimise losses
and retain video quality.
The manufacturer controls cable loss by choice of materials and how they are processed and assembled. The size and number of wires in each conductor and the dielectric material all affect attenuation. Even the outer covering influences cable attenuation. Some are specified only for use indoors where they will not be exposed to the rays of the Sun which may make them brittle and vulnerable to the effects of the elements.

In TV cables the losses caused by the dielectric are significant. Perhaps the best dielectric for the purpose would be a vacuum but this would be very expensive and difficult to maintain in practice. Next best is air but support is needed along the length of the wires, to prevent them from touching or shorting out.
Accordingly, low-loss feeders contain just enough solid dielectric to give this result. Foam dielectric in coax, and slotted ribbon feeder, are two examples. The dielectric in these cables consists largely of air and they may be more easily distorted or crushed out of shape than those which have a solid dielectric. They should be handled carefully.


## a low loss coaxial cable using spacers



## A LOW LOSS COAXIAL CABLE USING FOAM

## Practical practices

Understanding the factors which affect the attenuation and impedance of feeder cables such as ribbon and coax should help the installer to get better results from an installation. The job becomes more of a commonsense exercise rather than one of trying to remember a never-complete list of do's and don'ts.

For instance, allowing water to seep into the un-protected end of cable out in the weather will change the dielectric. This in turn will change the impedance. The water will oxidise the conductors and the cable insulation leakage and losses will increase. In time, a considerable length of cable may become affected by moisture and capilliary action in the cable. The antenna will be unable to efficiently transfer the signals to the feeder.
The exposed end should be sealed with a non-corrosive agent such as silicone rubber - before moisture enters the feeder. The sealant should be used effectively but sparingly so as not to change the characteristics of the cable.

Sharp bends in the feeder are another problem. They tend

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 GOLDRUSH: ETI 1500 KITSET. Super unit featuring:- Tune and discriminate - 4 modes of operation - VLF/TR design - Ground balance - Auto balance push buttons - Pre-wound search head - Very professional unit - Approximately $1 / 3$ price of many similar commercial built up locators - Audio and meter indication. Lets you know when to rejoice


## $)^{2}$

## Lay back your ears, here comes the B \& W model 808 <br> The conception of a design is generated from a requirement. It may be a requirement to produce a cost-effective product, or it may be a design to meet a wide variety of applications. <br> The requirement for $B$ \& W's Model 808 originated from the highly successful Model 801. B \& W say that the 801 has, over the five years of its life, been used as the reference standard monitoring loudspeaker in virtually all the major studios and recording companies of the western world for the recording <br> Many balance engineers in the studios engaged in recording popular music used the 801 and commented most favourably. <br> Their complaint, and the limitation on its use, were quite simply that it did not produce sufficiently high sound levels for their particular requirement. <br> 

and assessment of classical music.
It was therefore a logical progression that the musicians and balance engineers involved in all the various facets of popular music should also take an interest in what their colleagues in the classical division were using.

Some three years ago, EMI gave $B$ \& $W$ a specification for this requirement which was expressed in three discrete areas: - That the overall frequency linearity, freedom from colouration and distortion should be similar to 801 - That the sensitivity be doubled, to 91 dB for 1 Watt input, as opposed to 85
$d B$ for the 801 - That the new loudspeaker system should be capable of peak outputs in the studio listening room on the order of 125 dB .
These parameters resulted in the design and production of five totally new drive units which meet or exceed the

specifications laid down, particularly those of freedom from distortion at these extremely high sound pressure levels. The result is the Model 808.

The Model 808 is available now. Contact Geoff Matthews, Convoy International, on (02) 6987300.

## High grade, 4-hour chrome tape from BASF

BASF, who claim to be the only manufacturer of four hour high grade video cassettes in the market, has announced a hi-fi version of their best-selling chrome tape.
The new E240 chromdioxid hi-fi tape, specially formulated using thin film technology, offers significant benefits to tape users - even those without a video recorder with hi fi capability.
BASF Australia Consumer Products Manager, the ebullient Horst Hanfeld, says the tape offers improved luminance, chrominance, and signal-tonoise ratios in both mono and colour. The special formulation also has meant substantial reductions in the drop-out rate, an important pre-requisite for hi-fi recordings, say BASF.
Of particular advantage to music lovers, the new hi-fi cassette allows long audio recordings by three different means: recording the video picture with stereo hi-fi sound quality, recording picture from a video recorder and sound from a hi-
fi tuner, or recording audio only from a hi-fi tuner. The tape length is particularly suitable for longer classical recordings. BASF's Super Grade Hi-Fi video cassette is also available in the three hour, E180, length.


## Dastardly deeds dogged by Dick's dialler alarm

There's a whole lot of noise once alarm systems are activated. That may scare the intruder away, but there's little consolation for the neighbours. Noise pollution laws regulate that alarms cut off after about ten minutes, which is fine for the neighbours.
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broken windows or locks or perhaps the house still smouldering from the day's bonfire.
Once, guard dogs were our most reliable form of security. Even the rise of sophisticated electronic systems has left us little better off. They may be more sensitive, harder to fool, but the end result is still - a lot of noise.

Now, Dick Smith Electronics has introduced a " 21 st Century Guard Dog" security system, which produces immediate response. There's no need to wait for neighbours to phone you, that's if they're at home when your system sounds off.
At only \$249, a neat dialler can be fitted to the alarm (fitting extra) and your entire home can be monitored 24 hours a day, seven days a week.
Once the alarm is tripped it automatically dials Voice Call who monitor your system, and pre-arranged action will be taken. They may call the police, the security service, or yourself at work.
The system can also be linked to your pocket pager and a series of beeps or tones will alert you instantly, identifying various emergencies such as bur-
glary, fire, power failure, etc.
Available now from all Australian Dick Smith Electronic Stores, cat. no. L-5100.

## Receiver from Dual

Dual, the well known and respected turntable manufacturer from West Germany, has been expanding into other facets of the hi-fi industry.
Their latest product now released on the Australian market is the Dual CR1320 receiver. This is $2 \times 40$ watt receiver with a synthesised tuner, digital frequency readout, auto-scan, and a $7 \times 7$ station memory. The unit features facilities for connection of compact disc or two tape recorders with direct tape-totape dubbing.
Adjustments of volume, balance, bass and treble are by way of slider controls conveniently arranged along the lower portion of the front panel.

Finished in a handsome anthracite black and selling for $\$ 599$, the Dual CR1320 seems a fine example of quality product from the Dual stable. Details from Falk Electrosound, PO Box 234, Rockdale 2216 NSW. (02) 5971111.

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## New Nakamichi auto-calibration deck

Nakamichi has released a new auto-calibration cassette deck, the CR-7, offering: auto-caiibration for azimuth, bias and record levels playback head azimuth fine tuning (also via remote) - real-time counter auto fade Fl, central display system - cordless remote control.
The CR-7 discrete head cassette deck can be used in combination with normal hi-fi systems. and specifically conforms to the new Nakamichi

Home Audio Electronics System.

Released at the same time is the CR-5. Although the auto calibration and playback head azimuth tuning are not available on this model, the user is still assured of the same high quality performance.
The suggested retail prices are $\$ 2100$ for the CR-7 and $\$ 1400$ for the CR-5
Further information can be obtained from the Austratian distributor, Convoy International, on (02) 6987300.


## C64 SPEECH SYNTHESISER

Promark Electronics is running a special on a great little synthesiser for the Commodore 64. Costing just \$45, the Currah Speech 64 module plugs into the cartridge expansion port and a flying lead with attached DIN plug connects to the audio/video socket on the rear of the computer.

The unit has internal software with a text-to-speech interpreter, making it simple to use. All you do is type in SAY, followed by a phrase enclosed in quotes. When you RUN it, the Speech 64 articulates the phrase.

In addition, you can get it to say virtually any word in the english language by programming in 'allophones' - which are 'word parts'. The unit has iwo different-pitched 'voices' and you can program intonation on the allophones to give the speech 'life

Contact Promark Electronics, PO Box 381, Crows Nest 2065 NSW, or call toll free, on (008) 226226.

## aem hi-fi review



# The Nakamichi OMS-7 compact disc player 

Nakamichi's supremacy in the design and production of cassette tape machines is unquestioned. Entering the 'fray' late in the piece, Nakamichi spent a lot of money on 'homework', learning the fundamental physics of the whole system. The same approach is evident from our examination of the OMS-7. While others have reviewed the current crop of CD players and found them 'perfect', clearly Nakamichi disagreed. The results are unquestionably superlative, but there is still the odd surprise. The question is, can you improve on "perfection"?

AS WITH MOST Nakamichi products, it has all been worth the wait. That is, providing you are prepared to spend the money necessary to invest in the relatively large cost of either the OMS-7 or the lower cost OMS-5. We have reviewed here, with immense pleasure, the Nakamichi OMS-7 CD player. In all respects the OMS-7 certainly is a magnificent piece of equipment. In fact, our test instruments are probatly sick of the struggle to keep up.
There has been continuing debate concerning the audible
quality of compact disc players, particularly for reproduction of "acoustic" instruments. Nobody can fail to acknowledge the astonishing values obtained in laboratory testing from now quite economical compact disc players. However, many listeners will assert that their enjoyment is marred by the grainy or harsh reproduction by the players of the upper frequency range.
My own opinion is that the Nakamichi player is a laboratory standard source. When viewed objectively, the perfor- -
mance achieved by the OMS-7 player in conjunction with the Denon test compact disc is nothing short of staggering. It is very easy to become complacent since the performance of compact disc players is known to be so good. However, one cannot but be impressed by the accuracy of reproduction, both on a laboratory bench and in listening tests, by the Nakamichi. My only criticisms are of a minor nature and will probably be considered insignificant by many potential purchasers.

## Features

The most important feature of the OMS-7 CD player is unseen - the quadruple oversampling by the digital filter. One of the main problems experienced with compact disc players, in fact many digital signal processes, has been that, what started out as a smooth waveform becomes converted to a pseudo-smooth waveform made up of a large number of very small steps.
Perhaps it should be interpreted as a tribute to the discriminatory powers of the human ear that this analogue to digital (A/D) conversion remains audible, and the approach Nakamichi has taken has been to increase the filter sample rate, in this case to four times the normal 44.1 kHz . For each of the 44100 data samples per second contained on the compact disc the Nakamichi takes four samples and averages them to substantially reduce any read errors. That is, 176400 samples are taken each second ( 176.4 kHz ). This is truly approaching a laboratory standard of performance. The signal analyser which we use in our laboratory samples at 256 kHz , so is only just able to claim superior resolution.
The result of this very high sample rate is unusually smooth sound quality, particularly at higher frequencies and for traditionally difficult instruments such as violins and other stringed instruments.
Typical of Nakamichi, and showing their awareness that they do not need to blow their own trumpet, is that the owners manual which accompanies the player makes only passing mention of the benefits of the high sample rate. The manual, gives adequate description of the features of the player. In brief the manual covers:

1. Safety advice and disc care
2. Controls and features of the machine
3. Installation
4. Machine use
5. Remote control operation
6. Specifications

## The nuts and bolts

Constructionally I was not disappointed. As with the RX-505 cassette deck which we reviewed in the first issue (July '85) the quality of construction of the CD player is superb. Everything fits properly, is exceptionally robust, and gives the distinct impression of quality. Aesthetically, the machine is also unlikely to generate complaint, although I will repeat my criticism of the tape deck that persons with poorer eyesight will find the CD player difficult, at least at first. The aesthetics of the smaller labelling cannot be faulted, it simply makes the device more difficult to use.
The layout of the player is simple - a servo controlled disc

| REVIEW ITEM: | Compact disc player |
| :--- | :--- |
| MANUFACTURER: | Nakamichi Corp. |
| MODEL: | OMS-7 and OMS-5 (OMS-7 actually reviewed) |
| FORMAT: | Standard front loading deck, remote controlled |
| PRICE: | $\$ 2300$ rrp. |
| SUMMARY: | We're still gasping |

tray, keypad type micro switches permitting eject/load, play, pause, stop, track skipping forward or backward and fast forward and reverse, the latter being operable during playback mode to permit cueing. Both the OMS-7 and OMS-5 permit automatic repeat, and both provide an excellent facility whereby remaining total disc play time may be checked.

In addition, the OMS-7 provides indexing, track number memorization for rapid track recall and the three outstanding features which set it apart from the OMS-5, remote control operation, a numerical track select keypad whereby the required track number may be simply typed in and recalled, and, surprisingly, an output level adjusted headphone socket. The second feature is invaluable where the machine is being tested with multi-track test discs, or being used professionally in broadcast or similar theatrical applications.
Both machines use a central LED display panel to show operational status.

The OMS-5 is a totally manual operation machine, but with identical circuitry and electronic performance. For professional use the OMS-7 is essential.

## Laboratory test performance

Most of our laboratory testing of the OMS-7 player used the Denon Audio Technical Test Disk. My impression at the end of testing is that, far from the Denon disk testing the performance of the Nakamichi, the player rather interrogated the compact disc! In relation to out of phase tests the phase cancellation between the two channels is audibly perfect.

Feeding both CD player output channels into a mono preamplifier and raising the gain to maximum there was absolutely no audible sound at all. Being a digital sound source this is not mathematically surprising! However, it is none the less quite exciting to experience. It is also quite exciting if the disc player advances on to the next track before you manage to lower the system gain!

## Distortion

Included on the Denon test disc are a number of subjective listening tracks which I will comment on later. The first objective measurements which we took were total harmonic distortion tests at nominal frequencies of $100 \mathrm{~Hz}, 1 \mathrm{kHz}$ and 10 kHz at 0 dB levels.

The output traces are shown on Figures 1, 2 and 3, for a linear frequency scale and harmonic distortion products will centre on one of the vertical lines if they are present. The actual test frequencies were the digitally correct values of $100 \mathrm{~Hz}, 1001 \mathrm{~Hz}$ and 9999 Hz provided on the test disc as the nearest value to the traditional frequencies but being exactly a prime number divider of the sampling frequency of 44.1 kHz . Nominal THD in each case is -80 dB which simply represents the measurement limit of our Hewlett-Packard analyser. In the case of 100 Hz and 1 kHz there is no evidence at all of harmonic distortion components. However, the 10 kHz does show a trace of third harmonic distortion at 30 kHz . Figure 3 also shows a number of high frequency components, well beyond the audible range ( $34 \mathrm{kHz}, 54 \mathrm{kHz}$, 78 kHz and 98 kHz ), which are clearly not harmonic distortion products. These components are identifiable as intermodulation products between the sampling frequency of 44.1 kHz and the source frequency of $10 \mathrm{kHz}(44-10=34$, $44+10=54,2 * 44-10=78,2 * 44+10=98$ ) and are, clearly, the most exciting find that we made in the course of our testing. There will also be an intermodulation product at 24 kHz (34-10) although there is no evidence of this on the trace and its level is clearly very low.

Figure 4 gives two traces, the upper being a THD trace for


Figure 1. Left channel, 100 Hz THD. Right channel 0.11 dB ; THD -80.71 dB, crosstalk -93 dB.


Figure 3. Left channel, 9999 Hz THD. Right channel $\mathbf{- 0 . 2 5}$ dB; THD - $\mathbf{7 8} .82 \mathrm{~dB}$, crosstalk $\mathbf{- 9 0 . 9 2} \mathbf{~ d B}$.
an input of 20 kHz ( 19999 Hz actual) in which more ultrahigh frequency intermodulation distortion products are present (at $24 \mathrm{kHz}, 64 \mathrm{kHz}$ and 68 kHz ). Again these are identifiable (44-24, $44+24,2 * 44-20$ ).

This aspect of the performance of CD players at high frequencies warrants considerably more attention. The highest visible IM product for the 20 kHz fundamental is 68 kHz , which could also have come from IM between 64 kHz and $4 \mathrm{kHz}(24-20)$ and until the relative importance of these second-order effects is able to be properly assessed we cannot really identify the real causes of the IM distortion.

It is also impossible for us, at this stage, to determine how much the IM distortion effects are due to the player and how much is on the test disc itself. We have not used this particular test disc previously and despite many tests of CD players being reported in other magazines, in Australia and overseas, we have not previously seen one mention of the IM distortion we have so clearly found.

At this stage, all we can offer is to investigate this aspect further and to report our findings in a future article. We can say that as the fundamental frequency increases, particularly toward 20 kHz , the unwanted sideband products increasingly enter the audible frequency band - voila!. . . . . harsh sound, "something is wrong at the top end"!!!

All this is leading us away from the Nakamichi. As I said at the outset, my opinion is that the machine is a laboratory standard test item, and it does not seem at all inappropriate to be searching out fundamental problems of digital sound

RANGE: 5 dev


Figure 2. Left channel, $\mathbf{1} \mathbf{k H z}$ THD. Right channel $\mathbf{- 0 . 0 1 ~ d B ; ~}$ THD - 79.88 dB , crosstalk $-91.8 \mathrm{~dB}+$.


Figure 4. Left channel, 19999 Hz THD. Right channel $\mathbf{- 0 . 5 7}$ $\mathrm{dB}, \mathrm{THD}-80.49 \mathrm{~dB}$, crosstalk $\mathbf{- 8 3 . 6} \mathrm{dB}$.
using test results from the device. Particularly when one is dealing with a CD player that achieves excellent audition.

From subjective listening tests we know that the Nakamichi is at the top of the performance tree. Our next task will be to examine the performance of other CD players, perhaps across a price range, to see how their performance in respect of intermodulation distortion compares with the Nakamichi. The Nakamichi will be, I can assure you, the yardstick.

Back to the objective testing. Performance compatibility between left and right channels of the player was found to be excellent with the output level of left to right being at all frequencies within .02 dB . Total harmonic distortion at 0 dB is effectively identical for each channel and separation drops gradually from 93 dB at 100 Hz down to 90 dB at 10 kHz . THD at 20 kHz is again a nominal -80 dB , the system measuring limit. The lower trace of Figure 4 is channel separation with an input attenuation of 20 dB from which it is seen that channel separation at 20 kHz has dropped from the earlier values to 83.6 dB .

The effect of signal level on distortion performance of compact disc players has been noted before - it is the opposite of analogue devices in that THD lowers as level increases. The intermodulation distortion products with the filter frequency however remain relatively constant, so that the benefits of the exceptionally low THD performance at higher levels is somewhat lost as other components begin to affect performance.


Figure 5.


Figure 6.


Figure 7.

Figures 5, 6 and 7 show THD testing for a 10 kHz input tone. As may be seen, the harmonic distortion products (i.e. ON the dotted lines at $10,20,30 \mathrm{kHz}$ etc.) lower as the input level raises, with the THD value being given at the bottom of each figure. Observe, though, that the IM product frequencies given earlier remain constant and become strongly apparent.
Just to satisfy yourself, look also at Figure 3 again, remembering that the scale has been doubled. We have not been able to measure the absolute level of the IM distortion products, however, the trend of the distortion performance of the player can be gained from the results of Table 1. Blank entries simply mean we took no measurements.

Intermodulation distortion test results are given in Figures 8 and 9. Figure 8 gives the products and waveform for equal amplitude 11 kHz and 12 kHz at 0 dB whilst figure 9 shows 250 Hz and 8020 Hz , amplitudes $4: 1$, at 0 dB . In both cases it is difficult to find fault.
range: 5 dev


Figure 8. (a) - top. (b) - bottom.


Figure 9. (a) - top; $250 \mathrm{~Hz}+8020 \mathrm{~Hz}$, mixed 4:1
(b) - bottom; 0 dB and $-12 \mathrm{~dB}(-1.84:-14.1)$.

## Frequency response

The swept tone frequency response for the OMS-7 is almost an anti climax at $20 \mathrm{~Hz}-20 \mathrm{kHz}+1-0.5 \mathrm{~dB}$ as specified. Figure 10 gives the results from our $\mathrm{B} \& \mathrm{~K}$ level recorder using the test disc source and the trace is suitably impressive.

The slight steps at $1.7 \mathrm{kHz}, 10 \mathrm{kHz}$ and 16 kHz were repeatable and must therefore be considered true performance. We have set the trace 0.5 dB down on the chart just to assist the reader.

Figure 10. Frequency response, Nakamichi OMS-7, 0 dB input.



Figure 11. 401 Hz reproduction.


Figure 12. 1001 Hz reproduction.

## Tone bursts

Tone bursts at 401 Hz and 1001 Hz are shown on Figures 11 and 12 respectively. The traces show waveform and it is performance results like these - no overshoot or instability whatsoever - that shows just how accurate the digital source is.

Similarly, the 100 Hz square wave components and waveform shown in Figure 13 ( $a$ and b) show little instability, certainly up to the performance of many top amplifiers.

## Signal-to-noise

As usual, signal-to-noise is stunning at 92 dB linear, 200 Hz to 20 kHz , and $101 \mathrm{~dB}(\mathrm{~A})$ for the same bandwidth.

The Denon disc does not provide test signals with and without emphasis and our test values should be compared with reports concerning other equipment for non-emphasis source material.

## Linearity

Our linearity tests were a little frustrating since all we could conclude was that the test disc tones were not linear. As noted earlier, performance from one channel to another varied typically by only 0.01 dB .
Repeated averaging of the same test tone gave variations from one sample to another also of the order of 0.01 dB only so confidence that any one result was repeatable was high. Variation from one 1001 Hz tone to another elsewhere on


Figure 13. 100 Hz square wave at -10 dB (note error in amplitude).

the disc gave variation of 0.3 dB however, so the variations in linearity indicated below are very possibly due to the test disc as much as to the OMS-7. This conclusion was supported by the 100 Hz square wave test (Fig 13) stated to be -10 dB level but found to be something less than -7.8 dB .

## Comparison with specifications

In comparison with the manufacturers quoted specifications, we cannot find fault. Briefly the test results for the more imnortant parameters compare as follows:
tABLE 1: DISTORTION

| Total Harmonic Distortion |  |  |  |  | Filter Intermod Products |  |  |  |
| ---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Input Level | 0dB | -20 dB | -40 db | -60 dB | 0 dB | $\mathbf{- 2 0 d B}$ | -40 dB | $\mathbf{- 6 0 d B}$ |
| 100 Hz | -80 | -74 | -65 | -38 |  |  |  |  |
| 1000 Hz | -80 | -71 | -59 | -36 | -55 |  |  |  |
| 10000 Hz | -79 | -60 | -56 | -33 | -60 | -58 | -58 | $<-58$ |
| 20000 Hz | -80 |  |  |  | -53 |  |  |  |

TABLE 3: LINEARITY @ 10 kHz

| Source Level | Measured Level |
| :---: | ---: |
| 0 dB | -0.27 dB |
| -20 dB | -20.27 dB |
| -40 dB | -40.29 dB |
| -60 dB | -60.41 dB |

TABLE 2: LINEARITY © 1 kHz

| Source Level | Measured Level (L and R channels) |
| :---: | :---: |
| 0 dB | 0.0 (measured up to -0.3 dB ) |
| -24 dB | $-23.99(\mathrm{~L})$ |
| -60 dB | $-24.0(\mathrm{R})$ |

## TABLE 4 TESTS vs SPECIFICATIONS

|  | Manufacturer | Our Results |
| :--- | :--- | :--- |
| Wow \& Flutter | unmeasurable | we agree |
| Frequency Response | $5 \mathrm{~Hz}-20 \mathrm{kHz}+1-0.5 \mathrm{~dB}$ | $<20 \mathrm{~Hz}-20 \mathrm{kHz}+1-0.5 \mathrm{~dB}$ |
| Signal-to-noise | Over $92 \mathrm{~dB}(\mathrm{~A})$ | $101 \mathrm{~dB}(\mathrm{~A})$ |
| THD @ 1 kHz | $0.003 \%(-90 \mathrm{~dB})$ | $<-79 \mathrm{~dB}$ |
| Channel Separation | over 92 dB | nominal 91 dB |
|  |  | OdB.84 dB @ 20 kHz |
| IM Distortion | Not stated | Refer above |



Figure 14.
No comments are made in the literature concerning spurious effects due to the digital sample process. Before finally leaving that topic to a future article we have included Figure 14, a wideband analysis of four separately recorded tones at $10 \mathrm{kHz}, 16 \mathrm{kHz}, 18 \mathrm{kHz}$ and 20 kHz . Being separate recordings, the Figure does not show any effects due to intermodulation distortion between the four tones, only those due to the tones and the sampling filter, so the true picture will only be worse.

It is easy to see how a very wideband performance amplifier might be looking at a lot of energy that we never thought was there.

## Listening tests

One of the real difficulties in describing state of the art equipment is to find sufficient superlatives to describe performance appropriately to compare with alternative machines, whilst leaving room for the future improvements which will inevitably come. We can bet that Nakamichi at least will bring out a future machine which leaves us all drooling. For the moment I, for one, will find it difficult to better the sound quality of the OMS-7, at.least for compact disc.
The relative absence of background noise on compact discs I still find a little unnerving, at least for symphony and orchestral music which in the best instances are heard in an auditorium against the inevitable backdrop of (albeit quiet) audience noise. A compact disc can be nothing short of terrifying if the gain setting is wrong.

In the accompanying literature with your OMS-7 purchase you will find little to laud the acoustical performance of the machine - the manual simply states that quadruple oversampling completely eliminates sound deterioration common to the digital filter, and that is all. Nakamichi obviously rely on the discriminating listener to recognise the performance standard.
My own listening tests were conducted with a number of alternative loudspeakers and amplifiers and in a couple of rooms. I found the top end performance to be remarkably smooth, at both high and low levels. Stringed instruments do sound like strings, and whilst triangles probably sound quite good on most compact disc players I found them stunningly sharp without the square wave overtones commonly heard.

I was very impressed with the quality of orchestral sound - in my own opinion the most difficult source to reproduce well. detail is very good indeed, not so strident that one feels that one has just been rammed into the conductor's lap, but balanced in the way that only good string tone can permit.
Brass is another give-away, and whilst this source is prob-
ably more accurately a test of the loudspeaker, with a good loudspeaker or headphones I found the brass also to be very realistic.
The benefits of the wide dynamic range of the medium is apparent with orchestral recordings on compact disc, and particularly so on the OMS-7 player. Musician noise is frequently audible.
I did not have an acoustic piano source recording that I thought adequate to an objective listening task. At this stage on the basis of listening tests on electric piano material I will venture to say that, provided the softness achieved on string tone comes through, which I expect it should, then I will be very impressed. Certainly electric piano is excellent, being clearly identifiable for what it is with very accurate quality. The acoustic piano is, however, a much more stringent test since it carries such tonal complexity.
Pop music and electronic music on the OMS-7 compact disc player basically cannot be faulted, apart from the transparency of some very ordinary (so called) digital recordings being remakes of analogues on some obviously ordinary equipment.
It must be remembered that the Nakamichi is a top standard performer. As such, one of the problems a potential buyer will have will be to ensure adequate listening conditions. Do not expect to be discriminatory if listening in one room only and to one set of loudspeakers - probably against a cacophony of retail shop noise if you are unlucky.

The distinction in performance quality will be identifiable best at relatively low listening levels, but only with top grade amplification and louspeakers. The player costs roughly $\$ 2000$ and if the old rules of thumb are still applicable then the player should not be expected to perform to its potential unless you are spending $\$ 4000$ to $\$ 8000$ or more on the rest of the system.
If you are spending less, still look at it, if only to enjoy the quality of a beautifully built piece of equipment. You may also be lucky. It wouldn't be the first time a lower cost system just happened to go together well and outperform some much more impressive rivals, and you may still feel the expenditure worthwhile.
The low cost system is, however, almost certain to be missing the one feature without which you cannot expect the CD player, any CD player, to perform to it's potential - power, power and more power.

The Nakamichi player is a top-range machine capable of immense output peaks, and unless your amplifier and loudspeakers have similarly large overcapacity you simply will not get the performance. One of our test discs is the Telarc 1812 Overture with digitally recorded canons, and without about one kilowatt of power the orchestral level is simply too quiet at the output level at which the amplifier can handle the canons. So if you really do want top grade orchestral performance the OMS-7 (or 5 ) is the machine for you, bui increase the mains fuse capacity if you like Bach, Mahler or Saent Saens.
Lastly, buying at the state of the art may cost more than the commercial standard, but it is likely that your top grade performer will still be up high in the tree after quite a few years and costing you nothing when your friends are having to update. The oversampled filter may also have a few hidden benefits which may or may not ever come to anything - standard sampling for disc playing perhaps four times as currently available.

## Summary

Sigh.



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## COS5060A Features:

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- Multi-Mode Display
- Built in Delay-line
- 6 inch Rectangular flat faced

CRT with internal Graticule

- Dynamic Blas Circuit
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## DSS5020 Features:

Digital Storage

- A high resolution made possible by the 8 bit vertical and 1024 word horizontal axes.
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# Meet Elami Jr - your robot 'friend' 

IN THE SEPTEMBER ISSUE editorial I said, "I never cease to be amazed at the level of technology incorporated in electronic products designed and manufactured for use in the home." 'This little product reinforces that veiw very strongly indeed!

Elami Junior (or 'Jr.) is a product made possible by advanced semiconductor technology that permits the large-scale integration (LSI) of complex circuitry that can be housed in a relatively small, many-legged 'chips'. Inside Elami Jr are five LSI chips, one run-of-the-mill CMOS IC and a small number of transistors and diodes, all housed on a circular printed circuit board measuring about $130-140 \mathrm{~mm}$ in diameter!

For the technically curious, and for the record, Elami's electronics comprises a cusfom 68058 -bit microprocessor, a 4 K ROM, a General Instruments (GI) SP0256 speech synthesizer and two GI SPR 128128 K custom speech ROMs. The rest is incidental electronics, largely for controlling Elami Jr's functions.

## Features and functions

Elami Jr is a programmable robot with motordriven wheels in the base to provide motion, a liquid crystal 'face' capable of four "expressions', a 25-key 'sensor-touch' (not buttons) keyboard for programming, a speech synthesizer that provides a 'talking keyboard' plus built-in phrases and counting.

There is an infra-red sensor on the front, located at the top centre of the keyboard, that 'senses' objects by transmitting weak infrared rays and looking for reflections. Whenever it 'sees' an object, this automatically triggers the robot into backing off, at the same time sounding an alarm, then turning left before continuing its program. Around the base is a 'tactile bump sensor' ring. In the same way, if the robot bumps into a low object with this ring. it automatically moves away while sounding an alarm, then turns left and continues its program.

Surrounding the keyboard are 16 LEDs that provide the 'flashing lights' beloved of children, sci-fi fans and electronic hobbyists. On top of the 'head' are another six L.EDs for the same purpose. Two LEDs, located either side of the infra-red sensor atop the keyboard. serve as battery indicators.

The two arms can be positioned manually in 13 indexed positions and move in a roughly 180 degree arc (straight down to straight up). The grippers on the ends of the arms are simple spring-loaded jaws. The 'head' camot move.

The 'face' on Elami Jr must be its major attraction as people respond to it in a remarkable way. I suspect this feature has a strong psychological 'pull' because it makes the thing appear more human-like. It's a simple liquid crystal display with four "expressions" (all programmable!) - sleepy, happy, angry
and surprised. In addition. during speech, the face can be made to appear with an open mouth (static talking) or with a moving mouth (dynamic talking).

Programming has been greatly simplified by the use of 16 single-key commands, all of which are letters. You can program all movements and other functions with a straight forward combination of these commands. The D) key provides a demonstration which puts Elami Jr through its paces - and what a show!
The movement commands provide forward, buckward, left turn, right turn, left curve, right curve plus two speeds. You can also puuse for a selected number of seconds from one to nine and repeat commands from one to nine times. As you would expect, Elami Jr prefers hard. smooth surfaces but still works quite effectively on carpet. depending on the depth and resilience of the pile.

The speech synthesizer provides a selection of five phrases - "Hello. I am Elami", "Have a nice day", "I love you". "I am happy" and Follow me" - eight words (like "please", "rhankyou", "Good-nignt" - at least it has manners), plus counting from one to ten and reciting the alphabet. The worst word it says is the Americanese " Dommy". but I guess we could expect that. The speech functions are all activated by two-key commands comprising the letter T followed by a number from 1 to 9 . or a letter from $A$ to $F$. e.g: 'T'2 causes Elami to articulate "I am happy". See, you can even have the robot tell lies!

Each Elami comes with a little plastic laminated card showing its creation date. serial number and individual security code". You can only operate your robot by entering your security code. That's an attractive feature with a whole host of uses - like keeping big brother's hands off your robot!

Elami Jr stands 295 mm high and is about 220 mm wide, elbow to elbow. It is powered by four $C$ cells (for the motors) and four $A A$ cells (for the electronics) and weighs, all up. 1.5 kg with the batteries. Rechargeable batteries may be used and an optional charger is available. It is made of sturdy plastic of an off-white colour with red trim.

The 14-page handbook is clearly and simply set out and contains sample programs to illustrate the various fumctions. Phere is a comprehensive series of visually illustrated sample movement commands running from a simple forward-turn-and-circle to even zig-tag commands. Learning the rudiments of programming logic with E:lami Jr should not only prove educational. but fun too!

In addition to the manual, a laminated plastic card is supplied with "at a glance' operation instructions - a very handy idea.

While: Elami Jr appears - and appeals as a toy, it is more than that. Going through the step-by-step logic of programming Elami Jr to do even simple tasks is a good exercise

in clear thinking. a fundamental requirement to learning computer programming at any level. All childs" play (even adults play!) is largely a learning experience and this product can potentially provide a pretty powerful learning experience.

## The 'real' test

Well. the "acid test' is to have a young person do it for themself and report the results. Who better thean my 12 year-old son. I thought? So here's what Corey thought of Elami Jr.
"This fantastic robot is stacks of fun for poung and old. With its 25 -key kevboard you have around 40 commands to create programs.
"When I first turned Elami on, to my surprise it started talking! And it lalks very nicelo. indeed. The voice' is a little squeaky, but quite clear, not like some synthesisers t've heard.
-The manmal is set out in a wey such that the person reading it, no matter how little experience they have had with robots. can easity understand it. I think. in Eilami's case. what you see is not what you get - I not more than I bargained for.
"My first program amazed me - success at first try! I got filami to flash the lights. speak to me and put on a happy face. That encouraged the to try more things. The face makes Filami different to all the other robots fie seen. It's just great.

I would recommend Elami |r to young or old - as a playmaté or a novelty."

Roger Harrison,
Corey Harrison

December 1985

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## Tools give constructors <br> more scope <br> Scope Laboratories, the Melbourne-based manufacturer

 of soldering irons and related tools, has a super offer going for purchasers who select their 20 watt/240 V general purpose iron - pay an extra $\$ 1.00$ on the price of the iron and you get a small toolkit.The toolkit is an optional purchase, but worth having as it contain a small Philips head screwdriver, a small flat-blade screwdriver, tweezers and a coil of resin-cored 60/40 solder.
These days, you'll find Philips head screws securing the lids of most small plastic 'jiffy' boxes, diecast boxes etc, so you'll find the Philips head screwdriver just right for them. Most Asianbuilt electronic equipment uses Philips head securing screws, too.
Instrument knob grub screws are generally slotted and require a small flat-bladed screwdriver. The one in the pack is ideal for use on knobs, as well as the smaller BA and Whitworth screws which feature slotted heads.

You'll find the Scope iron in blister packs at hardware stores and selected electronics retail-

ers. Look for the "EXTRA" toolkit packed inside the blister. If you're local supplier doesn't stock the Scope iron with toolkit, Scope may be able to advise you of the nearist stockist. Contact Scope Laboratories at 3 Walton St, Airport West 3042 Vic. (03) 3381566.


JAYCAR'S BRISBANE STORE
If your're a Brisbane resident and you've not ventured into Jaycar's Logan Road store, you're probably doing yourself a disservice. It carries all the compoents and kits that were previously only available from Jaycar's Sydney stores or mail order

There's plenty of parking off Deshon Street and you'll get a friendly welcome from store manager Simeon Cran and his two sidekicks, Frank and John.

If you want to audition David Tilbrook's AEM6102 two-way speakers, then rock on over to 144 Logan Rd, Buranda.

## PROJECT BUYERS GUIDE

The AEM6010 preamp will be stocked as a kit by Jaycar and Dick Smith Electronics, so far as we know as this issue goes to press. In addition, Geoff Wood Electronics in Sydney has pc boards and can provide all your component requirements for it. All Electronic Components in Melbourne will likely be in a similar position.

The AEM4600 Dual-Speed Modem will be widely available in kit form, we understand. Jaycar, Geoff Wood Electronics and Dick Smith Electronics have indicated they're looking at stocking kits. Most parts for it are readily available. Refer to the '7910 data sheet for suppliers of the 'world chip' which is the heart of the project.

The AEM5502 Microwave Oven Leak Detector might be stocked as a kit by Dick Smith Electronics and possibly Altronics in Perth. Printed circuit boards, with black silk-screened annotation will be available from Geoff Wood Electronics in Sydney and possibly All Electronic Components in Melbourne. The meter is stocked by Altronics (cat. no. Q 0600), Dick Smith Electronics (cat. no. Q-2100) and Jaycar (cat. no. QP-5056). The Hewlett-Packard 2800 hot-carrier diode is widely available.
The AEM2500 Audio Signal Generator is another project that looks likely to be widely available as a kit. The only 'special' component employed in this project is the R53 thermistor made by ITT. Jaycar stocks it, cat. no. RN-3400. You might also enquire from All Electronic Components in Melbourne or Geoff Wood Electronics in Sydney.
If you don't need to purchase a complete kit of any of our projects, and are seeking pc boards, you'll find them stocked by All Electronic Components (Melbourne) and Geoff Wood Electronics (Sydney). In the event you can't get what you want, we keep limited stocks at the magazine. Call us on (02) 4872700.


## Locating lost keys

No doubt you've mislaid your car and/or house keys at one time or another. Murphy's law sees to it that they cannot be located when you need them most.
Hi-Com Unitronics, a Sydneybased electronics importer and retailer, has a great little alarm that operates on a change in light level. Open the draw, briefcase or whatever you put your keys in, and the device plays "It's a Small World After All".
If you left your keys on a desk or shelf top, just turn off the room lights for tens seconds or so. then on again, and the alarm will sound
Called the "Keylocator", the device has lots of other uses. Put in the biscuit tin and you'll know when someone's sneaking a bikkie!
The Keylocator costs just \$9.90. Contact Hi-Com Unitronics, 7 President Lane, Caringbah 2229 NSW. (02) 5247878.

## How could you resist?

Now, any hobbyist worth their weight in used solder must have a 'junk box'. It's the prime source of components for fiddling with circuit ideas or for saving money on the latest projects - buy only the bits you don't have in the junk box!

Every now and then, you need to 'top up' the junk box. One of the best ways to do it is to buy bags of 'specials'. Look around and you'll find bags of resistors, capacitors, semiconductors. etc. If you sneak down to any of Zap Electronics' stores in Sydney, you'll find halfkilogram bags of resistors on special for just $\$ 9.90$. They're all mixed values and wattages, but mostly quarter-watt types. As resistors weigh but a poofteenth of a gram, there must be Christmas-knows how many resistors in each bag.

You'll find Zap stores located at 16 Anderson St, Chatswood; 60 George St, Parramatta; 51 Burwood Rd, Burwood; and 270 George St, Liverpool. If you can't get to one of them, Zap takes mail orders sent to 10 A First Ave, Eastwood 2122 NSW. (02) 8582288.

## aem project 2500



# Build this simple audio frequency generator 

## David Tilbrook

> Here's an easy to build, cost effective audio signal generator offering wide frequency coverage from 10 Hz to 100 kHz , sine and square wave output and battery operation for portability and freedom from hum.

A GENERAL PURPOSE audio generator is one of the most useful pieces of test equipment for the professional or amateur electronics laboratory. Unfortunately the least expensive commercial units with reasonable performance cost several hundred dollars. With this in mind we set about to design a low cost audio oscillator that would be suitable for the vast majority of hobbyist applications by providing a realistic combination of features and performance.
After studying the performance of a number of commercial instruments it was decided to provide the oscillator with both sine and square wave outputs. The sine wave distortion performance had to be set at a reasonably low level without causing an unnecessary increase in the cost or complexity. Although there are applications which require an extremely low distortion sine wave these applications also require the use of expensive additional equipment such as noise and distortion analysers or Fourier analysers. For many applications the additional expense of providing superlative distortion performance simply is not warranted. If you require such an oscillator, however, keep reading AEM as we have one planned for the not too distant future.

## Design feałures

In order to obtain the required distortion performance, and at the same time obviate the necessity of sine wave 'shaping' ad-
justments, it was decided to employ the popular Wein Bridge oscillator circuit technique. A total frequency range from below 10 Hz to beyond 100 kHz is covered in four ranges. Level adjustment is provided by means of a stepped attenuator in conjunction with a fine output level adjust potentiometer.
The step attenuator provides ranges of $3 \mathrm{~V}, 1 \mathrm{~V}, 300 \mathrm{mV}, 100 \mathrm{mV}$ and 30 mV full output. Both this control and the fine attenuator are located in the circuit at the input of a final output buffer stage to ensure that the unit has a low output impedance that is not affected by the settings of the output level controls. The buffer stage is formed using an NE5534N operational amplifier which is capable of driving a 600 ohm load to its full output supply voltage.

A separate square wave output is also provided by using another operational amplifier, here a CA3130, to act as a comparator. In this configuration, the op-amp is used with no negative feedback applied to the stage to decrease its gain. The inverting input of the device is connected to earth while the sine wave input signal is connected to the non-inverting input. During positive half cycles of the sine wave input the full gain of the op-amp is applied and the output stage of the op-amp is driven hard against the positive supply rail. Similarly, during negative half cycles the op-amp output is driven hard against the negative supply voltage. The result is a square wave output with a peak voltage equal to the supply rail.
We decided to provide separate sine and square wave outputs so that their levels could be set independently and the separate outputs used simultaneously. The square wave output, for example, could be used as an external trigger to an oscilloscope to ensure a perfectly stable trace while using the sine wave to test the frequency response or gain of an amplifier stage. This is a particularly useful function if, for example, a low level amplifier stage is under test. In this case, the sine wave output of the oscillator is applied to the input of the stage and the output level attenuators would be set so that the generator provides an output level similar to that expected during operation. If the input level is very small, such as in the case of the low-level amp

stage, any hum or noise present in the stage will represent a significant proportion of the total output signal this often makes it extremely difficult to obtain a stable trace on an oscilloscope. To overcome this problem the square wave output from the generator is applied to the external trigger input of the CRO and a stable trace results.
The square wave output can, of course, also be used as a general purpose square wave source, but since the 1 k output attenuator, RV3, lies after the op-amp the output impedance on the square wave output terminals will be a function of the position of the wiper of the pot. The highest impedance (approx 1 k ) will occur when the wiper is setup approximately to its centre position. In practise, this does not cause a problem since the output is protected from excessive loads by the 560R resistor, R16.

## The Wein bridge

A detailed description of the operation of the oscillator is given in the circuit operation section of this article. However, an introductory discussion of the oscillator principle is warranted at this stage since the Wein bridge is a very common and important circuit. Figure 1 shows the basic circuit of the Wein bridge.

If an ac signal is applied to the input there are two voltage dividers formed by the network. The first of these is a simple resistive potential divider which is formed from R1 and R2. If we regard point $B$ as a ground reference we can see that any input signal applied to point A will be attenuated by the voltage

The printed circuit board mounts on a piece of blank pc board or metal plate (to provide some shielding), which is located toward the front of the case. Plastic snap-in spacers support the board. Note the short wire 'strap' securing the thermistor in the centre of the $p c$ board. The batteries were secured with double-sided sticky pads.


Figure 1. Basic Wein bridge circuit.
divider $R 1 / R 2$ and a reduced replica of the input signal appears between points D and B. Assuming ideal resistors (i.e: resistors with no capacitive or inductive component), this output voltage will be independent of frequency. Both the phase and amplitude will be constant for all input frequencies. The second of the two voltage dividers is formed by the resistor/capacitor networks R3/C1 and R $/ / \mathrm{C} 2$. Each of these networks gives rise to frequency dependent phase shifts.
The network C1/R3 gives rise to a phase lead, while C2/R4 gives rise to a phase lag. The phase of the signal at point $C$ with respect to the earth point $B$ will have the same phase as the input signal at only one frequency, when the phase lead and phase

## aem project 2500

lag are equal. At this point the signal is a maximum.
To build a sine wave oscillator using this phenomenon, we incorporate the Wein bridge as part of the feedback loop around an operational amplifier, as shown in Figure 2.


Figure 2. The Wein bridge incorporated with an op-amp to form a sinewave oscillator.
The outputs from the potential dividers are applied to the two inputs of the op-amp. If the ratio of R1 to R2 is set correctly, the circuit will have overall positive feedback at the frequency determined by the phase cancellation discussed above. So the circuit will oscillate at this frequency. The problem is to ensure that the two resistors have exactly the right ratio. If the ratio $\mathrm{R} 1 / \mathrm{R} 2$ is too high, the amplitude of oscillation builds up until it reaches the supply rails and a clipped waveform results. If the ratio is too small oscillation stops altogether. The adjustment of this ratio is absolutely critical if a low distortion, amplitude-stabilised sine wave is to be obtained.

To ensure that the ratio R1/R2 is always correct, resistor R1 is replaced by a negative temperature coefficient thermistor. This is basically a resistor that will vary its own resistance according to its power dissipation. If the amplitude of oscillation is too high the power dissipation within the thermistor will be high and its resistance will decrease. If the value of R2 has been chosen correctly, this decrease in resistance will decrease the ratio R1/R2 sufficiently to return the waveform to a clean sine wave. Similarly, if the sine wave amplitude starts to decrease, the
decreasing power dissipation in the thermistor causes an increase in its resistance and the level is returned to normal. This system works extremely well and this oscillator has an amplitude linearity within fractions of a dB over its entire frequency range.

All thermistors have a certain settling time during which the amplitude will tend to display some 'ringing', where the amplitude rises and falls before reaching a steady value. This fairly well damped, however, and stabilises after only a second or twa. This 'settling time' arises out of the thermal inertia of the ther-

## AEM2500 CIRCUIT OPERATION

The circuit is based around a Wein-bridge sine wave oscillator formed by op-amp IC1 and the associated Wein-bridge. To enable selection of the frequency range, switching is provided by enabling the resistance in the RC arm of the bridge to be varied. This is accomplished by using the pontentiometer RV1 and the series resistors R1 and R2 to form the resistance. This provides adjustment over approximately one decade. To enable frequencies over the complete audio spectrum to be generated, range switching is provided by switching the capacitors in the RC arm of the Wein-bridge using the dual rotary switch SW1. Amplitude stability is provided by the use of thermistor TH1 in the feedback loop around IC1. Capacitors C11 and C12 ensure stability of the oscillator by decreasing overall gain for frequencies outside the operating frequency range.
The output from the oscillator is fed to both a voltage comparator used to generate the square wave output, and via an attenuator to an output buffer stage to the sine wave output.

The sine wave attenuator consists both of a stepped attenuator, formed by resistors R6-R10, in conjunction with the 1 k resistance provided by the potentiometer RV2 which also provides a continuously variable output. The attenuator output is fed to a buffer stage formed by IC2 and its associated resistors and capacitors. This ensures that the sine wave oscillator has a sufficiently low output impedance to be useful in the vast majority of applications. The feedback loop is isolated from the output by incorporation of a series resistor R15 in the output while C16 provides dc isolation.
The voltage comparator compares the input sine wave with a ground reference present on its inverting input. The resulting square wave therefore has identical phase characteristics as the input sine wave. Resistors R17 and R18, together with the zener diode ZD1, buffer the supply to the comparator to prevent excessive coupling of the square wave into the sine wave output which would seriously degrade the sine wave distortion performance.


mistor. The thermistor changes its resistance as a result of the temperature of its resistive element; but this element has mass and will therefore take a certain time to change temperature. It is this thermal inertia that enables the device to average the power dissipation within it.

A problem arises, however, at very low frequencies. If the frequency of oscillation is decreased, the period of oscillation can start to approach that of the thermistor settling time. When this happens the thermistor cannot average the power dissipation over a sufficiently large number of cycles, and the temperature, and hence the resistance, of the thermistor begins to be modulated sinusoidally. The effect of this is to increase the distortion of the sine wave at low frequencies.

The only solution to this problem is to use a thermistor with more thermal inertia and hence a longer settling time. In fact, this is not a trivial problem to overcome! Even if the thermistor is replaced with an alternative scheme such as a FET feedback circuit, the accompanying circuitry must still have a certain settling time and all simple circuits suffer from the same problems as the thermistor.
During the development of this project we tried several other feedback systems, and devised one that gave extremely good performance. This system was, however, far too complicated for this project so we have reserved it for the extremely low distortion oscillator planned for a later date. None of the simpler systems tried were as good as the thermistor circuit and were consequently discarded.

## Construction

The construction of the oscillator is not difficult, particularly if you use our AEM2500 pc board design. The first step is to assemble the pc board which is then mounted in the chassis and connected to the components on the front panel via hookup wire.
Start by soldering the resistors and non-polarised capacitors to the pc board. Once these are in place, progress to the semiconductors. This circuit uses three op-amps which can be mounted using IC sockets if required. Be careful to insert the ICs the correct way around. If these are inserted incorrectly and the unit is powered up, they will almost certainly be damaged. Finally, solder the electrolytic capacitors in place being careful to ensure that these are inserted with the correct orientation.
The wiring between the pc board and the front panel components needs to be done carefully as it is a little complicated. The accompanying wiring diagram shows these connections and this phase of the construction should not be too difficult if the wiring is done neatly. The solder connections to the pc board can be done prior to mounting the pc board in the chassis. The con-

Printed circuit component overlay. The alphabetically labelled points have pc stakes inserted and connect to corresponding points on the front panel components (see over).

| PARTS LIST AEM4501 | Capacitors |
| :---: | :---: |
| Semiconductors | C1, C2 ....... . 820n greencap |
| IC1, IC2 . . . . . . . . . . NE5534N | C3, C4 ....... 82n greencap |
| IC3 . . . . . . . . . . . CA3130 | C5, C6 ....... 8n2 greencap |
| ZD1 . . . . . . . . . . . . . . 12V,1W | C7, C8 . . . . . . . 820p polyester |
|  | $\mathrm{C} 9 . . .3 .4 .470 \mathrm{u} / 25 \mathrm{~V}$ electro. |
| Resistors all $1 / 2 \mathrm{~W}, 5 \%$ | C10 ......... 10u/25 V electro. |
| R1, R2 . . . . . . . . . . . . . . . . . 1k8 | C11 . . . . . . . . . 15p ceramic |
| R3, R4 . . . . . . . . . . . 47R | C12 . . . . . . . 15p ceramic |
|  | C13, C14 ...... 560n greencap |
| R6 . . . . . . . . . . . . 22 R | C15 .............22p ceramic |
| R7 . . . . . . . . . . . . . . . . . 2k2 |  |
| R8 . . . . . . . . . . . . . . . . . 10k |  |
| R9 . . . . . . . . . . . . . . 33k | Miscellaneous |
| R10 . . . . . . . . . . . . . . . . 100k | AEM2500 pc board; TH1 . . RA53 |
| R11 . . . . . . . . . . . . 1M | thermistor; SW1, SW2 . . 2-pole, |
| R12 . . . . . . . . . . . . . . . 220k | 6 -position Lorlin rotary; |
| R13 . . . . . . . . . . . . . . . . 2k2 | SW3 . . . DPDT miniature toggle; |
| R14 . . . . . . . . . . . . . . . . . . 6k8 | $2 \times 9 \mathrm{~V}$ batteries type 216 ; plastic |
| R15 . . . . . . . . . . . . . . . 100R | instrument case; $2 \times 216$-type bat- |
| R16.... . . . . . . . . . . . . 560R | tery clips; assorted mounting hard- |
| R17, R18 . . . . . . . . . . . . 470R | ware; 35 pc stakes; hookup wire; |
| R19, R20 . . . . . . . . . . 100R | knobs, etc. |
| RV1 . . . . . . . . 20k/A dual lin. |  |
| RV2, RV3 . . . . . . . . . 1k/A lin. | Expected cost: \$60-\$72 |

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particular IR band The hear of the unit consisis of a particular IR band The heart of the unit consists of a
hugh gain lens（antenna？which has a Commutated hugh gain lens（antenna？）which has a Commutated
field of view Its reception pattern is comb like．but field of view Its reception pattern is comb like．but
highty funed to the IR wavelength of human bodies highly funed to the IR wavelength of human bodes
When a human passes within proximity of the pickup area．the lens will selectively pick up IR radiaton and theñ not Movement across the pickup area will resulk in a series of pulses sent to a detector cucurt
IR detectors are very teliable as they do not transm IR detectors are very teliable as they do not transmn and will not respond to non heat radiating objects
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nections between the pc board and the front panel can then be made after, soldering to the pins on the top side of the pc board. This tends to make it somewhat simpler to assess the necessary lengths of hookup wire required and helps to ensure a neat job.
The connections to the batteries are made using the standard battery terminals available for this purpose.

There is no specific set-up procedure required for the oscillator since no adjustments are necessary. The usual procedures should be adopted, however, before powering up. Check that all of the components are soldered in place correctly. Be sure to check the orientation of the ICs and the electrolytic capacitors. If all is well the oscillator can be checked either by connecting its output to a CRO or to the input of an amplifier. If the amplifier approach is to be used, set the frequency to around 1 kHz and switch the output attenuator and set the fine adjust attenuator to their minimum settings before turning the oscillator on.




A: square wave, about 10 Hz . (2 ms/div.)


B: square wave, about 1 kHz . (200 us/div.)


C: square wave, about 10 kHz . (20 us/div.)


D: square wave, about 100 kHz . (2 us/div.) It's juststarting to 'round off',

Left and below oscilloscope photographs of the generator's outputs. All displayed at $2 \mathrm{~V} / \mathrm{div}$.

Front panel drilling details for the H 0482 case.

## LEVEL

We expect that constructors of an
INTERMEDIATE
level, between beginners and experienced persons, should be able to successfully complete this project.

Once the oscillator has been switched on, wind up the output controls slowly. This applies particularly when testing the square wave output. If the output level control is set to maximum the oscillator will deliver around 9 V peak, which is more than ample to overload most power amps!
I trust you find the project as useful as I found the first audio signal generator I built (very much like this one), which I have now 'retired' 4


F: sine wave, about 1 kHz . (200 us/div.)


G: sine wave, about 100 kHz . (2 us/div.)

## aem project 5502

## Simple, effective microwave oven leak detector

David Tilbrook Roger Harrison


#### Abstract

The convenience, simplicity and speed of the microwave oven accounts for its tremendous popularity. An off-shoot of radar technology, the domestic units are well-designed 'mature' products, safe to use if properly cared for. But age, accidents, wear and tear can take their toll. This project provides a simple means of applying a periodic safety check, and is inherently 'fail safe'.


CONSUMER MARKET RESEARCHERS point out that the 'penetration' of microwave ovens in Australian homes is extremely high, especially when compared to other Western countries with similar domestic population make-ups. Australians, it seems, embrace 'new technology' consumer products with a wholehearted accceptance virtually unequalled elsewhere in the world. The introduction of colour TV, video recorders and compact disc players, along with microwave ovens, provide direct testament of this.
Cooking with microwaves has many advantages - as no doubt you've found, being a microwave oven owner. However, as we've all read in the popular newspapers, if not the technical press, microwave energy can be dangerous. Microwave oven designers have taken great care to confine the energy within the cooking chamber. The door fits flush and is held firmly shut, special seals preventing 'leakage' around the door. The viewing window incorporates an internal metal mesh grille to prevent microwave radiation through it. Safety 'cutout' switches are incorporated in the door that isolate the power from the microwave generator when the door is open.
However, Murphy's Law ensures that the real world is not perfect. A mishap or rough handling may warp the door, per-


The finished unit - simple, yet functional. Our prototype pc board was made with screen-printed instructions for use, as well as a component overlay.
mitting energy to 'leak' from the resultant gap - even though it may be very slight indeed. Thus, for one's peace of mind, it seems eminently sensible to have some means of checking for 'unsafe' leakage.

## Leaks and leak detection

Let us state right at the outset, that ALL microwave ovens leak to some extent. However, it is the level of leakage that is important. It has been established that a level of microwave radiation (at the frequencies employed by microwave ovens) around 50 milliwatts per square centimetre is a 'safe maximum', under given circumstances, for humans. The eyes are particularly susceptible, apparently. Given this, if you could detect leakage at levels around one-tenth to onehundredth the given 'safe maximum', then you'd have an adequate warning margin.
Commercial designs we have seen, while generally adequate in operation, have no means of indicating to a user that they are, in fact, operative. Thus, if for some reason the detector were faulty or damaged, the unit would always give a 'safe' indication. Frankly, this worried us a little.


## AEM5502 CIRCUIT OPERATION

A half-wave dipole antenna, made up of two tracks on the printed circuit board and resonated at 2540 MHz , is employed to pickup microwave energy at roughly this frequency.

A hot-carrier diode (D1), which has an extremely fast response, rectifies the energy impinging on the dipole. At each alternate half cycle, the cathode (k) of D1 goes positive with respect to the anode. This positive half-cycle pulse charges capacitor C1 via the two small indicators ('RF chokes') etched from copper on the board, labelled L1 and L2.

The meter M1 has a full-scale sensitivity of 250 uA. Here, it reads the voltage developed across C 1 . The meter's sensitivity is reduced by the series resistor R1 and the 'shunt' resistor in parallel, R2.

The pushbutton PB1 is a normally-closed type and when not operated, R2 is in parallel with the meter, thus providing the required sensitivity during 'LEAK TEST' operation of the instrument. When the button is depressed, R2 is removed from the circuit and the sensitivity of M1 is increased for the 'METER TEST' operation. With the values shown, the increase in sensitivity is about ten times.

## Design feałures

The design presented here can sense the (safe) 'stray' leakage of a microwave oven and thus indicate that the unit is properly working, so you can go on to making a leakage check with confidence. It incorporates a meter, not seen on commercial designs, which gives an indication of the strength of the microwave energy leakage. Commercial designs simply employ a "yes/no" LED indicator.

The unit is constructed on a printed circuit board, no case being employed. The board has been designed to be conveniently held in either hand. In addition to a silk-screened component overlay as a constructional guide, brief, but sufficient, instruction are also silk-screen printed on the board for convenience - the instructions are then always at hand! For this reason, we recommend constructors use a silkscreened pc board when building the project.

As it is recommnended that a leakage test be carried out with the detector at a distance of approximately 40 mm from the external surface of the oven, the 'business' end of the board extends 40 mm from the detector diode and antenna. You simply place the end of the board on the surface of the oven where you wish to test for a leak - no need to 'judge' the distance.

The pushbutton permits the instrument to be used for a leak test when it's not pushed. When you push the button the meter sensitivity is increased by a factor of about ten and you can readily obtain a meter deflection from the residual oven leakage.
The detector is basically a microwave crystal set! A dipole antenna, resonant at the frequency used by microwave ovens, picks up the leakage energy which is simply rectified by the diode placed at the centre of the dipole. The dc so produced is coupled to a sensitive microammeter which gives a reading according to the strength of the radiation. Resistors 'scale' the sensitivity of the meter so that full-scale deflection is given at the required energy level.

The unit has been designed to give a full-scale reading with a leakage level of about $5 \mathrm{~mW} / \mathrm{cm}^{2}$ with the button in the 'normal' (not pushed) position. the slightly humorous warnings under "leak test readings", thus indicate any leakage that is well below the $50 \mathrm{~mW} / \mathrm{cm}^{2}$ 'limit'.

## Construction

Assembly of the project is very straightforward. You must use the pc board design presented here, else repeatable results will not be obtained. The board has to have a fibreglass substrate, not phenolic, for the same reason. The silk-screened annotation printed on our prototype is not essential, but is nonetheless a handy feature.

## THE MICROWAVE OVEN

The 'thing' that generates all the energy in a microwave oven is known as a magnetron. These devices are a powerful, vacuum tube self-excited oscillator. They were developed during WW II for use in radar transmitters to provide the powerful pulses necessary. The types made for microwave ovens operate at around 2540 MHz ( 2.54 GHz ).

The microwave oven employs a simple power supply for the magnetron, and 'safety lock' switches are employed to prevent accidental exposure to the powerful microwave radiation during oven operation.

The cooking chamber simply comprises a metal-walled cavity with some means of ventilation to allow the escape of steam, etc. The microwave energy is coupled from the magnetron to the cooking chamber via a short 'waveguide' duct (which performs a similar task to the coaxial feedline used to conduct energy from a transmitter to its antenna). Some dispersal mechanism 'spreads' the energy within the chamber to prevent RF 'hotspots' and 'deadspots'. often a 'carousel' rotates the food within the chamber, or a set of metal vanes reflects the energy around.

Some means of controlling the power supplied to the cooking chamber is always incorporated, along with a timer to control how long the food is cooked.

Water in the food is the primary absorbing agent of the microwaves. Dry food and glass or ceramic containers are, for the large part, generally unaffected by the microwave energy. The heat generated by microwave absorption is conducted through the food, which aids the cooking process. In general, the microwave energy only penetrates $15-25 \mathrm{~mm}$ below the surface of the food, largely depending on the type of food.

Domestic microwave ovens generally have a power consumption of 1200 watts or more (not counting any electric 'browning' elements'). Magnetrons are relatively efficient devices, and around half the input power appears as microwave energy. This is markedly more efficient than conventional cooking appliances, hence the speed advantage of microwave cooking.

The 'wasted' magnetron power appears as heat in the device and for this reason, a fan is employed to keep it cool, exhausting via the rear of the cabinet in all the models we've seen.

## LEVEL

We expect that hobbyists who are
BEGINNERS
in electronics construction should be able to successfully complete this project.


The diode is soldered on the rear side of the board, in the middle of the dipole.

The meter mounts directly on the non-copper (component) side of the board. First check that the holes through which its terminals must pass are drilled the right diameter. The meter, however, is mounted last of all.
Start with the diode. It mounts on the copper side of the board, directly at the centre of the diple. Make sure you get it the right way round. It must sit flush against the board and be soldered in place with virtually no leads. Do this quickly, and with a hot iron. The diode is quite robust and should withstand this providing you don't apply undue mechanical stress or overmuch heat
The ceramic capacitor and the two resistors mount on the top side of the board. Solder them in place next. Mount the pushbutton and solder its two connections to the pads on the
board using short lengths of tinned hookup wire.
To mount the meter, place a small blob of Silastic, or simi lar glue, on the rear and mount it on the board, seating it firmly against the board face. Solder the two meter terminals.

That's it! If you so wish, the rear of the board can be protected simply by spraying-on clear lacquer.

Now you can test it.

## Using your leak detector

Place a bowl or plate of food in the oven and set it going for a period of a minute or so. Hold the board horizontal, with the meter facing up and place the end of the board against the oven surface. Depress the button with your thumb and move the detector around. You should see the meter needle 'kick up' at a variety of positions as you move around.

If the meter needle moves backward, you've got the diode back-to-front!

You should try turning the board so that the dipole's vertical (meter facing left or right) and repeat the procedure (still holding the button down).
With the button not pushed, repeat the procedure. You should see barely a flicker from the needle at the positions it read well up-scale in the previous tests. For the most part, it will likely not show any sign of movement.
Note that, when carrying out a leak test, it is always advisable to place a 'load' of food or water in the chamber. The manufacturers generally recommend that you always operate the applicance with something in the oven to avoid possible damage to the magnetron microwave generator.
You should expect a variation in response with different ovens. This is of little concern as the residual leakage of ovens will depend on a wide variety of factors and is, in any case, normally less than about $0.5 \mathrm{~mW} / \mathrm{cm}^{2}$.
If your microwave oven gives a significant indication of leakage, contact your distributor or service centre.



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# aem project 6010 



# An 'ultra-fidelity' preamplifier 

## This stage completes the construction, interwiring and assembly of the preamp into the chassis.

BY THIS STAGE, you will have completed the assembly of the four printed circuit boards. Give them all a careful visual inspection. A little time spent doing this now can save lots of frustration later. Check that you have the six capacitors indicated in the penultimate paragraph of Part 2, soldered to the rear of the 6010 ma board. All clear? Now we can get down to the 'final leg' - assembling them in the chassis and completing the necessary interwiring.

It is probably wise to use shielded cable for all the interconnections that carry audio signals within the preamp such as those to and from the rear panel sockets. Shielded cable is not difficult to use once a few basic techniques have been mastered. The area that causes most of the problems is usually that of removing the outside insulation without cutting the shield wires. The easiest way to overcome such problems is to invest in an inexpensive pair of insulation strippers. This is a very useful tool for an electronic experimenter and saves considerable time when preparing shielded cables. The easiest type of shielded cable to use is the lightest gauge, single conductor type although there are some excellent multiconductor cables available.
Once the pc boards have been constructed to this stage they should be positioned in the chassis to be made for the remaining lengths of hookup cable required. Each connecting cable should be cut to length, trimmed and soldered onto the low level amp and line amp pc boards. Once all of the cables are soldered into place the remaining pc boards can be bolted into place and the other ends of the shielded cables terminated to the front and rear boards. This is not difficult since the connections are made to the copper side of the pc boards.
The front panel pc board is held in place by the mounting nuts on the two Lorlin switches and then the three toggle switches. The toggle switches selected for this project are


Showing how the dual-gang volume pot is wired to the front panel pc board.
supplied separate from the black linear paddles which should only be fitted to the switches at the last stage. These switches are supplied with both a standard mounting nut and a black dress ring. The standard nut should be adjusted so that the dress ring tightens down correctly on the front panel of the preamp.


Part 3 David Tilbrook

Placement of the capacitors on the rear side of the board.

December 1985 - Australian Electronics Monthly - 51

## "CHRISTMAS GIFT ATLAETIA IOW COET 10 ATLATTIAIOW COET 10 AMp PIC/teII <br> MUICImerer mith treansforer tewt feaciliry <br> Amp Digitel Multimeter/ <br> \section*{Translstor Tester \&}

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Overall view of the front panel pc board, showing how the switches mount on the front (non-copper) side.

The toggle switches and DIN sockets fitted to the rear panel mount through both the rear panel and the rear panel pc board and are wired using the shielded cables and hookup wires already soldered to the main pc boards
The final stage in construction is to fit the toggle switch linear paddles and the knobs. The toggle switches have flats on the selector so the linear handles will only go on one way In the prototype unit we used 22 mm black knobs for the selector switch, balance control and power switch. A larger, 28 mm , black knob was used for the monitor volume control.

## Powering up

Before applying power to the unit recheck as much as possible. Check the orientation of diodes and polarised capacitors, check that the correct types of transistors have been inserted and check the power supply connection between the line and low level pc board. If these are incorrect it will almost certainly damage the low-level pc board assembly.

If all is well, connect a $35-0-35 \mathrm{~V}$ RMS transformer to a DIN plug according to the wiring diagram and apply power to the preamp. When the unit is switched on the amplifier muting relay should be heard to click approximately one second after switching on. The relay should switch immediately the preamp is switched off. Check that the power LED on the front panel is operating when the preamp is switched on.

If all is well turn the preamp off and connect it to a power amplifier, signal source and loudspeakers. Ensure that the balance is centred and the monitor volume is turned fully down. Switch the preamp on and slowly wind up the volume to a comfortable listening level and check the project behaves as you would expect.

If you intend to build the AEM6000 power amplifier (. . coming!) you will only need to use the external transformer temporarily. The power amp will be provided with a suitable ac outlet to run the preamp and other accessories that are planned for this range of AEM audio electronics. 1



Overall view of the rear panel pc board. In this case, the components mount on the copper side of the board.

Overall wiring diagram. Reference to this and the internal photograph makes it all clear.


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 Also covers the normal AMand $F M$ bands so you



5

# Fantastic February field day for families, friends and fans of amateur radio 

The Central Coast Amateur, Radio Club each February runs Australasia's most popular amateur radio field day. It's a fun-filled frolic for foxhunters, fiddlers, fantics, their families and friends.

Next February it will be held on Sunday the 23rd; the venue is the same as ever - the Gosford Showground, Showground Rd, Gosford. The fun flicks on at 8.00 am and fades off at 5.00 pm . If you haven't got wheels. it's easy to get to. Trains departing from Newcastle and Sydney arrive at Gosford between 8.30 am and 10.30 am and the Club provides courtesy
transport from Gosford station to the showground. Return transport is available in the evening.
Field day events include onair 'scrambles' (work as many as you can in the given time), direction-finding foxhunts (find the hidden transmitter) and 'talk-in' foxhunts. There will be plenty of commercial displays and the 'disposals' hall is always hugely popular for the bargains you can pick up. It's a great day for meeting those 'voices on the air' and renewing old acquaintences.
The field day always goes on, rain or shine. For full details send a stamped, self-addressed envelope to CCARC, PO Box 238, Gosford 2250 NSW.

## Amateur radio club special night \& display

The Liverpool and district amateur radio club is holding a 'special night' for its December meeting, featuring an operational display of portable amateur equipment typical of what might be used for 'emergency' operation.

Amateurs have often been the only line of communications immediately following disasters, such as during the recent earthquake that devastated Mexico City and ten years ago in Darwin, following Cyclone Tracey.
The Liverpool club extends a welcome to all and sundry to their December meeting which is to be held on Tuesday 10th, 1930 hrs ( 7.30 pm , right?) at Liverpool Public School, Bigge St, Liverpool NSW. Conveniently, the school is right opposite the Liverpool railway station.

If you're a local and want to get your amateur licence, the club conducts courses for both the full (AOCP) and novice (NAOCP) exams. They boast a very successful pass rate for candidates who, in the main. have had little or no experience in radio or electronic theory.

The courses run for about 13 weeks and cost $\$ 30$. Concessional rates apply for students and pensioners.
The Liverpool Club callsign is VK2AZD and they maintain the VK2RLD two-metre repeater. Further details on the club can be obtained at monthly meetings, the 2nd Tuesday of each month, or via PO Box 690, Liverpool 2170 NSW.

## If you can't beat 'em, join 'em . . . both

At last it seems someone's come up with a sensible approach to the time-wasting squabble over packet radio protocols, which has recently filled a lot of space in other journals.

In their continuing endeavour to promote and foster Packet Radio, the Sydney Amateur Digital Communication Group (SADCG) has commenced development of a complete Australian-designed Amateur Packet Radio System.
Rather than enter into conflict with various groups that have

## Not strictly for Bird fanciers

Sitting on my desk is the "Bird", which the family. realising it does not represent any extra-marital interest, has learned to treat with the respect it deserves. The Bird Thruline RF power meter has long been an industry standard for measuring RF in both directions, i.e. up the spout where it is supposed to go, and back down again where it isn't.

We now hear from the Bird Electronic Corporation of their Relative Field Strength Meter model 4041. "Most field strength meters are built with resonant reactive networks which limit their usefulness," chirps Bird.
"This new relative field intensity instrument employs a broadband non-reactive circuit and modern RF solid state technology
-"To make measurements is simple, quick, and convenient. In the presence of an RF field, turn the model 7041 on, adjust the gain for approximate midscale deflection and perform antenna-transmitter peaking simply by obtaining the maximum field intensity reading on the meter while optimising antenna match.'
Typical relative field strength
purchased semi-commercial equipment. the SADCG has set their design objectives around a system that will satisfy most groups. Both the Vancouver V. 2 and the ARRL AX 25 version 2 protocols will be handled.
Design has now been completed of a high performance AFSK radio modem board based on the AMD7910 'World Modem' chip. This modem provides both CCITT and Bell modem frequencies. It is tailored for use in UHF, VHF and HF voice grade channels and incorporates a PTT "watch dog" timer. The AMD 7910 was selected because of its ability to handle noisy conditions as found on HF and


Meter sensitivity with the gain control at maximum is fsd on the meter with one watt of radiated power at 150 MHz from a 2 -metre portable at 8 ' distance.

The self-sufficient model 4041 is similar in performance to Bird's Relative Field Strength Element Model 4030. which does not have a meter of its own, but is an accessory to portable Thruline Wattmeter models 43. 4431, etc.

To avoid readers flocking (groan! - Ed) to Cleveland, Ohio stalk down the distributors. RF Devices, PO Box 161, Miller, 2168 NSW, (02)607 8811.
satellite operations.
The modem will interface via RS-232/V24 to a TNC or computer. It can be easily interfaced to amateur transceivers. The circuit has the option to generate a DCD signal from either the external squelch signal from the radio or internally generated DCD from the 7910 . or both. Various connections are available for L.ED monitoring of circuit conditions such as CTS, TXD. etc.

The radio modem printed circuit board, together with construction information, is availahle for $\$ 22$ post free from the SADCG, PO Box 231, Frenchs Forest 2086 NSW.


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# A variable speed CW 

 memoryGeoff Wilson VK3AMK


#### Abstract

This device will send a brief CW message repeatedly at a speed which may be varied over a wide range. The message length is normally about 45 seconds which is adequate for short calls etc. It can be reprogrammed as needed in a matter of moments with the absolute minimum of fuss. It is ideally suited for use on bands where the normal levels of activity are low but the potential for working DX exists nevertheless. Operation over the last two years has proved it to be very useful for raising DX stations on what may appear to be "dead" bands.


ESSENTIALLY the device described will run a brief message such as most amateurs would use to call "CQ", run an ident etc. Certainly, a proper EPROM-based device would be better, but the idea behind this was firstly simplicity. It can be programmed rapidly without such things as EPROM burners and is highly flexible. If the band is likely to be open to ZL then put in "CQ ZL", if you want JAs on six metres then "CQ JA DE VK . . ", if you want to establish if two metres is open on sporadic E, then "DE VK . . " etc.
The basic device was first built about two years ago and is original, with ideas scrounged from the reference list. Apart from my own unit, others built from my design have been used by VK2BHO, VK3ANP and VK4ALM and used by me on six metres to raise DX. The only problems I have encountered have been: (a) Lack of a suitable board due to no facilities (such as computer-aided design!), and (b) the depar-

ture from common supply sources of the flashing red LED (LED3). For some reason, Dick Smith, stopped stocking them this year. It is not essential to have a flashing LED but it is a very handy indicator in this application.
I have made many variations on the basic circuit, including digital readout of the memory timimg, variable delay times between transmit cycles etc, all of which work well but don't really mean much for the average operator. After all, the aim was for something simple and I think that is best.

## The circuit

Refer to Circuit 1. When the key connected to J1 is closed. Q3 conducts, providing a data input to IC2 (the memory) and also turns on Q2. As Q2 conducts, IC1 (an opto-coupler) in conjunction with Q1, keys the transmitter. IC1 provides complete isolation of the memory circuitry from the transmitter keying line. LED1 (yellow) lights to show when the transmitter is being keyed. The use of LED1 \& R2 is optional, but recommended.

Keying is independent of the memory operation and a hand key connected to J1 can be used to key the transmitter at any time power is applied to the unit without having to change plugs etc. Note however, that there is 5 V across the key jack if using an external keyer which is voltage or polarity conscious. If desired. D1, Q1, IC1 and R1 may be replaced by a 5 V relay to switch high voltages or if relay noise is not an important factor during keying.

Data is entered into the memory via pin 11 of IC2. Output data from the memory goes from pin 12 of IC2 to the base of Q2 to switch the keying circuitry. Diodes D2 and D3 isolate the input and output data lines of IC2 from each other.

IC3 operates as a clock oscillator, the frequency of which is varied by RV1 (speed). This controls the keying period from a minimum of about 16 seconds to a maximum of about 66 seconds. The clock pulses from IC3 go to ICs 4 \& 5 which are dual binary counters providing the address lines to the memory, IC2.

The second half of IC5 is used to control the ratio of the keying and listening periods and does so by using the same clock pulses from IC3. This also operates the enable circuitry of IC2 and in conjunction with IC6(b), (c) \& (d) plus LEDs $2 \& 3$, indicates the approximate memory time available.

Operation of the timing circuitry can probably be better understood by reffering to Figure 1 which shows the waveform at pins $11,12 \& 13$ of IC5. The memory is enabled while pin 13 remains LOW, the transmit period, but is disenabled
. . from page 18.
to distort the regular pattern of the electric and magnetic fields which convey the energy down the line. Any discontinuity in the signal path, again, may cause reflections and ghosting. Too sharp a bend in coax is likely to place undue strain on both the shielding and centre conductor which might break.

Another problem with sharp bends, in both coax and flat ribbon, is that the distance between the conductors may vary in the vicinity of the bend. Signals on the line will see a different impedance at this point and some signal 'reflection' will take place. For similar reasons, the cable should not be squashed under foot, in doors, under clamps or while it is wound up waiting to be used.
While on the subject of clamps; metal staples, tacks and fixing clips should be used sparingly on TV feeders as they also affect the impedance of the cable at the point at which they are used. Certainly, nails and pins should not be driven into coax or through the dielectric between the conductors of a ribbon feeder. In coax they almost certainly will short the cable but in both instances the continuity of the cable characteristics will suffer.
In fact, even minor irregularities in the cable may manifest themselves as impedance changes and because no cable is perfect in every respect, reflections occur in most cables despite their being properly matched to other parts of the system. This fact is recognised in a cable specification, the "return loss ratio".

## Reflections and loss

The return loss ratio is a measure of how much energy travelling down a cable will be reflected by the cable itself, in an otherwise perfect system.

$$
\text { Return Loss Ratio }=\frac{\text { Energy reflected }}{\text { Energy fed into the cable }}
$$

In practice the return loss ratio is expressed in decibel form and should be as high as possible. Practical figures range from around 20 dB upwards and tend to indicate the evenness of dimension and material consistency along the length of the cable or feeder.
Special care is needed to maintain a high return loss ratio in low-loss feeders. The wire conductors in these cables are usually separated by small spacers set several centimetres apart along the feeder. Between the spacers the conductors are free to sag or come together if carelessly handled or

| RETURN LOSS - VSWR CHART |  |
| :---: | :---: |
| RETURN LOSS | VSWR |
| 2 dB | 8.71 |
| 4dB | 4.42 |
| 6 dB | 3.01 |
| 8 dB | 2.32 |
| 10dB | 1.92 |
| 12dB | 1.67 |
| 14 dB | 1.50 |
| 16dB | 1.37 |
| 18 dB | 1.28 |
| 20dB | 1.22 |
| 22 dB | 1.17 |
| 24dB | 1.13 |
| 26dB | 1.11 |
| 28 dB | 1.08 |
| 30 dB | 1.07 |
| 32 dB | 1.05 |
| 34 dB | 1.04 |
| 36 dB | 1.032 |
| 38 dB | 1.026 |
| 40dB | 1.020 |

stored. Changes in the spacing between them will cause variations in the line impedance which in turn will cause reflections.
The sum of all the reflections which occur along the length of the feeder is, in essence, the return loss.
Terminals and wire should be carefully cleaned before connections are secured. Corrosive substances in perspiration from the skin, and moisture may cause troubles in the future by increasing losses at the termination.
Impedance changes at the faulty joint might also cause reflection of the energy on the cable and ghosting in the picture. Increased attenuation will tend to make the pictures snowy.

Essentially, cables for TV installations should be selected primarily on their impedance. On this basis, similar cables used for, say, CB or a mateur radio antenna systems may not be suitable for use as television antenna feeders, and vice versa.

| Catalogue Number | Hills Industries |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | DSC 2.1 |  |  |  |
| Diameter of inner conductor | $\begin{array}{cc} & \text { DSC } 3.2 \\ \mathrm{~mm} & 0.81\end{array}$ |  | ${ }_{1.27} 2.1$ |  |  |  |
| Diameter over dielectric | $\begin{array}{ll}\mathrm{mm} & 3.64\end{array}$ |  | 5.59 |  | 2.33 |  |
| Diameter over outer conductor | $\mathrm{mm} \quad 4.51$ |  | 5.46 |  | 10.93 |  |
| Nominal overall diameter | mm 6.31 |  | 8.36 |  | 13.07 |  |
| Nominal attentuation at 50 MHz | dB/ | dB/100ft | dB/100m | dB/100ft. |  |  |
| at 50 MHz 100 MHz |  | 1.6 | 3.4 | 1.0 | dB/100m | dB/100ft. |
| 100 MHz 200 MHz |  | 2.3 | 4.8 | 1.5 | 1.9 | 0.6 |
| 200 MHz 400 MHz |  | 3.2 | 7.0 | 2.1 | 2.7 | 0.8 |
| 400 MHz 600 MHz |  | 4.7 | 10.1 | 2.1 | 4.0 | 1.2 |
| 600 MHz 800 MHz |  | 5.8 | 12.6 | 3.1 | 5.9 | 1.8 |
| 800 MHz 1000 MHz |  | 6.7 |  | 3.8 | 7.5 | 2.3 |
| 1000 MHz |  | 7.6 | 16.8 | 4.5 5.1 | 8.9 | 2.7 |
| Impedance $\pm 2.5$ ohms |  |  |  |  |  |  |
| Nominal Capacitance pF/m Velocity Ratio |  |  |  |  |  |  |
| Velocity Ratio |  |  |  |  |  |  |
| Nominal Return Loss Ratio | $d B$ |  |  |  |  |  |
| 10.300 MHz |  |  |  |  |  |  |
| $300 \cdot 860 \mathrm{MHz}$ |  |  |  |  |  |  |
| Minimum bending radius | mm |  |  |  |  |  |

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The Microcomm Sx 155 represents the latest developments in State-of-the-art LSI CMOS technology as applied to scanning monitor receivers it incorporates many features. a lot of which are not even found in todays larger base scanners
For example the SX 155 has 160 memory channels which can be programmed in either of two modes The first allows you to manually program the entire 160 channels. The second mode provides for manual programming of the first 40 channels with the top 120 reserved for use by the while in its SEARCH mode It uses these channels to automatically store frequencies on which it has found signals during the
search phase
The Sx ${ }^{\circ} 55$ also features a Priority Channel (for that important frequency) An LCD display providing readout of all receiver functions including an accurate crystal controlled 24 hour clock
Supplied complete with rechargeabie Nicad batteries. charger. and rubber duck antenna. the $x$; 5 bis a must for anybody with an interest in monitoring.


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Don't miss this opportunity to win a really top-line scanner - this Yaesu FRG-9600 scanner from Dick Smith Electronics. It has many functions and features not seen on other scanners - especially the ability to receive single sideband (SSB) signals. In reviewing the FRG-9600 in the September ' 85 issue of AEM, Roger Harrison said:
"The FRG-9600 is a well-thought-out unit, easy to use and with facilities and features that will appeal to many - whether newcomers to scanning on the VHF/UHF spectrum or 'old hands'."
Dich Smith Electronics, in conjunction with Australian Electronics Monthly, is offering a Yaesu FRG-9600 scanner as the prize in this simple contest. The unit provides continuous coverage from 60 through 905 MHz and features 100 memory channels. Five reception modes are provided - FM narrow and wide, AM narrow and wide, plus SSB funique to
the Yaesu.) The unit can scan over the full frequency range, preset frequency limits or across the memory channels. Provision is made for 'priority' channel selection, which is momentarily tested every three seconds, when activated, while listening to other channels. Selectable tuning steps are provided on the different reception modes. A 24 -hour clock/timer is incorporated. The large display shows frequency or time on a 7 -segment fluorescent readout along with a channel and mode display, plus a digital signal strength meter. A special feature is the funing knob, which is an index switch, but the unit can also be tuned using UP and DOWN channel-step keys. The FRG-9600 can be optionally computer controlled via Yaesu's CAT interface system. It operates from a nominal 13.8 V supply and may be bench (base) of vehicle (mobile) mounted.
All you have to do is complete the questions below and tell us in 30 words or less what it is about the Yaesu FRG-9600 scanner that attracts you.


Q1: What reception facility is unique to the Yaesu FRG-9600?
A1:
You may enter as many tumes as you wish, but you must use a separate entry form for each entry and include a month and page number cut from the bottom of this page You must put your name and address on the entry form and sugn it where indicated. That is, photocopies are acceptabie but an original month/page number from a copy of this month's magazine must accompany each entry form. Flease read the contest rules carefully especially if sending multiple entries.
The winning entry will be drawn by the Editor, whose decision is final, no correspondence will be entered into regard.
ing the decision.
Winners will be notified by telegram the day the results is declared and the winner's name and contest results pub.
lished in the next possible issue of the magaune lished in the next possible issue of the magazine.
RULES Contestants must enter their names and addresses where indicated on each entry form. Photostats or clearly written copies will be accepted, but if sending copies you must cut out and include with each entry an original page number and month cut from the bottom of the page of the contest. This contest is invalid in states where local laws prohiot entries. Entrants must sugn the declaration, accompanying the contest, that they have read the above rules CIOSING DATE OF THE CONTEST
CLOSING DATE OF THE CONTEST is the last marl of lanuary 311986 Entries recerved withun seven
days of that date will be accepted if postmamed pror to and including the date

Q2: Does the FRG-9600 employ triple or double conversion, or both?
A2:
Q3: Who first imported Yaesu equipment into Australia in the early 1960s?

Now tell us in 30 words or less what it is about the FRG-9600

A3:
Q4: About how long has Dick Smith Electronics been selling Yaesu equipment?
A4:
Q5: What is the minimum tuning step of the FRG-9600 and in what mode?

Postcode
A5:

## Name

Address
that attracts you: that attracts you:

## 

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# Follow-up to the AEM3500 Listening Post project 

Tom Moffat VK7TM<br>Tony Woods VK6ATW

## Response to this simple, versatile project has exceeded all expectations. Here we present software for the Commodore 64 and additional Microbee software (for Epson printers), plus how to choose and use modern shortwave receivers for those who are not familiar with them.

RESPONSE TO THE LISTENING POST project, published in the July issue* (the 1st!), has been just phenomenal. Electronics retailers stocking the kit report that it's a 'best seller'. The magazine has been inundated with requests for software for a variety of computers, Commodore 64 software being the most in demand, followed by something for the Apple. Some constructors reported difficulties 'finding their feet' with shortwave receivers. Well, here we present some followup details in response to reader feedback.

## Listening Post and the C64

Presented here is a listing, for Commodore 64 owners, that decodes radioteletype (RTTY) and radio facsimile pictures (FAX), courtesy of Tony Woods VK6ATW. Both tape and disk versions are given. The beauty of the software is that it takes advantage of the C64's bit-mapped screen and FAX pictures are displayed on-screen as you receive them. You can save them to disk or tape, too. To obtain a printout, the 'DOODLE" program from Omni Unlimited (available from most computer suppliers) is used and you can dump a FAX picture to most printers.

Here's a "SIG. WX" FAX weather chart printout from AXI Darwin on 10555 kHz, 1-10-85.


[^1]First, however, you have to hookup the Listening Post project to your C64.
"FSK/FAX OUT" (A in the pc board overlay) from the Listening Post connects to Pin H of the C64 user port when you want to receive RTTY. Connect it to Pin 9 of the C64 joystick port 1 when you want to receive FAX. The GND (C on the pc board) connects to Pin 8 of the joystick port, and the required +5 V power supply is also obtained from the ioystick port, pin 7. Actually, you can link Pin H of the user port and Pin 9 of the joystick port as the software selects each separately.

MICROBEE LISTING FOR EPSON PRINTERS














 М1F



















 (2)











In using the Listening Post, it was found easier to tune with a 1 n 5 greencap connected in series with C7 (4n7).
When you run your program, you select FAX operation with the F key, or RTTY with the T key.This will set up the input and give you the menu for the mode selected.Here are the two menus, as they appear on-screen:

| SHIFT=MOVE PICTURE TD LEFT TD ALIGN | FI | 45 BAUDS |
| :---: | :---: | :---: |
| 1 LDAD | F3 | 50 BAUDS |
| S SAVE |  |  |
| N NORMAL PICTURE | F5 | 75 BAUDS |
| R REVERSE PICTURE | F7 | 116 BAUD |
| F3 60 L.P.M. |  |  |
| F5 $120 \mathrm{L.P.M}$. | F8 | STOP |
| F7 240 L.P.M. | RADID | ELE TYPE MENU |
| F1 TO STOP |  |  |

The RTTY menu is straightforward. A selection of the four common Baud rates is provided. If you get a garbled display, you may be receiving the signal inverse to what the program expects. The R and N keys will select 'reverse' or 'normal while the message being received is being printed on-screen. The F8 key will stop current decoding and return you to the menu.
The FAX program has a number of features in addition to those already mentioned. Firstly, you can save a FAX picture to disk or tape - very handy for later viewing, especially if you don't have a printer. A FAX screen is saved just before the last few lines on the screen and takes about 20 seconds ( 37 blocks) using disk, around three minutes using tape. The file name for saved FAX is DDF. This was used so that the saved picture/map can be loaded into the DOODLE program for printing. To save other FAX pictures. re-name


Here's a print of a 'northern analysis' (wind) FAX weather prcture transmission received at my location in Karratha, W.A., from AXI Darwin on 7535 kHz, 1-10-85. Note the cyclone over the Phillipines.

Details on how to hookup the Listening Post to your Commodore 64.

RTTY
FAX
FSK/FAX OUT (A) Pin H, user port Pin 9, joystick port 1
GND (C) - Pin 8, joystick port
+5 V - Pin 7, joystick port

0800 00 0 0 08 8F 07 9E 3238 08083733000000000000 08100000800800000000 0818 00 A9 98 20 D2 FF 4C CD 082008 A9 9320 D2 FF A2 00 0828 BD 99 OA 20 D2 FF E8 E0 0830 CC D0 F5 20 E4 FF C9 00 0838 F0 F9 C9 4C DO 08 A2 00 08402050 0B 4C 70 0D C9 53 0848 DO E9 4C 8D 88 EA EA EA 0850 BD 5C 08 9D 35 OA E8 E0 085803 D0 F5 604 C 6508 EA 0860 4C $2 E 09$ EA 38 A9 01 A2 0868 01 AO 0120 BA FF A9 03 0878 A2 85 A8 0820 BD FF A9 08780020 D5 FF 4C 860090 0880 E3 4C AF 0808444446 0888 A9 9320 D2 FF A2 60 BD 08906508 26 D2 FF E8 E0 26 0898 D0 F5 20 E4 FF C9 00 F0 Ө8AO F9 C9 46 DO 03 4C 2108 อ8AB C9 54 DO F9 4C 59 OC 28 88B0 E4 FF C9 00 FO FG C9 85 $08 B 8$ DO FS 4C CD 08 A2 80 BD 08ce 60 08 90 35 0A E8 EO 03 08C8 DO FS 4C F8 08 AD 11 DO 08De 80 0 CO AD 18 DO 8D 01 0808 C0 AD 00 DD BD e2 CO 4C 08E0 88 08 EA AD OD CO 8D 11 0BEB DO AD 81 CO 8 CD 18 DO AD 08F0 02 C0 80 00 DD 4C 88 88 OBFs 20 E4 FF C9 00 FO FS C9 09004 E 0004 A9 108536 CS 090 B 5 D 004 A9 018536 EA 0910 EA 20 E4 FF C9 00 F0 F9 0918 C9 86 D0 03 4C EC 09 C9 092087 D0 03 4C F9 09 C9 88 0928 DO E7 4C 06 DA EA A9 08 093085 FE AS 40 BD 90 DC A9 09380085 FB Ag 50 B5 FC A2 0948 08 20 5B 09 AD 19 D4 OA 89485826 FE CA D0 F3 20 E9 8950 FF C9 859003 4C E3 08 8958 4C 6A 09 EA A9 198 DEE

## C64 LISTENING POST, for tape machines

0968 DD AD 0D DD 2901 FO Fs 096850 EA A5 FB 18596885 0978 FB AS FE 91 FB 90 CB EA 0978209309 E6 FB A6 FB E8 098808 DO E7 ES FC 981869 $8988 \quad 40$ AB 08 B3 20 BS $88 \quad 40$ 0996 3F 8988 A9 08854 A A9 0998 PF B5 48 A9 00 B5 4C C6 OGAD 4A DO FC C6 48 D0 F8 C6 09A8 4C EA DO FB C6 4A DO FT 0980 A9 80 B5 4A A9 7F 85 4B 0988 A9 $00854 C$ C6 4A D0 FC 09CO C6 48 DO FB C6 4C EA DO 09C8 FB C6 4A D0 F7 A99 00 B5 0900 4A A9 7 FF 8548 A9 0085 0908 4C C6 4A DO FC CS 4B DO USEO FB C6 AC EA DO FB CG 4A $09 E 8$ DO F7 EA 60 AS 47 8D 04 09F9 DD A9 0F 8D 05 DD 4C IE 99F8 OA A9 86 8D 84 DD A9 07 OAOE 8D OS DD 4C IE OA AS 4C OA®8 8D 04 DD A9 85 BD 85 DD OAIO 4C IE OA EA EA EA EA EA OAIB EA EA EA EA EA EA 2044 0A20 0A 2057 0A 2062 OA 20 OA28 81 OA 20 6D OA AD 11 DO 0A30 09 AO 8D 11 DO 4C 2 E 09 0a38 EA EA EA EA EA EA EA EA $0 A 40$ EA EA EA EA AD 02 DD 0s $0 A 48038002 \mathrm{DD}$ AD 08 DD 29 0A50 0009028000 DD 60 AD 0A58 18 D0 29 F8 09888018 CAEO DO 60 AD 18 DO 29 OF 09 0 A68 708018 DO 60 AS 36 A2 0 070 00 90 00 5C 9D 00 50 9D 0 O78 00 5E 9D 00 5F CA D0 F1 6A80 50 A9 6085 FC A9 00 BS OAB8 FB AO 0091 FB 88 DO FB OA9 E 6 FC AS FC EO 80 DO F3 OA9B 6800 BD BD 53484946 OAAO 5400 3D 4D 4F 564520 OAAB 5049430255524520 OABO $544 F 204 C 45465420$ ORBS $54 \quad 4 \mathrm{~F}$ 2Q $414 \mathrm{Cl} 4947 \mathrm{4E}$

OACD OD OD $00004 C 202020$ OACB 204 C 4 F 4144 OD OD 53 OADO 2020202053415645 $0 A D 80 D 004 E 2020200020$ OAEO 004 E 4 F 524 C 414 C 20 OAES 5049435455524500 OAFO OD 52202020002000 OAFB 5245564552534520 88005049435455524500 08080046332020002000 $0 B 103630204 C 2 E 502 E 4 D$ $0 B 18$ 2E 00004635202000 $08202000313230204 \mathrm{C} \mathrm{2E}$ 082850 2E 40 2E 00 004637 08302020002000323430 0838204 C 2 E 502 L 4 D 2 E 0 D 88400046312020200054 $08484 F 2053544 F 500000$ 08504352454154454420 $0858425920544 F 4 E 5920$ $0860574 F 4 F 4453000000$ 08684620202020464143 0870 $534940494 C 450000$ 08780054202020205241 $088044494554454 C 4554$ 0888595045 EA E6 FC A6 FC 0890 E0 7 C 9023 A9 01 A2 01 0898 A8 8020 BA FF A9 03 A2 QBAO 85 AO 0820 BD FF A9 50 OBAB 8525 AS 008524 AS 00 0880 A0 80 A9 2420 D8 FF 68 $0 B 88$ EA EA 0D 4631202020 0BCO 02 3520424155445 OBCB OD 0D OD $46 \quad 3320 \quad 2020$ 0BDO 3530204241554453 OBD O OD OD OD 4635202020 0BE0 3735204241554453 OBEB OD 0D 00 $46 \quad 37282828$
 OBFB 53 0D 0D 00 46382020 OC00 205354 4F 50 EA 20 E4 $0 C 08$ FF C9 00 FO FS C9 B5 DO 0C10 0D A9 F8 8D 84 DD A9 2A 0C 18 80 85 DD 4C 78 OC C9 B6

ดc20 D0 0D A9 OF 8D 04 DD AS อС28 27 8D 85 DD 4C 78 OC C9 $0 C 3087$ DO OD AS OA 8D 04 DD
 $0 \mathrm{C40}$ C9 88 D0 8D A9 C0 8D 04
 OC5 OC C9 BC D8 B1 4C $88 \quad 88$ OC58 EA A9 9320 D2 FF A2 08 $0 C 60$ BD BA 0B 20 D2 FF E8 E0 0C6B 48 DO F5 4C 66 OC EA 20 0c70 gF FF EA EA EA EA EA EA 0C78 A9 9320 D2 FF 2084 FF OC80 AS 008081 DD 8003 DD OC88 4 CBE OC 20 BE OC 48 AS OC96 FE 2D OE DD 8D OE DD EA OC98 EA EA EA EA EA EA EA EA OCAO EA A9 BI OD OE DD BD OE OCAB DD AD OD DD A9 01 2C 00 OCBO DD FO FB 6860208400 OCB8 2023 0D EA EA EA A2 05 -CCO AS 00 B5 FB 2088 OC 20 0CC8 18 0D 46 FB 05 FB 85 FB OCDO CA DO F1 C9 04 DO 04 A2 OCDE 0086 FC Cs 00 DO O4 A己 OCEO 2886 FC 1865 FC AA BD OCES 2F OD C9 0D DO 02 0920 OCFO 20 D2 FF 208 EC 20 EA 0CF8 FF C9 8C D0 03 4C 1988 0Dee cs 52 d0 04 As FF 85 FD 0088 C9 4E D0 04 As 00 B5 FD $00104 C$ B8 OC EA 60201800 0 D 18 FO FB 60 AD 01 DD 45 FD 002029106020150 D 208 Cl 8028 OC 20 18 0D FO FS 6006 0D30 02 $2 F 2 E$ 00 00 $3 F 3931$ 00383036243229283528 0040 3A 25 2C 02 34240037 0048382720 2D 0A 330000 $00505658402047424 F 51$ $0058505348574 C$ 5A 5448 $006843464 E 4 A 52440055$ 006849532041 OA 4500 EA $0 D 78$ AD 11 D8 8D 00 C0 AD 18 0 DFB DO BD 01 CB AD 00 DD BD 0080 02 CO 4C 1E OA EA 20 E4 0D8B FF C9 00 FO F9 C9 B5 DO 0090 FS 4C E3 8800800000
it as follows
OPEN 15，8，15：PRINT 15，R：DDF＝DDF
where you can have any value you choose．
When you select FAX mode，keys F3．F5 and F7 are used to select the＇speeds＇（lines per minute－L．PM）required．A little fiddling and you＇ll soon become familiar with this．If the picture comes up negative，use the $R$ key to invert it．the $N$ key operates the opposite way to the R key．
To save a FAX picture，press S．Save under file name DDF （as explained above）．To view a saved FAX picture，press L，
The programs are written in machine code because BASIC is too slow．Note that disk and tape versions are given．so use the one appropriate to your machine＇s configuration． Both programs have been written to occupy the beginning of BASIC＇s program area at 800 hex（ 2048 decimal）．To load one of these into your computer you may either purchase the software from themagazine（see coupon later）or use a machine code＇monitor＇program to hand load the hex code listing given here．
The Commodore ASSEMBLER or MON64 monitor are suitable programs for loading machine code listings．After entering the code and checking it against the listing，save the memory range 800 H to D 93 H against the name of the program entered．

## ＇NORMAL＇／＇INVERSE＇RTTY

Now it＇s circuit diagram time！The Listening Post project was designed to provide a logic high signal to the computer when a high audio tone is being received．Unfortunately，some transmitter operators see things the other way，and their tel－ etype signals may come out＂upside－down＂．With a short－ wave receiver this is no problem；you just switch to the opposite sideband．But on the two metre FM nets（146－148 MHz ，you have to take the tones as they come．


If you want to hear an interesting reaction，just tell the guy on the other end he＇s sending upside－down．
There seems to be some dispute about what is right and wrong（amateur radio politics again）so included here is a circuit to right－side－up the upside－downs．You connect it be－ tween the output of Listening Post and the data input of your computer．It could be built on a bit of Vero board，on a tag－ strip or even suspended by its own components．It should then fit into the project case．Then，when you find someone sending upside－down，don＇t stir up the hornet＇s nest；just hit

## C64 LISTENING POST，for disk machines

0800 00 18 $08 \quad 8 \mathrm{~F} \quad 07$ 9E $32 \quad 30$ 080837330000 2ด 000000
 081800 A9 90 20 D2 FF 4C CD 082008 A9 9320 D2 FF AL 00 0828 BO 99 OA 20 D2 FF E8 EO 0830 CC D0 F5 20 E4 FF C9 00 0838 F0 F9 C9 4C DO 08 A己 00 $08402050 \quad 084 C \quad 70$ 日D C9 53 0848 DO E9 4C BD 08 EA EA EA $0850 \mathrm{BO} 5 \mathrm{C} 08 \mathrm{9D} 35$ 日A E8 EO 085803 D0 F5 60 4C 6508 EA 0860 4C 2E 09 EA 38 A9 08 A2 086808 AO 01 20 BA FF A9 03 0870 A己 85 AO 0B 20 BO FF AS 08780020 D5 FF 4C B6 00 90 0880 E3 4 C AF $08004444 \quad 46$ 0888 A9 9320 D2 FF A2 OD BD 0890650 OB 20 D 2 FF E 8 EO 26 0898 D0 F5 20 E4 FF C9 00 F0 08AD F9 C9 46 D0 03 4C 2108 08A8 C9 54 DO F9 4C 59 OC 20 0880 E4 FF C9 00 F0 F9 C9 85 0888 D0 F5 4C CD 08 A己 ดの 80 $08 C 060089035$ OA E8 EO 03 08C8 DO F5 4C F8 08 AD 11 DO 080080 00 CO $A D 18$ DO 80 日 1 0808 CO AD 00 DO 80 日2 CO 4C 08 E 8808 EA AD OD CO 8 CD 11 08E8 DO AD 01 CO 8018 DO AD 08F0 02 C0 80 00 00 4C 8808 08F8 20 E4 FF CS 00 FO F9 C9 0900 4E DO 04 AS $1085 \quad 36 \mathrm{CS}$ 090852 DO 04 A9 D1 85 36 EA 0910 EA 20 E4 FF C9 00 FO F9 $0918 \mathrm{C9} \mathrm{B6}$ D0 03 4C EC 09 CS 092087 D0 034 CF OS C9 8B 0928 DO E？4C O6 OA EA A9 OO 093085 FE A9 4080 00 DC A9 $093 \mathrm{~B} 00 \mathrm{B5}$ FB A9 6085 FC A己 0940 0B 205809 AD 19 04 0A 09485826 FE CA DO F3 20 E4 0950 FF C9 859003 4C E3 08 0958 4C GA 09 EA AS 1980 OE

0960 DD AD 00 DO 2901 FO F9 0968 G0 EA A5 F8 $18 \quad 69 \quad 08 \quad 85$ 0970 FB AS FE 91 FB 90 CB EA 0978 20 9309 EG FB AS FB E 098008 DO ET EG FC $98 \quad 18 \quad 69$ 098840 A8 DO 832088 OB 4C 0990 3F 0908 A9 0085 4A A9 $0998 \quad 7 \mathrm{~F} \quad 85 \mathrm{4B}$ A9 00 85 4C C6 $09 A 0$ 4A DO FC C6 $4 B$ DO F8 C6 09A8 4C EA DO FB CG 4A DO F？ 0980 A9 0085 4A A9 $7 F 854 B$ 0988 A9 00 B5 4C C6 4A OO FC －9C0 C6 $4 B$ DO FB CG $4 C$ EA DO OSCB FB CG 4 A DO FT AS OO BS 0900 4A A9 7F $854 B$ A9 0085 09084 C C6 4A DO FC C6 4 B DO Q9E0 F8 C6 4C EA DO FB CG 4A $09 E 8$ DO F7 EA 60 A9 47 ED 04 O9F0 DD A9 OF 80 05 DO 4C IE 09FB OA A9 $8680 \quad 04$ DD A9 07 OAOO 8D 05 DO $4 C$ IE OA AS $4 C$ 0A08 8004 DO A9 058005 DD OA10 4C 1E OA EA EA EA EA EA OA 18 EA EA EA EA EA EA 2044 OA20 OA 2057 OA 20 G2 OA 20 $0 A 2881$ OA 20 GO OA AD 11 DO OA3O 09 AO $80 \quad 11$ DO 4 C 2E 09 OA38 EA EA EA EA EA EA EA EA OA40 EA EA EA EA AD O2 DO 09
 $04509009 \quad 028000$ DO 60 AD
 OAGO DO GO AD 18 DO 29 OF 09 OAG8 $708018 \quad 00604536$ A2 0 OTO 00 90 00 5C 90005090 0 OR8 00 5E 90 00 5F CA DO F1 0 A80 60 A9 6085 FC A9 0085 $0 A 88 \mathrm{FB}$ AO OO 91 FB 88 DO FB OA90 E6 FC AG FC EO 80 DO F 3 0A98 $60 \quad 008080534 B \quad 4946$

 OABO 54 4F $204 \mathrm{C} \quad 45 \quad 46 \quad 54 \quad 20$ OABB $54 \quad 4 F 2041 \quad 4 C \quad 4947 \quad 4 E$ OACO OD 00 00004 C 202020
$\begin{array}{lllllllll}\text { OACB } & 20 & 4 C & 4 F & 41 & 44 & 00 & 00 & 53\end{array}$ OADO $2020 \quad 20205341 \quad 5645$ OAD8 00 0D 4E 202020 00 20 OAEO OD $4 E 4 F 5240414 C 20$ OAES $50 \quad 49 \quad 43 \quad 54 \quad 55 \quad 5245$ 00 OAFO OD 52202020002000 $\begin{array}{lllllllll}\text { OAF } & 52 & 45 & 56 & 45 & 52 & 53 & 45 & 20\end{array}$ $080050 \quad 49 \quad 43 \quad 54 \quad 55 \quad 52 \quad 45 \quad 00$ 08080046332020002000 $0810 \quad 36 \quad 30 \quad 20 \quad 4 \mathrm{C}$ 2E 50 2E 40 $0 B 18$ 2E 00 00 $46 \quad 3520 \quad 2000$ 08202000313230204 CE 0B28 50 2E 40 2E 00 00 $46 \quad 37$ $083020200020 \quad 00 \quad 323430$ 0838 20 4C 2E 50 2E 4D 2E 00 0B40 00 $46 \quad 312020 \quad 20 \quad 00 \quad 54$
 $\begin{array}{lllllllll}0850 & 43 & 52 & 45 & 41 & 54 & 45 & 44 & 20\end{array}$ $0 B 58425920544 F 4 E \quad 5920$ 0 OEO 574 F 4 F 4453000000

 $\begin{array}{llllllllll}0878 & 00 & 54 & 20 & 20 & 20 & 20 & 52 & 41\end{array}$ $\begin{array}{lllllllll}0880 & 44 & 49 & 4 F & 54 & 45 & 4 C & 45 & 54\end{array}$ 0888595045 EA EG FC AG FC 0890 E0 $7 C 9023$ A9 01 A2 98 0898 A0 0020 BA FF A9 03 A2 OBAO 85 AO 0820 BD FF A9 5C $0 B A 885 \quad 25$ AS OO 8524 AL OD $0 B 80$ AO 80 A9 2420 D8 FF 60 0BBB EA EA 00 4631202020 OBCO 0235204241554453日BCE 00 00 00 $46 \quad 33 \quad 20 \quad 2020$
 $080 B 0000004635202020$ OBEO 3735 20 $4241 \quad 5544 \quad 53$ OBE OD OD 00 $46 \quad 37202020$ OBF $\begin{array}{lllllllll}31 & 3 & 30 & 20 & 42 & 41 & 55 & 44\end{array}$
 0ce0 $20 \quad 53544 F 50$ EA 20 E4 0C08 FF C9 00 FO F9 C9 85 DO $0 C 10$ OD AS F8 8004 DD AS 2A $0 C 188005$ OD $4 C 78$ OC C9 86 $0 C 20$ DO 0D A9 OF BD O4 DO A9 $0 C 28$ 27 $80 \quad 05$ DO 4C 7B 日C C9
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If you require 'hardware inversion' of RTTY signals (or FAX for that matter) for your Listening Post, here's how it's done. The Microbee software has no provision for signal inversion, so Microbee owners will need this add-on modification to obtain signal inversion.

## RECEIVERS, AND HOW TO USE THEM

The receivers being offered to enthusiasts nowadays are really remarkable value for money. Several years ago I worked at the NASA space tracking station at Bermuda. Back in those days, the American space programme had a blank cheque; only the best of everything would do. One requirement we had was to be able to continuously receive the American time and frequency standard station "WWV". Simple, reliable receivers were then available especially for WWV reception. They had crystal-locked channels on the WWV standard frequencies of $5,10,15$ and 20 MHz . But they weren't considered good enough.

What we had were three identical general-coverage tunable receivers made by the Collins Radio Company. These were the absolute top of the line, and I even remember the model number: 51-S1. Each one would have been worth thousands of dollars. They were mounted in a $19^{\prime \prime}$ metal rack, one above the other. The first was tuned to 5 MHz , the second to 10 , and the third to 15 MHz . And there was a little switch on another panel that selected which receiver was to deliver the audio signal you wanted to listen to.
Sometimes, when I needed to regain my strength from chasing women and boozing and all that other stuff you do in Bermuda, I would go back to the NASA station after hours to play with one of the Collins receivers. As I tuned around and twiddled its many knobs, signalswould fairly 'jump out' of the speaker as if the transmitters were next door instead of half a world away. It was glorious! But I knew I could never own one.

Well, I was wrong. I never got a Collins receiver, but I'd say the one I'm using now is just about as good as that old Collins was.
Technology has caught up; my latest receiver, a FRG-8800 by Yaesu, has its own microprocessor and Collins-level performance, and it retails for around $\$ 800$. It doesn't have quite as many knobs, but those that remain serve the same functions as those on the Collins. And they are meant to be twiddled in the same classic way that has served generations of radio operators. Maybe the best way to learn about the new breed of receivers is to go through them control by control.

## Tuning

Sometimes known as "the big knob in the middle", the tuning control is the one you'll have your hand on the most. The purpose of the tuning control, of course, is to shift the frequency of the receiver up or down to the channel you want $>$

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to receive. Simple? Maybe in the old days, when the tuning knob was coupled directly to a "variable frequency oscillator" or VFO. But now it's usually hooked to a microprocessor.

In those early VFO-based receivers, the receiving frequency was controlled by a variable capacitor (to which the knob was connected), and a coil. Together they formed a "resonant circuit" that was supposed to oscillate in a stable and constant manner. But other things came into it . . the air temperature, how long the receiver had been switched on, the power line voltage and, so it seemed, the phases of the moon and what you had for breakfast that morning! These circuits would quite happily trot off to some other frequency, completely abandoning the one you were trying to listen to. This is called "drift". Some really crook receivers could skip across half a dozen stations on their own in less than an hour! The best ones were fairly tolerable.

When large scale integrated circuits became available, the old free-running VFO bit the dust. Now we have synthesisers that can generate something like 30000 discrete frequencies, based on the oscillations of a crystal. Crystal oscillators drift only very little, so if you tune a station with a receiver that uses a synthesiser, it should stay tuned within very narrow limits. The knob on such receivers is sometimes connected to a perforated disk that interrupts a beam of light. As the knob spins. the beam pulses on and off, and each pulse steps the synthesiser up or down one of its 30000 frequencies. This arrangement, by the way, is still called a VFO by many manufacturers.
Synthesised receivers generally have a numeric keyboard, much like a calculator, that lets you enter some desired frequency directly. If you just want to sniff around, you use the kevboard to call up some frequency in the general area of interest and then use the tuning knob to zero in on interesting signals.

## Attenuator/RF gain

This would be one of the most misunderstood receiver functions. It varies the overall amount of amplification available in the receiver, and it's definitely NOT a case of "the more the merrier". The attenuator, or RF gain control, allows you to optimise the receiver's performance, given a certain level of unwanted noise, the strength of the signal you want, the strength of signals nearby, and the general condition of the Tuning controls. The
one at left is a
'traditional' type
knob tuning, while
that on the right is from a synthesised receiver - you enter the desired
frequency on the



The 'RF attenuator' or 'RF gain' may be a continuous control, as at left, or a switch-type, as at right.
frequency band you're working in. It all sounds complicated, but it's really just a matter of using your ears.

As an experiment, just tune the receiver to 25 MHz or so, and turn the RF gain flat out or the attenuator to 0. If you crank the volume up you'll hear a great rushing noise, but little else. With reception conditions like they've been for the past few months it's unlikely you'll hear anything, other than your receiver straining its little guts out to get something that isn't there. It represents the low extreme of signal levels.

Now, preferably at night, tune to the normal broadcast band, or maybe one of the lower shortwave broadcast bands; perhaps around 6 MHz . You will likely hear a great jumble of signals, all very strong, and few of them making sense, except for the big wallopers like Radio Moscow. Now comes the good bit: if you grab your RF gain control or attenuator and bring the signals down a bit, you'll discover you can begin to separate them . . . the strong and the weak are no longer mixed and you can pick and choose the ones you want. Your signal strength meter will probably fall, but who cares? You're listening with your ears, not your eyes, remember?

What was happening was this: with everything 'flat out', those big big signals were overloading some of the receiver's amplifiers, distorting, and spreading into adjacent channels. Weak signals in those channels were buried in the distortion products generated inside the receiver. Reducing the overall gain allowed the signals to pass through undistorted, in the correct relationship with each other. And at the far end, your ears picked them out, aided by the receiver's filter. Most receivers have a circuit that does a lot of this work for you
. it's called an "automatic gain control", or AGC. But it's still best to give it a head start with your manual control. This will provide the best possible performance.

## The filters: wide and narrow

We've just seen that proper twiddling of the tuning and RF gain controls can provide us with a nice collection of great big signals and little bitty signals and everything in between, to pick and choose from. Now, if the one you're after is the weak one (and it usually is!), you must try to reduce the influence of the big ones as much as possible. Ideally, we could just drive a couple of "wedges" down into the band and push the big fellers aside, exposing the little signals to our attention. This is the function of the filter.


The filter selects a slice of the band, and rejects everything else. The filter marked "narrow" probably allows through a chunk of the spectrum about 2700 Hz wide, or so. This is because the bandwidth of human speech is usually assumed to be from 300 to 3000 Hz . When you tune the receiver you're actually sliding this "slice" up and down through the spectrum. It's like a slit, and you look at a little piece of the spectrum through it as it passes, or when it stops. If you have a choice of a "wide" filter, it may be around 6 kHz to 10 kHz
wide to allow the higher audio notes of music to pass through. A nother filter may be for CW, that is, Morse Code. It may be only 300 Hz wide, as code comes through as a single tone.

You generally choose the filter for the application, using the wide filter for broadcast stations (unless you're trying to extract a weak one from among strong ones). The narrow filter is for voice (or voice-bandwidth signals), and the CW filter is sometimes useful for some kinds of radioteletype transmissions, apart from morse signals.

## Mode: AM, SSB, CW, FM

Without getting too technical, the single sideband method of transmission was developed because it gives an eight times advantage over an AM transmission of the same power. An AM signal consists of a "carrier", and two "sidebands". In SSB, the transmitter 'throws away' the carrier and one of the sidebands, transmitting the remaining sideband. This can be either the one higher in frequency than the carrier (upper sideband - USB), or the lower one (lower sideband - LSB). Some stations can transmit two programmes at once, one on each sideband (but that's of no concern here).


Mode selection. Just push the right button! With the Listening Post, the single sideband (SSB) reception mode is generally used.
A receiver for AM is pretty simple, as anyone who has built a crystal set knows. But an SSB receiver must re-supply the missing carrier the transmitter threw out before the signal can make any sense. It must then use a narrow filter to select either the upper or the lower sideband. If you are tuned to an AM signal, but have your mode switch on SSB, you will most likely hear a loud "howl" as the incoming carrier and the carrier your receiver is re-inserting "beat" against each other, providing an audible difference.
Conversely, if you are tuned to an SSB station but your receiver is set for AM, you'll hear something that sounds like Donald Duck with a hose shoved down his throat! You'll soon learn which position to use for what, just to minimise the strange noises.

FM (frequency modulation) is a pretty special case, normally used only if you can receive on the VHF bands. For radioteletype and facsimile (see the Listening Post project) you almost always use the SSB reception mode. This is because these signals are simply frequency-shifting carriers, and you have to use SSB to make them "howl", otherwise there will be no audio output at all.

That pretty well covers the receiving device itself, but we still have to contend with that old antagonist of neighbours and city councils, the antenna.

## Yes folks, the antenna!

First, let's repeat rule number one: You Must Use An Outside Antenna. YOU MUST USE AN OUTSIDE ANTENNA! Just about any type of antenna will work, but it must be outside the house, and the lead connecting it to your receiver must be shielded! SHIELDED! YOU HEAR ME? There. That's off my chest. If it isn't shielded, and you're running a computer nearby, you'll hear all computer noise and few signals. If you want some ideas on antennas, may I suggest you read the article on "Simple Antennas for Suburban Sites" in the first (July '85) issue of AEM.

You think you've got a good antenna? Been up for awhile, and it still works? I thought I had a good antenna, as I lectured other people about why they should have one. Mr Conmfidence! But now I must bury my head in shame and relate the following tale:

## Why my Listening Post died

Sunny Hobart, you must realise, lies straight in the path of the Roaring Forties. And when one lives halfway up the side of Mt Wellington, one must expect occasional breezes beyond those described as "gentle"

We've had three trees blown down this year. Actually, numbers two and three were the same tree blown down twice. One northerly storm knocked it over, where it lay in repose as I tried to figure out where I could bot a chainsaw. A couple of days later a southerly buster came through, stood the tree back up, and then blew it over the other way, onto the house. It was during this disturbance that my 30 foot Hills television mast, which holds up my shortwave antenna, also came down in a heap when a guy wire broke.
We must digress at this stage to introduce One-Eye, the family possum. She has lived here for at least five years, during which time she has appeared every few months with a baby possum on her back. The kids love it! One-Eye rules the roost here, banishing any other possums that might stray onto the property. This has resulted in many wild brawls, including the one that cost her an eye. One-Eye has some unusual tastes in food. Although possums are supposed to be vegetarians, she gobbles up meat scraps like they're going out of style. And she's developed a strong liking for vinyl.
During reconstruction of the fallen antenna mast, I noticed strange puncture marks in the covering of the coaxial cable that led from my Listening Post to the big wire antenna at the top of the mast. And that night, when I went outside, there was One-Eye with her hands wrapped around the rolled-up length of coax, gutsing herself on the vinyl covering. When I checked the puncture marks I had noticed earlier, I found that One-Eye had completely severed the shield of the coax, although the vinyl covering, from a distance, had appeared intact.

The entire length of coax was a wreck, and had to be replaced. But with the new piece installed, the Listening Post sprang into life as it never had before. The old coax, under the influence of One-Eye, had been getting gradually worse and worse, so slowly that the change was barely noticeable. And all this time I had been preaching to people to use a good antenna when my own had been just about non-existant!

The new installation has now been possum-proofed, with the coax within One-Eye's reach being run through aluminium tubing. And to think that all this time I had been blaming failing reception on the condition of the radio bands! It certainly pays to check things now and then.

## Ears for the Listening Post

Many people have asked about what kind of receiver to use for the Listening Post project. The answer is, in general, to get the best one you can afford. Any receiver used for communications monitoring with a computer MUST be able to receive single-sideband voice signals, since this is the reception mode also used for radioteletype, facsimile, and Morse code. The receiver MUST be able to be used with an ouside antenna; you must be able to collect the signals from an area outside the noise and interference radiated by your computer. In an earlier column (Bee Buzz, Sept '85) I discussed ways to cut down the noise from the computer, but some noise is always present and the signals we are looking for are sometimes very weak.
The first issue of AEM showed the Listening Post being used with a Bearcat DX-1000 receiver. This is the one I developed the project on and it's typical of the current breed
of synthesised receivers. (I'm using a Yaesu FRG-8800 now, who knows what will be next?)
It's also quite practical to use the older analogue (nonsynthesised) receivers with the Listening Post, but they must have fairly stable oscillators to stay on frequency. Even then you must manually give the frequency a tweak every few minutes. Still, they're becoming available second-hand at quite attractive prices, and you'll find they do a reasonable job. I used one (a Drake SSR-1) for several years, with good results.


The Bearcat DX-1000, the receiver I used in developing the Listening Post. I now use a Yaesu FRG-8800.
Since that first article (July '85) I've had a chance to try some synthesised receivers that are somewhat down-market from the Bearcat/Yaesu class.

Sony has a line of little portables that are fully synthesised, and also contain such features as pre-set channels and digital clocks. A popular one of these is the ICF 2001-D model. It receives AM, FM and single sideband, and includes the standard broadcast, longwave, aviation, and shortwave bands. It works well with the Listening Post, but only with an outside antenna. (Are you sick of me yet? Are you ready to scream?)

Another one from Sony is the ICF 7600-D. What a little gem! It's about half the size of the other Sony; I got one at home over a weekend to play with, which I carried home on my trusty bicycle, it's that portable. If I owned one of these I could see it going on a lot of bushwalking trips, as well as working with the Listening Post. Some of its disadvantages are not quite as much frequency coverage, and the lack of a "proper" single sideband detector. It seems to provide a "beat frequency oscillator" for this purpose which works all right, but it's not quite as crash-hot as its bigger brother, or the Bearcat or Yaesu. Still, it goes well with clear signals, and it's so tiny ...

As far as hooking all these lovely machines up to Listening Post, all you need is a bit of audio. The Bearcat has a "recorder" output on the back that works nicely. The Yaesu has a "line" output; again, no problems. One to watch out for is the Sony; its "recorder" output is at "microphone" level (all of them should be, really) but there's not enough "oomph" to drive the XR-2211 chip on the Listening Post. Plug into the speaker and you'll be OK.

You'll notice I haven't mentioned prices on these receivers. An approximation would be that the Sony 2001 is about $2 / 3$ a Bearcat, and the Sony 7600 is about half a Bearcat. With the devalued Australian dollar they're all going up at the speed of light, so any prices quoted would be terribly wrong by the time you read this. It's a shame, really. The Japanese stuff is so nicely made and presented, especially Sony. As well, the cost of Japanese labour is skyrocketing and if it keeps up none of us will be able to afford what they make. It happened to American and European goods, and Japan will be next if they don't watch it. 4

# Avtek Multimodem II features expansion bus for plug-in developments 

S
ydney-based modem manufacturer Avtek Electronics has just launched an exciting new version of the popular Multimodem. Designated the "Multimodem II" it provides all the features of its predecessor, plus important new developments that they say more than match overseas and local competitors.

The modem provides both 1200 and 300 baud as well as CCITT and BELL standards. Avtek continues to use the extremely reliable 'world modem' chip which incorporates digital filtering and error correction.
A dedicated IC is now used for the ring detect circuit. Most modems (including the earlier Multimodem) are sensitive to ring voltage and will auto-answer at different times, depending on the strength of the ring. The new modem has a circuit which actually distinguishes the ring from other line signals. The modem ansivers instantly on strong or weak signals.

## Attention modem constructors

Promark Electronics stocks the EF7910 single-chip modem, a Thomson-CSF device which is pin-compatible with the Am7910 'world chip'.

This is pin selectable for baud rates 300.600 or 1200 bits per second and is compatible with the applicable Bell and CCITT recommended standards for 103/115 108. 202. V21 and V23 type modems.

Digital signal processing techniques are employed in the EF7910 to perform all major functions such as modulation. demodulation and filtering.

The EF7910 contains on-chip analogue-to-digital and digital-to-analogue converter circuits to minimize the external components in a system. This device includes the essential RS-232/CCIT"T V24 terminal control signals with TTL levels.

For further information contact: Promark Electronics, PO Box 381, Crows Nest 2065, NSW. (02)439 6477.

## T.I. expands ROM family

Texas Instruments has announced a new readonly memory device, the

A unique feature of the modem is the expansion buss. The Multimodem can be expanded via a whole array of outputs on the circuit board.
The baud rate convertor option is now built into the modem. It is of special interest to those with microcomputers such as the IBM PC and Commodore, especially when Viatel facilities are needed. requiring a $1200 / 75$ split baud rate The baud rate convertor makes complex cabling and software arrangements unnecessary. For further information call Avtek Electronics, PO Box 651 Lane Cove 2066 NSW. (02)427 6688.

TMS2364, a 64 K ROM. It offers a 150 ns maximum access time. comes in the 28 -pin industry standard package and is designed to complement II's other 64 K ROM, the TMS 4764 $24-p i n$ device.
The chip represents a costeffective alternative to EPROMs in high-volume applications such as automative and industrial controls, printers, personal computers and telecommunications equipment, Tl say.
11 is fabricated using N channel silicon-gate technology for high speed and simple interface with TTL and CMOS circuits.

Further details from: Texas Instruments Australia Limited at North Ryde, NSW. (02)887 1122.

## Rich raids rip-offs

We are delighted to hear of the progress of the AntiSoftware Piracy Association which says it is now poised to launch its second major raid against Hong Kong software pirates.

The Association, which originally consisted of Imagineering. Australia's largest distributor of microcomputer software and peripherals, Lotus Development

ASPA has also moulded prominent opinion in the colony. He says that having had piracy brought to their attention, many leading corporate decisionmakers are less than proud of Hong Kong's reputation as a pirates' haven.

This is an opportunity for us to endorse the view that software development will only flourish if distributors and users will show a little fairness.

We believe that the computer shopkeeper who copies proprictary software illegally to help market his machines is no more honest than the shoplifter whom he would certainly prosecute, and those few teachers who encourage pupils to copy games and other software not in the public: domain are hardly teaching them a sense of right and wrong.

Corp and Ashton-Tate, now has MicroPro and Broderbund as members
Imagineering's managing director, Jodee rich, says MicroPro and Broderbund have contributed substantial sums of money to ASPA's fighting fund. which originally totalled $\$$ US100 000.
Rich says ASPA was happy with the results of its first major raid in Hong Kong, and believes a second raid will be even more successful. He says the Association will concentrate on Hong Kong for some time, as the market is big enough to support legitimate software sales and support.
"It will be even more attractive as a market for major software distributors if ASPA cleans up existing piracy - and that could be good for software buyers in the long run." he says.
Rich says the activities of

## BYIEWIDE



How Mozart's
keeping up with the Commodore

While "Amadeus" is packing them in at the cinema, other would-be Mozarts are seated at their home computers playing his music
Commodore has released
"Popular Classics', the first in a series of Music Maker Playalong Album Programs for the Commodore 64.
Using a musical keyboard overlay the Commodore "pianist" can play any of such famous works as Brahms' Hungarian Dance No. 5. Sonata in C Major by Mozart. Tchaikovsky's Concerto No. 1 in B Minor, or Emperor Waltz by Strauss (ancestor of the well known Levi).

In single-key mode our genius to-be can tap out the timing Each note of the melody will play. together with the accom paniment. Prompting on screen and computer-controlled timing are added in Rehearsal Mode With this tutoring budding Mozarts can quickly become efficient, we're told.
The chance to display talent comes with Performance Mode. A few beats of the built-in metronome and the Commodore musician plays the entire melody along to the accompaniment. He/she can speed up or slow the tempo according to his/her ability or preference.
By connecting the Commodore 64 to a Midi-equipped keyboard further sophistication is possible. Playalong albums have three musical parts to the melody which can be directed to different channels on the Midi.

## NO FLICK FOR VIC

It seems that with the demise of so many user groups and newsletters, the news of yet another good user group mag. getting 'the flick' is of little consequence these days.
However, the news that one IS TO CONTINUE DESPITE THE DEMISE OF THE ASSOCIATED COMPUTER really is news! What we're talking about is fondly known as "VIC", the journal of the ACT VIC-20 Users Association - and a right good journal, too!
Now in its third year of publication, with 16 bi-monthly issues under the belt, VIC continues strongly, catering for all VIC-20 colour computer owners, packed with "the right stuff", very little advertising/news/bump cluttering its pages.
All that for $\$ 2.00$ an issue, which must be value in this everinflationary world.
The ACT VIC-20 Users Association also distributes a public domain software collection, any item available for a small copying fee; the library comprises around 900 programs.
Full information from the indefatigable and still enthusiastic Chris Groenhout, 25 Keriord St, Watson 2602 ACT. (062) 412136.

This allows the keyboard sound to be split into different "instruments".
If all this sounds too much like work, then our weary musician can relax, put his feet up, pour himself a drink and select Concert Mode. Now he can listen to his favourite classics while watching exciting screen graphics.
Each Commodore Music Maker Playalong Album comes complete with User Guide, Music Book and both cassette and disk. Music Maker Payalong Albums are available from Commodore dealers at a cost of $\$ 25$ each.
Maestro!

## On the buses

Pro-Log. in Melbourne, has announced more STD bus products.
The 7614 64-line digital I/O bus card STD provides 64 lines of input/output which can be configured in groups of eight as inputs or as outputs with readback. Alternatively, they can be configured with as many as 32 lines of bidirectional I/O.
The card, which is also available in a previously announced CMOS version, features latching connectors to securely lock cables into place. The 7614 decodes either an 8 -bit or 10 -bit I/O address. thereby making it compatible with the Z80, 8085, 6800, and 8088 processors.
Priced at $\$ 346$ in single quantities, the 7614 is available immediately and comes with Pro-Log's standard two-year limited warranty on parts and labour.
Pro-Log has also announced three new CMOS cards for the STD bus. including an 80C88 processor card, a 64 K memory card. and a 64 -line 1/O card.
The 78C61 processor card integrates the 80C88 (a CMOS 8088) and five JEDEC memory sockets for EPROM or RAM into a card that can be populated with 136 K of memory and address a full 1 M . One socket is configured for a byte-wide RAM, a second socket is for EPROM. and the three remaining sockets can accommodate either RAMs or EPROMs. The 80C88 microprocessor has a 20-bit memory address bus and a 16 -bit I/O bus. The latter bus provides a 64 K I/O port capacity:

The 77C09 memory card can be expanded in 8 K byte-wide increments of either RAM or ROM to a maximum of 64 K . The
card works with STD bus systems using either a 20 -bit address bus (such as the 80 C 88 ) or a 16 -bit address bus (such as the CMOS Z 80 or 80 C 85 ). In addition, the 77C09 is available with an optional Lithium battery that can typically retain data for up to ten years.
The 76C14 is a general purpose, high-density l/O card that provides 64 I/O lines (eight ports) and latching header connectors for reliable cable connections to the system. The eight ports can be configured for input, output, or output with readback capability. Alternatively, the 76 C 14 can be configured as four bidirectional I/O ports. The card decodes either an 8-bit or 10 -bit I/O address, providing compatibility with any 8 -bit or 80C88 CMOS processor cards.
For details, call Prolog on (03)836 3533.

## New Intel DRAM controller

Intel has introduced the first CMOS dynamic RAM (DRAM) controller, a high performance VLSI chip called the 82CO8.

This chip is designed to easily interface 64 K and 256 K DRAMs to microprocessors made by Intel and other manufacturers.
It requires very low operating currents (both in active mode and in power-down mode, during which only the RAM is engaged), making possible a range of low-power and battery backup applications.
The new chip, a CHMOS (Intel's high-performance CMOS process) version of Intel's 8208, provides all the signals necessary to control 64 K and 256 K CMOS DRAMs, such as Intel's CHMOS 51C64 ( 64 K ) and 51C256 (256K) DRAMs.
It supports all Intel microprocessors, including the high-performance iAPX286, as well as Multibus-based systems. And because the 82 CO 8 operates synchronously or asynchronously, it also runs with other, non-Intel microprocessors.
The little beauty draws less than 30 mA in active mode, allowing a smaller power supply for greater compactness, lower cost and heat. In power-down mode only the system RAM receives power (the controller chip refreshes the RAM automatically), reducing current re-

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quirements to the microampere range.
The new chip directly addresses and drives up to 1 M of memory without external drivers, and offers automatic RAM warm-up, five programmable refresh modes and no-wait-state performance.
The 82CO8 is pin-compatible with the 8208, providing a lowpower upgrade path for existing systems, is available in sample quantities now, and is scheduled for volume production shortly. See Total Electronics.

## Take it easy

Imagineering has announced the release of Easy, a new wordprocessing package from MicroPro, that's perfect for the first-time user who doesn't do heavy-duty wordprocessing, they say.

Priced at \$225, Easy is designed for those who use wordprocessing as a secondary function to their use of a spreadsheet or database.
"If you have never used a wordprocessing package before. you can sit down with Easy on your computer and produce a document within 30 minutes," Imagineering's marketing controller for government and educational software, Renata Nelipa, says.
Easy is said to have a short learning curve because of the program's heavy reliance on prompts and menus and extensive on-line help screens. Most of the commands needed for day-to-day work are easy to remember.
Editing procedures for inserting and deleting text, moving and copying blocks of text and navigating around the document will seem very straightforward to experienced wordprocessing users. The novice user won't have any difficulty either because prompts rather than function keys lead the way.

Easy has the capacity for enhanced printing with boldface, underlining, superscript and subscript, It supports 121 popular printers, which Imagineering claim is more than any other wordprocessing program in this same category and price range.

MicroPro has given the Easy user an upward migratory path. You can progress to MicroPro's WordStar and WordStar 2000 because Easy's files are compatible with both these products.
Further information from Imagineering, 77 Dunning Ave, Rosebery 2018. (02) 6624499.


## Printers becoming increasingly dotty

Amongst the advances in technology which benefit both the computer hobbyist and professional the printer is in the forefront. Press releases arrive in rapid succession (and like bills, somehow in spite of postal strikes) trumpeting (or should it be matrixing) the latest product. Of course the latest printers are faster, quieter. cheaper and more versatile than the one I've just bought.
We hear that NEC has "put the industry into a panic" with the release of their P5 printer. This is expected to eat heavily into the daisy wheel printer market, they say.
Its strongest point is very high speed letter quality. This comes as a result of a newly engineered 24 -pin print head. Most printers still use 9-pin heads. NLQ (near letter quality) mode on these printers can only be achieved by multiple passes. This is very slow, often slower than an equivalent daisy wheel printer. However, NEC claims the P5 achieves 94 cps in letter quality mode.
The other very noticeable thing about the P5 is the low level of noise. Most matrix printers sound like a mixture of the buzz saw and dentist's drill. The P5 is very quiet anyway, but an exclusive "quiet mode" reduces noise to vanishing point, they claim.
The P5 will also operate at a 264 cps for quick drafts or computer listings.
The printer has a built-in buffer which can be upgraded to 40 K if required in two 16 K increments, so there is no need for external print buffers. Indeed, with the full 40 K , effectively the printer is its own spooler.
The other handy feature is plug-in fonts. The P5 has built in 10. 12, and proportional pitch fonts, and more can be added, plugging directly into the printer.
The price of the P5 is very attractive at $\$ 1765$ plus tax. Further information from: Natwick Management $2 / 25$ Burns Bay Road, Lane Cove NSW. (02) 4281666.

## A wee DRAM or two

Texas Instruments has begun volume production of its $256 \mathrm{~K} \times 1$ Dynamic RandomAccess Memory (DRAM).

This 256 K DRAM uses the same refresh scheme used in the 64 K DRAM and is directly upward compatible, allowing an immediate quadrupling of system memory capacity without redesign or additional hardware.

Alternatively, the device may be used to reduce physical system size while maintaining memory capacity.

The 256 K DRAM is expected to be the largest-selling component in the history of the semiconductor industry, with, according to Dataquest estimates, shipments of three billion units predicted worldwide for 1989, the peak year of production.
The TMS4256 and the 'I'MS4257 are the first of a TI family of 256 K DRAM components and modules using these components.

Further information from: Texas Instruments Australia Limited, 6-10 Talavera Road, North Ryde 2113, NSW. (02)887 1122.

## GALAH COMPUTING ANNOUNCES FIRST PRODUCT

Despite the Editor's mirth in The Last Laugh last month, Galah Computing is alive and well. Our first product has not been developed but this is only due to a lack of energy, money, and expertise. Our copious R \& D department, although engaged almost full time researching methods of completing forms for the AIRDIB grant, has a very clear idea of the ultimate personal computer, although if we were blessed with the three missing ingredients above we might still hesitate to join the never ending stream of new computer releases.

Seriously, however, here is our specification and if a unit exists to satisfy it we would like to hear about it:-

- Whole unit in single package with no ugly square IBM lookalike boxes, troublesome interconnecting leads etc, but with the screen tiltable within a reasonable range.
- In-built 100 Megabyte hard disk.
- Two 3.5" floppy disk drives - double sided double density.
- In-built modem with auto-dial auto-answer, decadic or DTMF dialling.
- RS232, IEEE and Centronics interfaces.
- High resolution colour VDU.
- 1 Mbyte RAM.
- In-built calendar and clock with battery backup, also protect ing RAM in the event of short term power failure.
- Full anti-spike and brownout power filtering.
- Really good RF shielding.
- Standard Qwerty keyboard with no attempt to copy IBM design.
- Numeric keypad and programmable function keys.
- Switchable compatibility, CP/M, MSDOS, etc.
- On-board firmware: a range of standard systems such as database, spreadsheet, wordprocessing. Easy user access to these in, say, lozenge form (. . . suck lozenge for one hour and know all about it? - Ed.)
- On-board sound generator (with speaker).

What is your ideal home/small business machine? Readers with comments or additions to the above specification are invited to drop a note to Bytewide. We might even consider an all-singing, all-dancing AEM design.
Meanwhile, Galah's purchasing team are scouring Lisle Street in London's West End (. . . remember all those war surplus shops that later became brothels? - Ed.) for sources of parts to build the above. But the last purchase was mystifying, and we haven't heard from them since.

Chief Galah

## Roy Hill

# Ever pondered about a 'strange' computer language muttered about in hushed tones at computer group meetings - called "Forth"? Do not be afraid, oh gentle hackers - it's not some exclusive code known only to a select group, it's more like the "tool" to set your computing creativity free! 

TO DEFINE the concept of Forth is rather difficult. For a start, Forth is really not a language, in the same way that databases aren't really languages. Neither is it a piece of applications software. Databases fit quite well into this category. Forth also fails to fit the "language" criterion in that it is both a compiler and an interpreter, almost at the same time. Forth has also taken an extremely long time to gain even a small acceptance in the computing fraternity. In the words of its inventor, Charles Moore, "Forth is a polarizing concept. There are people who love it and people who hate it. It's just like religion and politics. If you want to start an argument, say, 'Boy, Forth's really a great language' " ${ }^{(1)}$.

The most satisfactory definition of Forth, I believe, is my own. Forth is: "a programmer's version of the power drill with its accessory kit." In this sense, it is one of the tools that a builder can use to create the final product, which may, for example, be a house. How well the house is built depends upon one thing - how well the builder knows the uses of his tools.

## Forth is a programmer's version of the power drill with its accessory kit.

One of the most frequent criticisms of Forth is the lack of standardisation in the "language". This is just like criticising a builder because his house is not identical to the rest of the houses in the street. The whole "raison-de-etre" for Forth is this very lack of forcing square blocks to fit round holes. Allow me to continue to use the building analogy. If I wish to cut a piece of wood for my house, I may choose to use my power saw accessory. Another builder may very well choose to use the jigsaw accessory. Neither of us is either right or wrong in our selection of the tool, we may just have a reason for preferring one to the other.
Forth is, therefore, exactly what a 'proper' programming tool should be - a 'mind amplifier'. There are many documented examples ${ }^{(2.3)}$ of this fact. SAVVY, one of the latest generation of databases is written in Forth, a Forth system is floating around in a satellite in space, and a complete stand-alone Forth computer ${ }^{(4)}$ is available in Australia.
Forth was developed (rather than invented) over a four year period between 1969 and 1972 by Charles Moore, a programmer at the Kitt Peak Observatory in the USA, who was less than satisfied with the programming tools then available ${ }^{(1)}$.

## Lifting the hood

Let us now lift the hood and see what Forth is all about. Firstly, Forth is extremely compact. A fully operational system only occupies from 12 to 16 K of memory and "strippeddown" versions run in as little as 2 K . Forth is also very cheap to implement on most systems.

My favourite of Forth is a public domain version ${ }^{(5)}$ which costs the staggering sum of $\$ 38$ and contains a complete

40/80-column screen-oriented editor that uses all the familiar Wordstar control commands. This package also contains a complete program/file management system which operates on an index and obviates the need to access the rather messy Forth source code development system called "screens". The dearest system I know of costs from $\$ 700$ to $\$ 2000{ }^{(6)}$, depending on the options selected. However, this is a fully fledged commercial control version of Forth and is fully supported by the Australian distributor.

Control is where Forth really proves its value though. Forth is far quicker to write than and executes at about $60 \%$ to $80 \%$ as fast as assembly language, depending upon the ability of the Forth programmer. Programs which would be hopelessly slow in BASIC (e.g: real-time data acquisition programs for high speed processes and instruments) are ideally written in Forth. For highly time-critical routines (e.g: the actual data capture) nearly all versions of Forth have a fully implemented assembler which contains all of the conditional structured control commands that we would dearly like to see in a conventional assembler, e.g: IF-THEN-ELSE, WHILE-REPEAT-UNTIL, BEGIN-AGAIN.

The motor of Forth is the Stack. This is the same sort of stack that can be found on a variety of calculators that use a particular variety of formula processing called RPN. (Reverse Polish Notation - after its Polish inventor). Use of the stack involves placing numbers thereon, and then proceeding to apply calculations sequentially to these numbers. This is best illustrated using an example.

In conventional mathematics, a formula might be $x=(6+2)^{*}(7+8 / 2)$. This would evaluate to $x=88$. Using RPN, we would say $82 / 7+62+$ * and obtain the same result. Purists would argue that RPN is far more complicated than conventional parenthetical mathematics, but RPN has many advantages for the Forth user.

## Forth is far quicker to write . . . and executes at about 50\% to 80\% as fast as assembly language. . .

The stack is an ideal method of passing calculated values from one word to another. In fact, although Forth does provide the means for handling variables and constants, a clever Forth programmer can perform $90 \%$ of his data handling via the stack. One of the most important tasks of the Forth programmer is to keep track of the items on the stack at any given time. (A suitable Forth coding form is shown in Figures 1 and 2). To continue the electric drill analogy, if the motor of Forth is the stack, then the gear-box is most definitely the Forth word.
Words are to Forth what "reserved words" are to BASIC. They are means by which the Forth programmer manipulates data on the stack and in memory and also the means by which communication outside the program occurs.

On the simplest level, Forth words are very familiar programming symbols such as $+-/$ and *. However, the words SWAP DUP ROT and OVER may not be so familiar, but are equally as important. Forth uses words in the same manner that we, as humans, do. Forth places its words in a Dictionary and programmers may organise groups of specialised words into sub-dictionaries known as Vocabularies.

A Forth word may be executed by selecting the appropriate vocabulary (Forth itself is the main vocabulary) and then typing the name of the word to be executed. For example, I may have a vocabulary called ROCKET, which contains the words MAIN IGNITE ENGINE BOOSTER etc. I may then define a new word FIRE which comprises some (or all) of the words defined in this vocabulary.
A Forth programmer writes his program by selecting the appropriate Forth words to operate on his data and then combines these words into a single new word that accomplishes the desired result. A typical example of this would be to define a word called $S Q R$, which would calculate the square of any number placed on the stack. To perform this requires that we compile our new word SQR. This is signalled to the Forth compiler by using a colon sign to indicate the start of compilation and a semicolon to indicate the cessation of com-pilation:-

## : SQR DUP * .;

The colon tells Forth to place a new word in its dictionary and give it the name SQR. DUP tells Forth to duplicate the number on the top of the stack so that there are two copies '*'multiplies the two top numbers on the stack and the period is Forth's way of printing out the number left on the top of the stack. The semicolon tells Forth to cease the compilation process and if the compilation was successful, the new definition can be executed in the interpretive mode. There are several noteworthy points to mention here:

1. Unlike BASIC, Forth differentiates between upper and lower case, thus the word sqr is not interchangeable with the word SQR.
2. Most Forth operations perform in destructive manner on the stack. That is, they remove the numbers that thye are operating on from their positions on the stack.
3. It is important to differentiate between execution mode and compilation mode. In compilation mode, no numbers are being manipulated. This only occurs in execution mode. (There are some notable exceptions to this rule, but they won't be discussed here.)
4. Forth only operates on integer quantities. There are several floating point packages available, (several in the public domain ${ }^{[3])}$ but these are not standard on most systems.

In case the idea of integer only arithmetic seems a bit repugnant to the reader, there are two important facts to remember:

1. Integer arithmetic is compact. Because the computer doesn't have to keep track of decimal points, integer numbers require far less (usually half) storage than do floating point numbers.
2. Integer arithmetic is fast. Execution speeds for integer arithmetic can be up to six times faster than comparable floating point calculations. Pseudo-floating point operations can be performed by using scaling techniques if this becomes necessary. Control applications (the place where Forth is most at home) usually only deal with integer quantities.

One further argument in favour of integer arithmetic - the


Figure 1.
original version of Applesoft was an integer-only BASIC interpreter. A large number of fairly sophisticated programs were written in integer BASIC.
Perhaps one of the greatest disadvantages of Forth is the documentation process. Forth has been described ${ }^{(2)}$ as a "write only language". Most modern languages are fairly good at self-documenting. Forth is a notable exception. One has to force oneself to thoroughly document Forth source code (called SCREENS) or pay the penalty of bad memory. I have fallen into the documentation trap myself. A Forth program that I omitted to document took me two days to decode when a modification became necessary. In fairness to Forth, I should point out that most programs and applications can be made to be self-documenting, simply by choosing meaningful word names. The only justifiable reason for using abbreviated word names is when space considerations are important. For example, our definition of FIRE in the ROCKET vocabulary could either be like this:-

## FIRE MAIN-ENGINE IGNITE BOOSTER IGNITE;

or, alternatively, our definition could look like this:-

## : FIRE M-E IG BO IG ;

The first version is pretty well self-explanatory, but the second

Leo Brodie ${ }^{(7)}$ also makes this point quite well. To demonstrate what Forth documentation should contain, I have provided two accompanying figures. Figure 1 illustrates a Forth coding sheet ${ }^{(8)}$ showing several short colon defini-

tions, and Figure 3 shows these same colon definitions as part of a Forth source code screen.

## A peek at the SCREEN

As readers have probably realised, the main form of source code for Forth is the SCREEN. Colon definitions (i.e: compiled words) can be written directly from the terminal, but additions or corrections cannot be made to the compiled word. It must be re-defined with the additions/corrections included in the new definition. Forth screens are the main form of mass storage. These usually occupy 1 K of memory space, both within the computer and on disk or tape. Forth editors (used for the creation/modification of screens) take advantage of this fact and a screen is usually either 24 lines $\times 40$ columns for a standard 40 -column video (note: 64 bytes are inaccessible) or 16 lines by 64 characters for an 80 -column screen. My favourite version of Forth ${ }^{(5)}$ - I have seen quite a few - allows full access to the 1 K bytes in both modes. In 80 -column format I have $64 \times 16$ and in 40 -column format I have two half screens (I can toggle between the two) of 32 $\times 16$.

## A word on Editors

Editors range from very simple line oriented editors (ughh!!!) which are often found on minimal Forth systems, to fullblown screen oriented editors which can be implemented as overlays and removed from the system when not in use. Some of the latter can be 20 K bytes worth and can almost be used as word processors.

## Individually tailored

The most interesting feature of Forth is the ability to alter the system to suit the needs of the individual user. Words not required in a final system (e.g: a dedicated controller) can
be removed from the dictionary to save space. In fact, some systems ${ }^{(4.5)}$ even allow the space normally taken by the name of the word to be recovered. Forth is the only "language" I am aware of in which one can readily modify the compiler. That's right! My late, lamented SYM-1 microcomputer had a version of Microsoft BASIC with IF-THEN, but no ELSE. To write it in would have required a major programming exercise. With Forth, extensions to the compiler are so simple as to be trivial - modify existing compiler directives - add new ones - create new classes of variables and constants - the list is endless.

```
196 LIST
SCR # 109
(Fast Screen Duplicator 1/11/85 )
|}\mathrm{ VARIABLE LOSCR
\varnothing VARIABLE HISCR
g VARIABLE NUM
\varnothing}\mathrm{ VARIABLE CURR_BUF
: SET_BUF ( Set current Buffer to First
                    )
                                    FIRST 2+ CURR_BUF ! ;
: CALC ( Calculate the number of times
                    to loop and leave on the stack )
                    HISCR & LOSCR E - DUP 0< IF }
                        ERROR THEN NUM a /MOD ;
```

Figure 3.

## A final word

Finally, I would like to commend several books to the aspiring Forth programmer. During my apprenticeship with Forth, I have found several books to be invaluable. Two books by Brodie ${ }^{[7,9]}$ are excellent bedside reading and my reference guide is a two-volume set by McCabe ${ }^{(10)}$.

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## aem project 4600



# A 'dual-speed' direct-connect computer modem 

Robert Broomhead

Get into the computer communications revolution with this simple to build and operate modem. It features both 300/300 and 1200/75 baud operation which covers the majority of popular applications.

ONE OF THE MOST POPULAR accessories for personal computers these days is the modem. It is a gadget that plugs into your computer's serial port and provides a means of 'talking' to other computers over a telephone line. You can send and receive programs, messages, document files or whatever. A number of computer clubs, computer stores and private individuals have set up computer 'bulletin boards' which you can dial-up using a modem.

A bulletin board is a computer system with an automatically answering ("auto-answer') modem hooked up to it which, when you call it up with your computer and modem, provides access to a host of useful and interesting activities and services. You can copy ('down load') programs from the bulletin board, send and receive messages to other bulletin board users ('electronic mail'!), send ('upload') programs for others to use, etc. Bulletin boards are quite popular.

Then there's Viatel, Telecom's videotext service which is rather like a 'commercial' bulletin board. Banks, travel agencies, airlines, financial services, electronics companies etc, provide their services through Viatel. You can use a personal computer and modem to access Viatel, providing the computer meets the appropriate software and hardware requirements so that the video can be correctly received and displayed, and provided the modem meets the relevant Viatel communications requirements.

## Compułer communications

This is no place for a lengthy discourse on the subject of computer communications techniques and standards. However, a little background information will put you in the picture.

To get one computer to talk to another over a telephone line - or any 'long', two-wire line - you can't just connect the serial interface of each to the line. The serial interface on your computer uses direct current (dc) signalling. That is, it will drive the line voltage positive or negative, or just positive and zero, to send digital 1 s and 0 s . The serial interface expects to see some reasonable representation of the original voltage levels in order to work. The thing is, long lines don't transmit dc pulses very well. Telephone lines, in particular, do not provide a dc path between telephones across the city, or across the nation. Telephone lines expect ac signals - the voice (sound waves converted to electrical waves) - and will transmit voice-frequency signals, which generally span from about 300 Hz to about $3000 \mathrm{~Hz}(3 \mathrm{kHz})$, quite well.
Thus, to send on/off (dc) signals from a computer down the telephone line, the on and off levels ( 1 s and 0 s ) are first converted to tones in the voice-frequency band. To receive the transmitted computer signal, these tones have to be reconverted to dc on and off levels.
A device to convert the on/off levels to tones is termed a modulator, while a device that re-converts the tones to on/off levels is called a demodulator. Hence the term modem, from modulator and demodulator.
If you are at all familiar with your computer, you will doubtless know that, when using a serial printer, you need to set the computer's data output rate and the printer's data operating rate to the same 'speed'. Those on/off 1 s and 0 s you send out the serial port are called 'bits'. When you send one bit per second, the data speed is said to be 'one Baud'. The

Baud is named after Jean Maurice Emil Baudot, a Frenchman who devised the teletype code.

Now, it's quite possible to employ any Baud rate you want for sending and receiving computer data. However, a number of conventions have been established, for great and good reasons (some technical, some just for convenience), for specific applications. But before we examine the common speeds, there's another thing we have to consider.

When you type a character on your keyboard, and your computer sends it to another, down the line, that character can appear on your screen in one of two ways. Your computer can send it to the screen, or to the computer at the other end of the line can 'echo' the character, sending it back to you, whence it is displayed on your screen. The character is displayed on the receiving computer's screen upon receipt. This sending and receiving is called 'duplex' operation. When your computer displays the character, that's termed half duplex operation. When the remote computer echoes the character, that's called full duplex operation.


Full duplex operation assures you that the equipment at the other end is receiving you.

Back to the communications speeds. There are two widely used Baud rate standards: 300 Bauds in both directions plus 1200 Bauds one way, 75 Bauds the other. Other standard speeds are, of course, used - such as 600 Bauds both ways and 1200 Bauds both ways. However, this project has been designed to provide communications at the two most popular Baud rate standards. In general, full duplex communications is employed when using 300 Bauds, and half duplex when using 1200/75 Bauds. In the latter case, you must decide who's going to transmit at 1200 Baud or 75 Baud. Whoever transmits at 1200 Baud, must receive at 75 Baud. Thus, whoever receives at 1200 Baud, must transmit at 75 Baud. Otherwise, you won't be able to 'talk' to each other!
A complication arises because of the need to transmit in both directions. The tone pair used to transmit the 1 s and 0 s in one direction cannot be the same tone pair used to transmit in the opposite direction. Thus, separate tone pairs are employed. In order that one modem can 'talk' to another, the frequencies used for the tone pairs must conform to some common standard. To separate them, if you initiate the call, then you are said to originate it. The person you are calling is thus at the answer end. This terminology is used to sort out the tone pairs.

The standard frequencies for data communications employed in Australia were set down by an international communications consultative body called the CCITT. For 300 Baud communications, their document 'V.21', gives the specification. Because serial communications is used, these
relate to the RS232 signal levels where a digital 1 (termed a 'mark') is a negative voltage betwen -3 V and -12 V , and a digital 0 (termed a 'space') is a positive voltage between +3 V and +12 V . For 1200 Baud communications, the CCITT document V. 23 sets out the relevant frequencies. Table 1 sets out the various frequencies for the different modes and Baud rates.

## TABLE 1: The standard tone pairs

| Modem | Baud Rate | Duplex | Transmit Frequency |  | Receive Frequency |  | Answer <br> Tone <br> Freq <br> Hz |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | Space Hz | Mark Hz | Space Hz | Mark Hz |  |
| CCITT V. 21 Orig | 300 | Full | 1180 | 980 | 1850 | 1650 | - |
| CCITT V. 21 Ans | 300 | Full | 1850 | 1650 | 1180 | 980 | 2100 |
| CCITT V. 23 |  |  |  |  |  |  |  |
| (Mode 2) | 1200 | Half | 2100 | 1300 | 2100 | 1300 | 2100 |
| CCITT V. 23 Back | 75 | - | 450 | 390 | 450 | 390 | - |

## Project design features

The project supports 300 Baud full duplex operation and 1200/75 Baud half duplex operation. That is, you can transmit and receive at 300 Baud in both directions or 1200 Baud one way, 75 Baud the other. A switch has been incorporated allowing you to select either 1200 Baud transmit with 75 Baud receive, or 75 Baud transmit with 1200 Baud receive.

Most bulletin boards employ 300 Baud full duplex, a few have 1200/75 Baud facilities. Telecom's Viatel videotex service employs $1200 / 75$ half duplex operation. You transmit at 75 Baud and receive at 1200 Baud. This has the advantage that, when receiving at 1200 Baud, Viatel writes information to your screen at the fastest rate; four times the speed if you were using 300 Baud communications.

Four front panel toggle switches provide all the operating functions. One selects the 'speed'. To the right of this is a switch which allows you to select transmit/receive Baud rates in the 1200/75 Baud mode. Further to the right is the "answer/originate" switch. Four LED indicators allow you to keep an eye on what's happening at any time. The ON indicator is obvious. On its right is the 'CD' LED which lights up when you've detected another modem's signalling tone, termed the 'carrier'. This lets you know when communications can proceed. Then comes the ' Rx ' LED which flashes as data is being received. Last on the right is the 'Tx' LED which flashes as you transmit data.

A $25-$ pin $D$ connector on the rear panel of the unit provides for connection to your computer. The interface signals comply with the RS232 standard. Only the minimum signal lines have been implemented to make interfacing as simple and as 'universal' as possible.

The project was designed around the '7910 "world modem" chip which does pretty well all the work. A comprehensive data sheet on the ' 7910 is published elsewhere in this issue. This device was originally developed by the US-based company, Advanced Micro Devices, and is known as the

## LEVEL

We expect that constructors of an

## INTERMEDIATE

level, between beginners and experienced persons, should be able to successfully complete this project.

## aem project 4600

## TABLE 2: Main and Back channel pins

## Pin No. Function

10 Transmit data input, MAIN channel
Transmit data input, BACK channel
Received data output, MAIN channel
Received data output, BACK channel
Carrier detect output, MAIN channel
Carrier detect output, BACK channel
Request to send, MAIN channel
Request to send, BACK channel
Answer/Originate select line
300 Baud - 1200/75 Baud select line

TABLE 3: Pin 'activity' in each mode

| Pin | No. | 300B Orig. | 300B Answer | 1200B Tx/75B Rx |
| :--- | :---: | :---: | :---: | :---: |
| 10 | TRANSMIT | TRANSMIT | TRANSMIT | NOT USED |
| $\mathbf{1 0}$ | TRANS Rx |  |  |  |
| 28 | NOT USED | NOT USED | NOT USED | TRANSMIT |
| 26 | RECEIVE | RECEIVE | NOT USED | RECEIVE |
| $\mathbf{1 5}$ | NOT USED | NOT USED | RECEIVE | NOT USED |
| $\mathbf{2 5}$ | CARRIER DET. CARRIER DET. | NOT USED | CARRIER DET. |  |
| $\mathbf{2 7}$ | NOT USED | NOT USED | CARRIER DET. | NOT USED |


| +5 V | +5 V | +5 V | 0 V |
| ---: | ---: | :---: | :---: |
| 0 V | 0 V | 0 V | +5 V |
| 0 V | +5 V | NOT USED | NOT USED |
| 0 V | 0 V | +5 V | +5 V |

## AEM4600 CIRCUIT OPERATION

The 'heart' of this project is the ' 7910 'world chip' modem device, IC1. A full data sheet on this device is reproduced elsewhere in this issue. The function of this device is, firstly, to generate two tones in the voice-frequency band - one tone to represent a digital 1 , the other to represent digital 0 - which may then be transmitted down a two-wire data line or a telephone line; secondly, the device can detect the presence of two such tones transmitted to it and convert them to digital is and Os. In this way it can transmit and receive digital data over a two-wire voice-frequency line. It can transmit and receive at a variety of selectable data rates, or Baud 'speeds', employing a variety of standard tone frequencies.

The most widely used Baud-speed standards in this country are 300 Bauds in both directions ('300/300') and 1200 Bauds one-way, 75 Bauds the other (1200/75). The most widely used standard tone frequencies are the CCITT V. 21 and V. 23 tones, detailed in Table 1

The easiest way to treat the world chip is as an audio oscillator, and two filters. Take the 300 Baud mode. The digital data to be transmitted is applied to pin 10 of the '7910. When it goes low, then the oscillator switches to Frequency No. 1. When pin 10 goes high, the oscillator switches to frequency No. 2. The output of the oscillator appears at pin 8 where it is fed into a 'hybrid' circuit (more on this later).

Data is received on pin 5 of the ' 7910 . If frequency No. 1 arrives here, the corresponding internal filter (No. 1) will detect it and pin 26 will go low. If frequency No. 2 arrives, filter No. 2 will detect it and pin 26 will go high. The actual frequencies vary with the particular mode of operation selected.

Thus, in this way, digital data sent by your computer, or intended for reception by your computer, is converted from the digital form (on/off - 1s and Os ) to the analogue form (two tones) when sending, and from the analogue form to the digital form when receiving.

The hybrid circuit comprises the op-amp IC2 and surrounding components. Its function is to enable two-way data transmission over a single pair of wires.
A transmitted signal coming from pin 8 of the ' 7910 (IC1) is passed to the line isolation transformer, TR2, via R11-C7-R12-C13. The back-to-back zeners، ZD1-ZD2, prevent excess signal level being coupled to the line by 'clipping' the signal peaks at $+/-3.9$ volts.
When receiving. signals from the line are coupled to the noninverting input of op-amp IC2 via the line isolation transformer
and C13. The output of IC2 feeds the receive input of the '7910, pin 5, via a 2:1 voltage divider comprising R15/R16. R14 provides feedback for IC2. The two zeners, ZD1 and ZD2, clip any 'spikes' or other high level signals coupled in from the line, preventing overload of the '7910's receive input.
On the other side of IC1 we have the 'main' and 'back' channel digital inputs, pins 10 and 28 respectively, along with main and back channel digital outputs, pins 26 and 15. These are TTL-level inputs and outputs. The main and back channels are kept separate on the '7910 as there are a few computers on the market requiring them to be used that way. However, most personal computers (e.g: the Microbee) use only two data lines - transmit and receive, so therefore we can link our main and back channel inputs and outputs together. Hence, pins 10 and 28 are linked directly together, and pins 26 and 15 are combined with an AND gate, IC6a.
For those wondering what is meant by 'main' and 'back' channels, let me explain.
When transmitting at 300 Baud you are doing so in the 'full duplex' mode, i.e: sending and receiving data at 300 Baud at the same time. We have common Baud rate, so therefore we can use the main channels for both transmitting and receiving data. However, when using the 1200175 mode we have two separate Baud rates - 1200 Baud one way and 75 Baud the other - and we are transmitting in the half duplex mode. That is, we can only transmit OR receive at any one time. Here it is necessary to use the BACK channel.
By convention, 75 Baud is always transmitted through the back channel. Therefore, to receive at 1200 Baud and transmit at 75 Baud, the main channel would be used for receiving data, and the back channel for transmitting data. Conversely, if we were receiving at 75 Baud and transmitting at 1200 Baud, this time we'd be using the back channel to transmit the data and the main channel to receive the data.
In this project, the main and back channels of the modem chip are linked together and I have allowed the internal switching of the modem IC to take care of selecting the respective main and back channels for the transmitting and receiving of data.
The only thing we have to tell the ' 7910 is the Baud rate,

N.B. Missing caption under overlay. p. 84 - All circled pads are soldered top and bottom on component leads. or linked There are some minor differences to the prototype pictured


whether we want to be in the originate or answer mode ( 300 Baud mode only), and whether we want to send data at 75 Baud or receive data at 75 Baud (1200/75 Baud rate mode only). Table 2 lists the ' 7910 main and back channel pins and the associated function select pins. Table 3 shows the pin functions, signals or levels applied in each mode.

The received data output appears at pin 6 of IC6b. This TTLlevel signal is buffered by IC4a, one buffer from a 1488 line driver IC. Its output, pin 3, drives pin 3 of the RS232 connector (RXD). The ' $R x$ ' indicator, LED2, is driven by IC5b, one buffer from the 7404, its input (pin 3) being taken from the output of IC6a, where the received data appears.
Data for transmission comes into the modem on pin 2 of the RS232 connector (TXD). IC3a, one buffer from a 1489 line receiver, buffers this providing a $T \mathrm{~T}$-level out from the +1 swing of the RS232 input; its output (pin 6) driving the commoned main and back channel transmitted data pins of IC1 (10 and 28) which require a TTL-level signal. Pin 6 of IC3a also drives a buffer from the 7404 hex inverter, IC5c, which drives LED1, the 'Tx' indicator.
The two main and back channel carrier detect outputs, pins 25 and 27 respectively, are commoned with an AND gate (IC6a), the output of which is buffered by one inverter from IC5 to drive LED3, the 'CD' indicator.
The two line drivers, IC3 and IC4, provide a 'true' RS232 interface. IC4a swings the RXD output (pin 3 of the RS232) between +5 and -5 V , while IC3 will accept either correct RS232 level or TTL-level inputs.
The project is intended as a direct-connect modem, that is, it does not require an acoustic coupler when connecting it to
a line. Connection to the line is made via a 600 ohm line isolation transformer (T2). This transformer has two functions, firstly to match the line impedance (nominally in the vicinity of 600 ohms), and secondly to provide ac/dc isolation of at least 3.5 kV between the modem and the line. Capacitor C13 also provides a degree of isolation.

The power supply employs a pc-mount transformer, T1. Its secondary is bridge-rectified by $\mathrm{B1}$ to provide a positive and negative rail. Capacitors C 1 and C 2 smooth the rectifier output and two three-terminal regulators, IC7 and IC8, provide regulated +5 V and -5 v rails respectively. Capacitors C 3 and C 4 , keep the regulators stable. C14 is a supply rail bypass for IC4.

Power-on reset for IC1, with a short time delay, is provided by R17/C8.

The modem IC's internal oscillator is used (pins 23 and 24), employing the required 2.4576 MHz crystal. Capacitors C 9 and C10 are as-specified in the ' 7910 data sheet.

Likewise, R18 and C11, linking pins 6 and 7 of the modem chip IC1, are as suggested in the data to provide correct operation of the internal analogue to digital conversion.

The 'minimal' handshaking pins of the RS232 connector, pins 5 (CTS), 6 (DSR) and 8 (DCD) are linked together and pulled high via R1. The RS232 connector signal ground (SG) and frame ground (FG) pins, 7 and 1 respectively, are linked via the common ground line on the pc board.

The modem is switched on-line and off-line by SW5, a singlepole, double-throw toggle switch. In the off-line position, the line side of T2 is shorted so that line noise coupled across the switch is not picked up by the receive section of IC2, which would otherwise light the 'carrier detect' (CD) indicator.

## aem project 4600



## AEM4600 PARTS LIST

| Semiconductors |  |
| :---: | :---: |
| B1 | WO4. 1 A bridge |
| IC1 | Am7910, EF7910 |
|  | FSK modem chip |
| IC2 | LM301. $\mu$ A301 |
| IC3 | DS1489 line Rx |
| IC4 | DS1488 line Tx |
| IC5 | 7404 |
| IC6 | 74LS08 |
| IC7 | 7805 |
| IC8 | 7905 |
| LED1. LED2 5 mm green LED |  |
| LED3 . . . 5 mm yellow LED <br> LED4 . . . . . . 5 mm red LED |  |
|  |  |
| 2D2. 2D2 | 3V9/1 W zener |
| Resistors |  |
|  | all 1/2 W. 5\% |
| R1 | 1 k |
| R2. R3 | 2208 |
| R4. R5 | 330 R |
| R6. R7 | 1 k |
| R8 | 22k |
| R9. R10 | 1 k |
| R1: | 100R |
| R12 | 470R |




Am7910. A pin and function compatible device was recently released by the French manufacturer, Thomson Semiconductors, and is known as the EF7910. AMD is represented in Australia by R\&D Electronics, while Thomson Semiconductors is represented by Promark Electronics.

While the ' 7910 is capable of a wide variety of operating modes on several standards, I have only implemented the popular ones here.

Simple, single-board construction has been employed to keep costs down and to simplify assembly. A double-sided board is necessary, but through-plated holes are not required. The power supply is on-board. In laying out the board, and in other aspects of the design, I have taken care to comply with the relevant safety standards and type-approval specifications (see accompanying panel). In particular, the mains and line isolation transformers specified in the parts list meet the relevant requirements, while C 13 is a special type to provide additional isolation. It is distributed by Captron in Melbourne. The prototype was housed in a common, sturdy plastic instrument case with a Scotchcal front panel.
The photograph inside the prototype shows a pc-mount, right angle RS232 connector. However, provision has been made on the pc board to mount a solder-type connector over the edge of the board, soldering to the pads provided. Link pins $1,2,3,5,6,7$ and 8 to the pads used by the other connector type with short lengths of hookup wire.

Construction
The construction of the modem is fairly simple, and should be within the capabilities of most electronic hobbyists who've had some constructional experience.

The first step is to ensure that you have all the necessary parts required to build the modem, refer to the parts list for a list of everything you will need. make a visual check of the pc board to ensure all the holes are drilled and of the right diameter. See that there are no solder 'bridges' between closespaced pads or tracks.

Referring to the circuit board overlay, load all the components onto the circuit board, stage by stage, starting with the lowest profile components, the resistors and capacitors, work your way up to the highest profile components (transformers etc). This avoids the circuit board from becoming overcrowded while the small fiddly components are being installed. See that you get polarised capacitors and the semiconductors the right way round. Instead of an IC socket, I used Molex pins for IC1. Note that the two supply filter capacitors (C1 and C2) need to sit up about $4-5 \mathrm{~mm}$ from the board to enable you to solder them top and bottom sides of the board. Also, note where pads are soldered on both sides of the board.

Next, take up your soldering iron and carefully solder all the components to the board, taking care not to allow any tracks to be shorted together by the use of excessive amounts

## aem project 4600

of solder, or to damage tracks by overheating. Having soldered your board it is a good idea to check your work one more time for any mistakes as it is often cheaper to fix them now, than after the modem has been powered up.

Check the LED mountings against the internal photograph. There should be enough lead length to bend the LED over in order to fit them through the panel holes.

With your board completed you can now start preparing your case. Take the rear panel layout and mark out your rear panel with a scriber or some other sharp pointed tool. Next, using sharp drill bits of the right size, drill the plastic rear panel taking care not to crack or split it by applying too much pressure, or by using a blunt or damaged rill bit. Having drilled the panel, take your needle files or small round files and enlarge the hole for the RS232 connector to the correct size. Check that it fits the hole. On completing the rear panel, take the same approach with the front panel and drill suitable holes for the switches and LEDs, again taking care that the holes are in their correct places.

If you have a Scotchcal label, now is the time to carefully attach it to the front panel. Wetting the panel and sticky side of the Scotchcal allows you to position it more readily.

You are now at a stage where the board can be mounted in the case. Using cable-grip grommets, mount the power and line cords to the rear panel, allowing enough length of cable inside the case so as to be able to terminate them to the circuit board at their respective points. Use small nuts and bolts when mounting the RS232 connector to the rear panel.

Next, mount the board in the case. Cut four small spacers from $3-4 \mathrm{~mm}$ plastic tubing about 10 mm long. These are used to give the circuit board a bit of height off the bottom of the case used. Use 15 mm long self-tapping screws to mount the board to the bottom lid of the case, aligning the screws with the mounting holes provided.

The next step is to terminate the mains cable, power switch and line connection to the circuit board. Take care with this wiring as loose strands of wire left floating around could be hazardous. Note that the mains active wire to the power switch goes to the upper contact, not the middle (pole) contact.

With all this completed, leave IC1 (modem chip) out of it's socket and power the modem up. With a multimeter, check that you have +5 volts on pin 2 of the world chip socket, and -5 volts on pin 4 with respect to ground (pin 9). If you have the correct voltages, power your modem down (waiting for the main filter capacitor to discharge) and re-install your world chip. Of your voltages were not correct, re-check your circuit board wiring.

## Getting it 'on the air'

Drag out and dust off your old microcomputer and fire it up with a 300 Baud terminal program. The protocol required is 300 Baud full duplex, 7 data bits, 1 stop bit, no parity.
With an RS232 lead, connect your computer to the modem. If you don't have one, consult your computer's operating manual for details of the RS232 pinouts and a cable can be made up. For most computers (e.g: the Microbee) the easiest way to make up an RS232 cable is to use a length of 25-way ribbon cable and fit a DB 25 ID (insulation displacement) connector to each end, providing a 1-to-1 pin wiring.

With the modem connected to your computer, power it up and type some characters on your keyboard. You should notice the Tx light flashing on the front of the modem. If your Tx light fails to flash, you may possibly have a problem with your RS232 cable. If your computer is a Microbee, the easiest way to check the cable is to disconnect it from the modem and with an alligator clip, short pins 2 and 3 of the DB25 plug together. Now, type something on your keyboard and you should see it being echoed to the screen. If not, check your cable. If the problem is not with your cable, re-check all your wiring of the circuit board.

If your Tx light flashes, connect your modem to another modem and computer which also has a 300 Baud terminal program installed. With the two modems connected together, set them both to 300 Baud, one in the originate mode and one in the answer mode. You should note that the carrier detect (CD) light on your modem has lit, indicating that your modem is receiving a carrier that it recognises from the other modem. Typing characters on your computer's keyboard should appear on the other computer's screen and vice versa. If this works OK then you have a working modem!

It should also be noted that, as data is being received from the other modem, the Rx LED on your modem should flicker, indicating the flow of data.

There you go, you now have a functioning modem!
The 1200/75 Baud rate can be checked in the same fashion as the 300 Baud was tested, but making sure one modem is set to 1200 Baud transmit/75 Baud receive, and the other set to 75 Baud transmit/1200 Baud receive. It will also be necessary to set the Baud rate of your terminal program to match the Baud rate of the modem to which it's connected. 4

SEE 'RETAIL ROUNDUP' FOR A GUIDE TO KIT SUPPLIERS WHO MAY STOCK THIS PROJECT




ARMED WITH DOCUMENTATION of the pin assignments of your equipment's RS232 connector, you can proceed to hook it up with confidence whenever the need arises. Even if all you know about a piece of equipment is just a list of the pins used on the D-connector, you can work out the pin assignments and at least have a starting point.

## The hookup

First of all, we'll assume you've obtained the right connectors (check the sexes! - both male?, both female?, one of each?). You'll need a cable with at least the minimum number of required conductors. It is advisable to use shielded cables for runs in excess of six metres ( 20 feet), although I've seen plenty of instances where 30 metre-plus runs of unshielded cable were used without misadventure.
Before going any further, let us state the Golden Rule: ALWAYS WIRE PIN 7 TO PIN 7.
The simplest connection is the 'straight-through', where the equipments are of opposite sex (i.e: one's a DTE, the other's a DCE). You could just join all pins directly to their counterparts, pin 1 to pin 1 , pin 2 to pin 2, etc. But that's a rarity. If no handshaking is involved, and the equipments are of opposite sex, then all you have to do is make up a threewire 'straight-through', as shown here, linking pins 2, 3 and 7 on each:


The simplest hookup - a DTE to a DCE, no handshaking required.

Where handshaking is used, the prime functions implemented are RTS (request to send - pin 4) and CTS (clear to send - pin 5). In addition, you'll often find DSR (data set ready - pin 6) and DTR (data terminal ready - pin 20) implemented with occasionally, DCD (data carrier detect -
pin 8) thrown in for good measure. If you're hooking up equipments of opposite sex, with handshaking between them, your cable is wired like this:


Linking DTE to DCE, with handshaking. Note than pin 8 may also be implemented, in which case link them at each end.
Where both equipments are of the same sex and no handshaking is required all you need is a three-wire cable, but this time, pin 2 on one links to pin 3 on the other, with pin 7 straight through:


Linking same-sex equipment, DTE-DTE or DCE-DCE, no handshaking required. Also called a simple 'null modem'.

This hookup is generally known as a null modem and is often used to link together two computers so that they can 'talk' to one another. It's great for transferring files. Pins 2 and 3 are cross-connected because each computer will be a DTE and transmit data on pin 2 and receive data on pin 3. Some computers and terminals have a switch which allows reversing pins 2 and 3 . If you're that lucky, the simple straight-through cable can be employed and you throw the switch - but only on one if both have the facility.

If your computer or terminal requires handshaking signals, you can defeat it and 'fool' the computer by 'strapping' pins 4 and 5 together at each plug if RTS and CTS are implemented. If DSR, DCD and DTR are implemented, you can strap
these three pins together at each plug. Your null modem hookup will now look like this:


Null modem with 'fooled' or defeated handshaking. This gets the two DTEs 'talking', but there is really no handshaking between them.

A variation involves linking pins 5-6-8-20 where RTS (pin 4) is not implemented, usually only on DCE-sex gear. This just fools the equipment at each end into telling itself "all systems go". Such a hookup looks like this:


Null modem with defeated handshaking, generally used for linking DCE to DCE where RTS is not implemented.
When linking a DTE to a DTE, with full handshaking, then RTS on one connects to CTS on the other, in addition to DCD and DTR being cross-connected between equipment. As DSR is usually implemented also, strap it to DCD at each end. Such a hookup is wired so:


Linking DTE to DTE, with full handshaking.
Linking a DCE to a DCE with full handshaking is a little different. Here, DSR and DCD are cross-connected with DTR linked to DCD at each end. You get a hookup like so:


Linking DCE to DCE, with full handshaking.
That should cover the vast majority of hookups you'll encounter. However, if you have to battle along without documentation, or, despite the documentation a hookup refuses to get the equipment talking, then you need to try out a variety of hookups. Even if you go to the tedium of wiringup seven different cables, as above, there'll be times when you encounter some perverse peripheral which has been implemented half-DTE/half-DCE.
The solution, which should be cheaper and more "univer-

## SOFTWARE HANDSHAKING - X-ON/X-OFF

Where hardware handshaking is not implemented, the software may employ handshaking signals, known as " X -on/ X -off". This is usually 'installed' in the DTE setup procedure (on a terminal or computer), along with parity, baud rate, etc.


A low-cost RS232 patch box. This is the Arista 'mint patch box' where short links can be soldered between the required pin connections to test out a hookup. When it works, you just clip the lid on. It comes complete with brief instructions and suggested hookups. (Courtesy Geoff Wood Electronics).
sal' than the seven cables, is a 'patch box'. This simply comprises a pair of DB25 plugs mounted on a small box, or trailing on ribbon leads from the box, with all 25 pins brought out to some sort of connector scheme allowing you to link anything to anything with short 'patch' leads. Very handy. They cost anywhere from around $\$ 25$ to $\$ 100+$.

When using a patch box, remember the Golden Rule - link pin 7 to pin 7. Some patch boxes have them permanently linked, anyway. The more sophisticated, and expensive, patch boxes provide indicator LEDs on each line so that you can see which lines are high, low, active or inactive.

## Troubles and troubleshooting

If, despite documentation, or if you're attempting a hookup without documentation, your two equipments refuse to talk to one another, you'll need to determine just what's going on.

The first thing to do is check the integrity of the cable. See that there really is a connection between the pins implemented. The next thing to do is check the activity on the nine main pins. Pin 1 may not be used, but if it is, it should link the equipment chassis. Pin 7 is your signal reference (note that it may be linked to pin 1). Refer to Figure 3 (Part 1) for what to expect.
What equipment do you use? Ordinary logic probes can be pressed into service, but their main drawback is that you can only check one line at a time. You may need to see what's


A simple RS232 'mini-tester'. This has two plugs wired straight-through, with LED indicators on the seven main signal lines. (Arista product, courtesy Geoff Wood Electronics).
happening simultaneously on several lines. A multi-channel oscilloscope can be handy - but expensive. As you're only looking at 'highs' and 'lows', simply LED indicators are sufficient for may purposes. Simple RS232 testers are available, comprising a pair of DB25 plugs and an array of LEDs which show the status of the signal lines. The simplest just has seven LEDs for the seven main signals: TXD (2), RXD (3), RTS (4), CTS (5), DSR (6), DCD (8) and DTR (20).


Some equipment (usually a computer) will incorporate an RS232 interface where the TXD (transmit data) line is a TTL device output and low is only somewhere between +2.4 V and 0 V , rather than -3 V and -12 V , as specified in the RS232 standard. The Microbee is a classic example.
As often as not, most peripherals - printers, modems and the like, will be quite happy with this situation, but the occasional equipment will not tolerate it, having 'true' RS232 input which needs a low greater than -3 V to operate correctly. Thus, despite having the correct hookup, your DTE will not drive your DCE.
The solution is to provide a 'level-shift' interface, where the TXD line, instead of swinging between the TTL levels, is made to swing between a positive voltage of between +3 V and +12 V and between -3 V and -12 V , as required. A suitable circuit is illustrated in Figure 4. This employs an Intersil ICL 7660 positive-to-negative voltage converter, to provide a-7 V supply rail. An NPN-PNP transistor pair provides an uninverted TXD signal at the output, swinging between the positive supply rail and the -7 V negative supply rail, from a TTL input.
The BC548 turns on and off with the TTL-level drive signal, its collector turning on and off the base current to the

## LINE IMPEDANCES, DRIVERS AND RECEIVERS

The RS232 standard requires that a 'line input' have a dc resistance of between 3000 and 7000 ohms, with a parallel capacitance of no more than 2500 pF . In addition, the standard specifies it should not be inductive and that the open circuit voltage not exceed 2 V . The source impedance and capacitance of a 'line output' is not specified, but it must be able to deliver between 5 V and 15 V (when the line input is at O V9 and the open-circuit ouput voltage must not exceed 25 V . In addition, a line output should not deliver more than half an amp ( 0.5 A ) when shorted to any other line (including ground).

National Semiconductor, many years ago, introduced a pair of ICs specifically for interfacing DTE and DCe devices. The DS1488 is a "quad line driver", while the DS1489 is a "quad line receiver". Both meet the RS232 and CCITT V. 24 specifications. Typically, the 1488 has an output resistance between 3000 and about 1000 ohms. The 1489 has an input resistance of 4 k .


## DUPLEX - THE ECHO PROBLEM

You will sometimes come across the term 'duplex' in conjunction with terminals, computers operating in 'terminal' mode, or modems. A hardware or software switch provides for "full duplex" or "half duplex" operation.

Full Duplex: when the terminal equipment transmits a character, the receiving terminal will immediately 'echo' the character back to the transmitting terminal equipment which then displays it on-screen. It's used to assure you that the equipment on the other end is listening to you. Some bulletin boards operate in full duplex mode you may notice.

Half Duplex: in this case, the terminal equipment puts the transmitted character on its own screen and the receiving terminal does not echo it. If the receiving terminal is not awake and you're transmitting in half duplex mode, you won't know if the characters are being received.

A problem will arise if your terminal is set to half duplex operation and the receiving terminal echoes the received characters. In this case, your screen will display each character ttwwiiccee!

BC558. When a high is transmitted, the BC548 turns on, turning on the BC558, the collector voltage of which rises to almost the positive supply rail voltage. When a low is transmitted, the BC548 turns off, having no base current supplied, and the BC558 turns off. The output will then be 'pulled down' to the negative supply rail via the 1 k and 2 k 7 resistors.

A circuit such as this is readily constructed on matrix board or Veroboard. It could be powered from the computer if a positive supply between about 8 V and 15 V is available on the DB25 connector.

## The check list

When commissioning an RS232 hookup, there are eight checks you should run through every time.

1) Bits per character? - $5,6,7$ or 8 .
2) Stop bits? - 1 or 2.
3) Baud rate? - $110,150,300,600,1200,1800,2400,4800$, 9600 (or other).
4) Parity?

On Tx - none, even, odd, set to 0 , set to 1 .
On Rx - none expected, ignore, expect odd, expect even.
5) Duplex? - full or half.
6) On line? - if on/off-line switch provided, set it on.
7) Mark high or mark low? if switch provided to invert polarity of mark/space, set for negative mark on data lines. For handshaking high is GO, low is STOP.
8) Handshaking? - via the interface or X-o i-off.

Armed with the information presented here, you should be able to solve the majority of difficulties you may encounter with RS232 'standard' interfaces. Good luck!

## SoftTalk

# The good ol' 88 

There are many personal computers on the market today, but most of them have one thing in common. VLSI and LSI chips form the heart of the designs. These chips generally belong to one of four basic design families. These are the Motorola 6800/68000 family, the Intel 8080/8085/8086 family, the Zilog Z-8O /Z8OOO family and the Rockwell 6502 family. The chips are not necessarily manufactured only by those companies, but they introduced the design and have licensed others to manufacture them. There are many other microprocessor chips available, but those mentioned have a huge proportion of the personal computer market. The recent crop of personal computers has moved to the 16-bit architecture of the 8086 and 68000 families, but support chips from earlier 8 -bit versions are frequently used.

WHEN IS a microprocessor a 16 -bit microprocessor? The transition from 8 -bit processors to 16 -bit processors has caused some confusion in naming conventions. Some manufacturers have provided a hybrid configuration as well as a true 16 -bit processor. An example of this is the Intel 8088 . a member of the 8086 family. The internal bus is 16 bits wide, but the interfaces external to the microprocessor chip are only 8 bits wide. It executes exactly the same instruction set as the 8086, which is a 'true' 16 -bit microprocessor. A similar compromise is appearing in the 32 -bit microprocessors that are now beginning to appear.

One benefit of the hybrid versions is to support the peripheral hardware from the earlier architecture. When the 8086 first appeared, the existing support cards (memory, I/O, etc,) did not support the 16 -bit data bus. A hybrid configuration allows the power of the improved instruction set to be used with older support cards. From a software view, the hybrid processors can be considered 16-bit (or 32 -bit) machines whenever the machine code can be run on either the 'hybrid' or 'full' processor. Code should be written to maximise performance on the widest data path possible.

## The $\mathbf{8 0 8 6}$ family

Intel's 8086 was the first of the 16 -bit architecture microprocessors to get a substantial foothold in the personal computer market. The 8088 and, to a lesser extent the 8086 , are now very widely used.

In the last couple of years 'improved' versions have also been making an appearance. There are the 80186 (or iAPX186) and 80286 (or iAPX286) microprocessors. These chips provide all the function of the earlier 8086-type chips and offer new features and speed. Depending upon mode of operation, they are code compatible with earlier versions.

## iAPX 88/10 8-BIT HMOS MICROPROCESSOR 8088/8088-2



CPU Functional Block Diagram


Pin Configuration

## The 8088

Probably the greatest number of Intel-based personal computers currently use the 8088 for a microprocessor chip. This microprocessor provides a 16-bit instruction set and an 8 -bit data interface. Although operations may be performed upon 16-bit data, the processor requires two 'fetches' to get the data into the chip. This chip is hardly state of the art, but we can expect it to be around for quite some time yet. Don't forget, there are still quite a few $\mathrm{Z}-80$ s being installed in brand new PCs today.
What kinds of instructions can the 8088 perform? Lots! The first thing to note is that the 8088 can perform almost every instruction upon a byte of data or upon a 'word' ( 16 bits) of data. There are five general groups of instructions: Data Transfer, Arithmetic, Bit Manipulation, String and Program Control. In addition, there are processor control instructions which are used to control the operation of the microprocessor chip or external devices. For each type of 'logical' instruction there may be several machine instructions from which to choose. An example would be the move (MOV) instruction. This simply tells the processor to transfer a byte or word of data from one location to another.
There are actually seven different machine instructions that can be used to move data, varying in length from two to seven bytes and requiring from two to 28 clock cycles to execute. The fastest moves data contained in the instruction to a register and the longest moves data contained in the instruction to a memory location that is not in the current data segment. Unless you are trying to write in the binary "machine code', the Assembler has the job of sorting out which version of the 'logical' instruction to use.

I just walked onto the first 'chicken and egg' trap! What's a data segment? About nine or ten paragraphs from now, there's a bit on segments and segment registers, but not in the previous paragraph. The concepts of instructions and the flags, registers and memory locations which they reference are quite inter-twined. So, before taking a look at the various families of instructions, a very brief rundown on the register names, addressing and flags might be in order.

There are eight data-type registers called AX, BX, CX, DX, SI, DI, BP and SP. These are generally used for data manipulation or as an additional component when calculating a memory address. There are four other registers used exclusively for addressing. These are the segment registers CS, DS, ES and SS. The segment registers each point to a 64 KB block of memory. They could all point to the same 64 KB block, but seldom do. The CS register points to block where the instructions are kept. The DS and ES registers point to the block (or blocks) where data is held. The SS register points to the block for the stack segment. Addresses are calculated from the values obtained from the segment register, an 'offset' into the block as specified by the instruction and maybe values contained in one or two of the 'data registers.' And there are a whole heap of restrictions as to who can keep company with whom! More on that later. A common form of writing one of these combinations is, say, DS:DI. This means: 'Use the value obtained from the DS segment register and add to it the value in the DI data register.' And presto, you have an address.

Flags are one-bit memory locations which keep track of certain activities within the MPU chip itself. An example of how a flag would be used occurs after an addition. It would be nice to know two things. First, was the sum too large for the destination register? If you tried to add $200+200$ and place the result in the 8 -bit register, you would end up with an an-
swer of 144, which isn't really what you had in mind. Each of the operands (200) fit into an 8-bit field, but the answer needed nine bits. When this occurs, the 'overflow flag' would be set. Second, if you are using signed arithmetic, it would be nice to know if the result was positive, negative or zero. There are two flags to tell you this, the 'zero flag' and the 'sign flag.' The zero flag is set to 1 if the result is zero, and 0 if it is not. The sign flag is set to 0 if the result was positive and 1 if it was negative. By the way, zero is considered a positive number. These flags can be tested after the addition to make sure the answer fits where you thought it would or to see if it is in the correct range. A sign flag set to negative after calculating your bank balance could be used to set off an alarm, dial up your automatic banking system and hurriedly transfer funds into your account. Useful things these flags!

## Instruction families

The Data Transfer Instructions consist of MOVe, PUSH, POP, XCHG (exchange), XLAT (translate), IN (read I/O) and OUT (write I/O). PUSH and POP use the memory location at address SS:SP as a last-in first-out (LIFO) stack, automatically updating SP to point to the last entry in the stack. Three other instructions are used to set up registers and segment registers for addressing. These are LEA (Load Effective Address), LDS (Load Data Segment) and LES (Load Extra Segment). XLAT provides a simple mechanism to change a byte of data from one format to another. XCHG swaps the contents of two data locations in one instruction. This ensures that the processor cannot be interrupted when checking the value of data in a particular location. It's also an easy way to shift data between registers without anything being overwritten. The first three instructions may also have the 'flag bits' as operands.

The arithmetic instructions are Add, Subtract, Multiply, Divide, Increment, Decrement, Negate and Compare. These instructions are performed on either signed or unsigned binary integers. Should the data be in a decimal (packed) representation, there are a multitude of instructions to reformat the data after a binary manipulation has been performed. Negate provides the 2 s complement of the selected data. Two instructions can be used to convert a byte in AL to a word in AX or a word ( 16 bits) in AX to a double word in the AX-DX register pair.
Bit manipulation instructions operate on either a byte or word of data, but affect the data on a bit by bit basis. The instructions are NOT (invert), AND, OR, XOR (exclusiveOR), TEST, SHIFT, and ROTATE. The first four simply perform the Boolean operations.

Test allows one or more bits of the data to be selectively tested. This is the only instruction which can directly access a single bit of data. Shift and Rotate alter the data by moving it one bit to the right or left. Shift fills in vacated bit positions with zeros, whereas Rotate moves the bit out from one end into the other. Rotate may also use the Carry Flag as an intermediate holding location which can be tested by other instructions.

The string instructions provide the facility to move or test up to 64 K bytes or 32 K words with one instruction. For these instructions, DS:SI form the address of the source data and ES:DI form the address. CX is always used as the counter for the operation. The Direction Flag determines whether CX will be incremented or decremented. These instructions are normally preceded by the REPEAT command. Various forms of the REPEAT provide testing of data equality (or inequali-

## SoftTalk

ty) as well as testing the CX counter. If the conditions are met or the counter reaches 0 , the 'repeat loop' is terminated. The instruction is interruptable after each byte or word operation takes place.
Program control, or program transfer, instructions are used to alter the sequence of instructions to be executed. These are Call, Return, Jump, Conditional Jump, Loop, Interrupt and Return from Interrupt.
Call and Return perform the usual operations. During a Call, the return address is pushed onto the stack and control is passed to the address specified in the Call instruction. Return "pops" that address off the stack and returns control to the main program. Interrupt and Interrupt Return do much the same except that the flags are also saved on the stack.
Jump and Conditional Jump transfer control to another part of the program. There is no return. A conditional jump tests one or more flags. If the conditions are met, the jump is taken. If not, the next sequential instruction is executed. Conditional jump instructions have a very limited range. The target of the jump must be within the range of -128 to +127 bytes from the address of the conditional jump instruction. This can often cause a bit of a problem when programs are being modified.

The LOOP instruction operates much like the REPEAT prefix. If the CX counter is non-zero and the equality (or inequality) condition is met, control is transferred to the address specified in the LOOP instruction. Otherwise, the next sequential instruction is executed. The Direction Flag determines whether CX is to be incremented or decremented each pass through the loop.
Process or control instructions are used to set or clear Flags, Wait or synchronize external devices. The Carry and Direction Flags may be directly altered by the program. The Interrupt Enable Flag may be cleared to stop external interrupts from being processed. When set, interrupts will again be allowed.
The 8088 may be set in a Wait state by two instructions. HALT stops the processor until a reset or hardware interrupt is received. WAIT stops the processor until the /TEST line goes low. A third, 'quasi wait,' condition exists in the No-op instruction. The processor simply performs no operation each time it is encountered.
To synchronize with external devices or a shared bus, the ESCape and LOCK instructions are provided. The ESC instructions sets pins on the 8088 which may be sampled by an external chip (say an 8087 arithmetic co-processor). When the Escape is executed, the co-processor can read data as the 8088 fetches it into storage. The co-processor can then operate on the data and place the result in main storage. The 8088 continues to process as normal.
The Lock instruction allows the 8088 to tell the bus manager that it requires exclusive access to the bus during the following instruction. This ensures that no other processor sharing the same bus devices will interfere with the operation to be performed. Most PCs do not make use of this instruction.

## How the instructions are executed

The 8088 can be internally divided into two functional components. The first is the 'Execution Unit (EU)'. This contains the Arithmetic/Logic unit (ALU), the data registers and the status flags. The second component is the 'Bus Interface Unit (BIU)', This component contains the Segment registers, instruction pointer and instruction cache and provides the interface to memory and I/O devices. The recent AEM 4500 MicroTrainer project provided functions similar to the Exe-

| 8088 FUNCTIONS |  |  |
| :---: | :---: | :---: |
| EXECUTION UNIT |  | BUS INTERFACE UNIT |
| Data Registers |  | Segment Registers |
| AH | $A L \quad[A X]$ | CS |
| BH | BL [BX] | DS |
| CH | CL [CX] | ES |
| DH | DL [DX] | SS |
| SI |  |  |
| DI |  |  |
| BP |  | Instruction Pointer |
| SP |  | IP |
| FLAGS |  | Cache |
| C TF [Trap Flag] |  | 1 |
| $\qquad$ DF [Direction Flag] |  | 2 The next four bytes |
| C IF [Interrupt Enable] |  | 3 sequence are stored |
| S OF [Overflow Flag] |  | 4. |
| S | SF [Sign Flag] |  |
| S | ZF [Zero Flag] |  |
| S | AF [Auxiliary Carry] |  |
| S PF [Parity Flag] |  | Bus Interface |
| ALU |  |  |
| Functional Components of the 8088 processor. Flag Definitions: $\mathbf{C}=$ Control Flag $\mathbf{S}=$ Status Flag |  |  |
| Figure 1 |  |  |



Memory Organization
cution Unit. The BIU takes over the function of setting the input switches for the MicroTrainer, plus a bit more.

The two components function in tandem. The BIU fetches instructions from memory and stores them in the 'instruction cache'. The instruction cache is a four byte storage area. When the EU has no work to do, an instruction is transferred from the cache to the EU. While the EU is executing the instruction, the BIU begins fetching another instruction from memory. In this way the EU should have another instruction waiting when it completes the current instruction. There should be little or no waiting for the next instruction to be fetched from memory. Should the currently executing instruction require DATA from memory, the request is passed to the BIU to fetch the data. In this instance, the EU must wait for the data fetch to complete (four to five clock cycles) before it can complete the instruction and go onto the next one.

## The execution unit

The Execution Unit is shown in Figure 1. There are eight 16-bit registers available (AX, BX, CX, DX, SI, DI, BP, SP). Four registers (AX, BX, CX, DX) may also be used as pairs of 8 -bit registers. AH is either the high order byte of $A X$ (bits $15-8$ ) or is an 8 -bit entity in its own right. The AL register is either the low order byte of AX (bits 7-0) or an 8-bit entity. The AH and AL registers may be loaded and manipulated independantly or AX loaded as a 16 -bit chunk of data with the high and low order bits independantly available for computations (or whatever). The BX and DX registers may be used similarly.
The SI, DI, BP and SP registers are 16-bit only registers which in practice are used primarily for addressing, but which may almost be used as 'general purpose' registers. BP is referred to as a 'base register'. SI and DI are referred to as 'index registers'. The SP register, called the Stack Pointer, is used to index into the stack segment. BX may also be used as a 'base register' to provide register-modified addressing.
A 'general purpose' register is one that can be used in any instruction for any function. The 'index registers' just mentioned cannot be used for some arithmetic instructions, hence the 'almost.'
Intel has created an architecture more suited to main storage variables than register variables. None of the registers are true 'general purpose' registers and many instructions for the 8086 family require that specific registers be used as operands. This limits the number of registers available to hold a variable over a long period. Figure 2 describes the more common 'implied' usages.
The flags fall into two categories, 'control' and 'status'. The control flags indicate how the processor will perform certain functions. The control flags are set or cleared under program control. Instructions have been provided to do this. The 'interrupt flag' states whether interrupts from hardware external to the chip will be processed. At times, a routine may be so sensitive that its processing must not be interrupted. An example of this is an interrupt handler itself. The 'direction flag' indicates whether 'auto-increment' or 'autodecrement' will be used. The 'trap flag' is used to provide single-step operation. This allows the execution of a program to be examined instruction by instruction. To do this requires a 'debugging' program to also be run in order to display the results at each step.
The STATUS Flags provide information concerning the instruction just executed. Not all Status Flags are changed after each instruction and in fact some instructions will not alter any Status Flags at all. The Status Flags are used to determine whether a CONDITIONAL JUMP will be taken or

| REGISTER USAGE |  |  |
| :---: | :---: | :---: |
| Word | 8yte | Function |
| AX | AL | These registers are the traditional accumulators. All arithmetic operations may be performed. Multiplication and division must use these registers. They cannot be used for addressing. When using $A X$ or AL as the destination register, the instructions are often shorter, resulting in greater throughput. |
| BX |  | The BX register may be used as an index register for address calculations. It can be used as the source or destination register for most other operations. This is the nearest thing to a general purpose register in the chip. |
| cx | CL | CX is used as an implied counter in string, repeat and loop operations. It is the only counter allowed in these operations unless you wish to generate the logic yourself. CL is used a a counter for shift and rotate operations. Again, this is the only register allowed. It can be the source or destination for most instructions. but cannot be used for indexed addressing. |
| DX |  | DX is used in multiplication or division when the product or dividend requires 32 bits. It cannot be used as an index register. |
| SI.DI |  | Both registers are for word operations only. They are required for string operations. SI conventionally points to the source memory address and DI points to the destination address. Both registers can be used for most arithmetic and bit operations. |
| BP |  | $B P$ is a 16 -bit index register which is based upon the stack segment register rather than the data segment register. This can occassionally lead to confusion. Most arithmetic and bit operations may be performed. |
| SP |  | SP is the stack pointer register. Much care must be taken when manipulating this register as CALLs. RETURNs and INTerrupts depend upon its integrity. The register is incremented or decremented by POP. RETURN. PUSH and CALL instructions. |
| CS |  | Code Segment register. This is used to form the effective address of the next instruction to be executed. The value obtained from CS is always added to the Instruction Pointer to form the nonindexed portion of the effective address. |
| DS |  | Data Segment register. Points to a 64 K segment used to store data. Most memory-based operations assume that DS points to the correct data area. The data segment register generally contains a different value than the CS register. Valid index registers are BX . SI . DI and BP . (Take care when using BP). |
| ES |  | Extra Segment register. Ponnts to an additional 64 K data area. It is used in string operations to point to the destination segment. Often. DS and ES point to the same segment unless large amounts of data are involved. |
| SS |  | Stack Segment register. This register is used to point to an area where PUSH. POP. CALL and RETURN instructions can store data. This area is normally independant of other data segments. Some programs use the stack segment for storage of variables. SP and BP are the index registers. |
| Regis | con | entions for 8088 and 8086 processors. |

Figure 2

## SoftTalk

not. The various forms of these flags are shown in Figure 2.
The ALU performs the operations defined by the instructions. The types of operations it performs are defined by the instruction set of the microprocessor. The ALU in the 8088 will perform much more complex operations than that of the MicroTrainer. When the instruction is to operate upon data obtained from storage, the request for the data is passed to the Bus Interface Unit (BIU). The operation will complete only when the requested data has been fetched by the BIU and then passed to the ALU.

## The bus interface unit

The BIU is responsible for the transfer of data from the microprocessor chip to external memory or I/O devices. It also calculates the Effective Address in memory from the information provided in the instruction. Some of this information is explicit and some is implied. To calculate an address the BIU uses four registers (CS, DS, ES and SS). These registers are referred to as the SEGMENT registers. Each segment register is a 16 -bit pointer to a 64 kilobyte (KB) block of storage. The instructions assume that all data or other instructions can be located in one of these 64 KB blocks. The 8088 has the ability to address 1024 KB of storage, so it must be possible to change the value in the segment registers to access all memory. This can be done by normal arithmetic type instructions or by special instructions for the segment registers.
The CS register points to what is normally called the CODE SEGMENT. The BIU fetches instructions only from the Code Segment. Control of the data in the CS register is therefore of great importance. If it is corrupted, the program goes off into never-never land. The address of the current instruction is calculated from the CS register and the Instruction Pointer (IP). The IP may be incremented to point to the next instruction or altered by a program control instruction. When the address of the new instruction is available within the Code Segment, it is called a NEAR instruction. If a new CS pointer is required, it is called a FAR program transfer. A FAR program transfer will change the value in both the CS and IP registers.
The DS register points to the DATA SEGMENT. Instructions which use memory locations to hold data assume that the data is contained in the current data segment.
The ES points to what is known as the EXTRA SEGMENT. This is much like the data segment, except that only a few instructions make use of the pointer in the ES register. For the most part, those that do are string instructions which engage in a transfer of data from one memory location to another without passing data through one of the registers. Some programs may not use an independant Extra Segment but initialize it to point to the same 64 KB block as the DS register.
The SS points to the STACK SEGMENT. All programs require a stack segment. The SS register, and the SP register mentioned earlier, are used to determine the location in the stack segment available to store an address or data. The stack segment is often referred to simply as the STACK. The Call and Return instructions use the stack to remember where in the program the routine was called from. Push and Pop instructions use the stack to temporarily store data while the registers are used for some other purpose. Variables may be stored on the stack by some programs.
And now, having nicely identified which segment register is used for what, we find there is an instruction known as a 'segment override'. If you wish to use an instruction which assumes that data is in the DS segment, but the data is really in the ES segment, you can insert an instruction which says,
'instead of using the DS register to find the address of the data, use the ES register'. Segment overrides may be used on most instructions and can refer to any one of the four segment registers. The override is valid for the execution of one instruction only.

## Calculating the effective address

There is now a plethora of registers which may take part in the calculating of an address. There are base registers, index registers, segment registers and a thing called the displacement. The displacement is coded in the instruction and is an offset into the appropriate segment. All of these numbers are maximum 64 K , so how do we put them all together to make an address from 0 to 1024 K ?

First, there are the segment registers. These do not point to a single storage address, nor do they define a unique 64 KB block of storage. They point to a unique 16 -byte block of storage! $(16 \times 64 \mathrm{~KB}=1024 \mathrm{~KB})$. The value in the segment register is multiplied by 16 and placed in a 20 -bit register. The values from the base and index registers and the displacement are added to that 20 -bit value. That means of course that the segments can overlap since they are on 16 -byte boundaries. This allows for more efficient use of storage, as only the allocated storage should be addressed in any segment. Why not start the data segment 1000 h bytes after the beginning code segment, if only OFFEh bytes are used for the instructions? The program shouldn't grow!

The absolute address of a storage location is known as the Effective Address (EA). The most complex EA is calculated as:

$$
\begin{aligned}
\text { EA }= & (\text { (segment reg. } \times 16)+\text { base reg. } . \\
& + \text { index reg. }+ \text { displacement })
\end{aligned}
$$

The more registers included in the computation, the longer it takes the 8088 to work it all out and hence, the longer it takes to actually execute the instruction. When defining the number of clock cycles required for an instruction one of the little add-ons is +EA.
Intel has developed a, shall we say, unique method of storing 16 -bit addresses (and data) in storage. Instead of simply putting bits $15-8$ in the first byte and bits $7-0$ in the second, Intel does it just the opposite. Bits 7-0 are stored in the first byte and bits $15-8$ in the second. Just to confuse us all, when the data is loaded into a register, it is combined correctly. When defining data in storage, you have to remember how you want to access it. If you load it a word at a time the data must conform to the 'reverse representation', but if it is to be accessed a byte at a time, the normal order is used. This format also applies to the displacement coded as part of the instruction.


## Interrupt processing

Many I/O devices communicate with the microprocessor chip by issuing an interrupt when there is data to be processed. This frees the processor from continually 'polling' the I/O devices to see if there is any data to be processed. It also frees the programmer from a lot of headaches.
When the INTR (interrupt) line is active the processor completes the instruction that is currently active and checks the interrupt flag. If Interrupt-Enable is set, the processor sig-
nals the interrupting device that the interrupt is acknowledged. The interrupting device then places an 8-bit code on the data bus. The processor then translates this 'interrupt code,' into an 'interrupt vector' by multiplying the code by four. The bottom 1024 bytes ( $256 \times 4$ bytes) of storage are reserved for addresses of routines to handle interrupts. In true Intel fashion, the addresses are in reverse order. That is, the first byte is the low order byte of the displacement. The second byte is the high order byte of the displacement. The third is the low order byte of the segment and the fourth byte is the high order byte of the segment.

```
DISPL-LOW DISPL-HIGH SEGMENT-LOW SEGMENT-HIGH
```

Upon receiving the interrupt, the flags and the address of the next instruction that would have been executed are pushed onto the stack and control is transferred to the address specified in the interrupt vector. The Interrupt Flag is cleared (interrupts disabled). The interrupt routine executes and returns control back to the program by issuing an IRET (Interrupt Return) instruction. The interrupt routine is also responsible for enabling interrupts.
Interrupts may also be issued by the program. In this case the interrupt code is part of the instructions. Except that no hardware is involved, software interrupts are handled in the same manner as hardware interrupts. This is useful for testing interrupt routines or for communicating between programs. Most operating system facilities are invoked via software interrupts.
There is another interrupt pin on the 8088 known as the Non-Maskable Interrupt (NMI). This pin has been assigned Interrupt Code 2. It is intended to be used whenever there is a catastrophic error in the system. The Interrupt Flag is ignored and the interrupt processed with the highest priority. Otherwise, it's just another interrupt.

## In summary

What does all this mean? The 8088 microprocessor is a powerful but complicated chip. To drive (program) it correctly you'll have to understand some of its workings and have adequate documentation available. Intel provide hardware and programmer's guides that are essential reading and go into much more detail than this article. But it may make it a bit easier to get going. Other sources of information are assembler or compiler manuals. Good programming. \&-

[^2]
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Here is our fifth crossword with a prize of the magnificent Weller WTCP. Controlled Output Soldering Station for the winner. This month we are back to some fairly tough words, but to make it a little easier we ve once again included the dreaded 'Cryptic Crushers'. Post us your answers, even if you ve missed one or two (it might still wint by no later than lanuary 17. Our Crosswords are prepared usting 'Crossword Magic' supplied by Edsoft Piy Lid. 20 Blachburn Rd. Blackburn Victoria.

The announcement of the winner of the October 1985 crossword has been delayed due to the extensive NSW postal strike. The winner together with the November winner will be announced in the next issue. The answers to las months crossword are on page 31

## SEND YOUR ENTRY IN BY LAST MAIL JANUARY 17

## WELLER CROSS WORD COMPETITION NO. 5



## TRE PR 运



## ACROSS

1. Transient distortion of a pulse signal which the response temporarily exceeds the final value
2. When the carfier wave is not transmited in AM radio transmission
3. Emission of light from the screen of a CRT after removal of excitation by the electron beam
4. An artificial load
5. A circuit for the production of pulses of predetermined duration the leading edge of which is triggered by the leading edge of input pulses. (US )
6. A signal which prevents an action happening
7. Computers - in which the more positive of two voltages selected as the logic levels is designated the 1 state
8. The property and structure of a semiconductor among other device
9. Known as Ground in the $U S$
10. The range of frequencies over which the gain of amplifier falls within the prescribed limits
11. Term used to describe peripheral equipment under the control of a computer

## DOWN

2. A transmission path from one point to another via the ionosphere
3. A prcture tube in which the electron beam scans a single phosphor strip in a horizontal direction
4. A device for providing power from the mains in a unit where batteries were formerly used
5. The property of an electrical resonant curcuit which causes a progressive fall in the amplitude of free oscilations
6. Radio waves of constant amplitude and frequency
7. A metal structure designed to dissipate heat

## CRYPTIC CRUSHERS


2. Hol washing up place

## SEND YOUR ENTRY IN BY LAST MAIL DECEMBER 17.

The competition is open to all persons normally resident in Australia or New Zealand, with the exception of members of the staff of Australian Electronics Monthly, the printers, Offset Alpine, and/or associated companies. The winning entry will be drawn by the Editor, whose decision is final; no correspondence will be entered into regarding the decision.
Winners will be notified by relegram the day the result is declared and the winner's name and contest results published in the next possible issue of the magazine.

Cut out or photocopy the entry form. complete it and send to

"Weller Crossword"<br>Australian Electronics Monthly<br>PO Box 289,<br>Wahroonga NSW 2076

We will accept entries postmarked no later than December 18

In casc two or more entrants corrcctly complete the crossword we'll have to judge who's best at waxing lyrically. in 30 words or less, over: "Why I think the Weller WTCPN is the soldering station for me".

## Name <br> Address

Postcode

## NEW PRODUGTS NEWS

## Light reflections on the time domain

For those who have become tired of playing around with mere wires, there are always fibre optics. The wellknown Vicom group of companies has entered this interesting field with an instrument by Laser Precision Corp of USA known as an optical time domain reflectometer (OTDR).

This instrument, known as the TD-9220, is said to offer unsurpassed accuracy for testing and trouble-shooting multimode or single mode cable under any environmental condition. lt offers real-time display, digital storage, and a host of other features.
The latest developments from Laser Precision include a fully portable ruggedised OTDR with
plug-in modules for various fibre optic standards including the 1300 nm singlemode system.
Other instruments in the range include the AM-4000 attenuation/power meter and the AP4200 optic power source.

Full technical information is available from your nearest Vicom office at Melbourne, Sydney, Brisbane or Wellington.

Choke ... cough, gasp

Many RFI problems arise from cables acting as antennas. Mains, microphone loudspeaker, telephone and data cables can all emit or receive unwanted signals which spoil the performance of electronic equipment.


Usually these are of the "common mode" type; that is, they can be visualised as travelling along the 'outside' of the cable and can be reduced without affecting the normal function of either the "culprit" or the "victim' circuit.
A neat little device to solve the problem has been announced by EMC Datacare Ltd of Power Court, Luton, Bedfordshire, England.

The picture shows the clip-on radio-frequency choke which is designed to help solve problems of radio-frequency interference (RFI) that can afflict domestic radio, television and audio equipment, as well, as computers, process control and
telecommunications systems.
The common-mode chokes, comprising pairs of ferrite " U " cores, can be installed on cables of up to 10 mm diameter in-situ, without removing any connectors; it is not even necessary to have access to the cable ends.
For large or rigid cables, several pairs of cores are required; for smaller flexible cables, multiturn chokes may be made from the same components to provide substantial impedance to interference currents.
The clip-on choke is available as a kit known as D918 which comprises eight D910 choke cores and associated hardware. It is supplied complete with application notes to help the user install them successfully.

## Count on them

Racal-Dana has introduced a new family of universal counters to supplement its already wide range of counters and general test equipment.

The 1900 family are a new generation of processor controlled counter/timers that provide many new features not previously found on this type of equipment.

All models feature frequency and period resolution of nine digits in one second, whatever the frequency. Single-shot time interval resolution of 1 ns enables rise and fall times to be measured on pulses as narrow as 5 ns .
Peak signal amplitude measurement adds a new dimension

to system performance with phase measurement being another important new feature.
The microprocessor control allows mathematical functions to be performed and readouts given directly to any convenient unit such as mph , rpm, litres-per-second, parts-per-million, or any exponent format, thereby allowing results to be interpreted quickly and easily - no conversions, no calculations.
Further details from: Racal Electronics Pty Ltd at North Ryde, NSW. (02)888 6444.


## The Armstrong method of pcb design

For those unable to afford the computerised autorouting pcb design package described in last month's AEM, there is always the old manual method. I don't mean back to marking pens and shaky hands but the use of printed circuit drafting aids: DIP patterns, tapes, rings, squares, teardrops, ovals and donut pads, not to mention component silkscreen stick-ons, grid underlay cutter. and drillng targets.
Armed with tungsten carbide cutter, compasses, rolling cutter, and microscope ( 10 X ), the humblest hardware hacker be-
comes an overnight pcb draftsman par excellence lat least in his own eyes).
All the above goodies are laid out mouthwateringly in a beautiful brochure by DMC Drafting Aids, distributed by Koloona Industries at Riverwood. An indispensable Koloona catalogue for those who do produce their own boards, the booklet is free of any of the peculiar Japanese-to-English translations often seen.
Talk to Koloona about the catalogue, and perhaps they will send you an up to date pricelist with it. They can be found at 1/68 Belmore Road South, Riverwood 2210 NSW. (02) 538228.

## The Fujitsu hold

Mechanical relays are still alive and well, and Fujitsu holds a good grip on the market, we're told
IRH Components has just released additional relays in the Fujitsu FBR600 series, which have a substantial foothold. These are miniature pc-mounting types and feature 16 amp switching capacity at 240 Vac in SPST format. Single coil magnetic latch types are also available in SPS'T and DPDT format.
The Fujitsu FBR600 series is popular because of its 5 kV RMS or 10 kV impulse contact to coil isolation and has SECV approval (CS80157V).
These relays also meet the requirements of Telecom Australia, specification 1053 Issue 2 and 1054 Issue 2 with Approval Number RA80/157.
Coil dissipation is only 0.5 watt at $20^{\circ} \mathrm{C}$ for all types. The coil range available covers five to 60 V with $5,6,12,24$ and 48 $\checkmark$ being preferred types.
Full details from IRH Components, Kingsgrove, NSW. (02)750 6444.


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## GIT GIOIGE 3

Best quality West German screwdrivers from C.K.
We use these in our laboratory and consider them excellent as they are specifically designed for eletronics reauirements.

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## aem data sheet

## The＇7910 FSK

Advanced Micro Devices introduced their＇world－chip＇modem IC，the Am7910，in 1981，offering great simplification of mod－ em design and construction for computer communications using frequency－shift keyed voiceband frequencies over the switched public telephone network．
The Am7910 is fabricated using N －channel metal oxide sili－ con（MOS）technology and is housed in a standard 28－pin dual－ in－line（DIL）package．Advanced Micro Devices are represent－ ed in Australia by R\＆D Electronics，PO Box 206，Burwood 3125 Vic．
Thomson Semiconductors，part of the French Thomson－CSF group，introduced a second－source pin and function compati－ ble device for this modem IC in 1985，known as the EF7910． This is fabricated using HMOS technology and only a few minor parameters differ from the AMD device．Thomson Semicon－ ductors are represented in Australia by Promark，PO Box 381， Crows Nest 2065 NSW．

The 7910 is a single－chip asynchronous Frequency Shift Keying（FSK） voiceband modem．It is pin selectable for baud rates of 300,600 or 1200 bits per second and is compatible with the applicable Bell and CCITT recommended standards for $103 / 113 / 108,202$, V． 21 and V． 23 type modems．Five mode control lines select a desired modem configuration．

Digital signal processing techniques are employed in the 7910 to per－ form all major functions such as modulation，demodulation and filtering． The 7910 contains on－chip analog－to－digital and digital－to－analog con－ verter circuits to minimize the external components in a system．This de－ vice includes the essential RS－232／CCITT V． 24 terminal control signals with TTL levels．

Clocking can be generated by attaching a crystal to drive the internal crystal oscillator or by applying an external clock signal．

A data access arrangement（DAA）or acoustic coupler must provide the phone line interface externally．

The 7910 is fabricated in a 28 －pin package．All the digital input and output signals（except the external clock signal）are $T T L$ compatible．Power supply requirements are $\pm 5$ volts．
－Complete FSK MODEM in a 28 －pin package－just add line interface
－Compatible with Bell 103／113／108，Beil 202，CCITT V．21．CCITT V． 23 specifications
－No external filtering required
－All digital signal processing，digital filters and ADC／DAC included on chip
－Includes essential RS．232／CCITT V． 24 handshake signals
－Auto－answer capability
－Local copy／test modes
－ 1200 bps full duplex on 4 ．wire line
－Pin－programmable mode section．

PIN ASSIGNMENT


Figure 1． 7910 Block Diagram


| रING $\square^{1 \cdot}$ | ${ }^{28}$ | $\square$ ato |  |
| :---: | :---: | :---: | :---: |
| $\mathrm{vce}{ }^{\text {a }}$ | 27 |  |  |
| $\overline{\text { RESET }}{ }^{3}$ | 26 | คо |  |
| $\mathrm{v}_{88}$－$^{\text {d }}$ | 25 | $\square^{\text {co }}$ |  |
| ac ${ }^{5}$ | 24 | 万хィ | ，／／C |
| CAP，${ }^{\text {－}}$ | 23 | 曰хı |  |
| $\mathrm{CAP}_{2}$－ | 22 | Пog |  |
| тc ${ }^{8}$ | 21 | 曰mc． |  |
| agno－ | 20 | 万мс |  |
| To $\square^{10}$ | 19 | 尸мс |  |
| $\overline{\text { BRTS }} 11$ | 18 | $\square \mathrm{mc}$ |  |
| रTS ${ }^{12}$ | 17 | $\square \mathrm{mc}$ |  |
|  | 16 | 万от |  |
| －Ecrs $\square_{14}$ | 15 | $\square^{\text {日r }}$ | － |

ABSOLUTE MAXIMUM RATINGS

Storage Temperature
Ambient Temperature with Power Applied
Commercial (C) Devices
Industrial (I) Devices
$V_{C C}$ with Respect to VDGND
$V_{B B}$ with Respect to $V_{D G N D}$
All Signal Voltages with Respect to VDGND
$-6510+125^{\circ} \mathrm{C}$
$\ldots .0$ 10 $+70^{\circ} \mathrm{C}$
$-4010+85^{\circ} \mathrm{C}$ RATINGS may cause permanent device fallure Functionality at or above these imits is not mplied Exposure to absolute maximum ratings for extended periods may affect dewce reliabovily.

OPERATING RANGE

| Positive Supply Voltage | 5 to +5.25V |
| :---: | :---: |
| Negative Supply Voltage | -4.75 to -5.25V |
| $V_{\text {AGND }}$ | -50 to +50 mV |
| VOGND | V |
| Temperature |  |
| Commercial (C) Devices . | 0 to $+70^{\circ} \mathrm{C}$ |
| Industrial (l) Devices. | -40 to $+85^{\circ} \mathrm{C}$ |
| Operaung ranges define those the device is guaranterd | he functionatry of |

## Am7910

DC CHARACTERISTICS over operating ranges unless otherwise specitied
Digital Inputs: TD, $\overline{\mathrm{RTS}}, M C_{0}-\mathrm{MC}_{4}, \overline{\mathrm{DTR}}, \overline{\mathrm{RING}}, \mathrm{BTD}, \overline{\mathrm{BRTS}}$
Digital Outputs: RD, $\overline{\mathrm{CTS}}, \mathrm{CD}, \mathrm{BRD}, \overline{\mathrm{BCTS}}, \mathrm{BCD}$


The products described by this specification include internal circuitry designed to protect input devices from damaging ccumulations of charge. It is suggested, never theless, that conventional precautions be observed during storage, hand ling and use in order to avoid exposure to excessive voltages.

## EF7910

ELECTRICAL DC CHARACTERISTICS over operating range, referred to $V_{\text {DGND }}$
$0^{\circ} \mathrm{C} \leqslant \mathrm{T}_{A}<+70^{\circ} \mathrm{C}, \mathrm{V}_{\mathrm{CC}}=+5.0 \vee \pm 5 \%, V_{B B}=-5.0 \mathrm{~V} \pm 5 \%, V_{A G N D}=0 \mathrm{~V} \pm 50 \mathrm{mV}, V_{\text {DGND }}=0 \mathrm{~V}$ Digital inputs : TD, $\overline{R T S}, M C_{0}-M C_{4}, \overline{D T R}, \overline{\text { RING }}, \mathrm{BTD}, \overline{B R T S}$
Digital outputs: RD, $\overline{C T S}, \overline{C D}, B R D, \overline{B C T S}, \overline{B C D}$

| Charectaristic | Symbol | Min | Typ | Mox | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Output HIGH voltage $\left.\\|_{\text {OH }}=-50 \mu \mathrm{~A}, \mathrm{C}_{\text {LD }}=50 \mathrm{pF}\right)$ | VOH | 2.4 | - | - | $v$ |
| Output LOW voltage (1OL $\left.=+2 \mathrm{~mA}, \mathrm{C}_{\text {LD }}=50 \mathrm{pF}\right)$ | $V_{\text {OL }}$ | - | - | 0.4 | $\checkmark$ |
| Input HIGH voltage | $V_{\text {IH }}$ | 2.0 | - | VCC | V |
| Input LOW voltage | $V_{\text {IL }}$ | -0.5 | - | + 0.8 | V |
| External clock input HIGH (XTAL ${ }_{1}$ ) | $V_{\text {IHC }}$ | 3.8 | - | $V_{\text {cc }}$ | V |
| External clock input LOW (XTAL ${ }_{1}$ ) | VILC | -0.5 | - | 0.8 | $V$ |
| External reset input HIGH ( $\overline{\text { RESET }}$ ) | $V_{\text {IHR }}$ | 3.8 | - | $\mathrm{V}_{\mathrm{CC}}$ | V |
| External reset input LOW ( $\overline{\mathrm{RESET}}$ ) | $V_{\text {ILR }}$ | -0.5 | - | 0.8 | V |
| Digital input leakage current $0 \leq \mathrm{V}_{\text {IN }} \leq \mathrm{VCCl}$ | IIL | -10 | - | $+10$ | $\mu \mathrm{A}$ |
| $V_{\text {CC }}$ supply current | ${ }^{1} \mathrm{CC}$ | - | - | 125 | ma |
| $V_{\text {BB s supply current }}$ | ${ }^{\text {IB8 }}$ | - | - | 25 | mA |
| Output capacitance (f $\mathrm{C}=1.0 \mathrm{MHz}$ ) | Cout | - | 5 | 15 | pF |
| Input capecitance ( ${ }^{\text {f }} \mathrm{C}=1.0 \mathrm{MHz}$ ) | $\mathrm{CIN}^{\text {I }}$ | - | 5 | 15 | pf |

ANALOG INPUT (RC) :

| Inpant resistance ( $-1.6 \mathrm{~V}<\mathrm{V}_{\mathrm{RC}}<+1.6 \mathrm{~V}$ ) | $\mathrm{R}_{\mathrm{IN}}$ | 50 | - | - | $\mathrm{k} \Omega$ |
| :--- | :---: | :---: | :---: | :---: | :---: |
| Operating input signal | $\mathrm{V}_{\mathrm{RC}}$ | -1.6 | - | +1.6 | V |
| Allowed DC input offset (REF $\mathrm{V}_{\text {AGND }}$ | $\mathrm{V}_{\mathrm{RCOS}}$ | -30 | - | +30 | mV |

## ANALOG OUTPUT (TC): <br> Output voltage ( $R_{L}=600 \Omega$ ) <br> Output DC offset

## INTERFACE SIGNAL DESCRIPTION $\mathrm{MC}_{\sigma}-\mathrm{MC}_{4}$ (CONTROL INPUTS)

These five inputs select one of thirty-fwo modem configurations according to the Bell or CCITT specitications listed in Table 1. Only 19 of these 32 modes are actually available to the user.
Modes $0-8$ are the normal operation modes. The 1200 Baud modes can be selected with or without a compromise equalizer.
Modes 16-25 permit loop back of the EF 7910 transmitter and receiver. No internal connection is made. The user must externally connect the TRANSMITTED CARRIER pin (Figure 2) to the RECEIVED CARRIER pin it analog loopback is required. For digital loopback, external connection of RECEIVED DATA and TRANSMIT TED DATA is required. Whenever a mode in this group is selected, the effect is to set all transmit and receive filters to the same channel irequency band so that loopback can be performed.
Modes 9-15 and 26-31 are reserved and should not be used DATA TERMINAL READY (DTP)
A LOW level on this input indicates the data terminal desires to send and/or receive data via the modem. This signal is gated with all other TTL inputs and outputs so that a low level enables all these signals as well as the internal control logic to function. A HIGH level disables all TTL IVO pins and the internal logic.
REQUEST TO SEND (RTS)
A LOW level on this input instructs the modem to enter transmit mode. This input must remain LOW for the duration of data transmission. The signal has no effect it DATA TERMINAL $\overline{\text { READY }}$ is HIGH (disabled). A HIGH level on this input turns off the transmitter
CLEAR TO SEND (CTS)
This output goes LOW at the end of a delay initiated when REQUEST TO SEND goes LOW. Actual data to be transmitted should not be presented to the TRANSMITTEDDATA input until a LOW is indicated on the CLEAR TO SEND output. Normally the user should force the TD input HIGH whenever CTS is off (HIGH). This signal never goes LOW as long as $\overline{\text { DTR }}$ is HIGH (disabled). CLEAR TO SEND goes HIGH at the end of a delay initiated when REQUEST TO SEND goes HIGH.

## CARRIER DETECT (CD)

A LOW on this output indicates that a valid carrier signal is present at the receiver and has been present for at least a time. tCDON. where tCDON depends upon the selected modem configuration (Table 3b). A HIGH on this output signifies that no valid carrier is being received and has not been received for a time, TCDOFF. CARRIER DECTECT remains HIGH when DTR is HIGH. Values for tCDON and tCDOFF are configuration dependent and are listed in Table 36 .

## TRANSMITTED DATA (TD)

Data bits to be transmitted are presented on this input serially: HIGH (mark) corresponds to logic 1 and LOW (space) corresponds to logic 0 . This data determines which frequency appears at any instant at the TRANSMITTED CARRIER output pin (Table 3a). No signal appears at the TRANSMITTED CARRIER output unless DT'R is LOW and $\overline{R T S}$ is LOW.

## RECEIVED DATA (RD)

Data bits demodulated from the RECEIVED CARRIER input are available serially at this output; HIGH (mark) indicates logic 1 and LOW (space) indicates logic 0 . Under the following conditions this output is forced to logic I because the data may be invalid:

1. When CARRIER DETECT is HIGH
2. During the internal squetch delay at hall-duplex line tum around (202/V. 23 modes only)
3. During soft carrier turnoff at hati-duplex line tum around (202 mode only)
4. When DTR is HIGH
5. When RTS ON and BRTS OFF in V.23/202 modes only
6. During auto-answer sequence

# aem data sheet 

## $\overline{B A C K}$ REQUEST TO SEND ( $\overline{B R T} \bar{S}$ )

Since the 1200 bps modem configurations, Bell 202 and CCITT V.23. permit only half duplex operation over two-wire lines, a low baud rate "backward" channel is provided for transmission from the main channel receiver to the main channel transmitter. This input signal (BRTS) is equivalent to REQUEST TO SEND for the main channel, except it belongs to the backward channet. Note that since the EF7910 contains a single transmitter, $\overline{\text { RTS }}$ and $\overline{\text { BRTS should not be asserted simultaneously. BRTS }}$ is meaninglul only when a 202 or V .23 mode is selected by $\mathrm{MC}_{0}-\mathrm{MC}_{4}$. In all other modes it is ignored.
For V. 23 mode the frequency appearing at the transmitted carrier (TC) output pin is determined by a MARK or SPACE at the back transmitted data (ETD) in put (Table 3a).
For 202 mode a frequency of 387 Hz appears al TC when BRFTS is LOW and BTD is HIGH. No energy ( 0.0 volt) appears at TC when BRTS is HIGH. BTD should be fixed HIGH for 202 back channel transmission. The signal. $\overline{\text { BRTIS}}$, then is equivalent to the signal. Secondary Request-to-Send, for 202 S/T modems. or Supervisory Transmitted Data for 202 CID modems.

## BACK CLEAR TO SEND (BCTS)

This line is equivalent to CLEAR TOSEND for the main channet, except it belongs to the back channel. $\overline{\text { BCTS }}$ is meaningtul only when a V .23 mode is selected by $\mathrm{MC}_{0}-\mathrm{MC}_{4}$. This signal is not used in Bell 202 back mode.

## BACK CARRIER DETECT ( $\overline{\text { BCD }}$ )

This line is equivalent to CARRIER DETECT for the main channel. except it belongs to the backward channel. $\overline{B C D}$ is meaningtul only when a 202 or V. 23 mode is selected by $M C_{0}$ $M C_{4}$. For V .23 back channel mode, $\overline{\mathrm{BCD}}$ turns on when either the MARK or SPACE frequency appears with sufficient level al the received carrier (RC) input.
For 202 back channel mode. $\overline{B C D}$ turns on in response to a 387 Hz tone of sufficient level at the RC input. In this case BCD is equivalent to the signal, Secondary Received Line Signal Detector, for $202 \mathrm{~S} / \mathrm{T}$ modems. or Supervisory Received Data for 202 CID modems.

## BACK TRANSMITTED DATA (BTD)

This tine is equivalent to TRANSMITTED DATA for the main channel, except it belongs to the back channel. BTD is meaningtul only when a 202 or V .23 mode is selected by MCo-MC4. For 202 back transmission of on/olf keying, BTD should be fixed at a HIGH level.

## BACK RECEIVED DATA (BRD)

This line is equivalent to RECEIVED DATA (except clamping) for the main channel. except it belongs to the back channel. BRD is meaningful only when a V .23 mode is selecled by $\mathrm{MC}_{0}-\mathrm{MC}_{4}$. Under the following conditions this output is forced HIGH:

1. $\overline{B R D}$ HIGH
2. $\overline{\text { DTR }}$ HIGH
3. V. $21 / 103$ mode
4. During auto-answer
5. When $\overline{\text { BRTS }}$ ON and $\overline{\text { RTS }}$ OFF in V. 23 modes only

## TRANSMITTED CARRIER (TC)

This analog output is the modulated carrier to be conditioned and sent over the phone line.

## RECEIVED CARRIER (RC)

This input is the analog signal received from the phone line. The modem extracts the information contained in this modulated carrier and converts it into a serial data stream for presentation at the RECEIVED DATA (BACK RECEIVED DATA) output.

## $\overline{\text { RING }}$

This input signal permits auto-answer capability by responding to a ringing signal from a data access arrangement. It a ringing
signal is detected (RING LOW) and DTR is LOW. the modem begins a sequence to generate an answer tone at the TC output.

## XTAL 1, XTAL2

Master timing of the modem is provided by either a crystal connected to these two inputs or an external clock inserted into XTAL. The value of the crystal or the external clock frequency must be $2.4576 \mathrm{MHz}=.01 \%$.
$V_{c c}$
+5 volt power supply. ( $\pm 5 \%$ )
$V_{B B}$
-5 voll power supply. ( $\pm 5 \%$ )

## DGND

Digital signal ground pin

## Aand

Analog signal ground pin (for TRANSMITTED CARRIER and RECEIVED CARRIER).

TABLE 1.

| $\mathrm{MC}_{4}$ | $\mathrm{MC}_{3}$ | $\mathrm{MC}_{2}$ | $\mathrm{MC}_{4}$ | $\mathrm{MC}_{0}$ |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 0 | 0 | 0 | 0 | 0 | Bell 103 Originate 300bps full duplex |
| 0 | 0 | 0 | 0 | 1 | Beil 103 Answer 300bps full duplex |
| 0 | 0 | 0 | 1 | 0 | Bell 202 1200bps half duplex |
| 0 | 0 | 0 | 1 | 1 | Bell 202 with equalizer 1200bps hall duplex |
| 0 | 0 | 1 | 0 | 0 | CCITT V. 21 Orig 300bps full duplex |
| 0 | 0 | 1 | 0 | 1 | CCITT V. 21 Ans 300bps full duplex |
| 0 | 0 | 1 | 1 | 0 | CCITT V. 23 Mode 21200 bps hali duplex |
| 0 | 0 | 1 | 1 | 1 | CCITT V. 23 Mode 2 with equalizer 1200bps half duplex |
| 0 | 1 | 0 | 0 | 0 | CCITT V. 23 Mode 1 600bps hall duplex |
| 0 | 1 | 0 | 0 | 1 |  |
| 0 | 1 | 0 | 1 | 0 |  |
| 0 | 1 | 0 |  | 1 |  |
| 0 | 1 | 1 | 0 | 0 , | Reserved |
| 0 | 1 | 1 | 0 | 1 |  |
| 0 | 1 | 1 | 1 | 0 |  |
| 0 | 1 | 1 | 1 | $1)$ |  |
| 1 | 0 | 0 | 0 | 0 | Bell 103 Orig loopback |
| 1 | 0 | 0 | 0 | 1 | Bell 103 Ans loopback |
| 1 | 0 | 0 | 1 | 0 | Bell 202 Main loopback |
| 1 | 0 | 0 | 1 | 1 | Bell 202 with equalizer loopback |
| 1 | 0 | 1 | 0 | 0 | CCITT V. 21 Orig loopback |
| 1 | 0 | 1 | 0 | 1 | CCITT V. 21 Ans loopack |
| 1 | 0 | 1 | 1 | 0 | CCITT V. 23 Mode 2 main loopback |
| 1 | 0 | 1 | 1 | 1 | CCITT V. 23 Mode 2 with equalizer loopback |
| 1 | 1 | 0 | 0 | 0 | CCITT V. 23 Mode 1 main loopback |
| 1 | 1 |  | 0 | 1 | CCITT V. 23 Back loopback |
| 1 | 1 | 0 | 1 |  |  |
| 1 | 1 | 0 | 1 | 1 |  |
| 1 | 1 | 1 | 0 | 0 | Reserved |
| 1 | 1 | 1 | 0 | 1 |  |
| 1 | 1 | 1 | 1 | 0 |  |
| 1 | 1 | 1 | 1 | $1)$ |  |

Figure 2. Loopbsck Configuretions


Figure 3. Transmitter Block Disgram


## $\mathrm{CAP}_{1}, \mathbf{C A P 2}$

Connection points of external capacitorlresistor required for proper operation of on-chip analog-to-digital converter.
Recommended values are: $C=2000 \mathrm{pF} \pm 10 \%$,

$$
R=100 \Omega \pm 10 \%
$$

## RESET

This input signal is for a reset circuit which operates in either of wo modes. It autornatically resers when power is applied to the device, or it can be activated by application of an external active low TTL pulse.

## TMEORY OF OPERATION

The EF 7910 MODEM consists of threa main sections, shown in the block diagram of Figure 1 - Transmitter, Recaiver, and Interface Control.

## TRANSMETER (Modulator)

The transmitter, shown in Figure 3, receives binary digital data from a source such as a UART and converts the data to an analog signal using frequency shift keying (FSK) modulation. This analog signal is applied to the phone line through a DAA or acoustic coupler. FSK is a modulation technique which encodes one bit per baud. A logic one applied to the TRANSMITTED

DATA (TU) input causes a sine wave at a given frequency to appear at the analog TRANSMITTED CARRIER (TC) output. A logic zero applied to input TD causes a sine wave of a different trequency to appear at the TC ouput. As the data at the TD input switches between logical one and zero, the TC outpul switches between the two frequencies. In the EF7910 this switching between frequencies is phase continuous. The frequencies themselves are digitally synthesized sine functions.
The frequencies for each modem configuration available in the EF7910 are listed in Table 3a.
The process of switching between two trequencies as in FSK generates energy at many more frequencies than the two used in the modulation. All the transminted information can be recovered from a frequency band B Hz wide, where B is the bit rate or maximum rate of change of the digital data at the TD input. This band is centered about a frequency, $\mathrm{I}_{\mathrm{C}}$.
where $t_{C}=f_{1}+\left(l_{2}-f_{1}\right) / 2$
( $\mathrm{f}_{1}$ = lower of two FSK frequencies)
( $\mathrm{f}_{2}=$ higher of two FSK frequencies)
In addition to this primary information band, there exist side bands containing redundant information. It is desirable to attenuate these bands for two reasons:

1. The phone companies have specifications on the amount of energy allowed in certain frequency bands on the line.

## 7910

2. If two independent information channels are present simultaneously on the line (e.g. 300 bps full duplex or 1200 bps half duplex with back), the redundant transmitter components may fall in the frequency band of the local receiver channel and interfere with detection. In the EF 7910 these redundant and undesirable components are aftenuated by digital bandpass filters.

Following the digital bandpass filters, the filtered FSK signal is converted to an analog signal by an on-chip DAC operating at a high sample rate. This analog FSK signal is finally smoothed by a simple on-chip analog low pass filter.

## RECEIVER (Demodulator)

A simplified block diagram of the EF 7910 FSK receiver is shown in Figure 4. Data fransmitted from a remote site modem over the phone line is an FSK-modulated analog carrier. This carrier is applied to the RECEIVED CARRIER (RC) pin via a DAA or acoustic coupler. The first stage of the demodulator is a simple on-chip analog low pass anti-alias fitter. The output of this is converted into digital form and filtered by digital bandpass fitters to improve the signal to noise ratio and reject other independent channel frequencies associated with the phone line in the case of full duplex configuration. The bandpass filtered output is digitally demodulated to recover the binary data. A carrier detect signal is also digitally extracted from the received line carrier to indicate valid data.

## INTERFACE CONTROL

This section controls the handshaking between the modem and the local terminal. It consists primarily of delay generation counters. two state machines for controtling transmission and reception, and mode control decode logic for selecting proper transmit frequencies and transmit and receive fitters according to the selected modem type. Inputs and outputs from this section are as foliows:

## REQUEST TO SEND (Main and Back)

CLEAR TO.SEND (Main and Back)
CARAIER DETECT (Main and Back)
$\overline{\text { RING }}$
MCO-MC4
DATA TERMINAL READY
Internal logic clamps protocol signals to different levels under certain conditions (e.g., initial conditions).
When Bell $103 / 113$ and V. 21 modem configurations are selected, the back channel signals are non-functional.
Figures 7 and 8 depict the sequencing of the two state machines. State machine 1 implements main or back channel transmission. and the auto-answer sequence. State machine 2 implements reception on main or back channel.
The state machine powers on to the state labelled INITIAL CONDITIONS. Handshake signals are set to or assumed to be the levels listed in Table 2. The machine then waits for DATA TERMINAL READY (DTR) to be turned on. Whenever DTR is turned to the OFF state from an ON condition, each state machine and external signals return to the initial conditions within 25 microseconds. After $\overline{\text { DTR }}$ is turned ON the EF7910 becomes operational as a modem and the state machines pro ceed as depicted in the flowcharis.
The detinitions of the terms Full Duplex and Hall Duplex used in these flowcharts are depicted below(Figs. 5and6)."Full Duplex* applies to all 103/113. V. 21 modes. "Half Duplex" applies to 202 and V.23, both forward and backward channel.
Full Duplex: Data can be transmitted and received simultaneously at a rate of 300 baud. Two independent 300 Hz channets are frequency multiplexed into the 3000 Hz bandwidth of the phone line. The EF 7910 configurations for the Bell 103/113 and CCITT V. 21 can be operated full duplex.

Half Duplex: In half duplex with back channel. the modem may transmit at 1200/600 baud and receive at $5 / 75$ baud. Alternatively it may transmit at $5 / 75$ baud and receive at $1200 / 600$ baud. Examples are Bell 202 and CCITT V. 23.

TABLE 2.
INITIAL CONDITIONS

| Data Terminal Ready ( $\overline{\mathrm{DT}} \overline{\mathrm{R}}$ ) | OFF |
| :---: | :---: |
| $\checkmark$ Request to Send (RTS) | OFF |
| Clear to Send ( $\overline{C T S}$ ) | OFF |
| Transmitted Data (TD) | Ignored |
| Back Channel Request to Send ( $\overline{\text { BRTS }}$ ) | OFF |
| Back Channel Clear to Send ( $\overline{B C T})^{\text {a }}$ ) | OFF |
| Back Channel Transmitted Data (BTD) | Ignored |
| Ring ( $\overline{R 1 N G}$ ) | OFF |
| Carrier Detect ( $\overline{C D}$ ) | OFF |
| Received Data (RD) | MARK |
| Back Channel Carrıer Detect ( $\overline{B C D}$ ) | OFF |
| Back Channel Received Data (BRD) | MARK |

## CALL ESTABLISHMENT

Before two modems can exchange data, an electrical connection through the phone system must be established. Although it may assist in call establishment, a modem typically does not play a major role. A call may be originated manually or automatically and it may be answered manually or automatically.
Manual Calling - Manual calling is performed by a person who dials the number, waits for an answer, then places the calling modem into data transmission mode.
Automatic Calling - Automatic calling is typically performed by an automatic calling unit (ACU) which generates the appropriate dialing pulse or dual-tone sequence required to call the remote (called) modem. The ACU also has the ability to detect an answer tone from the called modem and place the calling modem into data transmission mode.
Manual Answering - Manual answering is performed by a person who hears the phone ring. lifts the receiver, causes the called modem to send an answer tone to the calling modern, and places the called modem into data transmission mode.
Automatic Answering - Automatic answering is performed by a called modern with a data access arrangement (DAA). The DAA detects a ringing signal, takes the phone circuit off-hook (corresponding to lifting the recerver) and instructs the called modem to commence the auto-answer sequence. Next the called modem sends out silence on the line. followed by an answer tone. When this tone is detected by the calling modem. the connection is considered to have been established.
The EF7910 provides assistance for automatic answering through the $\overline{\operatorname{RING}}$ signal as follows. Observe the upper right-hand portion of Figure 7(a). Assume that DATA TERMINAL READY (DTQ) has recently been asserted to cause exit from the initial conditions. Note that it $\overline{\mathrm{DTR}}$ remains OFF. $\overline{\text { RING }}$ is ignored. Assume also that $\overline{\operatorname{RTS}}$ and $\overline{B A T S}$ are OFF and that the mode control lines (MCO-MC4) select a normal modem configuration, not a loopback mode. Automatic answerng is inittated by receipt of a LOW level at the $\overline{\operatorname{RING}}$ input. causing entrance to the autoanswer sequence depicted in Figure 7(c).
The EF 7910 outputs silence ( 0.0 volt) at its TRANSMITTED CARRIER (TC) output for a time. 'SIll. followed by the answer tone for a time. $t_{A T}$. The CARAIER DETECT $(\overline{C D})$ pin is clamped OFF and the RECEIVED DATA (RD) signal is therelore clamped to a MARK (HIGH) during the auto-answer sequence. Upon completion of the answer tone, $\overline{\mathrm{CD}}$ is released. If the mode lines (MCO-MC4) select a 202 or V. 23 mode. the fransmit filters are set to the forward channel and the receive filters are sel to the back channel during the auto answer sequence.
At the end of the auto-answer sequence, return is made to point $A$ in the loop at the upper right-hand portion of Fig. 7(a). Note that since the answer flag has been set, the auto-answer sequence cannot be entered again unless DTR is first lurned OFF. then ON. At this point the phone line connection has been established and data transmission or reception may begin.
The $\overline{\operatorname{RING}}$ input may be activated from a conditioned DAA Ring Indicator output for automatic answering or it may be activated by a switch for manual answering. Tying $\overline{\operatorname{RING}}$ HIGH will disable the auto-answer function of the EF7910.

## DATA TRANSMISSION <br> \section*{Full Duplex}

Following call establishment, full duplex data transmission can be started by either the called or calling modem. In other words, if the connection has been established and the modern is looping through point $A$ in Figure $7(\mathrm{a})$, it no longer matters which is the called and which is the calling modem. Data transmission is initiated by asserting $\overline{\text { REOUEST TO SEND }}$ ( $\overline{\operatorname{RTS}}$ ). At this time the TRANSMITTED DATA (TD) input will be released and a modulated carrier can appear at the TRANSMITTED CARRIER (TC) output. Following a detay, trCon. $\overline{\text { CLEAR TO SEND }}$ ( $\overline{\mathrm{CTS}}$ ) will turn ON. At this time, data may be transmitted through the TD input. It is a common protocol for the user to always present a MARK at the TD input betore $\overline{\operatorname{RTS}}$ is asserted and during the ifRON delay.
Data transmission continues until $\overline{\operatorname{RTS}}$ Is turned OFF. Following a short delay. trcoff, $\overline{C T S}$ turns OFF. As soon as $\overline{\mathrm{ATS}}$ goes OFF. the TD input is ignored and the TC output is set to 0.0 volt (silence). After CTS turns OFF. the state machine returns to point A in Figure 7(a).

## Haf Duplex

When a half duplex mode is selected (202 or V.23), data transmission can be either on the main channel at 1200/600 baud or on the back channel at $5 / 75$ baud. In normal half duplex operation a single modem is either transmitting on the main and receiving on the back channel or vice versa. In the EF 7910 control of the transmitter and receiver filters to the proper channel is perlormed by $\overline{\operatorname{RTS}}$. When $\overline{\text { RTS }}$ is asserted. the transmitter filters and synthesizer are set to transmit on the main channel; the receiver tilters are set to receive on the back channel. Therelore, whenever $\overline{\mathrm{RTS}}$ is on, $\overline{\text { BRTS }}$ should not be asserted since the transmitter cannot be used for the back channel. When $\overline{\mathrm{ATS}}$ is OFF and a half duplex mode is selected, the transmitter filters and synthesizer are sel to the back channel; the receiver filters are set to the main channel. If $\overline{\mathrm{RTS}}$ and BRTS are asserted simultaneously. $\overline{\text { ATS }}$ wilt take precedence. However. If $\overline{\text { BRTS }}$ is asserted betore $\overline{\mathrm{RTS}}$ and the back channel data transmission sequence has been entered (Figure 7(b)). $\overline{\text { RTS }}$ will be ignored untıI BRTS is turned OFF.
The state machine sequences lor marn and back channel transmission differ slightly and are depicted in Figure 7. Assume the state machine is idling through point $\mathbf{A}$ in Figure 7(a).

## Main Channel

This transmission sequence is entered if a 202 or $V .23$ mode is selected and $\overline{R T S}$ is asserted. Since the receiver is now forced to the back channel. the RECEIVED DATA (RD) signal is clamped to a MARK; and the CARRIER DETECT signal is clamped OFF. The TRANSMITTED DATA input (TD) is released and a carrier appears at the TRANSMITTED CARRIER output which follows the MARK/SPACE applied to TD. $\overline{\mathrm{R} T S}$ lurning ON initiates a delay. trCON, at the end of which the CLEAR TO SEND (CTS) output goes LOW. When $\overline{\mathrm{CT}}$ goes LOW data may be transmitted through input TD. Data transmission continues until $\overline{\text { RTS }}$ is furned OFF. At this time several events are initiated. FIrst a delay, trCoFf. is initiated at the end of which $\overline{\text { CTS }}$ turns OFF. The TD


Figure 7(b). Transmit Back Channel State Diagram
Figure 7 (c). Auto Answer State Diagram
at any time after the phone connection has been established. Reception is independent of transmission. When the receiver detects a valid carrier for at least a time. CDONN . the output $\overline{\mathrm{CD}}$ is turned ON, the RECEIVED DATA (RD) output is released, and valid data can be obtained at RD. Data is received until the receiver detects loss of carrier for at least a time. lCDOFF. At this time the $\overline{\mathrm{CD}}$ output is turned OFF and RD is clamped to a MARK. The state machine returns to the idle loop at point $E$.

## Half Ouplex

As discussed in the data transmission section above, when a half duplex mode has been selected. the signal RTS controls whether the main channel is transmitting or receiving. The back channel can only do the opposite from the main. If ATS is OFF. then CARRIER DETECT may be asserted and the data reception sequence is identical to that discussed above for full duplex reception. As long as RTS remains OFF. BACK CARRIER DETECT will never be asserted. If $\overline{R T S}$ is ON, then CARRIER $\overline{\text { DETECT }}$ will never be asserted. Instead the receiver will look for a valid carrier in the back channel frequency band. If a valid carrier exists for at least a time. iBCDON. the output BACK CARRIER DETECT ( $\overline{B C D}$ ) is turned ON, the BACK RECEIVED DATA (BRD) output is released and valid data can be obtained at BRD. Data is received untl the receiver detects loss of back channel received signal for at teast time. tecooff. At this time the $\overline{B C D}$ output is turned OFF. Data output, BRD, is clamped to a MARK if a V. 23 mode is selected. For 202 back channel mode. $\bar{B} \overline{C D}$ represents the received data. The BRD output can be ignored. The state machine returns to the ide loop at point $E$.

## LOOPBACK

Ten modes exist to allow both analog and digital loopback for each modem specification met by the EF7910. When a loopback mode is selected. the signal processing (filters. etc.) for both the

## aem data sheet



Figure 12. Clock Generation

(b)


TABLE 4. CLOCK PARAMETERS

| Symbol | Parameters | Min | Typ | Max | Units |
| :---: | :---: | :---: | :---: | :---: | :---: |
| ${ }^{\text {c }}$ - | Clock Period | 406.86 | 406.9 | 40694 | ns |
| ${ }_{\text {LCH }}$ | Clock High Time | 165 |  |  | ns |
| ${ }^{\text {chel }}$ | Clock Low Time | 165 |  |  | ns |
| ${ }^{\text {L CR }}$ | Clock Rise Time |  |  | 20 | ns |
| ${ }^{\text {c }}$ CF | Clock Fall Time |  |  | 20 | ns |

[^3]transmitter and receiver is set to process the same channel or frequency band. This allows the analog output. TRANSMITTED CARRIER, and the analog input. RECEIVED CARRIER, to be connected for local analog loopback. Alternatively the digital data signals. TD and RD or BTD and BRD. can be connected externally. allowing a remote modem to test the local modem with its digital data signals looped back.
When a loopback mode is selected, the state machine sequences are attered slightly. First. auto-answer is disabled Second. If a half duplex loopback mode is selected (202 or V.23), the local $\overline{\text { CARRIER DETECT}} / \overline{\mathrm{BCD}}$ is not forced OFF when $\overline{\mathrm{RTS}} / \overline{\mathrm{BRTS}}$ is asserted.

The 202 and V. 23 main loopback modes allow use in a 4 -wire configuration at 1200 bps

## CLOCK GENERATION

Master timing of the modem is provided by either a crystal connected to the $X_{T A L_{1}}$ and $X_{T A L_{2}}$ inputs or an external clock applied to the $X T A L_{1}$ input.

## Crystal

When a crystal is used it should be connected as shown in Figure 12. The crystal should be a parallel resonance type, and its value must be $2.4576 \mathrm{MHz}=.01 \%$.

## External Clock

This clock signal could be derived from one of several crystaldriven baud rate generators. It should be connected to the XTAL $_{1}$ input and the XTAL 2 input must be left floating. The liming parameters required of this clock are shown in Figure 12 and the values are listed in Table 4

## POWER ON RESET

The reset circuit operates in either of two modes

## Automatic Reset

In this mode an internal reset sequence is automatically entered when power is apphed to the device. One resistor and one capacitor must be connected externally as shown in Figure 13. Values shown will work with most power supplies. Power supply (VCC) rise l.me should be less than one hatl the RC .lime constant

## Externa: Reset

In this mode the device may be forced into the resel sequence by application of an active LOW puise applied to the AESET input The reset must not be applied until the $V_{C C}$ supply has : eached dt least 3.5 V . Timing is diagrammed in Figure 14.

TABLE 3(b). TIMING PARAMETERS

| Symbol | Description | Bell 103 Orig | $\begin{array}{\|c\|} \hline \text { Bell } 103 \\ \text { Ans } \end{array}$ | $\begin{aligned} & \text { Ccitt } \\ & \text { V. } 21 \\ & \text { Orig } \end{aligned}$ | $\begin{aligned} & \text { CcitT } \\ & \text { V. } 21 \\ & \text { Ans } \end{aligned}$ | $\left\|\begin{array}{c} \text { ccrt } \\ \text { V. } 23 \\ \text { Mode } 1 \end{array}\right\|$ | $\begin{gathered} \text { ccITT } \\ \text { v. } 23 \\ \text { Mode } 2 \end{gathered}$ | $\begin{gathered} \text { CCITT } \\ \text { V. } 23 \\ \text { Mode } 2 \\ \text { EO } \end{gathered}$ | Bell 202 | $\begin{gathered} \text { BeNl } 202 \\ \text { EO } \end{gathered}$ | $\begin{aligned} & \text { CcITT } \\ & \text { V. } 23 \\ & \text { Back } \end{aligned}$ | Bell 202 Back | Units |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Pacion) | Request-to-Sand to Clear-10-Send ON Delay | 208.3 | 208.3 | 400 | 400 | 200.3 | 206.3 | 208.3 | 183.3 | 183.3 | - | - | $\begin{array}{\|c\|} \hline \text { msec } \\ \pm 0.3 \% \\ \hline \end{array}$ |
| ${ }^{\text {tracioli) }}$ | Aequest-to. Send to Clear-to-Send OFF Delay | 0.5 | 0.5 | 0.5 | 0.5 | 0.5 | 0.5 | 0.5 | 0.5 | 0.5 | - | - | $\begin{aligned} & \operatorname{msenc} \\ & =0.25 \% \end{aligned}$ |
|  | Beck Chamel Request-to-Send to Clear-to-Sond ON Oway | - | - | - | - | - | - | - | - | - | 82.3 | - | $\begin{aligned} & \text { masec } \\ & =0.84 \% \end{aligned}$ |
| 'BRCIOI') | Beck Channel Request-lo-Send to Clear-to-Send OFF Dalay | - | - | - | - | - | - | - | - | - | 0.5 | - | $\begin{aligned} & \text { msec } \\ & \pm 25 \% \end{aligned}$ |
| ${ }^{1} \mathrm{CO}(\mathrm{On})$ | Curner Detect ON Delay | $\begin{gathered} 94 . \\ 106 \end{gathered}$ | $\begin{aligned} & 94 . \\ & 106 \end{aligned}$ | $\begin{aligned} & 301 . \\ & 312 \end{aligned}$ | $\begin{aligned} & 301- \\ & 312 \end{aligned}$ | $\begin{aligned} & 11.4 . \\ & 15.4 \end{aligned}$ | $\begin{aligned} & 11.4 . \\ & 15.4 \end{aligned}$ | $\begin{aligned} & 11.4 \text {. } \\ & 15.4 \end{aligned}$ | 18.22 | 18.22 | * | - | msac |
| ${ }^{\text {( }} \mathrm{CO}(\mathrm{OH}$ ) | Carner Detect OfF Deday | 21.40 | 21.40 | 21.40 | 21.40 | $\begin{gathered} 5.4 . \\ 13.3 \end{gathered}$ | $\begin{array}{r} 5.4 . \\ 13.3 \end{array}$ | $\begin{aligned} & 5.4 \\ & 13.3 \end{aligned}$ | $\begin{gathered} 12.4 . \\ 23.4 \end{gathered}$ | $\begin{aligned} & 12.4 . \\ & 23.4 \end{aligned}$ |  |  | mesec |
| 'ecoron) | Beck Channet Carner Detect ON Dellay | - | - | - | - | - | - | - | - | - | 17.25 | 17.25 | meac |
|  | Back Channel Carner Dotect OFF Deday | - | - | - | - | - | - | - | - | - | 21.38 | 21-38 | msac |
| $t_{\text {AT }}$ | Answer Tone Duration | - | 1.9 | - | 3.0 | 3.0 | 3.0 | 3.0 | 1.9 | 1.9 | - | - | $\begin{gathered} s e c \\ =0.44 \times \end{gathered}$ |
| ${ }^{\text {s }}$ IL | Silance interval before Tranamssion | 1.3 | 1.3 | 1.9 | 1.9 | 1.9 | 1.9 | 1.9 | 1.3 | 1.3 | - | - | $\begin{aligned} & s e c \\ & =0.64 \% \end{aligned}$ |
| ${ }^{\text {t }}$ S | Recenver Squalch Duration | - | - | - | - | 156.3 | 156.3 | 156.3 | 156.3 | 156.3 | - | - | $\begin{aligned} & \text { meec } \\ & =3.3 \% \end{aligned}$ |
| 'sio | Transmatter Solt Turn-OII Durabon | - | - | - | - | - | - | - | 24 | 24 | - | - | $\begin{aligned} & \text { msec } \\ & \pm 2.3 \% \\ & \hline \end{aligned}$ |
| ${ }^{\text {t }}$ I | Memumum $\overline{\mathrm{Ri}}$ Low Durabon | - | 25 | - | 25 | 25 | 25 | 25 | 25 | 25 | - | - | $\mu 8$ |

NOWINAL PERFORMANCE SPECIFICATIONS
TRANSMITTER (All Modem Typea)
input Data Format: Serial, asynchronous, standard TTL levels Modulation Technique:
Binary, phase-coherent Frequency Shift Keying (FSK)
TC Output Level: - 30 Bm into 600 N
Frequency Accuracy:
$\pm 0.4 \mathrm{~Hz}$ all modems except Bell 202 (mark)
+1.0 Hz Bell 202 (mark)
Harmonics: -45 dB from fundamental for single tones
Delay uncertainty for TD logic input change to TC frequency change: $\leqslant 8.3 \mu \mathrm{~s}$
Out-of-band energy : See Figure 15
RECEIVER
Output Data Format: Serial, asynchronous, TTL levels
Demodulation Tectinique: Differential FM Detection
Sensitivity at Receiver Input: 0 dBm to -48 dBm
Frequency Deviation Tolerance: $\pm 16 \mathrm{~Hz}$
Carier Detect Threshold:

## ON $\quad>-43 \mathrm{dBm} \pm 1 \mathrm{~dB}$ <br> OFF $<-48 \mathrm{dBm} \pm 10 \mathrm{~B}$

Figure 13. Automatic Reset


Figure 14. External Reset
$1 \mathrm{OR}=$ delay from the lime $\mathrm{V}_{\mathrm{CC}}$ reaches 3.5 V and the falling edge of RESET signal ( $>1 \mu \mathrm{~s}$ )
$t_{\text {RL }}=\overline{\text { RESET }}$ LOW duration time ( $\gg_{M C K}=406 n \mathrm{~s}$ )


.. from page 61.
as pin 13 goes HIGH. During the period that pin 13 is HIGH, Q4 is off, removing the supply to LEDs $2 \& 3$. This period is half the duration of the transmit cycle and forms the receive period.


Transistor Q4 is switched by IC5 to ensure that the green \& red LEDs are only on during the appropriate portion of the operating cycle. The LEDs are run at full current rating via Q5 and Q6 to give maximum brightness, which can be important in strong light. IC6(b) is a NAND gate and combined with the inverters IC6(c) \& (d) switches the inputs to Qs 5 \& 6 as required. NOTE: IC6(a) is reserved for later modification and must be connected as shown.

During the WRITE period (when keying is being entered into the memory) and the READ period (when the memory is keying the transmitter) LEDs $2 \& 3$ operate as follows: From the start of the period to $75 \%$ of the period LED3 (flashing RED) is on indicating that the available time in the memory is running out and keying should cease as soon as possible after the LED starts flashing.
The ratio between the green and red LEDs remains $75 \%: 25 \%$ regardless of the keying speed set by RV1 but the number of times that LED3 flashes each cycle will vary with the setting of RV1. If RV1 is set for, say, 16 seconds per cycle, LED3 may flash nine or 10 times. If RV1 is set for 66 seconds LED3 may flash 35 to 40 times.

During the READ period the LEDs indicate the program timing. If this facility is not required, change S2 to a DPDT and add the second switch section between R10 \& D5 so that it is closed in the WRITE position and open during READ. The timing LEDs will then operate only during the WRITE cycle.
S1, S2 \& RV1 are the only controls required for all functions. S1 (MEMORY ON/OFF) controls the supply to the clock oscillator, binary counters and timing display LEDs. IC2 must be supplied with 5 V at all times to maintain data in the memory. Turning S1 to OFF does not destroy data as IC2 is supplied directly from the positi\%e rail.
Switch S2 (READ/WRITE) select.; either the READ or WRITE mode by taking pin 3 of IC2 HIGH or LOW. RV1 (speed) sets the READ or WRITE rate as required.

To ensure that each time a message is transmitted it starts at the beginning and not at random, a reset pulse is applied to the binary counters each time S1 is turned ON. A brief positive pulse goes from the +5 V rail to pins $7 \& 15$ of ICs $4 \& 5$ via C7 and D10. This resets the counters to zero. A similar reset pulse is sent via R14, C5 and D8 at the end of the receive period to initiate the next cycle as pins 12 \& 13 of IC5 go HIGH.
DO NOT exceed 5 V on the supply rail, the CMOS ICs will operate on higher voltages but the memory is a 5 V device. Diode D4, in series with the supply rail, protects the circuit from accidental reverse voltage and over-voltage protection is provided by ZD1.

To enter a message, set S2 to WRITE and RV1 to mid-range. Set S1 to ON and the GREEN LED will light. After about 30 seconds the green LED will go off and the flashing red LED then operates. When the flashing red LED goes off, the trans-
mit period has finished and the memory is disenabled. This cycle clears any random noise in the memory, or a previous message.

Wait until the green LED comes on again, then start keying immediately. Stop keying as soon as possible once the red LED starts flashing. Any keying after the red LED stops flashing will not be entered into the memory. When the red LED has gone off, set S2 to READ. Set S1 to OFF, then reset it to ON. The CW should then commence immediately (as the green LED comes on), unless you started keying a little late while writing your message in.
If the message was too long for the available time, rewrite it again but key a little faster. For best results do not write a message with RV1 set below mid-range, otherwise the reliability of the keying may suffer. Any speed may be used to READ a message.
To stop the message, set S1 to OFF. If S1 is left ON the message will auto-repeat as long as power is supplied.

## Modification for 2K memory

The maximum period of any message which can be written into the memory is limited to about 45 seconds. If a message exceeds this time the keying may become erratic and the characters unreadable.

Another problem arises when the input keying is at a slow rate and the full memory period elapses before the message has been completed, e.g: when using a slow, simple, computer-generated message. [See "CW using the Microbee").
The stability and length of message problems can be overcome by the addition of another 2102 memory (IC7) in parallel with the existing memory (IC2). This increases the period available from around 45 seconds to 90 seconds which is more than adequate, even with slow keying.

It is then possible to WRITE a message relatively slowly and READ it at a faster rate while maintaining good keying quality. (Refer to Circuit 2).
By using two NOR gates (IC8(a) \& (b)) and three inverters (IC6(a), ICB(c) \& (d)) memory 1 (IC2) and memory 2 (IC7) can be sequentially switched to produce a 2 K memory without any significant break in the data input or output flow. Each memory is enabled as pin 13 is taken LOW and disenabled as pin 13 goes HIGH.
The memories are enabled as follows: Pins $13 \& 14$ of 1 C 5 both LOW turns IC2 ON but hold IC7 OFF. Pin 13 of IC5 HIGH and pin 14 of IC5 LOW turns IC7 ON and holds IC2 OFF. Pin 13 of IC 5 LOW and pin 14 of IC5 HIGH holds BOTH IC2 and IC7 OFF and forms the receiving period of the cycle. (Refer to Figure 2).


Figure 2.
All pins of IC7 are wired in parallel with the matching pins of IC2 except pin 13, the enable line. Two additional 10n capacitors, Cs $12 \& 13$, are used to bypass pin 10 of IC7 and pin 14 of IC8. Resistors R8, 18, 19 \& 20 are connected to the +5 $V$ rail and hold the inputs to IC6(a), IC8(a) \& (b) HIGH. Diodes D12-15 isolate the input gates and allow them to be taken LOW by IC5.

Circuit 3.

|  | TONE DECODER PARTS LIST |
| :---: | :---: |
| C14 | ..... . 1u/16 V. electrolytic |
| D16 | 1 N914 |
| P1 | Plug to suit J1 |
|  | . . . Plug to suit recorder external speaker socket |
| Q7 | BC548 |
| R21 | 4 k 7 |
| T1 | 8R:5k speaker transformer |


|  | 2K MEMORY PARTS LIST |
| :---: | :---: |
| C12, C13 | ....... 10n/50 V ceramic |
| D12-15.. | ...... 1N914 |
| IC7.... | ..... $21021 \mathrm{~K} \times 1$-static RAM |
| IC8.... | .... 4001B NOR gate |
| R18-20 ... | ...... 1M, 1/4 W 5\% |
| Note: IC6(a) | d R8 are part of Circuit 1. |

When changing to the 2 K option the following modification is made to the outputs of IC5. Pins 5 \& 6 of IC6(b) are changed so that pin 5 of IC6(b) goes to pin 12 of IC5 instead of pin 11 and pin 6 of IC6(b) goes to pin 5 of IC5 instead of pin 12. Diodes D5 and D6 are changed so that they both go to pin 14 of IC5.
Diode D7 remains connected to pin 6 of IC6(b). Pin 13 of IC2 is connected to pin 10 of IC8(c) instead of pin 13 of IC5. Pin 13 of IC7 is connected to pin 11 of IC8(d).

## CW using the Microbee

Many amateurs now have access to a microcomputer and for generating messages such as those used with this memory, they are ideal. It is relatively easy to program a correctly spaced message that would be very difficult to equal with hand sending.
The Microbee will produce CW by using the following BASIC program and the PLAY tones. Tone number 24 was selected as being the most suitable for this purpose. The spacing used is one space between each dit or dah, three spaces between letters and seven spaces between words. Refer to the Microbee User's Manual, pages 115 \& 116 for further information.
"CQ DX" is written as below:

```
00100 REM "CQ DX"
00110 PLAY 0,10
00120 PLAY 24,3;0,1;24,1;0,1;24,3;0,1;24,1;0,3
00130 PLAY 24,3;0,1;24,3;0,1;24,1;0,1;24,3;0,7
00140 PLAY 24,3;0,1;24,1;0,1;24,1;0,3
00150 PLAY 24,3;0,1;24,1;0,1;24,1;0,1;24,3;0,7
```

In this example, CQ is treated as two letters, followed by a word space, D is followed by a letter space, X is then followed by a word space. $24,1=$ dit; $0.1=$ space between dits and dahs; $24,3=$ dah; $0,3=$ letter space; $0,7=$ word space. The space 0,10 in line 100 leaves a short gap between starting the program and the first letter.

After the message has been composed and played to ensure it is correct the tones can be recorded onto tape from the Microbee either by placing a microphone close to the internal speaker or by a direct patch to the recorder. When replayed, the output from the recorder can then be used with a simple tone decoder circuit to operate the CW memiory. A series of messages can be made up for use as and when required to program the memory. The only critical settings are the recocd and play levels but they can quickly be set to the optimum to give clean keying.

The decoder operates very simply: (Refer to Circuit 3). Transformer T1 interfaces with the recorder external speaker outlet and the audio tones are rectified by D16. The voltage across C14 causes Q7 to conduct, switching the input line to the memory unit exactly as though a key is being used each time an audio tone reaches D16.

## References:

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Amateur Radio, October 1983, p.19, 20.
5. "Beacon Generator", author unknown, Kyoto 6 Metre DX Club, Japan.
6. "User's Manual, Microworld 16 K BASIC", Microworld, 1982.
7. "Digital Electronics Revealed" by Colin Mitchell, Talking Electronics, 1983.
8. "Circuit Techniques", Volumes 1-4, An ET'I publication series, 1981-1984.

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# Christmas tree lights with a difference 

The multicoloured lights that one can readily buy are of a simple steady on or flashing design. After watching such lights for a while, one can get bored. Another disadvantage is that, because they operate from mains, there is a small but certain risk of shock - especially with metallic decoration touching the lights. So I decided to sit down and cook up a Christmas tree lighting circuit with a difference. This was six years ago, and the circuit has been on the tree many times and been admired by many visitors and suffered no component failures.
The circuit uses simple CMOS components - shift register, data selector, counter and eight light circuits, to create a number of different lighting sequences. The sequences start with the lights turning on one circuit at a time at a one-second rate. They stay on for eight seconds then gradually turn off. Once off, a blue floodlight turns on for eight seconds, simulating a moonlight. Next, the lights start turning on gradually again, but in an alternating pattern. The light circuits stay flashing in alternation for eight seconds, then gradually turn off, after which the tree is moonlit once more. The sequences then repeat.
To achieve the above lighting sequences, a 4015 dual 4 -bit shift register (IC1) was used. Its eight outputs control the eight light circuits with four series-connected bulbs in each. Because each light circuit requires about 250 mA of current, the register outputs are buffered by 4049 inverting buffers (IC4, 6) and BD139 transistors. A 4.7 ohm resistor in series with the collector of each BD139 protects the transistor against current surge when the cold bulbs are turned on. This also serves to prolong the life of the bulbs, in addition to the bulbs operating on reduced voltage.

The data that controls the on/off state of the lights is clocked from an eight channel data selector 4512 (IC5 pin 14) into the shift register by a nominal 1 Hz oscillator made from IC3, pins 3 and 4.

The same oscillator also clocks a 4520 counter (IC2) which controls the selection of data to be clocked from the data selector into the shift register. The following table lists the address lins A2, A1, A0 of the data selector and the effect of the selected data on the lights. The addresses 011 and 111 are used for the moonlight activation. Whenever A1, A0 are 11, NAND gate IC3 pin 11 goes low, which, after inversion by the buffer, IC6 pin 6, turns on the BD139 and MJE3055 transistors (in Darlington configuration) which activate the moonlight. Each address state is active for a nominal eight

[^4]seconds. Since there are eight address states, the light sequences repeat after a nominal 64 seconds.

A2A1AO
000

001
010
$011 \quad X_{3}=1$ maintains all circuits off for eight seconds while moonlight is on.
$100 \quad X_{4}=0101 \ldots$ (produced by $1 / 2 \mathrm{~Hz}$ square wave from IC2 pin 11) is shifted into register. This causes CCT 7 to turn on. A second later, CCT 6 turns on but CCT 7 turns off. Another second later, CCT 5 turns on, CCT 6 off, CCT 7 on, etc.
$101 \quad \mathrm{X} 5=0101 \ldots$ causes CCT 7, 5, 3, 1 to flash in alternation with CCT 6, 4, 2, 0 at a one-second rate.
$110 \quad \mathrm{X} 6=1$ - this gradually turns off the alternating pattern, starting with CCT7.
$111 \quad \mathrm{X} 7=1$ - all circuits are off for eight seconds while moonlight is on.
An optional switch can be connected which, when on, will keep the counter and the register reset, which will turn all lights on.

The circuit can be squeezed onto a $95 \times 76 \mathrm{~mm}$ Veroboard (although a bigger board would make construction easier). The majority of components listed are available from Jaycar Electronics. A 7812 regulator can be used instead of 78L12. Also $4 \times 3 \mathrm{~A}$ rectifiers can be used instead of a 2 A bridge. Although the 4051 is functionally similar to the 4512 , the pin connections for the two are different and the 4051 has not been tried out in the circuit.

Extra care is always necessary in wiring in the mains cable, ensuring that the exposed metal parts (such as the aluminium box) are connected to the green earth wire of the mains cable. The active and neutral connections of the mains cable to the transformer need to be insulated with sleeving or insulation tape for safety.

Probably the biggest problem that can be encountered in this project is obtaining the 3.8 V screw-type (MES) torch bulbs - they appear to be less readily available now than they were a few years ago. Two alternatives are available. One is to use 2.5 V screw bulbs with six per circuit, but this adds up to 48 bulbs total instead of 32 which may be excessive. The other alternative is to use four 2.5 V screw bulbs and add a 12 ohm 1 W resistor in series with each 4.7 ohm resistor already at the BD139 collectors.

George Polkorn, Castle Hill, NSW


PARTS LIST - XMAS LIGHTS̄
$1 \times 4520$ dual binary counter
$1 \times 4512$ (or 4051) 8-channel multiplexer
$1 \times 4015$ dual 4-bit shif! register
$2 \times 4049$ hex inverting buffers
$1 \times 4011$ quad 2 -input NAND gate
$1 \times 78 L 123$-terminal $12 \mathrm{~V}+$ ve regulator
$9 \times$ BD139 (or similar)
$1 \times$ MJE 3055 (or similar)
$1 \times 2 A$ bridge rectifier (or $4 \times$ diodes)
$1 \times 1 \mathrm{~A} / 100 \mathrm{~V}$ diode
$9 \times 1 \mathrm{k5}, 1 / 2$ W 5\% resistors
$1 \times 220 \mathrm{k} 1 / 2$ W 5\% resistors
$2 \times 1 \mathrm{M} 1 / 2 \mathrm{~W} 5 \%$ resistors
$8 \times 4$ R $71 / 2 \mathrm{~W} 5 \%$ resistors
$1 \times 470$ n ceramic or polyester cap
$2 \times 47 n$ ceramic or polyester cap
$1 \times 470 \mathrm{u} / 25 \mathrm{~V}$ electrolytic cap
$1 \times 12$ V/2 A transformer
$1 \times 12 \vee 1-2$ A globe
$32 \times 3.8 \mathrm{~V}$ MES torch bulbs (see text)
$32 \times$ bulb holders
$1 \times$ box. say $150 \times 100 \times 70 \mathrm{~mm}$
25 metres of hookup wire
2 metres of mains cable
$1 \times 3$-pin mains plug
$1 \times$ grommet
$1 \times 3$-way mains terminal block
$1 \times$ mains-rated toggle switch
Veroboard, about $76 \times 95 \mathrm{~mm}$

## letters

## Motional feedback

Dear Sir,
I read with interest the review article in the September issue of the Philips F9638 motional feedback (MFB) loudspeaker. The problems with handling high levels of signal, the detection of an apparent resonance at approx. 44 Hz and difficulties in handling faster rates of sweep frequency excitation all point to problems in execution of the design. Also, a first crossover frequency near 900 Hz (assuming the choice of a bass driver with negligible flexure modes up to, say, 1000 Hz ) would improve the benefits to be derived from systems of this type.
The original Philips MFB system was subjectively free of these problems but was relatively expensive for market expectations at that time. Subsequent attempts to produce a smaller, cheaper unit proved subjectively unsatisfactory, mainly due to an apparent design misapprehension that the MFB could overcome problems of excessive cabinet resonances. It did not, of course.
Not mentioned in the review article were the benefits possible from the use of MFB:

1) Substantial reduction of distortion due to nonlinear compliance of the suspension.
2) Substantial reduction of nonharmonic distortion caused by cohesive (internal) friction in suspension materials (particularly at low signal levels.
3) Supression of resonances of the voice coil movement (bass resonance effects) due to diaphram and voice coil inertia and compliance of suspension and sealed box volume.
These benefits are valid only as long as the drive amplifier is not overloaded, and also depend upon freedom of the diaphram from break-up modes, since it is the motion of the voice-coil that is normally controlled.
If only the bass driver, to avoid complexity, is subject to motional feedback, it is desirable that the bass diaphram should be significantly free of break-up at least to a frequency approaching 1000 Hz , so that the low frequency crossover should be close also to 1000 Hz .
Features (1) and (3) taken together mean, also, that low frequency response is insensitive to amplitude dependence. Also, useful response, provided overloading is avoided, can be extended below the bass resonant frequency, as is also done in some specialised bass woofer designs. Feature (2), provided a fair-
ly high low frequency cut-off is achieved, is more important than is often realized, since it extends the useful dynamic range of the loudspeaker system as compared with the same units driven passively.
Speakers, driven passively, sound much more effective when driven at high levels, and at normal to low levels appear to be much less "transparent" than feedback controlled equivalents. Feature (2) is of great significance if full benefits of wide dynamic range are to be achieved using CD digital systems. Also it is important for improved listening quality at comfortable levels for home listening.
From the description given of subjective effects of the F9638 loudspeaker system, it appears that it may be subject to relatively early onset of overloading. Also, the relatively low bass frequency crossover suggests that the bass driver may not be as effective as it could have been to extract the best qualities possible from such a system. The problems no doubt arise in part from attempts to control the cost factor. That it performs well when played soft and low suggests that the muddying affect of cohesive friction have been reasonably effective. The apparent detection of a bass resonance is disconcerting and is presumably caused by the early onset of overloading.

## H.W. Holdaway Coogee, NSW

## 8-channel relay interface

Dear Sir,
Congratulations on an informative and interesting magazine. My letter is prompted, however, by concern over the design used in the 8 -Channel Relay Interface for Computers in your October 1985 issue (project AEM4501).
The relay switching transistors are driven by the 74LS259 direct, being switched on in the high state of the outputs. According to the description given, the current flowing from these outputs is typically 7 mA . However, in common with most other 74LS devices, the output in the high state is only rated to deliver a maximum of 400 microamps. The 'top' transistor in the totem pole outputs of the 74 and 74LS devices is a much smaller transistor than the 'bottom' transistor, as its usual application is to drive other 74 and 74LS device inputs to the high state where they require very little current. These figures can be seen in any data sheet, including the one for the 74LS259 in your October issue.

From my experience, I believe the circuit will work initially in most cases, but will probably suffer from a high failure rate of the 74LS259.
I propose a simple solution which will avoid the need to modify the pc board or circuit arrangement. The BC547 transistors can be replaced with Motorola MPS-A12 Darlington transistors, and R3 to R 10 replaced with 12 k resistors. This will limit the output from the 74LS259 to well within its capabilities, while still providing sufficient drive to operate the relays.

> R.N. Lehmann, Elizabeth East, S.A.

Many people misunderstand the ratings of TTL and other 'family' devices. The crux is that the ratings are designed so that a complete system of devices gates, counters etc - can be interconnected and will work together (providing fanout limits are not exceeded). The 400 u A 'output high' current quoted in the data sheet means that the minimum LSTTL high level voltage of 2.4 V is guaranteed, providing you don't take more than 400 uA from the output in question. It has nothing to do with any absolute maximum rating that would cause device failure if exceeded.
Your suggestion to substitute Darlington transistors for the BC547s and to increase the base resistors would marginally increase the maximum ambient temperature before failure, but I consider the original circuit to be entirely adequate in this regard.
When you use an LSTTL device to drive non-TTL loads you can draw much more than 400 uA , albeit at lower output voltages, as the typical output characteristics in the graph here shows. This is actually Figure 4, reproduced from page 3.3 of the Signetics TTL data book. The graph is actually for standard TTL, but note that LSTTL can source nearly twice the current shown.
The crunch to all this 'extra' current comes in the form of heat which must be dissipated to stop the chip from heating up to the point of failure. The manufacturers supply the information necessary to calculate the extra heating in the 'packages' section of the data books. The relevant figures are the thermal resistance from junction-to-ambient, which tells you how many degrees the junction will reach when dissipating one watt of power to the air through the case with no heatsink.
For the 74LS259's 16 -pin plastic DIP package, we find a rating of $137^{\circ} \mathrm{C} / \mathrm{W}$ for Signetics devices. (Motorola quotes $120^{\circ}$ C/W for their 16-pin DIP). The maximum



Totem pole output circuit of TTL devices

supply current is 36 mA . If we multiply this by the Vcc rating of 5 V , we get the normal power dissipation of 180 mW . The normal junction temperature is therefore 180 mW times $137 \mathrm{C} / \mathrm{W}$ or $25^{\circ} \mathrm{C}$ above ambient.

The extra power due to the loading in the relay driver circuit is 7 mA times 2.8 V , or 20 mW . The worst case total for all eight outputs is 160 mW . Multiplying the power by the previously quoted thermal resistance results in a temperature of $22^{\circ} \mathrm{C}$. This means that the maximum junction temperature is now $47^{\circ} \mathrm{C}$ over ambient because of the extra current being drawn. The point at which the collector-base junctions break down is around $150^{\circ} \mathrm{C}$, so the maximum ambient temperature rating is just over $100^{\circ} \mathrm{C}$ for the circuit as published. The TTL data books quote $70^{\circ} \mathrm{C}$ as the maximum ambient for commercial grade devices, so the additional current should not cause problems.
If that sounds ominous in regard to reliability, then consider this - a few years ago I built a BIG digital clock which used 74LS374s to drive transistors, only the current being drawn from each of the outputs was 45 mA ! The clock is still running perfectly and has never had a component failure, despite operating outdoors in all weathers.

Geoff Nicholls


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SELL: SERIES 5000 power amp, audiophile grade passive components, less 5 W resistors. $\$ 20.00 /$ channel, including post. P. Roberts, One Training Group, Sanananda Barracks, Wacol 4076 Qid. (07) 2710281.
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SELL: CRADLE-TYPE RELAYS, $48 \mathrm{~V}, 110 \mathrm{~V}$, $2 \mathrm{C} / \mathrm{O}, 4 \mathrm{C} / \mathrm{O}$ contacts. Total of 64 relays. All brand new, Potter \& Brumfield brand. V. Savaris. (07) 2651259.

SELL: BACK ISSUES ELECTRONICS MAGAZINES, Australian and overseas, from 1957 (Amateur Radio Action, CQ, Electronics World, ETI, etc). Over 300 issues. $\$ 1.00$ each or the lot, $\$ 150$. David. A.H. (03) 7073281.
SELL: VZ300 COLOUR COMPUTER, used once. All original accessories and pakaging. $\$ 80$ ONO. BEETHOVEN MICROBEE SOUND SYNTHESISER in good condition, with software. $\$ 80$ ONO. Phone Douglas (02) 5608348 , afternoons.

WANTED: ROCKWELL AIM65 with LED display, printer and full keyboard. Ring Sydney, (02) 5021029 A.H., ask for Michael.


AN ACQUAINTENCE of ours works as a specialist consultant in the medical electronics field. He designs, has manufactured, commissions and services some very sophisticated patient monitoring systems and medical laboratory instrumentation. Lovely equipment it is, just bristling with knobs, dials, flashing LEDs, with probes and sensors for placement in, on over or around various bodily orifices or appendages as appropriate.
He says he gets that "warm inner glow" whenever his machines 'wake up' with a spectacular display of urgently winking lights, rolling LCD digits, discreet beeps and similar indications as a patient drifts out of a deep coma to semiconsciousness. I think, thank goodness the poor patient is too groggy to be aware of the machine's neurotic freneticism. else they'd want to slip back and leave the worries of the frantic world behind! But I digress.
Said acquaintence was launched on his career at about age 10 when he explored behind the family radiogram. A
wonderful thing of design and craftsmanship it was. The sort of thing you see in inner-city "junque" shops these days. It had a wood-veneered cabinet (with an open back), stood as tall as he did, proudly boasted a Rola ('the finest loudspeaker in the world") $12^{\prime \prime}$ speaker and sported a large square, glass-fronted dial with a large wire pointer that moved behind the stations painted on the rear of the glass. Did I digress again?
Anyway, in the rear he pushed, prodded and poked until he gained some sort of response - a little hum as he touched the grid cap of the low level audio stage. Lifting the chassis, he prodded underneath. Aha! much more response - a healthy 'bite' from the hundred volts or so of high tension! ("Ooh, mummy look at all the pretty coloured stars'!?
Drawn by the commotion, his mother admonished him with "Richard! Don't you know yet not to push or poke things when you don't know what they do?!'
Undaunted, Richard went on to a career in electronics engineering. When the solid-state revolution swept through, Richard saw an opportunity in medical electronics, having spent some student vacations servicing X-ray machines. He found the field lucrative.
A short while ago, he acquired a long-lusted-after possession. A fire-engine red Porsche. Cherishing his acquisition, he designed, constructed and installed a 'smart' burglar alarm. Richard does not speak about its principle of operation as his patent attorney has the papers. Rumour has it that this alarm can 'smell' the owner and alarms itself even on 'unfamiliar' passengers. (No, it's not a Queensland blue heeler).

A short time after acquiring the Porsche, Richard acquired a new girlfriend. We'll omit a description to avoid embarrassment. Let your imagination have free reign. (Aahh, Norman Lindsay. why did you leave us in the lurch?)

One evening, Richard and friend went out for a late-ish dinner. However, when picking her up, he warned her that he'd have to make a few short calls before getting to the restaurant. Call number one was to his inner-city service centre and took 15 minutes. Friend cooled her heels in the car. Call number two, at a largeish inner-city public hospital 15 minutes away, took about 20 minutes. Friend impatently cooled her heels in the car. Call number three was to a small, quiet private hospital in a leafy peninsular suburb on the harbourside. Richard parked in the visitors' car space out front. All was quiet, only a discreet light at the entrance glowed gently in the darkness.

Searching for something to read, to while away the increasingly lengthening 'calls', friend opened the glove box. "Maybe I can read the street directory," she muttered between clenched teeth. Inside, there was no road map, but she spied a pushbutton. She pushed it . . . no response. She pushed it again ... no response. Presently, a light on the dash began to flash on and off about twice a second. Quickly, she opened the glove box and franticly pushed the button again. Too late! Honk-Honk-Honk!, went the horn as all the car's lights flashed on and off in unison.

Charging outside, Richard found friend cringing in her seat in front of the open glove box. It all flashed back in his mind's eye, the day he explored behind the radiogram, as he tersely echoed his mother's words, "Don't you know yet not to push or poke things when you don't know what they do?!'

## Galah discovers Viatel

We just knew that story about Galah computing wouldn't go away!
The Chief Galah got on the blower the day the November issue came out. "Great stuff," he said, "but let me tell you about the latest product our R\&D team's working on." (Loud editorial groans - press releases should be submitted on typed paper, not phone in. The editor's shorthand has RSI).
It seems the Chief Galah splurged on a Viatel terminal and paid a sub. to Telecom to see what it was all about. (Expensive curiosity, he has). Well, after exploring Microtex on page 666 (games, user bulletin boards, etc), booking his holiday in Surfer's and checking out the overdraft in his Commonwealth Bank joint account (maybe he's into real estate and buys a lot of joints? . . .), Chief Galah returned to his newspaper clippings file for press releases on Viatel. He says they're more informative than the real thing - and cheaper, too.
He found one that said, eventually, you'll be able to do the family's weekly shopping through Viatel. That's when he had his inspiratorial (combination of inspirational and conspiratorial) flash. Jusi what every home needs - an RS232 grocery interface capable, in the first instance, of digitising chicken slices and margarine tubs and reconstituting them at speeds of 1200 bauds! 'I'll make a fortune!," he speculated.
"Just think," he enthused, spitting into the 'phone, "I'll make another fortune selling my next model when MacDonalds becomes a Viatel service provider - it'll transmit and reconstitute whole hamburgers!" \&

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[^0]:    *If you would like to inspect either of these oscilloscopes, call into our office any weekday during business hours. We're located at: WB Building, Cnr Fox Valley Rd and Kiogle St, Wahroonga NSW. The entrance is in Kiogle St.

[^1]:    *Back issues are aruilable for \$3.60. post paid anywhere in Australia, A\$4.60 to PNG or NZ.

[^2]:    ## AEM4500 MICROTRAINER DEMO SOFTWARE SIMULATOR

    If you're a student learning about microprocessing, or a lecturer teaching microprocessing, or just an interested hobbyist, you might be interested to know that Bill Thomas has written a software simulation of the AEM4500 Microtrainer project (Sept. 1985 issue) that runs on the IBM PC.

    The software displays the relevant circuit on-screen to show both 4 -bit and 6 -bit operation. The on-screen display exactly emulates the operation of the Microtrainer, including input switch operation and all the indicators so that you graphically see everything that happens following input of data and clock toggling. All it costs is just:

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    Just send us a blank $51 / 4$ "disk and we will provide you with the Microtrainer Demo file and documentation. Cheques or Money Orders should be made out to Australian Electronics Monthly. Don't forget to include your full name and address. Send orders to:
    AEM Microtrainer Software Simulation, PO Box 289, Wahroonga 2076 NSW.

[^3]:    Figure 8. Receiver Main/Back Channel State Diagram

[^4]:    Benchbook is a column for circuit designs and ideas, workshop hints and tips from technical sources of the staff or you - the reader. If you've found a certain circuit useful or devised an interesting circuit, most likely other readers would be interested in knowing about it. If you've got a new technique for cutting elliptical holes in zippy boxes or a different use of used solder, undoubtedly there's someone - or some hundreds - out there who could benefit from you knowledge.

    We'll pay from $\$ 10$ to $\$ 100$ for each item published. Send your gems to 'Benchbook', Australian Electronics Monthly, PO Box 289, Wahroonga NSW 2076. Please include your postal address for publication with your item(s).

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