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# Electronics Today

FEBRUARY 1985

FEAT	URES
Made in My Image The revolution in communication using images	16
Special Survey: A Closer Look at CROs The latest models and what they can do	40
Keeping the Message Clear IBM's constrained coding technique for recording, transmission	81
Computerised Circuit Analysis Take the sweat and guesswork out of designing amplifiers	103
PROJ	IECTS
ETI-342: Pulse-Shaped CDI System All of the benefits of CDI, without the problems	62
ETI-170: Precision CRO Calibrator Low cost unit lets you check voltage and time settings	74
ETI-742: 'Speech-Tailored' Communications Speaker Tailored response improves readability!	83
ETI-666: Parallel Printer Switch No more plug swapping to change printers	92
RE	VIEWS
NAD 7155 Stereo Receiver The emphasis is on performance, rather than looks	29
JVC GR-C1 Video Camera/Recorder The VHS answer to the Betamovie!	36
Powersonic NiCad Batteries Just what can you expect from modern NiCads?	48
Commodore Plus/4 Computer	119

#### Casio Calculator, Keyboard and Computer Offer ETI Mail Order Books Benelec Digital Multimeters Offer

8

23

53

58

88

109

98

**News Digest** 

Sight & Sound News

New Equipment

New Components

**Computing News** 

**Communications** News

Ideas for Experimenters

**OFFERS** 71 89,90-91,118 97

## DEPARTMENTS

99
114
116
118
120
122

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#### EDITORIAL

#### ADVERTISERS' INDEX

Acme	70
Active Electronics	100,101
All Electronic Components	
Altronics	52
Applied Technology	6,7
Aust. Government	19
Aust. School of Elect	96
Data Parts	
Delsound	
Dick Smith 14,15,46,	
Disco World	
Electronic World	120
Ellistronics	112,113
Elmeasco	OBC
Emona	45
Emtronics	110
Energy Control	21
Hewlett Packard	34
Hi-Tech Light & Sound	
Jaycar	
Leisure Sound	110
Lemic	59
Mayer Krieg	67
Micro Analog Gen.	59
Microtrix	94
Parameters	IFC
Philips	18
Pioneer	35
Prepak	
Rod Irving	
Rose Music	
Soanar	
Scientific Devices	
Scope	
	20
Sony	
Swann	
Sydney Opera House	
Vanfi	
Len Wallis	
W.I.A.	
Geoff Woods	

**COVER:** Design by Ali White; Picture by Greg McBean WHAT'S THIS — a different face on the editorial page? Sorry about that, but at least my face mightn't be too unfamiliar to many of you. Perhaps you might remember it from the past, when it appeared regularly in 'another wellknown electronics magazine'.

known electronics magazine'. Normally I wouldn't inflict myself on you, because good Managing Editors are supposed to lurk silently in the background. The reason for this current intrusion is that we've had a few changes here. Our old friend Roger Harrison has moved on, and for the last few weeks I've been manning the tiller of the good ship ETI myself. Seems like old times!

Fear not, though, you won't have to put up with my visage for long. From



next month's issue, ETI will have a new Editor. We've been able to appoint David Kelly, who until very recently was Editor of the highly respected trade and professional journal 'Electronics News'.

David is very talented and enthusiastic. I'm confident that he'll be able not only to maintain ETI's existing high reputation but also to take it to even greater heights. So from next month, look for David's smiling face and words of wisdom on this page — plus even more goodies than before in the rest of the mag.

Talking about ETI itself, you've no doubt noticed another change already. We've gone back to having the full list of contents on the very front page, where most people seem to prefer it. This also saves some space, so we'll be able to fit more 'meat' in each issue.

Over the next few months you'll see a few more changes to the magazine. Nothing drastic; just things like simpler layouts, cleaner typestyles and a return to staple binding.

We're making these changes for two reasons. One is to make the magazine easier for you to read. The other is to try and contain our production costs, which have been rising alarmingly.

All in all, we're confident that these changes will help make ETI even better reading and value for money than it has been. So stay with us — you'll be glad you did.

Jim Rowe Managing Editor

### SERVICES

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The smartest new development in the Microcomputer World is the new high density low cost 3.5" Disk drive. Already standard on many 'big name' systems the 3.5" disk has the same or more capacity than the standard 5.25" disk, but occupies only 30% of the volume.

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When you buy a microbee personal computer rest assured you have made the right decision. Your microbee will never become obsolete - on the contrary your microbee is fully upgradeable and can grow from 16K to 32K to 64K and even to 128K. Disk drives can be added as can a whole host of useful and interesting peripherals such as printers, plotters and modems. Whatever level you choose to start at your microbee will grow with you providing challenges and encouragement along the way.

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# ANZCAN — the new link

Long distance cables are not dead. Now that the earth is girdled with satellites one might be forgiven for thinking that they should be, but the opening of ANZCAN proves that there is still a place for old solutions.

The 14 000 km undersea communications cable was launched recently with a conversation between Communications Minister Michael Duffy in Sydney and his opposite number in Vancouver, Marcel Masse.

A message from Queen Elizabeth was beamed via satellite to simultaneous opening ceremonies in Sydney, Auckland, Suva and Vancouver.

The big advantage of cables over satellites for telephony is that the cable eliminates the time delay caused by the tremendous distance up to the satellite. This delay is unavoidable, since it's caused by the finite time taken for the radio waves to travel up and back.

Even at the speed of light, this delay amounts to some quarter of a second. It gets longer if there is more than one satellite path in a particular circuit, as there would be with a circuit going around the world, say from Australia to Europe.

Another advantage of undersea cables is that they last longer. ANZCAN is specified to last for 25 years. Aussat is specified for only seven. The main reason for this is that satellite power systems fail over time as the efficiency of solar cells goes down.

ANZCAN runs from Sydney, via Norfolk island and Fiji to Vancouver with a spur cable running from Norfolk to Auckland. At maximum capacity it can carry 1300 telephone circuits. Total cost was \$400 million. The cable is owned by a 14 nation consortium and replaces the COMPAC cable, which could only carry 80 circuits.

The repeaters for the cable were built in Sydney's western suburbs at the STC plant in Liverpool. The cost of STC's contract was \$39.2 million. STC had to spend \$10 million to build a clean room for the manufacture of the repeaters.

The manufacture of such repeaters is an exacting task, ranking with satellite and other advanced technologies. Essentially, the specification calls for a package of amplifiers capable of giving maintainance free service on the sea bed for appoximately 25 years. Failure of any of the nearly 1000 repeaters on the cable would completely dissable it, so special care needs to be taken during manufacture. Redundancy is also built into the design, using the philosophy developed for space craft circuits.

With ANZCAN, Australia became only the fifth western nation with the capability to manufacture repeaters, along with the US, Britain, Japan and France.

STC has won the contract for the construction of repeaters for the next big cable out of Australia, the AIS cable, linking Australia, Indonesia and Singapore.



# Swedish niche for the 'Bee

A fter some three months of intensive testing and evaluation by the Swedish School Department it was announced that Microbee is to be one of the very few approved computers used in Swedish schools.

Requirements for getting approvau are extremely demanding and even brand names such as Apple and Apricot and Sinclair failed to fulfill them.

Among the approved computers Microbee was by far the most cost efficient. This will, no doubt, echo strongly and positively throughout the rest of Europe. Well done Microbee.





The International Trade Fair for New Technology is to be held in Ghent, Belgium, from 25 February to 3 March 1985 inclusive. This fair was initially held in Ghent in May 1983, and its outstanding success ensured worldwide participation for 1985. The three cornerstones of the fair are

micro-electronics, bio-engineering and new materials.

Arlec Pty Ltd has now appointed an agent for north west New South Wales. Redman Agencies which is based in Newcastle will have commenced operating as the Arlec Agent from October 1, 1984. They will stock the full range of Arlec products, servicing customers from Woy Woy to Grafton and west to Forbes. Customers are invited to contact Redman Agencies at 11 Hall Street, Newcastle NSW 2302. (049)25896.

# New video projection system

A new, versatile colour video projection system, the National TC-10010 PSN, has been launched in Australia by GEC. Designed for front and back projection from a ceiling or bench mounting, the unit will reproduce PAL, SECAM and NTSC (4.43) VTR signals onto a range of flat or curved screen sizes from 1.3 m (50") to 2.5 m (100") diagonal. Each projector comes complete with a fullfunction remote control unit.

The new projection system is being marketed by GEC Australia Limited Video Systems Division. They believe the versatility of the TC-10010 PSN makes it ideal for presenting to small or large audiences in a variety of settings whether at conferences, exhibitions or sales/training seminars.

The TC-10010 PSN has been designed to automatically detect PAL, SECAM or NTSC (4.43) input signals and reproduce them in the correct mode. If automatic detection is not required, the unit can be preset for any one format.

However mounted, the new National projector is claimed to give a high quality picture with flat or curved screens from 1.3 m (50") to 2.5 m (100") diagonal image size. Three special high-brightness 7" projection CRTs and f 1.1 lenses give a high resolution of 450 lines and luminous flux of 170 lumen. A complete built-in cross hatch generator is included for precise convergence adjustment without the need for external sinc.

Each projector comes with a remote control unit (pre-wired to 1.5 m but available with optional 15 m, 30 m and 50 m cables) for routine alignment and on-the-spot adjustment. Power on/off, brightness, colour, contrast, vertical hold and static convergence can thus be conveniently controlled, re-



New National video projection system designed for front or back projection, and celling or bench mounting.

gardless of the projector mounting position.

The National TC-10010 colour video projection system is avail-

able from GEC Video Systems Division at 2 Giffnock Ave, North Ryde NSW 2113. (02)887-6222.



#### "... FEEL THE WIDTH!"

IBM (East Fishklll, N.Y.) has announced the production of experimental 8" (203 mm) diameter silicon crystals, the largest crystals ever produced by IBM.

The East Fishkill site, one of IBM's most advanced semiconductor and packaging manufacturing and development facilities, houses the industry's largest crystal pullers for producing silicon crystals from which computer chips are built.

Standing more than two stories high and weighing nearly nine tons, each of the crystal pullers regularly manufactures crystals that yield 5" (127 mm) wafers. The wafers are eventually processed and cut to produce individual computer chips.

The crystals are manufactured through a number of complex processes. Chunks of purified silicon, derived from common sand, are reduced to a liquid in temperatures exceeding 1400°C, 25 per cent as hot as the sun. At this point, a silicon 'seed' the size and shape of a pencil, is dipped into the molten silicon. The seed is then lifted slowly, pulling with it a trail of molten silicon which hardens to form a crystal.

The crystal column is drawn up into the cooling chamber of the crystal growing machine and eventually removed. The diameter of the crystal is determined by the temperature of the molten silicon and the speed at which the seed is lifted. The entire process takes about 24 hours.

The finished crystal is ground into a solid cylinder and sliced into wafers about 2 mm thick. The wafers are pollshed to a mirror-like shine in preparation for chip fabrication. Then the wafers are subjected to hundreds of manufacturing processes that build computer chips on their surfaces. The chips contain thousands of electronic circuits.

Five inch wafers hold nearly 850 chlp sites. The experimental 8" wafers have the capacity to hold more than 2000 chlp sites, with an expected Increase in productivity and reduction of overall cost per chip.

## **NEWS DIGEST**



## Arianespace to launch Aussat

The Government's satelliteowning company, Aussat Pty Ltd, is formally to sign a \$A24 million contract with the European company Arianespace to launch Aussat's third domestic satellite.

According to the Minister for Communications, Mr Michael Duffy, and the Minister for Defence Support, Mr Brian Howe, Australian industry and OTC (Australia) would receive orders for work and services valued at about \$18 million through offsets commitments agreed to by the Commonwealth and Arianespace. Offsets work would include the manufacture in Australia of aluminium and steel forgings and castings valued at between \$7.5 and \$9 million for the Ariane family of space launch vehicles; and this will include, if required, the transfer of appropriate technology to Australian companies to enable forgings and castings manufactured in Australia for the Ariane launch vehicles to meet the stringent quality con-trol requirements for space vehicles.

The Ministers said Arianespace would also arrange the placement of orders on OTC worth \$8.5 million for tracking and telemetry services at Carnarvon and Perth between 1985-1996 with possible extensions.

The contract provides a

launch window extending from mid 1986 to mid 1988. The arrangements under which the third launch can take place in mid 1986 are consistent with the Government's recently announced initiative to provide commercial direct broadcasting to the remote areas of Australia at the earliest possible date.

Arianespace will use an expendable Ariane three-stage rocket to launch the satellite. "The Ariane launch site, located at Kourou in French Guiana, close to the equator enables the satellite to be placed directly into its geostationary transfer orbit 36 000 km above the equator with considerable savings in the fuel carried on board the satellite and thus increasing the life of the satellite by more than two years," said Mr Gosewinckel, General Manager of Aussat

Arianespace is a private European commercial consortium formed in 1980 and jointly owned by thirteen major European banks, thirty-six European aerospace and electronics manufacturers, and the French National Space Agency.

For further information contact Mr W G Gosewinckel, General Manager Aussat, (02)238-7800 or Mr Leighton Farrell, Manager — Public Affairs, Aussat, (02)238-7892, (02)683-1261 (ah).

# New aids for making pc boards and labels

Melbourne manufacturer Sesame Electronics is now producing three inexpensive pieces of equipment to assist in the manufacture of pc boards in the small lab or at home.

The Maxilight, the Etching Station, and the Squarecutter are respectively an ultra-violet light exposure box, a quick means of etching a pc board, and a simple way to cut blank material into rectangles.

The Maxilight is supplied in kit form, and full assembly and operation instructions are provided. It will develop artwork up to 150 x 180 mm, and is priced at \$139.

The Sesame Etching Station comprises an adjustable heater/ blower mounted on a base, directing hot air into a plastic dish that contains the pcb and the etching solution. The heating of the fluid, and the moving currents produced by the fan, greatly speed up the action so the etching is completed, hands free, in about 10 minutes. The Etching Station is priced at \$39.

The Squarecutter will enable a pcb to be cut into a rectangle without the bother of squaring, scribing, and filing. To use it you place the material on the platform and feed it into the saw, then turn it against the perpendicular fence and repeat the cutting. A rectangle is quickly produced. The Squarecutter is priced at \$39.

All prices quoted include postage. For a brochure containing more information contact Sesame Electronics, PO Box 452, Prahran, Vic 3181. (02)527-8807.



# Robot makes 'phones

Standard Telephones and Cables Pty Ltd (STC) has installed a robot at its manufacturing plant in Alexandria, Sydney, to do work previously performed by an operator in the manufacture of telephone handsets.

The robot is a 5-axis ASEA Rb 60, which STC selected because of its absence of hydraulics, its quietness and its physical size.

While STC has always been in the manufacturing forefront and has installed at its Alexandria plant many items of equipment employing new technologies aided by various pick and place robots, the ASEA Rb 60 is the company's first application of a large scale universal robot.

Installed in the plastics moulding division, the robot forms part of an integrated system. Before the robot was introduced. each moulding machine was attended by an operator, who was required to precisely locate the removable core pieces, maintaining a high degree of precision throughout the shift as well as perform the arduous task of removing the mouldings, operating the guarding system and performing supplementary operations. The robot has now taken over those tasks and performs ten functions in one complete operating cycle rearranging and manipulating the required objects.

The complex is safeguarded by a series of sensors which protect the tooling and equipment against operator mislocation of the core pieces or any malfunction of the moulding machines, robot or rotary table.

# NOW a Production Quality 20W Iron that air cools your fingers.

SAFETY STAND MODEL STS 2

> SCOPE COMMODORE MODEL PH20 370°C\* 240VAC.

 Designed for continuous production line use
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			HIGH-FR	EQUENCY HO	RNS			
Model	Туре	Low-Freq Limi		ntry Diameter or Adaptor Required		Dimensions (cm) (Height x Width X Depth	Weight	
2028	Exponential	1200 1	lz 1.8	cm (Use 1034 Drive	er}	12.0 x 28.0 x 18.5	0.95	5 Kg
2040	Exponential	800 1	lz 2.54	cm (Use 1044 Drl	ver)	16.5 × 39.2 × 19.0	1.6	K
2052	Exponential	500 1	lz 3.56	orm (Use 1072 Driv	ver)	19.6 x 49:4 x 24.0	3.4	K
2042	Exponential	800		5 (Use 1044 Driver 5 (Use 1072 Driver		17.0 x 40.2 x 19.6	2.0	K
2048	Sectoral	800 1		5 (Use 1044 Driver) 5 (Use 1072 Driver)	22.0 x 47:5 x 34.4	5.2	ĸ	
2060	Sectoral	500 1		5 (Use 1044 Driver, 5 (Use 1072 Driver	26.6 × 60.0 × 39.5	7.6	K	
2070	Multicell	1001		3835 (Use One 1072 Driver) 3870 (Use Two 1072 Driver)		32.0 x 73.5 x 46.5	16.4	K
		al allowed	HIGH-FRE	QUENCY DRIV	ERS			
Model	Nominal Impedance	Power Capacity	Frequency Response	Sensitlvity (dB/1m, 1W)	Voice Coll Diameter	Dimensions (cm) (Diameter x Depth)	Weigh	ę
1034	8 ohms	50 Watts	1200 - 18000Hz	102 dB	3.44 cm	10.8 × 6.6	1.7 kg	
1044	8 ohms	75 Warts	800 - 15000Hz	104 dB	4.44 cm	12.6 × 8.1	3.6 kg	
1072	8 or 16 ohms	100 Watts	500 - 12000Hz	106 dB	7.20 cm	16.3 × 9.2	7.0 kg	
			N	ETWORKS				
Mode!	Crossover Frequency	Impedance	Power Capacity	High Fred Attenu		Dimensions (cm) (Height x Width x Depth)	Weigh	e
4012	1200 Hz	8 or 16 ohms	100 Watts	0-4-8-12 0	B, switch	16 x 8 x 7.0	0.5 Kg	
4008	800 Hz	8 or 16 ohms	150 Watts	0-4-8-120	IB, switch	20 x 10 x 7.0	0.9 Kg	
4005	500 Hz	8 or 16 ohms	200 Watts	0-4-8-120	B switch	19 x 15 x 8,5	1.4 Kg	

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# Music computer to win

Sonics, Australia's magazine for music-makers, is running an interesting competition in its January/March issue. The prize is Yamaha's new MSX computer, the CX5M, plus a 49-note music keyboard, three software packages and a Taxan Vision-EX colour monitor, worth over \$1600 in total!

As well as being an MSX-standard computer with 32K ROM and 48K RAM (16K VRAM), the CX5M incorporates Yamaha's 'FM voice generator' chip, which provides eight octaves of high-quality sound and the ability to produce eight different voices at once. In other words, this computer is a real music-making tool.

The accompanying software is very useful too. An FM Music Composer allows you to write music onto on-screen staves from either the typewriter or the music keyboard in up to eight 'parts' with full control of time and key signatures, tempo, dynamics and phrasing. Compositions can then be played by the CX5M, or by up to eight other instruments equipped with MIDI (the Musical Instrument Digital Interface) under the control of the CX5M.

An FM Voicing program allows you to edit and alter the pre-programmed voices or create totally new ones; and a Music Macro program adds a special set of commands to the MSX BASIC language, permitting control of the FM voice generator from within BASIC programs.

The Taxan/Kasa Vision-EX monitor has been kindly donated by Colex Australia, and is an excellent medium for reproducing the CX5M's comprehensive 16-colour graphics, although a conventional colour TV can also be used.

Entry forms are to be found in the January/March issue of Sonics magazine.

# Defense fairs to boost Australian exports

A recent successful series of Australia-United States defense contractors fairs held in five mainland capitals during November last year are expected to lead to Australia's defense industry becoming a significant supplier of military spares and electronic equipment to the US Defense Department and its prime contractors.

Some 70 US participants, representing logistic support agencies and contractors, visited Brisbane, Perth, Adelaide, Melbourne and Sydney seeking Australian suppliers for goods from metal castings to printed circuit boards for electronic equipment. The Americans carried actual bid packages to discuss with Australian businessmen. The US defense department budget for military spares alone this year is worth \$US30 billion. "Judging by the enthusiastic response from Australian industry representatives, the US visitors did not re-turn home disappointed," Mr Howe, Minister for Defense Support said.

This was underlined by the overall attendances at the fairs. Some 718 Australian defense industry senior managers, representing 460 separate companies, attended the series of one-day fairs. Of these, 325 companies lodged serious inquiries concerning contract opportunities with the United States Defense Department (which has a 1985 budget projection of some \$272 billion)

Mr Howe said the fairs had done much to show the US Defense Department and its prime contractors that Australia possessed skills which could be used for sound commercial reasons. "The US visitors were clearly impressed by the capability of our defense industries, particularly the prices and short lead times being quoted," he added.

The US Secretary of Defense, Mr Weinberger, has ordered a wide range of reforms in procurement practices, and US Congress also recently enacted the most significant changes in US procurement laws in over 40 vears

Mr Howe said the reforms paved the way for more competition from overseas suppliers for US Defense Department contracts.

# More bytes

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#### That's the new Bondwell 14 Portable computer: EXCLUSIVE to Dick Smith Electronics

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### Look at what else you get:

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# MADE IN MY IMAGE

A little philosophising on the coming high-tech revolution. Imaging is as old as evolution, according to Malcolm Goldfinch. Imaging is the reproduction of something in another form. Thus, DNA molecules are a form of imaging; animal cries, speech, writing, painting, photography, computing, video - all involve imaging, no matter how indirect the method may seem. Intrigued? .... read on!

### Malcolm Goldfinch

FOR INVESTMENT analysts in Wall-Street, Gnomes in Zurich, Shogun of the Yen in Tokyo, the new 'buzz-word' is imaging. At last we appear to have a word that encompasses the whole field of endeavour which high-tech electronics is principally about. For humanity, imaging has been a fundamental achievement of gods. Just flip back the pages of the Bible to Genesis and find on the first page, "And God said, 'Let us make man in our image after our likeness So god created man in his own image male and female he created them'.

There is no denying, by either creationalist or evolutionist, that imaging is the fundamental process for the continuation of life as we know it. Only recently has it been revealed to us that the reproduction of the cells that make up our body and all living matter, is the result of the chaining together in double helix form, simple substances, like nitrogen, phosphorus, sugar, to form the DNA molecule. DNA is the original imaging device that self-replicates. It is able to do this by following a long series of coded instructions contained in its helix; not unlike a computer tape. The human brain can now 'peak' and understand these chromosome codes, and we have the ability to 'poke' new bits into them by biological engineering, thus changing the very patterns of life. It seems incredible that we still use the term 'biological engineer' to describe someone bashing bits of code in and out of a chromosome helix in a DNA molecule. Perhaps 'imagineer' would be more acceptable.

That word 'imaging' has bugged me for

some months. It was used in the Economist. by a Polaroid press release, and an investment analyst in the US trying to de-tech and group together a lot of different high-tech goings on that seemed to turn out the same sausage. I thought it was valid and it looks more useful each day.

#### A hard look at imaging

If imaging is the critical basis of our present existence and progress it is worth examining in detail. It comes in two forms just like computer data, 'real-time/on-line' or 'timeshift'. Probably the first form of real-time or 'now' imaging, was a flipper, wing, arm, or leg, waggled in a certain manner when dan-ger threatened. Possibly the original waggler died a squillion years ago, but those of the tribe who interpreted the waggle as danger and took off, survived to reproduce other wagglers, who also saw and imaged danger by a waggle. Those who did not were mostly eaten and never imaged themselves. It is not difficult to project evolutionary progress to show that grunts and screams allowed even more survivors than waggles did; language started to describe the threat, and so on. Speech was the only real-time imaging generally used on earth for some millenia, but with the imaging explosion in the 1800s there has been rapid progress; telegraph, telephone, radar, to an early-warning missile system - the current ultimate in real-time imaging!

Sadly, time-shift imaging only goes one way at present. Forward, it would revolutionise punting. Time-shift imaging probably started when some form of life noticed that if you drank from a pool at a certain spot, you got pulled in by the nose and disappeared. Probably it was a humanoid that first started to sketch, with a stick burnt in a bushfire, a drawing of an alligator with a stick-figure in its mouth. This imaging of danger was stored in time until the carbon washed off the rock. Of course, those who drew and understood such danger signals spread their DNA codes through life much more than those who became a croc's dinner. If you also put this situation on a personal computer with a program for 'future' projection, you could find on the screen a high-resolution graphic of a couple watching an XXX blue-movie in colour and stereo-hi-fi sound, on a video. Odds are this couple will do more to spread their DNA code in future generations than a couple without a video and playing tiddlywinks. The tiddlywink players will probably be consumed by boredom before going to bed. Video is the present in ultimate in time-shift imaging.

In real-time imaging we have sounds, speech, signs from the body as primary sources. As long as you agree that a timeshift of less than a second is not a time-shift of mental significance, we may include telegraph, telephones, television and all their permutations which include computer data processing within this real-time span.

In the time-shift mode dancers image past behaviour; artists image past sight; singers, musicians and actors image past sounds; writers image past thought. This last time-

### SIGHT AND SOUND



The phonograph was the first major advance in capturing the 'real' audio image.

#### TOWARDS A PERFECT IMAGE

Most of us in the business of bashing electrons on their way are likely to be considered part of this great imaging industry. Just how did it overtake us? It all started in the last century from a number of different directions.

The first reality imaging was called 'photography' (light-writing). Mande Daguerrre and William Fox Talbot were the prominent pioneers. In the process a silvered copper plate was exposed in a camera and was developed by depositing mercury fumes which amalgamated with exposed areas. Reflective and non-reflective areas against a dark background produced the photograph image.

We all know how later, others put photos together and we had the first, moving reality Imaging, in time-shift, to store contemporary life; the reality, not a painting or a series of words. Such early imaging is now part of our priceless archives.



Daguerrotype stereo pair coloured Images from 1853. They produced a lifelike stereo image when the case in which they were mounted was folded out.

The next step was telegraph imaging, then telephone imaging, both in real time. Then an historic time-shift by Thomas Edison, imaging of the voice, music and any sound. It was a purely mechanical device that is now electronic.

Photography is also halfway across the chemical river into the electronic world. Typewriters, telex copylng machines, fax, data storage (digital-photographic), TV, video, music synthesis, hi-fi, PA, calculators, computers of all type.

All this may be a longshot from my Editor's request for a steer as to where video is going, but context in writing is somewhat similar to Einstein's fourth dimension. Without this it would be ridiculous for me to say what I wanted to at the beginning, "In the 1980s, humanity achieved its greatest emancipation through the perfection of imaging". As a rider I would add, "The advent of digital video is the final and most important link in the imaging chain".

#### SIGHT AND SOUND

shift, of writing, and to an extent drawing and dancing, is concerned with the time shifting of thought or as I prefer it *consciousness*, from the Latin 'conscius', com = with + scire = to know.

My dictionary defines philosophy in part as, "The rational investigation of being, knowledge, and right conduct". Surely this is almost a synonym for the definition of 'consciousness', excluding "right conduct" (very contentious). All this is pertinent to imaging because it shows that writing as a means of imaging is a very blurred medium and the mental images from written codes will vary from person to person, yet writing images was all mankind had to shift consciousness through time. Consider how much better we would have understood the philosophy of, say Shakespeare, if he was now to read to us, passages from his plays with inflections of speech, gestures and facial expressions, using the time-shift to perfection on any home video.

Perhaps now, or in the near future, we will have a brilliant bard who can do this for posterity. Once etched into a digital glass disk, such imaging would be there in archives for all time. For thousands of generations the full image of the human consciousness projected by this person would be handed down, crystal clear and with less room for interpretation to twist and blurr real meanings.

The blurring of past time-shift, both in language and the symbols of writing, could have been a prime cause of human conflict, between tribe or nation. Where there is a grey area, the interpreters ply their trade. A study of history shows most wares were justified by a consciousness or philosophy which one tribe wished to impose on another, against their will.

Today computers can interpret languages with agreed definitions. Video can record

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and convey the spirit and meaning of agreement. There should be little room for manipulators of past images of fact and philosophy.

#### How video imaging impacts

Imaging by video is of course much fuller than by writing. Words can evoke specific details and the rest of the picture is imagined through association or not at all. My grandson can say "I want it in video". Okay. Press a button, up comes, for example a horse walking. In the time it takes to read, "big horse walking" Grandson has now seen it all and in a flash, learnt what it really looked like; not his mental image with all errors and defects. He saw, not only a big horse walking, but also, that it was a rhone stallion, white sox on two fore and one hind leg, steaming because it has been galloping in a valley on a frosty morning and making a great whinnying noise.

This is the reality image. We now live in a whole new world of realism. You may like our dream world of the past but reality is here to stay. There is a million years or more between the first written imaging and this latest high-tech, reality-imaging in video.

If this is what imaging is about, then world financiers are right to regard most electronics based industries as part of this new wider field and want to invest and participate in the action. We technocrats may have missed the mainframe because we were looking at the chips, but suddenly we find ourselves in the new world of reality; be it real-time or time-shift.

#### **One stop imaging**

For years we have had vacuums to clean, washing machines to wash, automobiles for mobiling, but we have had to have a large number of devices to do our imaging. The camera, phonograph, radio, TV, hi-fi, tape/cassette recorder-player, copying machine, microfiche . If the people in these industries could now see themselves as all part of the faculty of imaging, and instead of working in hundreds of different directions they work towards providing the best common means of imaging, then this new era will really have arrived. The consumer could be happy with one imaging device for all these functions.

No matter what governments or multi national organisations plan for the marketplace to-day, the consumer is dictating, worldwide, what they will and will not accept. There has been a large number of ill conceived products foisted on consumers with the technological explosion of our age. Many have been caught with junk machines of a type or a format for which there is no film, tape, cassette, or parts and technology.

Quadraphonic sound was the first megabuck disaster and there are several consumer-dictated disasters of great magni-

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#### SIGHT AND SOUND

tude, either taking place or likely to, in video/tape, video/disc, video/cameras, film/camera's and perhaps most of all in computers. Consumer dissatisfaction has resulted from a lack of awareness by some manufacturers as to where all this new technology is heading. Consumers are not going on a trip to anywhere.

# Specifications for imaging centre

The vision I see for the consumer in the immediate future, is a home imaging centre using a sophisticated dual, digital-disk system, with a disk size similar to the 5¼-inch, capable of multi-megabite digital storage and quick retrieval. It will have disk to disk copying and interchangeable drives also chainable to extra drives, and all with common inputs and outputs.

The home disk library will contain all the family still and video pictures, like a photo album, music, records or documents recorded on videocamera or fax, computer programs and data files. An analogue to digital, and reverse converter will be part of the system to interface with the telephone and or optic line. It will answer and image calls or give stored messages from voice recognition names to correct callers via computer control. The computer will be on line and in concurrency with the imaging centre at all times to take and send electronic mail and advise daily of appointments and birthdays.

All orders, receipts, income costs and expenses will be fed in and compiled into budgeted accounts and cash positions on line. All old analogue material will be digitally converted and imaged to disk for access as required. An optic cable with ports in every room will allow infrared remote control of the imaging centre from any convenient location, as well as the use of portable TV/computer hi-resolution colour monitors with infrared coupling to the fibre optic daisy-chain.

Hi-fi audio will be located with the imaging centre and remotely operated. Portable speakers will sense infrared digital audio input, from the fibre optic chain, and incorporate Carver type, high-power low-mass mini-amps.

Good news for the cleaners is no wire trails around the room. A powerpoint connection to monitor-TV and speaker-monitor will be all that is required. The computer will be controlled by an IBM-PC Junior type infrared keyboard. It is not a dream; all this exists now. Making it in lots of 100 000+ will correct the price.

#### **Examples of use**

Taking the vision further to using this imaging centre requires just a little 'imagination'.

Wanting hi-fi? Just select music from the computer databank by cursor or mouse from menu. Retrieve the imaging disks listed by computer, insert and wait for the transfer of tracks then remove from the imaging unit on computer instructions. Program now on disk 2. Return music disks to the library. Wheel speakers to the favoured location for listening, point infrared sensors to chain port, plug in speakers to any powerpoint, use controller to start selected program and continue control. Switch to FM/AM as desired.

Video? Repeat the process as for hi-fi and add portable video-monitor between speakers or wherever convenient. Replay selected discs for family or commercial video programs, family photo album, stored documents, select TV channels, optic cable programs, TeleText, etc.

Or perhaps portable video: use a normal camcorder such as the JVC GR-CJ and/or videocamera/VCR combination to shoot scenes and return to imaging centre and edit while replaying to imaging disk through analogue/digital interface.

The home computer to be used with the imaging centre would have memory in the order of 0.5 to one megabyte, always on line and loaded with a housekeeping program for home function, timing, reminding, phone messaging, electronic mail, database interrogation. Just program the menu and transfer control as described for hi-fi and video. All memory data would be saved to imaging disk and in a later housekeeping session would be transferred manually under computer instruction to correct library disks. Keyboard or other input peripheral sessions could be carried out in any location on the infrared optical chain where a keyboard and monitor are located. With multi-monitors concurrent computer games and keyboard operation would be possible without inhibiting housekeeping computing.

#### Conclusion

This futuristic home situation may sound as though it is for the very well-heeled. Think back five years. Only the very well-heeled were supposed to have a personal computer that could word process and print an acceptable letter, as well as play games. The idea of having a central imaging with one, two or more chained disk units that are made to a world standard and replace the plethora of imaging devices of different types we are using to-day, will rationalise the interchange of imaging as well as make a dramatic cost reduction.

Like hire-purchase deals it will be possible to start with only the optical chain around the home and add the imaging and peripherals as the finances allow. For the time being existing turntables, cassette decks, compact discs, VCRs, video disks, as well as computer DOS will be required to build up the imaging centre disk library. Amplifiers, TV or monitors and speakers will go on forever, but gradually order will come out of the present incompatible and duplicated chaos. The revolt against spaghetti-like tangles of wires, spread all over the carpets and shelves will at last be put down; or will it?

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## SIGHT AND SOUND NEWS

# Micrographics breakthrough from WA

A bout five years ago Perth businessman Gene Kostecki found himself with a problem more of us should have. He had a bundle of money, and nowhere to put it.

The traditional answer to his problem among Australian businessmen is to put the money in real estate, or to buy a smart accountant and put it at the bottom of the harbour. But Kostecki wanted to do something useful with his money. So he joined together with another businessman of similar mind called George Brown. Two bundles of money looking for a good idea. They found their idea in the

They found their idea in the person of Siegmund Raith, an electronics engineer with an interest in archival systems. Briefly, the situation he faced was that we live in the information age. It's an age when the accepted wisdom is that more information is necessarily better.

The problem is just this: how do you know what's available and where it is? Also, how do you store it and how do you get at it? Anyone who has been inside a decent sized library, especially a library orientated to research work, will understand what a problem this is.

The solution, in use for many years now, is microfiche or 'fiche' (pronounced 'fish'). A fiche is a small transparency, just like a photographic slide only bigger. Material can be photographed and then accessed by retrieving the slide and using a projecting viewer called a 'reader'. It's small, dirt cheap, easy to store, easy to use and given reasonable handling, lasts a long time.

The methods used to make and store microfiche were worked out about thirty years ago, and nothing much has changed since. Raith's idea was to take the whole microfiche system and apply modern computer techniques to it.

Four years and a million dollars later Raith and his co-workers stepped out of the laboratory with the fruits of their labours. the result was a camera with better resolution and greater flexibility than any other on the market.

At the centre of the Eagle Eye is a 6809 microprocessor. This controls the display, keyboard, servo motors and memories that give the camera some of its unique features. It gives precise positioning of the head (in the order of micrometres) to allow reductions from 1/8 to 1/36 in size. It also controls the positioning of the film, which uses a special vacuum system to align it precisely. The result of positioning the optical path so precisely (2.5  $\mu$ m overall) is optical resolution significantly better than any other camera available. According to independent assessments, the Eagle Eye can resolve 220 lines per millimetre with black and white film, and 180 lines per millimetre with colour.

The camera can intermix large maps or drawings with items down to postcard size. The infinitely variable shutter constantly adapts to conditions of illumination and glare to ensure uniform exposure.

The 6809 also controls an interface which allows electronic titling via a fibre optic link. It also controls data transfer between the camera and an external computer system. This allows documents to be titled by computer, then recorded and stored automatically. The information can thus be used to form a data base from which information can be recalled at will.

The question arises: why use micrographics at all, why not just store information on disc or some other electromagnetic medium, where it could be directly accessed by computer? The answer is just one of cost. A single fiche contains some 10-15 gigabytes of information, at a cost of less than a postage stamp. And it can be used for maps and other graphic displays that are very difficult to digitise.

Armed with their lovely new camera, Browne and Kostecki headed off to the USA to try and drum up some sales. Before they left they approached the Department of Trade, which lined up various meetings for them. Each of these led to other contacts. Eventually they stuck the mother lode. The giant aerospace company, McDonnell Douglas, looking for some local offsets to set against the F-18 fighter program with the RAAF, agreed to act as marketing agent in the US.

With an assured market in the US and expressions of interest from Asia and Europe, Kostecki and Browne have decided to expand the operation from a six person R & D operation into a proper assembly line to turn out about 250 cameras a year. Cost? According to Gene Kostecki you won't get much change from \$100 000. Sounds like the start of a healthy business enterprise!

# **Topline BASF tape**

**B**ASF has released its new chrome audio tape technology as the Chromdioxid Maxima II.

It combines ultra-fine Chromium Dioxide tape with a high precision cassette housing. According to BASF, the goldlabelled audio cassette performs better than the industry standard IEC II reference tape. It conforms to the reference in sensitivity and operating point, but excells in frequency response from 8 kHz, increasing the brilliance of the reproduction and leading to a higher sound transparency. The Maxima technology also balances possible errors in the cassette recorder, such as slightly worn recording heads. Maxima II's new patented cassette housing benefits from BASF's experience in plastics research and development.

In traditional cassette housings steel pins and axles are used to guide the tape and any deviation — as small as one fifth of one degree from the vertical affects the tape path resulting in audibly muffed treble sounds. But BASF's Maxima II employs the latest technology to mould plastic axles and pins with the housing to guarantee optimum vertical alignment and tape-to-head contact.

For more information contact BASF, 55 Flemington Rd, North Melbourne Vic 3001.

### SIGHT AND SOUND NEWS

# Bright future for video tape

A ustralians have the highest consumption of videotapes per head of population of any country in the world.

That makes it one of the most important countries in the world to Matsushita, the manufacturers of National videotape and one of the largest tape manufacturers.

In fact the General Manager of Matsushita's Export Department, Mr Mark Urakawa, visited Australia recently to discuss Australian requirements in bulk tape sales with his Australian agents, the Video Systems Division of GEC Australia Limited.

While here he made a few preductions about the future of videotape, and came up with a warning to technology buffs.

"Development of the  $\frac{1}{2}$ " videotape technology still has some way to go," he said during a visit to one of Australia's major tape duplication houses, The Duplication Centre in Sydney.

"We are gradually increasing the density of magnetic particles of National tapes to improve picture quality."

Mr Urakawa said videotape sales in Australia were already booming, with Australians



showing the highest per capita consumption of videotapes of any country in the world.

"That is likely to rise dramatically once VHS hi-fi videotapes become available," he said.

He forecast that videotapes would dominate the home market for at least the next decade or two.

"Videodiscs, which are often talked about as the replacement for videotapes, just haven't been developed to a stage where they can be used commercially."

Discs will have their uses, such as data storage, but they will not replace videotapes any more than audio discs have replaced audio tapes.

Before the advent of videodiscs, we'll see the development of high-density recording tapes to improve picture and sound quality.

And we'll also see the arrival of metal evaporated tapes with a plastic mirror surface to further improve the quality.

Much of the expansion of the market — and certainly in Australia — is going to be caused by the advent of hi-fi video. That has enthused Gordon Richmond, Managing Director of The Duplication Centre, who says that music video-cassettes will be one of the most popular units in the coming 12 months.

"Hi-fi music is already sparking a demand among consumers that will change the market," he said. "It will mean that our tape sales will go beyond the libraries and will encompass the home market. That will eventually mean the development of catalogue sales to the consumer.

Hi-fi music video is completely different to all our previous experiences. It can be retailed at a reasonable price using the highest-quality reproduction available anywhere in the world," he said.

## Low cost CD player

Marantz has fired a new shot in the CD player price war, with the release of this under-\$500 model. Called the CD44, it features a rugged zinc diecast chassis for extended trouble-free operation.

Other features include ultra-

smooth motor powered front loading, repeat and pause function and programming for playing up to 20 tracks in any desired



order. Tracks can be skipped quickly both forwards and backwards, using the player's fast forward and reverse functions.

The CD44 has a multi-function digital display which can indicate total playing time, elapsed playing time, number of tracks on the disc, the track currently being played, or the programmed track numbers. Internally the player also features digital filtering, which Marantz claims provides improved sound quality.

Finished in black satin, the CD44 is a compact 320 mm wide. It is currently selling for \$499. For further information contact Marantz (Australia) Pty Ltd, 19 Chard Road, Brookvale NSW 2100. (02) 939-1900.



Ortofon have always dedicated themselves to pursuing the world's finest sound reproduction.

So when they discovered that the existing moving magnetic systems missed much of the sound detail, they did something about it.

They designed, developed and patented a new cartridge principle that could pick up as much detail as possible, accurately, from the record groove.

(Of course, as one of the recognized world leaders in sound reproduction, Ortofon were well-qualified in this area.)

They called it the VMS Principle.

The VMS Principle (Variable Magnetic Shunt) utilizes a light tubular armature of magnetic conducting material.

This is attached to the cantilever and encircled by a powerful ring magnet. When the cantilever moves the armature closer to the ring magnet, the armature shortcircuits part of the magnetic field, generating a voltage in the coils.

And the result is an increased high frequency tracking ability, low distortion and superb



Symmetrical flux fields with the armature in the central (neutral) position. These flux fields emanate from the magnet positioned in front of the pole pins. As the fields are in balance, there are no changes in flux around and inside the colls. Therefore, no voltage is generated.



When the cantilever moves, the armature is brought closer to the ring magnet and acts as a shunt, short-circuiting part of the magnetic field. As a result, the flux field in front of the pole pins varies, and a voltage is generated in the coils.



10 ¢0 HZ 30 100 200 .	300 1000 2000 20,0
Technical data	VMS 30 MK II
Weight Type of stylus Equivalent stylus tip mass	
Type of stylus	
Equivalent stylus tip mass	
Frequency response	
Output voltage at 1000 Hz per 5 cm/sec	
Channel separation at 1000 Hz	
Channel balance at 1000 Hz.	2 dB
Compliance static, vertical	
Compliance dynamic, lateral (10 Hz)	
Recommended tracking force	
Tracking force range	. 10-16 mN (1.0-1.6 g)
Tracking ability at 315 Hz lateral	
Vertical tracking angle	
FIM distortion	
DC resistance	
Inductance)	
Recommended load resistance	
Recommended load capacitance	

transient reproduction – right down to the very last detail.

As if that wasn't enough, the VMS also reduced sensitivity to hum pick-up and minimized distortion and non-linearity in the magnetic system.

Presumably, the cost of an Ortofon VMS Magnetic Cartridge would be high.

Surprisingly, it's not. In fact, it's especially affordable.

Check the facts on the VMS 10E MK II, 20E MK II and 30 MK II at your hi-fi or Ortofon dealer.

And while you're there, find out how to home-test your stereo equipment with the Ortofon Pick-up Test Record.

It's another sound fact in detail.

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by the editors of ABRAIN Cricket

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# The better the drive, the smaller the disc

True half height 5<sup>1</sup>/<sub>4</sub>" floppy disc drives competitively priced from Siemens. Designed for office electronics, business computers and OEM applications, this slim-line FB500 series has four versions with unformatted storage capacities from 250 K to 1 M bytes. Apart from

compactness, these drives offer

fast access time by an advanced stepping motor and steel belt mechanism. The brushless direct drive DC motor is highly reliable and very quiet. The full range is now in stock together with Siemens relentless determination to provide complete service and technical assistance throughout Australia.

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# Siemens. The discs may be flexible, but never our standards.

64**6/**1189A

#### SOUND REVIEW

# QUALITY 'NO FRILLS' RECEIVER



THE BRAND NAME 'NAD' is not as well known in Australia as many other major manufacturers'. The reasons for this relate to the company's previous posture with respect to advertising and public relations in general. This attitude has perceptibly changed in the last year as a result of a shakeup in company management in the USA where the company's headquarters is located.

NAD is somewhat different from all of the other companies with which I am familiar in that it is owned by the distributing companies and dealers in the five continents where the products are sold, and while the company's attitude to public relations has

#### NAD 7155 STEREO RECEIVER

420 mm (wide) x 108 mm (high) x 380 mm (deep)
9.18 kg
NAD Electronics, Lincoln,
Massachusetts, USA
\$869

A modest, unobtrusive stereo receiver peeps out of the shop front window, retires into the lounge room corner. But under the test, the NAD 7155 shines. This receiver gives a performance worthy of much attention, with innovative extras and quality sound.

obviously changed in the last year, the basic design philosophy underlying its product design development and market has not. The company executives and dealers are very quick to state their attitudes on product design and performance and tell you that their aim is "to market the best possible product at the lowest possible price".

Your response to that statement, like mine, many well be "isn't that everybody's?". Well, yes it is, except that NAD believes implicitly that the correct path to achieving that philosophy lies in the deletion of unnecessary frills avoiding garish and expensive front panels and reducing the number of functional controls provided to the bare minimum.

This is aimed at not only keeping down

the cost, but more significantly in producing a simpler, more straightforward design which most purchasers really prefer in the end. NAD tells you that "this puts the emphasis on performance rather than on appearance".

#### **Design and function**

The 7155 'digital' stereo receiver does not immediately appear to be an exciting piece of equipment. Although it offers multiple features, some of these tend to be understated in terms of the factors that we would normally expect a manufacturer to stress in his literature. Because of this approach, the most exciting features tend to be the things it doesn't do rather than the things it does do.

### SOUND REVIEW

The frontal appearance of this receiver is the least assuming of any receiver I have yet reviewed. It is neither 'brassy bronze' nor 'flashy silver' but features a neutral flat grey front panel with white screen printed lettering. The main controls and displays are set out on two basic levels with relatively few controls in each.

On the upper level, centrally located, is a smoked escutcheon digital display panel behind which station frequencies may be seen, with clear plasma display designations for FM or AM. These are supplemented by a set of five rectangular light emitting diodes (LEDs) to indicate station signal strength and a neat pair of arrows to indicate that your carrier is off frequency and which flank a central rectangular LED; this illuminates when the receiver is correctly tuned to the carrier frequency.

To the right of this display are five pushbuttons for selecting preset station frequencies and an entry button and a switch for selecting AM or FM. Above these controls are two small amber LEDs that indicate that an FM stereo station is being received and a SOFT CLIPPING indicator which shows that the soft clipping switch on the back panel has been activated. To the right of the display is a rocker bar SEARCH control that allows you to increase or decrease frequencies, and below which is a SEARCH MODE button which automatically sets the tuner into a scanning mode to search out station carriers rather than incrementally stepping the frequency control.

There are relatively few controls on the bottom row. On the left is the POWER/ON switch and the HEADPHONE socket. Besides these, there is a rotary speaker selection switch for 'A' system, 'B' system, OFF and 'A + B' system. To the right of these are a BASS control, a TREBLE control and a BASS EQualisation switch which boosts the frequencies below 60 Hz to compensate for drooping speaker low frequency response.

The next control is an INFRASONIC DEFEAT switch which disables the rumble filter roll-off characteristic, a standard feature of the input circuitry. This normal rolloff characteristic provides 13 dB of attenuation at 10 Hz. With the infrasonic defeat control activated, the low frequency attenuation is reduced to 2 dB at 10 Hz and less than 0.5 dB at 20 Hz.

A mono push button provides the ability to combine the two stereo channels, which is a function that would not often be required. The fourth control is a push button which disconnects the internal DYNamic SEParation circuit. This particular circuit selectively reduces the stereo separation at high frequencies in order to maintain a stereo signal when a weak incoming RF signal is inadequate for the maintenance of normal stereo listening quality.

One area where the functional controls seem to be more comprehensive than you might expect is in the area of functional recording and playback controls. In this

area the 7155 provides two completely separate switches by means of which the recording function and the listening function can be completely separated. The designations for each of these controls are precisely the same: CD/AUXiliary, PHONO, TUNER, OFF, TAPE 1, TAPE 2. This provides the wherewithall to record off one input, playback to another and then separately listen to a third.

Two other push buttons are provided labelled LOW LEVEL, which is another title for a mute switch, providing 20 dB of attenuation. The other switch is a LOUD-NESS COMPensation control which activates a loudness equalisation curve boosting the low frequencies and high frequencies to compensate for the subjective physiological response to low level audible signals.

The last control provided is a circular volume control with a centrally located indented balance control offering one of the best ergonomic design features for this control function that I have yet seen.

The rear of the receiver is neat and functional with the connections, controls and switches laid out in two rows. In the upper row is an AM ferrite loop stick antenna which the designers have carefully labelled with the caution "THIS IS NOT A HAN-DLE"! Beside this are spring loaded terminal connections for an external AM antenna, a balanced 300 ohm FM antenna and ground connections for the AM antenna. Unlike many other receivers, this unit also incorporates a 75 ohm coaxial socket to facilitate direct connection of a properly wired FM antenna.

In the top centre of the back panel are eight clearly labelled colour coded universal terminals for connection to the loudspeakers. The bottom row of controls features a pair of phono-sockets with a switch for changing from moving coil to moving magnet and a separate switch for setting the parallel input capacitance to 100 pF, 200 pF and 300 pF. RCA coaxial sockets are provided for CD player/AUXiliary input. This is the first unit that I have seen which acknowledges the existence of CD players and goes to the trouble of ensuring that the input circuitry is specifically designed to cope with the CD signal levels and CD player dynamic range. The adjacent sockets are provided for connecting two separate tape recorders. The pre-amplifier outputs and amplifier inputs are externally linked by two simple linking bars so that you can take connections out of the pre-amplifier or feed them back into the main amplifier input should you require it.

Three other switches are provided for setting the output impedance for 8 ohms or 4 ohms, a switch for activating the soft clipping capability and the third an extremely useful, but little used, switch for bridging the inputs so as to be able to obtain twice the individual channel's power rating but only in the mono mode.

The inside of the amplifier is extremely well laid out with the front of the amplifier

containing two separate large printed circuit boards. These are mounted one above the other with the RF circuitry on the top board and the amplifier circuit located on the bottom board. The RF circuit board makes effective use of large scale integrated circuits for tuning and display functions, but most of the important tuning elements are still discreet transistors with clearly labelled circuit designations to assist maintenance, should it ever be required.

The power transformer is located at the extreme rear of the amplifier beside the large multi-finned heatsink which lies between a well perforated top and bottom panel. Behind this heatsink is a separate RF input stage pre-amplifier board to which the connections from the individual input terminals for both RF and audio frequency circuits are directed. This facilitates remote switching by means of mechanical linkages extending from the front panel. The designers have obviously gone to considerable trouble to ensure that the interaction and screening problems that many other receivers experience, are not duplicated in this particular receiver.

The construction of the chassis is well executed and rigid, incorporating a large area of perforated ventilation slots in the top cover to ensure that the unit is capable of providing proper cooling under almost any foreseeable situation — excluding the total obstruction of the air path. Even this however has been considered by the designers, there is a series of internal protection circuits backed up by circuit board fuses to provide two levels of defence in the event of overheating.

#### **Test performance**

The functional and objective assessments revealed that this is a superlative unit with basic characteristics that are in most respects better than many, if not most of, the competing receivers within the same price range. The frequency response of the amplifier is ruler flat from 20 Hz to 20 kHz and extends from 10.4 Hz to 53 kHz at the -3 dB points. The sensitivities and overload points of both the auxiliary inputs and cartridge inputs have been carefully designed and selected to cope with an extremely wide range of optional units which might be connected. The amplifier distortion levels are particularly low although not the lowest that we have seen.

Notwithstanding, this performance is as good as you could ask for and better than you can reasonably utilise in normal stereo listening. The IEC high frequency total difference frequency distortion figures are good, but not exceptional. The lowest distortion is achieved at approximately the 40 watt peak level where I would expect most serious listening to take place.

The soft clipping function which NAD has stressed as a feature of its amplifier design for the last five years, proved to be particularly interesting.

As a photo of the fast Fourier analysis re-

## SOUND REVIEW



1.6

Our Ref: 4629A.219A/E140

MEASURED PERFC	DRMANCE OF NA	D MODE	L 7155 REC	EIVER	
Serlai No :	202313			1	
FREQUENCY RESPONSE :	Tone Control	ls Centre	d		
(-3dB re 1 Watt, 0.5V	Left	10.1			
Input to Aux.)	Right	10.4	Hz to Hz to	54.0 53:0	kHz kHz
SENSITIVITY :					
(for J Watt in 8 ohms)	Aux.	Left 19.5	Rig mV 19.0		
	Таре		mV 19.0 mV 19.0		
	Phono M/M		ыV 410	Vu	
	Phono M/C		µV 31	NV	
	Overload M/M		mV 165	mV	
	Overload M/C	17	mV 16.5	MV	
OUTPUT IMPEDANCE : HARMONIC DISTORTION :	95 milliohms (@	IkHz)			
(A) (At rated power of 55 Wa	tt s				
into ohms = 21 Voits					
		100H	z IkHz	6.3kHz	2
	2nd	-87.0	-96.5	-96.5	dB
	3rd	-78.0		-88-8	dB
	4th	-111.		-96.8	dB
	Sth	-94.9			dB
	THD	0.013	0.0019	0.00341	•
(B) (At 1 Watt into 8 ohms					
		100Hz	IkHz	6.3kHz	
	2nd	-97.2	-93	-91.7	dB
	3rd	-82.2		-92.9	dB
	48h	-107.5	-		dB
	Sth	-104.4			dB
	THD	0.0079	0.0022	0.0034	%



AUX.	-83.5	dB(Lin)	-87	dB(A)
PHONO M/M	-57	dB(Lin)	-70	dB(A)
PHONO M/C	-65	dB(Lin)	-71	dB(A)
76.0 VP-P				
= 90.3 Watts				
= 2.15 dB (re	55 Watt	ts)		
	PHONO M/M PHONO M/C 76.0 VP-P = 90.3 Watts	PHONO M/M -57 PHONO M/C -65 76.0 VP-P = 90.3 Watts	PHONO M/M -57 dB(Lin) PHONO M/C -63 dB(Lin) 76.0 VP-P	PHONO M/M -57 dB(Lin) -70 PHONO M/C -65 dB(Lin) -71 76.0 VP-P = 90.3 Watts





#### SOUND RÉVIEW

veals (see Figure 1) in the normal clipping mode the magnitude of even order harmonics is close to that produced by the odd order harmonics. This results in a subjectively disturbing sound and typifies the worst feature of amplifier clipping. In the soft clipping mode however, the even order harmonics are at least 25 dB lower than the odd order harmonics and even though the amplifier excursions may extend into the clipping region, the subjective impact is far less disturbing and the quality of sound is 'almost acceptable'.

It is interesting to note that a number of other manufacturers have recently picked up this concept and now include it with their latest generation of amplifiers.

The output impedance of the amplifier is 95 milliohms which is quite low and ensures that with reasonable speaker cables and even with low input impedance speakers, you are capable of transferring maximum power output.

The tone controls and base equalisation switch provide usable, complementary and effective boost and cut, although most people tend not to use tone controls to any great extent, particularly when the listening rooms are well designed or incorporate adequate amounts of soft furnishing and carpet.

The signal to noise ratio of the amplifier is particularly good achieving 87 dB(A) re the 1 watt output on the auxiliary input, 70 dB(A) re the phono moving magnet input and 71 dB(A) re the phono moving coil input. These figures ensure that the amplifier noise is always well below the source noise levels on any practical programme that you may listen to. The channel separation and cross-talk capabilities are extremely good, being better than 70 dB separation at frequencies up to 2 kHz and 60 dB separation at 20 kHz. The amplifier delivers a genuine 90 watts peak output into 8 ohms, 125 watts bridged into 8 ohms and has a single channel dynamic headroom of 2.15 dB above the rated power output. This falls a little short of the manufacturer's claimed 3 dB headroom capability.

NAD makes a number of claims regarding the superior performance characteristics







of its tuner section which incorporates higher performance ceramic IF filters, optimised gains and losses for each IF stage and NAD's new dynamic separation circuit to reduce noise in weak FM stations by a happy match of conventional high blend circuitry and interchannel cross-feed at lower frequencies. These claims appear to be confirmed by our objective measurements which achieved better than 70 dB mono separation at 40 dBF and higher and better than 62 dB channel separation at 30 dBF and higher.

The maximum performance figures are greater than those shown on our graph which are limited by the signal generator rather than the receiver. The FM linearity is -3 dB from 22 Hz to 16 kHz and channel separation better than 35 dB from 100 Hz to 4 kHz. Even the AM performance is reasonably good, being 3 dB down at 70 Hz and 3.7 kHz and offering a better overall AM peformance than most other receivers currently available.

The most outstanding feature of the two tuners is their sensitivity which no other receivers in their class has yet bettered.

The subjective performance of the NAD 7155 receiver is positively outstanding. I listened to it with a wide range of inputs including CD players (Sony CDP-101), cassette recorders (Nakamichi Dragon), record players (fitted with Shure V15-IV and Audio Technica AT30F cartridges) and I listened to speakers including B & W 802Es, KEF 104s, OUAD electrostatics and even Pioneer electrostatic headphones. The performance was positively exhilarating until I reached the power limit of the amplifier, which is typically around the 100 watt level into normal speakers and slightly more into the KEF 104s.

On a few occasions when listening to other speakers with even lower efficiencies, I would have liked a little more power, but for the vast bulk of my listening the power handling capacity proved adequate. Whilst the performance was outstanding with CD players and record players, it was positively marvellous when connected up to provide the audio output from the two stereo VCRs (see November ETI).

With FM listening I was more than a little surprised when I found the receiver pulled in five local FM stations without any aerial attached and achieved maximum modulation sensitivity with only a simple folded dipole antenna lying on the floor. This sort of performance has not been achieved by any other receiver or tuner I've reviewed and is a measure of the sensitivity of the input circuitry used in the NAD 7155.

#### **Highly commended**

The selling price of the NAD 7155 does not reflect the 'no frills' concept; it is comparable with and in some cases higher than that from other manufacturers offering similar output powers from the amplifier stage. Nevertheless this receiver offers extremely good RF sensitivity which few other manufacturers can equal.

If I was to go out and select a receiver for myself I would be hard pressed to find a better unit If you like simple 'no frills' concepts, then the NAD 7155 is most probably what you have been looking for. If you are looking for that type of subdued visual impression, combined with exceptional RF sensitivity, selectivity and particularly good audio amplification capability, then you need look no further.

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### **VIDEO REVIEW**

# JVC's VideoMovie camcorder — gladiator in the video colosseum

Malcolm Goldfinch

FOUR YEARS ago on a winter's morning, I attended a gathering of the photographic industry and the opening feature was a very dark and underexposed, super-8 film of the beach at Nielsen Park in Sydney. A lovely young lady, horribly overexposed and probably goosepimpled in the winter chill, danced along the water's edge. In her wake came a hobbledy-hoy figure with an enormous video recorder around his neck, grasping in both hands a gigantic camera. The cord from camera to the recorder wound around his legs and he stumbled and fell while the nymph raced out of sight. Just then a neat figure with a small super-8 camera appeared, jogging nonchalantly past the fallen video man to catch up with Miss Goosepimples and shoot her down with a long burst of film. This scene came to mind when I first picked up the exciting new JVC camcorder, 'VideoMovie' at the Perth Consumer Electronics Show. That monstrous home-video system I saw on film had been reduced to a single, light piece of equipment which while not fitting into the palm of you hand, has a 'prosthetic' handgrip and it is easily held and operated single-handed.

The new VideoMovie GR-C1 weighs under 2.5 kg — a figure which includes battery for an hour, and a 30 minute cassette, yet it is only the size and weight of big racing binoculars. It incorporates a full video, record-playback system that will replay on any TV and also dub what it plays on to any VCR, Beta or VHS! A review of the JVC GR-C1 video camera/recorder, released at the Perth Consumer Electronics Show last August. It's destined to be the first product to strike a blow to the popular Betamovie camera/recorder — the other combatant in the video colosseum.

# Film and video, (chalk and cheese)

There is an enormous difference between film and video. The heavy video gear, hanging on the man on the beach equated with a camera with a sensitivity that would require film of some thousands of ASA degrees of speed, a colour and sound film processing laboratory, (in order to instantly process pictures) and a projector and screen, so the photographer could see the picture he had taken to check and correct it before leaving the subject. The videoman carried all of this in two packs, total weight say 10 kilos.

Nevertheless millions of consumer dollars worldwide have gone into video at the expense of photography. Sales of super-8 film gear to new users are just about nil. One point missed by the film people was that the video man's early morning beach scene would have been brightly exposed, not dim like the film man's. The film man did not know he was underexposed until the film came back from processing. Lights to take good video on the other hand, are quite unnecessary today.

After the Perth CES show I caught up with the VideoMovie and made it part of my family for over a week. Women accepted it at once, welcoming it as a wanted, one-piece video, simple and light like the home camera. They could now handle a video to record the family 'golden moments'. Some women are great photographers for this reason. But some men (and women) with video knowledge, were inclined to react more energetically, and saw it as revolutionary.

The VideoMovie and others to follow will undoubtedly revolutionise what we have known as home photography, despite its price at approximately \$2500 recommended retail. Running costs, however, are minimal: no developing and printing charges. If the VideoMovie is a goer and first sales suggest it will be,
RF unit. This RF converter permits VideoMovie to be connected to most TV sets for playback ...



VHS-C (compact-VHS) becomes a major world video format as an add-on to VHS, just as VCR video only succeeded because it was an add-on to the billions of TV sets and their TV stations already in the marketplace with the service technology already at hand.

#### VHS-C's video niche

It would be misleading to describe the VideoMovie without first outlining the entirely new technical environment introduced by sub-VHS format.

Although the Shogun of world video has at least decided on the new 8 mm format tape, JVC, always the revolutionary, launched the VHS-C (compact-VHS) format tape, now used in the VideoMovie. two years ago. The cassette (90 (w) x 60 (d) x 20 mm (h)) is roughly the size of a 20 pack of cigarettes and has a 30 minute (in PAL) supply of standard VHS tape inside. The small package is something I feel the industry has needed for years. As a past 9.5, 16 mm and super-8 film user I felt a three minute film was a long time; a three hour tape, ridiculous for portable video! You lose whole scenes and never find them again without long searches. A whole lot of neatly labelled 30 minute cassettes would suit most cine users. I can hear the question, "But what about my three or four hour timeshift of TV films?". Well, VHS-C is an add-on. It is not intended for timeshift of big programmes. If you accept that, then VHS-C becomes a great new cine medium.

The VideoMovie does not have a tuner/timer, or the docking facility of the first 8 mm portable videos announced by Kodak and Polaroid for delivery sometime in 1985. The term docking is adapted to video from spacetalk. It refers to a simple no-wires method of connection, to help the technically dyslectic to couple their portable VCR into the home TV and turn the portable into a home VCR with

tuner/timer. The logic of this escapes me as there is a reputed limit of about one hour for the maximum recording time for an 8 mm video camera. It is my belief that VHS-C has not gained worldwide popularity in the past for the same reason. It was considered a hopeless timeshift format. Until now, few saw it as a brilliant cine format: nothing to do with timeshift.

Oddly JVC's close associate, National, has failed to follow with hardware for VHS-C, where Sharp and Philips have. As far as I can ascertain, National has no immediate plans to do so, but during 1985 will announce a full VHS format, minicamcorder. The 'mini' concept will be limited by the large size of a VHS cassette, just as Betamovie suffers from this problem, but it will be interesting to see what National comes up with.

#### Easy to use

For the consumer the reason that the VideoMovie is such a leap ahead from the add-on Betamovie, is not only because you can see the video you are taking in a TV viewfinder as your shoot, but also the instant playback available anywhere there is a convenient TV set.

One novel activity the VideoMovie allows is to screen what you have taken on a TV, while recording another copy on the VCR. I found this simple and easy to do. It works on all formats of VCR, Beta, 2000, U-matic or even TV studio gear. All that is required is that you carry in the pocket or bag, a black box RF unit with connectors, smaller than a cigarette pack. Connectors go from the unit to the video-out port on VideoMovie, where it also gets power, and to the TV aerial input. An RF modulator for video and audio, it converts video and sound from the VideoMovie to a UHF TV signal with adjustable frequency to avoid local channels. A second port is for the TV aerial to be plugged into the unit and a switch allows either VideoMovie or TV

input to the VCR. This signal is tuned by TV or VCR in the normal way.

But the VHS-C 64 dollar question in the US is, "Okay, I would like to play VHS-C shots on my, or my friend's home VHS-VCR: how?"

Answer, "No problem. JVC has produced one of those gismos that makes a messy conversion a slick trick". The C-P3/U adaptor is the same size as an ordinary VHS video cassettte and looks like one without tape spools. The VHS-C is easily placed inside it and as the catch snaps shut it comes alive in your hands. Inside a tiny motor whirrs; levers open the compact cassette, remove the tape and spread it out, a pointer on a dial shows progress and as you watch, it become full VHS format compatible. It takes four seconds for the metamorphosis and plays back or records like a normal video cassette on any VHS video. To change back, the catch is pushed and again the battery motor reverses the process; the lid opens for removal and a red wedge comes out the side to prevent accidental insertion without a VHS-C inside. It is a fun thing to use

Existing VCR owners not anxious to duplicate their machinery just to go video-portable will find the VHS-C adaptor a useful piece of equipment.

Editing out the inevitable flub or dull bit while making a dub is fairly easy also. Just put the copying VCR in and out of pause in the record mode, copying only the scenes played on the monitor which you wish to retain. Editing is one of the great opportunities of video and often overlooked or regarded as too hard, and a small loss in quality, if good machinery is used, is far outweighed by the tight professional result.

#### VideoMovie specifics

The JVC VideoMovie at first looks to be an ordinary video camera. A small window revealing a tape spool gives the secret away. It is a 'cancorder', a combined videocamera and VCR.

In size, the VideoMovie is less than many of the videocameras I have reviewed recently. With lens-hood, TV finder and battery it measures 17.5 (w) x 13.6 (h) x 34 (d) cms. It comes with a shoulder rest/strap; conveniently, it hangs lens down and is not very cumbersome. Nett weight of the VideoMovie is specified as 1.9 kg! On my balance it weighed 2.25 kg with shoulder frame and carry strap, but without battery or videocassette. With 245 gm for a 1 hour battery and compact cassette at 15 gm, it was all up less than 2.5 kg! A Betamovie with battery and 130 minute video cassette and no auto focus weighted in at 3.06 kg. Combining full videocamera/VCR features and VHS compatibility, the VideoMovie represents a clear breakthrough in weight, size and convenience. Is it therefore reasonable to expect some serious quality compromises?

# VIDEO REVIEW

Under test, using as benchmarks my lab monitor system, there were some faults, but not of great importance. The quality of replay from the VideoMovie via the blackbox RF converter was not top quality, but better than many economy videocamera/VCRs I have tested. Using the direct video output by BNC connector to monitor, it was of a very good standard and marginally better again when I tested the video on compact cassette, played back on my 'benchmark' VCR by using the adaptor C-P3/U. I doubt if the loss of definition compared to top video systems would be detectable on average home TVs.

The secret of the VideoMovie's size and weight lies in the entirely new head drum and loading system which has been reduced by one-third in diameter. The VHS signal pattern has been maintained by a revolutionary 4-head sequential recording system and a new 270 degree wrap/parallel loading system. Full marks to JVC!

The features in VideoMovie are really 'state of the art', with very good auto or manual white-balance with three hour memory to hold setting. It also includes iris, auto and manual, coupled to the high band saticon half-inch camera tube with a high sensitivity and a low level select, for colour down to a minimum of 15 lux with little lag (trails from bright lights on black background).

The viewfinder is detachable and as a half-inch screen CRT, it shows over the video picture, computer style function and warning messages, as well as date titling.

The lens system is a small but efficient, f/1.2, f=8-48 mm and 6:1 + macro with power zoom, daylight or tungsten filters and tripod mount.

VCR functions are full scale and at your fingertips. Every stop is auto-backspaced for clean cuts. An automatic recording-lock system allows power conservation without editing loss. The 'review', a flash of a few seconds in the viewfinder showing the last take is almost an essential (but only included in the best video systems).

The two way shuttle search is also a great feature in playback and can be operated with the VideoMovie at eye-level during takes to check results. The light weight and shoulder frame allow the VideoMovie to be left at eye-level for long periods without fatigue. Shoulder and head-rested shots are many times more steady than shots with a hand-held videocamera.

#### **Many accessories**

Making semi-essentials accessories allows cost control of the initial purchase but they should be continually reviewed by owners. Your video can be only half alive without a good range of accessories.

Batteries are important components for video and the VideoMovie has an option

#### WINNERS & LOSERS

There are winners and losers in every arena. The video gladiators have been locked in a bloody contest of claim and counter claim, feature and counter feature. At this moment, with the release of JVC's GR-C1 VideoMovie camcorder (industry name for a VCR and Videocamera in one piece), the originator of the VHS format has a foot firmly on the throat of Beta, JVC is looking for a thumbs-up, or thumbs-down to finish the contest.

The consumers, who are the audience in this colosseum, are in a bad mood about the two formats. They prevent free interchange of video as possible in audio cassettes. It looks as though the thumbs will go down for a lingering departure of Beta over the next tew years.

There is no room for two champions in the video arena and the release of the GR-C1 camcorder looks like a mortal blow to Betamovie which lacks both a TV viewfinder and instant playback, both features included in VideoMovie. If Betamovie does survive JVC's thrust, National, JVC's Samurai comrade, will probably deliver the coup de grace when they release a small camcorder with full features using a standard VHS cassette.

If this were not enough, Beta thought its hi-fi would give them an edge, but JVC has just eleased a video hi-fi in VHS format. It plays for eight hours, using four-hour video cassettes; audio response is in the CD audio specification area and allows recording. This format fight involves billions of dollars of turnover in years to come. It makes a football final look like a cussyfoot contest.

The article here is a hands-on assessment of this latest product of the video industry, by an expert in photography, audio, video, and Australia's pioneer of Univac, the first in computers. He also happens to have been a journalist, off and on, for 47 years

of battery packs. One is approximately 30 minutes and the NB-P6U is 45 minutes. In practice 1 found it more like 60 minutes. They are very light and small: 0.245 gm. You could carry several in the pocket or bag. The AA-P1EG charger will run the VideoMovie without battery as well as recharge. A very small BB-P1EG charges batteries only. They take only about 30 minutes for a charging. A great feature is that these very thin batteries easily slide on or off the back of the VideoMovie or charger and lock firmly. No more flapholes to find, or wrong way ups and arounds.

The black box TV/RF unit, shoulder frame and carry strap, also the VHS cassette adaptor, have been described as essential parts of the Videomovie; there is a car battery adaptor, pause remote control and super directional microphone, extension cable, as well as the usual BNC and TV connector cords.

The VideoMovie is housed in a port wine coloured carry case with a black soft moulded interior to nest in the camcorder and all its main accessories, safe from shock and lockable from little fingers. There are handles top and side.

The last thing you would expect in a hand-held camcorder is a very comprehensive titling system, but this is just what VideoMovie offers. It is a light, small unit that shoes on to the top of the camcorder and looks like a pocket calculator. It offers some of the most sophisticated character generation I have seen in video; 46 different characters and time, set Dymo fashion, date stopwatch, Iap-time. Titles can have 60 characters per frame and 14 pages of text can be memorised, one for date, lap-time and

title, seven for standard titles, four for *zooming titles*, and two for *scrolling titles*! If that is not enough, you can reduce/enlarge and move them to a corner of the screen where they do not cover the bride and groom's kiss.

#### Summary

There is no way that the advent of JVC VideoMovie can be described as other than a revolution in video. Radio was never really portable before the transistor and neither was video really portable before this one piece camcorder. Getting all video's technology into something similar to a lady's handbag, and no heavier, when only six years ago it weighed over 10 kg, must be one of the great technical feats of this century. JVC has again shown the way and unless they can patent size and design, the competition is so fierce others must follow.

Eight mm video camcorders are close and sizes may be smaller and lighter, but will the features match VideoMovie? One great feature is the compatibility with many millions of the VHS-VCRs all over the world. The Pathe 9.5 mm home movie film with centre sprocket holes dominated home movies in the 30s, until someone showed you could expose half the commercial standard 16 mm film with 8 mm frames, then turn it over and expose the other side in the opposite direction. The processor then split it in half and you got twice the length back. The cheaply available standard was 16 mm film. Soon, pre-split 8 mm film was the big seller and 9.5 mm with a better format and bigger frames vanished. "Look behind to see what may be just ahead."



## SPECIAL ETI SURVEY

# CROS — a closer look

ETI's mega CRO survey — all the cheapest CROs plus a bit on how they work.

THE BEST PIECE of test gear ever invented is the human brain. It's flexible, transportable and extremely cost effective. Certainly, if you can't use it you'll have difficulty using anything else.

It does have a few disadvantages though. For one thing, it can't sense electricity directly. You can't hear electricity, or see it, and feeling it could be an extremely terminal sort of test. That's where a CRO comes in.

A CRO is a device to allow you to see what's happening within a circuit. Used properly, it's an extremely powerful piece of equipment.

The basics of CRO operation have been dealt with before, both in this magazine and in numerous other articles and text books. In this article we want to take a more indepth look at certain parts of a CRO. We have also constructed a table of most of the cheapest CROs available in Australia at present, together with some of their more important features.

#### Triggering

The purpose of a CRO is to 'paint' a waveform on the screen of a cathode ray tube (CRT). The vertical movement of the trace is arranged to be proportional to the voltage level of the input signal. The horizontal position of the trace is made proportional to time, so that a graph is plotted showing voltage against time.

The horizontal 'timebase' movement of the trace is achieved by feeding a sawtooth waveform of known slope to the deflection plates. This makes the trace move steadily across the screen from left to right. When the trace gets to the right hand side of the screen the beam is turned off (blanking) and moved rapidly back to the starting point again (flyback).

These two functions, the vertical and horizontal deflections of the trace, must be synchronised to present a meaningful picture on the screen. To see why, imagine what would happen if we tried to input a simple sine wave with no synchronization. The trace would oscillate up and down the screen under control of the input, while at the same time racing backwards and forwards across the screen under control of the timebase. If the timebase was set to be slow we would see a vertical line moving slowly

from left to right. If it was fast we would see a broad band, it's width determined by the peak to peak voltage of the input.

But if the two were synchronised so that the timebase was locked to some exact multiple of the input frequency, then the trace would move through exactly the same path again and again. The sine wave would be drawn on the screen, an exact replica of the voltage on the input.

The process of synchronising the horizontal and vertical defections of the trace is known as triggering. Triggering is achieved by taking some of the signal being applied to the vertical amplifier and applying it to a gate circuit.

When this signal reaches some preset level the gate circuit emits a pulse, which starts a sawtooth generator running. This sawtooth is the controlling voltage for the horizontal deflection.

The exact speed at which the sawtooth generator runs is set by the time/div switch on the front panel. The level at which the triggering circuit fires a pulse to start the sawtooth generator is also set from the front panel, by a control usually labelled 'Trig level' or somesuch.

This system works well provided the signal on the input is reasonably uncomplicated. If it is a simple sine or square wave the CRO will provide a picture with the trigger level set anywhere within the peak to peak voltage of the input.

But this basic triggering circuit is not foolproof. Bear in mind that it is set up so that it fires the timebase, waits for the flyback, then waits for the voltage level to cross a certain threshold before starting the timebase again.

If the signal is not a simple repetitive wave, it can fool the trigger circuit in a number of ways. Perhaps the simplest is a complex wave where two or more waves of similar frequency but different amplitude are summed together.

In Figure 1, notice that when the trigger is set at level Y, and the sweep only runs to Z, it will provide trigger pulses both at points B and D. The trigger will not fire at C, since it is sensitive to the direction of the input, as well as its level. This can be controlled with the  $\pm/-$  switch on the front panel.

As a result two waveforms will be painted

on the screen. First it will trigger at B, and run through to Z. Then it will look for the next negative going crossing of the trigger level voltage, which occurs at point D.

The resulting picture is shown in Figure 2. Note that if the timebase is shortened or lengthened, other combinations are possible. However if the trigger level is moved up to X in Figure 1 then triggering must take place unambiguously at E or F.

Of course, a waveform can be complex in other ways. A pulse train, for instance, could consist of a repeated but irregular sequence of marks and spaces, as in Figure 3. Assuming the first trigger point is at A, and assuming the timebase is set to sweep as far as the line Z, the next trigger point will be B. Once again, this will result in a confused image on the screen.

One way of curing this problem is to extend the timebase so that it falls around D, in which case it will trigger at C. If, for some reason you don't want to do that, another very useful function is available. This is the 'hold off' function. It actually stops the sawtooth generator for a suitable interval, to prevent retriggering. In Figure 3 this would be the time period between Z and D.

#### Delay

It often happens that the most interesting part of a signal can't be seen with sufficient detail in the normal display mode. This might be something like the ringing that occurs when a voltage level changes dramatically, as it does in a pulse train. (See Figure 4.)

One way of getting a closer look at any given point on the display is with the trigger delay and magnification functions. The delay holds the sawtooth generator for a period of time determined by the user, before releasing it to run normally. The effect of this is to move the displayed waveform to the left. The magnification just speeds up the trace, thus spreading it out across the screen.

When these two functions are used together the result is to magnify any given piece of the trace for closer examination. Some CROs make it possible to observe the entire trace on one channel while using a delay function of the same input on the other. Sometimes the area under inspection is highlighted, so that it is possible to precisely select a particular area for inspection.

Most CROs give the user a selection of the way in which the trigger circuit is connected to the input. Commonly, a choice of ac, dc, hi-pass, low-pass and TV input coupling is provided.

AC and dc are self explanatory. Hi-pass and low-pass filtering are there to prevent noise from causing false triggering. TV is used to optimize the trigger for looking at TV signals, either frame or line. A special TV input is used because TV signals form a difficult subject for CROs to latch onto, but at the same time are very common. Using the TV setting in conjunction with the timebase delay control, it's possible to look at

## A CLOSER LOOK AT CROs



Figure 1: An irregular waveform applied to the input of a CRO. The horizontal lines are the trigger levels.

both frames and any of the lines within the frame.

#### **Multi-trace operation**

It's difficult to find single trace CROs these days. In fact there are only seven out of 64 in our survey.

There are two ways multiple traces can be displayed on the screen. One is the multigun approach, in which one input is fed to one gun where it forms an image in the normal manner, and the second input is fed to another gun to form a second image in the same way. This method is expensive, but it does result in very high quality CROs.

A more realistic alternative is to use one gun and one electron beam to form two traces. This is done in a number of ways. The trace can actually paint alternate lines on the screen, first doing channel 1 and then channel 2 and back to one and so on. This option is selected with the 'Alt' (alternate) switch. Another method is to switch rapidly between both traces. This is called 'Chop'.

All these modes of operation have advantages and disadvantages. For a given acceleration voltage, the multigun assembly gives the brightest display. It also operates completely independently of the frequency of the signals being displayed.

On the other hand, the single gun pattern is significantly dimmer when displaying two traces, other things being equal. It has to be, since the trace must now cover twice as much area as before — or if you like, it only spends half its time on each trace. Naturally, this problem gets worse the more traces there are.

Of course, other things are seldom equal. The accelerating voltage can be increased in multi-trace scopes to allow for the dimming of the screen. As a result single gun scopes are the standard and multiguns a rarity.

There are two other modes of operation that appear on most CROs. These are the 'Add' and 'Sub' functions. When either of these is engaged, the two inputs are added or subtracted and the resultant displayed on the screen.

There are a number of specialist applications where these functions can be useful. One rather more general one is in noise cancelling. Figure 5 shows a pulse train riding on a 50 Hz mains hum. If the mains hum can be introduced into the second channel and then subtracted from the first, the result will be waveform C.

Strictly speaking it is not necessary to have both an add and subtract function.



Figure 2: Display resulting from setting the trigger level too low on a complex waveform.

goes before it can register as more than a flash of the trigger lock LED. What is required is a *storage* CRO. This is a type of CRO that allows you to retain an image of a single transient event.

There are a number of ways this can be done. The cheapest, and historically the first, method is the variable persistence CRO. Visual persistence is the name given to the phenomenon that allows you to see a moving spot as a line (or indeed, a succession of still images in the movies as motion.)

The phosphors that coat the inside of the CRO screen have a definite persistence as well that aids this effect. That is to say, when illuminated by the electron beam they do not immediately switch off once the bream has passed, but remain glowing for some small finite time.

It is possible to vary this time to quite a large degree. The result is that a semipermanent record remains of the path of the trace. Naturally this is continually updated



Figure 3: An irregular square wave presents a very difficult subject for a CRO trigger circuit. In this instance there is a definite sequence and it is possible to see some order in the madness.

Figure 4. Close up of one of the pulses in the pulse train in Figure 3. Notice that the rapid and near Instantaneous transition from lo to hill level leads to ringing in the output circuits. This can be a source of problems, leading to false states being generated in digital circuits.

w

most CROs have the facility to invert one or both traces, so it is always possible to implement a subtract function by inverting one of the channels and adding.

#### Storage

There are a number of very important applications where we wish to record the response of a circuit to a non-repetitive event. An explosion, the closing of a switch or any other form of one-off transient are examples.

A normal CRO is nearly useless in this type of application. The event comes and



there are facilities to turn the trace completely off when the CRO is operated in this mode, to avoid oblitering the wanted waveform.

In fact it is often possible to operate in a single-shot mode. When this function is engaged the trace will make only one sweep after the trigger circuit has been activated. It must be manually reset in order for the next line to be displayed.

This mode, together with variable persist-

# SPECIAL ETI SURVEY

ence, makes it possible to capture very short events. However it is done at the cost of turning the screen accelerating voltage up to abnormally high levels. This has all kinds of unacceptable consequences for the life of the display, so other methods of achieving the same end are preferred.

The modern solution is digital storage. This involves the sampling of the input waveform at a predetermined rate, usually several hundreds of kilohertz, and the subsequent re-display of the same. This system involves no special manipulation of the screen at all.

The input is converted into a digital word, eight to twelve bits long, and stored in on-board RAM. The display then effectively is used to read out the contents of memory.

The accuracy with which digital storage can work depends on the size of the memory available, and the rate at which the analogue to digital conversion can be made. It's usually quoted in terms of words, each word consisting of a sample or description of the voltage level at a particular point across the screen. Figures of 400 words are common for the types of CROs in this survey.

#### Sweep magnification

Sweep magnification is another useful function for examining portions of the waveform in greater detail. We have mentioned it before in connection with trigger delay and this is without doubt its most profitable use. However, it is common to find magnification even on CROs without any of the fancier functions. Usually, this will be of the x5 variety, although there are some with x10 and even x100.

All of these work by stretching the timebase, so that it appears that the screen is just a small window into a portion of the whole line. The big disadvantage with using magnification is that it results in a dimming of the trace.

There have been some modifications recently to this basic idea. One of these is the 'Mix-Mag' function, in which it is possible to extend part of the trace while leaving the rest as normal.

#### **Component testers**

Component testers are on optional extra that plug into the input of many different types of CRO. One CRO in our survey has a tester built in. Generally they work by passing a variable current through a component and measuring the voltage across it. The tester sends a trigger pulse to the timebase to synchronise the timbebase with the current test.

The result is a series of characteristic patterns on the screen for given types of component. A resistor shows up as a straight diagonal line — a perfect geometric expression of Ohms' law. A capacitor is an oval sloping from bottom left to top right, and an inductor slopes the other way around.

Semiconductors have their own individ-



Figure 6. Output from a component tester. The horizontal axis should be interpreted as current, vertical as voltage.

ual shapes. Diodes tend to show flat traces in their cut-off regions and vertical traces as the junction barrier is overcome. It is possible to generate a whole family of characteristic curves for a transistor using one of these devices, as we've done in Figure 6.

#### **Bandwidth and risetime**

Probably the most fundamental measure of the quality of a CRO is the bandwidth of the vertical amplifier. As with most other devices it is measured at the -3 dB point, i.e. the point at which the amplitude response is 0.707 that of the maximum response. (See Figure 7.)

Risetime is a closely related figure. It tells you how long it will take the amplifier to go from 10% to 90% of the full value of an instantaneous change in voltage applied to the input. So it's a measure of how fast the output of the vertical amplifier can track the input.

In the case of a sinewave these two fig-



ures would represent different sides of the same coin, related by the formula



However, it's not difficult to imagine waveforms where this is not the case. A square wave for instance, may have a very low frequency but extremely fast rise times.

In cases like this there is still a very close relationship between the two. The quickest way to explain it is to use Fourier analysis, a mathematical technique that starts from the premise that any repetitive waveform can be expressed as the sum of a number of sine waves. Using Fourier analysis it turns out that some quite complex looking waves can be expressed as the sum of a small number of sinewaves. A good approximation to a square wave can be had by adding together just three sine waves, the first three odd harmonics, i.e: a sine wave of the same frequency as the square wave, plus ones of three and five times this frequency.

This leads to a rough rule of thumb when ordering a CRO. Buy one with a risetime equivalent to a bandwidth at least three times faster than the fastest square wave you are ever likely to need.

Of course, in all of this we have been considering the bandwidth as it applies at the input of the CRO. In practice, you don't have access to the CRO inputs so much as the CRO probes. In reality then, we need to consider the performance of the CRO *including* the probes.

As a matter of practical fact the probe leads can have a very real effect on CRO performance, especially at high frequencies. The input resistance and capacitance, should be of a similar magnitude to the input specifications of the CRO to avoid degrading the performance of the whole measuring system.

To put some numbers on this, it's worth memorising the formula:

#### Tr = 2.2RC

where R is the input resistance of the probe in parallel with the output resistance of the source, and C is the probe capacitance. Tr is then the rise time of the measuring system.

MAKER	DISTRIBUTOR	PRICE	B/W	СН			TIVIT		RISE	CRT	INPUT	Zin	REMARKS
		(\$)	(MHz)			LTS MIN (mV)	MAX (s)	ME MIN (µs)	TIME (nS)	(kV)	VOLTS (max)	(M//pF)	
Kikusul 538A	Dindima	256	5	1	5	10	0.2	1	70	1.2		1//30	
Goodwill GOS955	Emona	269	5	1	10	10	10	10	70	-	600	1//35	
Goodwill GOS3310	Emona	297	10	1	5	5	10	0.1	. 35	1.3	600	1//35	
Kikusui 559A	Dindlma	399	5	1	1	10	10	1	70	1.2		1//30	
Hung Chang OS620	Paton	450	20	2	20	5	0.5	0.2	17	2	600	1//20	and and so many
National VP5215A	Scientific Devices	450	15	1	2	1	0.5	0.2	23.4	2	600	1//30	<b>C</b>
Meguro MO1251 Aron BS601	Elmeasco	465 465	20	2	20	5	0.5	0.2	17	2	600 600	1//20	Component tester
Kikusul 5509	Elmeasco Dindlma	405	10	2	20	10	0.5	1	35	1.6	000	1//20 100k//28	
Trio CS1560All	Parameters	545	15	2	20	10	0.5	0.5	23	2	600	1//22	
Kikusui 5519	Dindima	560	20	1	10	5	0.5	0.2	17.5	2	-	1//30	
National VP5216A	Scientific Devices	567	15	2	2	1	0.5	0.2	23.4	2	600	1//30	
Kikusul 5513	Dindima	590	10	2	5	10	0.1	1	35	1.6	-	1//30	
Iwatsu SS5702	Electrical Equipment	598	20	2	10	5	0.2	0.5	17.5	2	500	1//30	7
Hung Chang OS615S	Paton	600	15	2	10	2	0.5	0.5	24	1.5	-	1//20	Portable
Trio CS1012	Parameters	611	10	2	5	1	0.5	0.5	35	2	500	1//35	
Hitachi V212	Standard	613	20	2	5	5	0,2	0,2	17.5	2.	500	1//30	
National VP5220A	Scientific Devices	620	20	2	2	1	0.5	0.2	17.5	2	600	1//30	and the second second second
BWD 824	BWD	625	35	2	20	2	1	0.2	10	6	800	1//30	0.11
Aron BS310S	Elmeasco	625	15 35	2	10	2	0.5	0.5	24	1.5	600	1//20	Portable
Hung Chang OS635 Kikusul COS5020	Paton Emona 539	630 631	20	2	10	5	0.5	0.1	17.5	2.2	600	1//20	Delay, single shot
Trio CS1022	Parameters	649	20	2	5	1	0.5	0.2	17.5	6	500	1//32	Delay, single shot
Kikusul 5502A	Dindima	658	5	2	1	10	.01	1	70	1.6	-	1//30	
Meguro MO1252	Elmeasco	675	35	2	10	5	0.5	0.1	10	6	600	1//20	Delay
Aron BS635	Elmeasco	675	35	2	10	5	0.5	0.1	10	6	600	1//20	Delay
Hitachi V222	Standard	749	20	2	5	5	0.2	0.2	17.5	2	500	at // <b>30</b>	
Kikisui COS5021	Dindima	767	20	2	5	5	0.5	0.2	17.5	22		_	Delay, single shot
National VP5231A	Scientific Devices	770	30	2	2	1	0.5	0.2	11.7	6	600	1//30	
Trio CS1352	Parameters	845	15	2	10	2	0.5	0.5	24	1.5	600	1//22	Portable
Hitachi V203F	Standard	849	20	2	5	5	0.2	0.2	17.5	2	500	1//30	Portable, delay
BWD 821	BWD	850	50	2	20	5	1	0.2	7	6	-	1//30	Mix-mag.
Hung Chang OS645 Kikusul COS5041	Paton Emona	940 951	45	2	5	5	0.5	0.2	7.7	15	600	1//20	Delay, single shot Delay, single shot
Kikusul COS5040	Dindima	978	40	2	5	5	0.5	0.2	8.8	12		1//30	Delay, single shot
Aron BS625	Elmeasco	998	45	2	5	5	5	0.2	7.7	15	600	1//20	Delay, single shot
BWD 830	BWD	999	35	2	20	5	1	0.2	10	6		1//30	Delay
Iwatsu 5706	Electrical Equipment	1000	30	3	10	5	0.5	0.1	17.5	12	-	1//32	Delay
Hitachi V209	Standard	1049	20	2	5	5	0.2	0.5	17.5	1.5	500	1//30	Portable
Iwatsu 5705	Electrical Equipment	1100	40	3	10	5	0.5	0.1	8.7	12	800	1//32	Delay
Hitachi V353F	Standard	1119	35	2	5	5	0.2	0.2	10	5.2	500	1//30	Delay
Trio CS1577A	Parameters	1156	35	2	10	2	0.5	0.1	10	6	500	1//22	
Trio CS1040	Parameters	1175	40	3	5	1	0.5	0.01	8.8	12	500	1//27	
Hitschi V422	Standard	1249	40	2	5	5	0.2	0.2	8.8	12	500	1//30	
Philips PM 3211 Kikusul COS5060	Philips Dindima	1355	15	2	10	2	0.2	0.5	23	4	800	1//25	Delau
Trio CS1060	Parameters	1392 1495	60 60	3	5	5	0.5	0.05	6 5.8	12	500	1//27	Delay
Hitachi V650F	Standard	1549	60	2	5	5	0.5	0.05	5.8	10	500	1//30	Delay
BWD 835	BWD	1550	60	2	20	5	1	0.05	5.8	10	-	1//30	RAN standard,
				_									delay, portable
Iwatsu SS5710	Electrical Equipment	1550	60	2	10	5	0.5	0.05	5.8	15	800	1//32	Counter and DMM available as extras
Kikusul DSS5020	Emona	1678	20	2	5	5	1	0.55	17.5	2	800	1//25	Digital storage
Hitachi V509	Standard	1795	50	2	5	5	0.2	0.1	7	12	500	1//30	Portable, delay
Hitachi V134	Standard	1837	10	2	5	5	2	2	35	2	500	1//30	Storage
Trio CS2075	Parameters	1995 1999	70 100	4	5	5	0.5	0.05	5 3.5	12	800	1//22	8 trace, delay Delay, single shot
Kikusul COS5100 Kikusul COS5020ST	Emona Dindina	2010	20	3	5	5	1	0.02	17.5	3.15		1/1/20	Variable
Kikusul CO3502031	Chindina	2010	20	2	5	5		0.0	17.5	5.15			persistence, storage
Philips PM3217	Philips	2020	50	2	10	2	0.5	0.1	7	10	800	-	Dual timebase option delay
BWD540	BWD	2100	100	2	50	5	1	0.05	4	12	- 1		Portable, single shot
Trio CS1100	Parameters	2158	100	2	5	5	0.5	0.02	3.5	16	500	1//22	Delay
Iwatsu SS5711	Electrical Equipment	2200	100	2	5	5	0.5	0.02	40	20	500	1//25	DMM available as option, delay, counter
Kikusul COS6100	Dindima	2300	100	5	5	5	0.5	0.02	3.5	20	_	1//20	Delay
Hitachi V1050F	Standard	2395	100	2	5	5	0.5	0.02	3.5	20	500	1//30	Delay
Trio CS2110	Parameters	2495	100	4	5	5	0.5	0.02	3.5	20	800	1//20	Delay, 8 trace
Kikusul COS6100A	Emona	2512	100	5	5	5	0.5	0.02	3.5	20	-	1//20	US Military, delay

## SPECIAL ETI SURVEY

#### The survey

We have arranged the 64 cheapest CROs we could find in price order. This is not an absolute ranking, because prices can be subject to fluctuations for all sorts of reasons. So a few words of caution: firstly, these prices do not include sales tax. If you have to pay it, add 20% for the government.

Secondly, some manufacturers include the cost of probes in their price while others don't, so you have to make allowances for that ( $\pm$ \$50 for a decent set). Thirdly, in the nature of the case, this is a very competitive market place, so special offers and deals are likely to abound. Confirm the prices of all the models you are interested in before making a decision. And don't be afraid to haggle.

Having said all that, though, the table probably gives a pretty fair indication of what you can get for your money. As far as we are aware it is a comprehensive look at the market below \$2500.

The only omission is kit CROs. These usually sell for around \$220 from Dick Smith, Jaycar, Altronics and other hobbyist supplies. We have restricted our table to CROs you can buy ready to run.

I believe you'll find it interesting reading, even if you are not actually going to rush out and buy a CRO. The big surprise is just how cheap things have become over the last few years. The market starts at just \$256. For \$450 you can get a very respectable 20 MHz CRO. That would be sufficient for most hobbyist applications, including TV and digital troubleshooting. For less than \$500 you can obtain models with timebase delay.

If you have a little more money, notice that digital storage is now available for as little as \$1678. In fact, two different models of digital storage CRO and one variable — Persistence CRO are available for under \$2500.

It doesn't seem to be true that cheap CROs are necessarily lacking in quality, either. At least two of these surveyed have satisfied stringent military requirements the BWD 835 is in service with the RAN and the Kikusui COS 6100 with some sections of the US Military.

#### The movers and shakers

The makers and distributors have been listed in the first two columns. We use the term 'maker' rather loosely. In the nature of the case oscilloscope manufacture is a fairly incestuous business, so it can be difficult to establish just exactly who does what, and where.

There was a rumour doing the rounds that there was one big factory somewhere between Tokyo and Seoul where all the Asian CROs come from. But it was only a rumour. We do know that Aron, Application, Hang Chung and Meguro all refer to the same beast in different clothing. Others may be in the same boat.

One company does stand out from the rest, and that is Kikusui, a Japanese manu-

facturer which aims to take over from Tektronix as the biggest maker of CROs in the world. For the last few years they have been marketing aggressively in the USA, particularly at the low cost end of the market with the results you see in the table.

Surprisingly enough, the distribution system seems relatively well ordered. A few of the big manufacturers distribute their own product, as does the only Australian maker in our survey, BWD. Otherwise, the distributors are the familiar ones who dominate the electrical market place for all sorts of products.

The only slight confusion you might find springs from the fact that both Emona and Dindima distribute Kikusui CROs. Their price structure tends to be somewhat different so it's worthwhile making a comparison before selecting.

#### The table

In all cases technical specifications are those that appear in manufacturer's data. It was obviously impractical to test them all ourselves. You may feel inclined to take certain figures with a pinch of salt.

In any event, the bandwidth figure is taken at the claimed -3 dB point, and rise time represents the time taken to rise from 10% to 90% of final value. In our survey we found values between 5 MHz and 100 MHz. This should be sufficient to cover most applications outside some rare high

frequency situations.

The maximum and minimum values of sensitivity for both amplifier and timebase are taken from the front panel labelling on all the machines. In general these will be extendible by five or sometimes ten with the magnification function.

Where available we have included the maximum permissible input voltage and the input impedance. This may be also be extended with the use of appropriate probes.

We have also included a column for the accelerating voltage used in the CRT. This is of some value when assessing cheap CROs, because it gives you some idea of the brightness of the trace during operation. As a rule of thumb when the voltage drops under 2 kV, you will have difficulty seeing a timebase running at 1  $\mu$ s or faster.

In the remarks column we have included some of the extra facilities that can justify some CROs being more expensive than one would expect given their bandwidth and number of channels. Such things as delay lines, component testers, portability and storage facilities come into this category.

#### References

ETI April 84 — Bandwidth, probing, and precise Interval measurements — goes Into probe considerations In some depth.

ETI December 79 — The ublquitous oscilloscope — looks at the fundamentals of how a CRO works. ETI January 79 — Use a scope — why you need one.

#### **DISTRIBUTORS INDEX**

Altronics: Stirling Street, Perth WA 6000. (09)328-1599. Supplies a Goodwill CRO in kit form.

**BWD:** Miles St, Mulgrave Vic 3170. (03) 561-2888. Australian manufacturer and distributor.

Dindima: PO Box 106, Vermont Vic 3133. (03)873-4455.

Dick Smlth Electronics: PO Box 321, North Ryde NSW 2113. (02)888-3200. Supplies a kit CRO.

Electrical Equipment: 192 Pacific Highway, Arncliffe NSW 2205. (02)597-1155.

Elmeasco: PO Box 30, Concord NSW 2137. (02)736-2888.

Emona: 1st Floor, 720 George St, Sydney NSW 2000. (02)212-4815.

Hewlett-Packard: 31 Joseph St, Blackburn Vic 3130. (03)895-2895. A manufacturer of a fine range of oscilloscopes. Unfortunately all out of our price range.

Jaycar: 117 York St, Sydney 2000. (02)

267-1614. Another hobby supplier with kits available.

Parameters: 41 Herbert Street, Artarmon NSW 2064. (02)439-3288.

Paton Electrical: 90 Victoria St, Ashfield NSW 2131. (02)797-9222.

Philips: 25 Paul St, North Ryde 2113. (02)888-0443.

Scientific Devices: 2 Jacks Rd, South Oakleigh Vic 3167. (03)579-3622.

Standard Communications: 6 Frank St, Gladesville NSW 2111. (02)816-4755.

Tektronix: 80 Waterloo Rd, North Ryde NSW 2113. (02)888-7066. Probably the biggest oscilloscope maker in the world, with some high quality products available. Unfortunately, it was all too expensive, and none of its CROs were included in our sample.

Vicom: 68 Eastern Rd, Sth Melbourne Vic 3205. (03)62-6931. Distributes a number of makes and models of CROs. Unfortunately, no details were forthcoming before we went to press.

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"Many thanks for your efficient and courteous service." M.F. O'Connor, ACT

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Thank you for your letter and the advice it contained. l appreciate that advice. T.R. Balnarring, Vic

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## EQUIPMENT REVIEW

Some time ago ETI was asked to review some NiCad batteries by a local agent, Power Sonic. Well, that is a bit of an odd request, and not one which can be done quickly, it turns out. Nevertheless, here is the result, and we trust that it is informative.

## **Jonathan Scott**

# "POWER TO THE PEOPLE"

WHAT ARE THE most interesting attributes of a battery? Well, some idea of the capacity of a cell (i.e: the actual amount of energy delivered per charge) is important. Also the span of the cell (i.e: the number of charges it will deliver before expiring) is important. These two are, of course, related, as will be shown, for the capacity varies with age, measured in time and usage. In addition some measure of how much punishment a cell will take would be informative. Availability of the less usual sizes and shapes is a final factor of concern to people who want custom cell configurations or whose gear does not use the ubiquitous 'penlite' (AA) cell.

The latter requirement is easily summed up, for Power Sonic offers a large range of cells: the standard penlite (AA) cell, C cell and D cell; the AAA cell which is thinner than the AA, the N cell which is both thinner and shorter, the 2/3AA which is not surprisingly 2/3 as long as an AA; a cell called the 'SC', which is a short C cell (clearly) and a short D cell, you guessed it, the 'SD'. There are also the F cells, which are long D cells.

In addition to the above range of standard cells Power Sonic offers the C cells and the short C cells in a fast charge type, and AA, 1/3AA, SC, C and D cells in high temperature types. We reviewed only the standard sorts, so we cannot make any comments on the special types mentioned last.

Cells can be purchased by OEMs in any configuration required, all welded together and encapsulated on heatshrink envelopes, if so desired. This facility is very useful for manufacturers who wish to minimise bad connection problems and/or prevent accidental mixing of old and new cells in their equipment, etc. Certain cell combinations are available with PCB mount connections intended as memory backup cell packages. While this is probably of little interest to individuals, it at least indicates some serious commitment on the part of the local people.

#### Capacity

Initially we verified that the cells delivered their quoted capacity. This they all did, with some in hand. The capacity of a battery is usually measured in Ampere-hours, or Ah. For a moment let us measure in milliWatt hours per cubic centimetre (mWh/cm<sup>3</sup>) which is a method more often used to compare batteries of various types, because it reflects the energy stored per unit volume. The PS cells were quoted as having between 50 and 85 mWh/cm<sup>3</sup>, which is pretty standard. It rises from the minimum with the smallest cells, because they have a lot of case per unit of active ingredients, to the maximum with the F and D cells, which are pretty hefty and have a lot of insides for the case. By way of comparison, a lithium cell



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(inc. tax)	C (2 Ah)	\$9.40
	D (4 Ah)	\$15.90

3. 61-

2010. \$3.30

might have as much as 800 mWh/cm<sup>3</sup> (the spec for Hellensens cells, 1983), while a sealed lead acid type offers typically up to 55. The PS AA cell we tested first was specified as offering 70, but delivered 80 mWh/cm<sup>3</sup>. (All the tests, we should note here, were carried out in a room at 25°C, at the 10 hr rate, unless specified otherwise.)

Back to familiar Ah. The 500 useful charges which you may see quoted in advertisements for NiCads seemed to us to be very optimistic. If a penlite cell was to be charged and discharged at the recom-mended rate (14 + 10 hrs) this lifespan represents 500 days of continuous testing. Even ETI's enthusiasm does not stretch to over a year and a third per cell. It was thus decided to do only a couple of cells, and to both charge and discharge them at a fast rate say 1 amp. This represents really working them hard indeed! The results are shown in Figure 1. Ignoring the small variations, it can be seen that the cell started at just over the 500 mAh quoted, rose to about 600 mAh, and did not fall to 500 again until about cycle 300. The average capacity (the integral under the curve) did not fall to 500 mAh until almost 500 charges (at the accelerated rate!) had elapsed. We have not done this to another brand for comparison, but we figure it is pretty good. The small aberrations in the figure will be discussed - they have been deliberately shortly included to indicate certain responses to abuse. Cells not subjected to the odd circumstances produced similar results to the one whose graph is shown.

#### **Thrashing!**

Next we decided to see what kind of thrashing a cell can take. We noted in the literature that cells below C-size could take fast charging, by which the manufacturer means



Bolled. The 1/3AA cell destructively tested lost its 'innards' harmlessly via the end seal.



about double rate. We had run the penlite at the 2 C rate, which means it would be expected to discharge in half an hour if it were ideal. Ratings usually assume the C/10 rate, or discharge over 10 hours; 2 C is 20 times overspeed already. Although we did not do a comprehensive test (that is, work the cell to complete death) we noted no serious deterioration over tens of charges at 4 C. At 10 C, however, a cell overheats. It appears to be as we suspected -- it is the heat that boils the internals and breaks the seal which brings about the demise of a cell. At 10 C a PS-1/3AA, rated at 110 mAh, died quickly, exuding its lifeblood swiftly but harmlessly. So we have verified (in limited trial) that the cells will run quite happily at 2 C, or twenty rated, but that they will die by overheating at 10 C.

It should be noted here that the cells are rated up to 45°C charging and 65°C discharging. The lower charging temperature is because more power is dissipated when charging. When nearing full charge the cell dissipates what energy it cannot assimilate as heat, requiring greater dissipative capability for the same internal temperature.

Thus the above operation at 2 C has assumed that you will be careful not to overcharge the call. What does happen if the cell is overcharged by a factor of two. provided that the seal is not badly ruptured and the contents escape? What happens if the cell is discharged to less than 0 volts. such as might happen if one cell in a stack has less capacity than the others, and is reverse charged by the others still discharging? These two conditions were simulated on the cell whose 'lifeline' is pictured in Figure 1.

At the point marked A, or rather between the discharge capacity plots at either end of the short jump, the cell was doubly charged and rested until quite cool (overnight). Its temperature exceeded 60°C, but did not give indication of gassing. As you may see from the graph it exhibited elevated capacity for some charge cycles afterward. The capacity fell back to about where it was expected to have been in 10 to 20 cycles. How the cell appears to have held some of the excess, we are not sure.

The cell was only discharged until the terminal voltage fell to .9 V at 1 A load, so a small increase in resistance could have masked some charge retained. Alternatively, the cell may have suffered some beneficial restructuring of its electrodes. In either case the cell returned to its former self. (We favour a mixture of the above reasons. We cannot eliminate the second because we are quite sure that the cell was being fully charged at each charge phase. Also the rest may have been the beneficial factor, rather than overcharging.)

At point B the cell was reverse charged about 20% on one C. Again the effect lasted some charges, but this time it was such as to deplete the apparent capacity. Again the cell returned to being its former self. This indicates a fairly acceptable response to mild abuse, as so often arises in the daily use of, say, batteries in a portable appliance.

The final point of interest is the continuous array of 'noise' on the curve of Figure 1. We kept the temperature of the test room constant, and the current and times were set to within 5 mA and 14 seconds. There is clearly some other factor for which we were not controlling. We have honestly no idea what it might have been, but we are likewise sure that you will not be controlling for it either, so it does not matter greatly -- the curve represents what you might expect. In summary, though we have no good

reference with which to compare the performance, it was most satisfactory in our opinion. A cell rarely gets used to the end of its natural life in our erratic and experimental environment, so the tests are not representative of what we would expect from a cell, but can be taken as good representative data for a cell used carefully and regularly in a cassette player, provided it is charged according to requirements, etc.

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## NEW EQUIPMENT

# **Fluke Series 20**

**J**ohn Fluke Manufacturing has expanded its line of handheld multimeters with a new family of heavy-duty analogue/digital multimeters.

The new Fluke 20 Series, like the recently introduced 70 Series, combines a digital meter with an analogue meter. The 31segment analogue bar graph makes measurements such as peaking, nulling and capacitor checking and provides an easy way to test for erratic or unstable signals.

To keep operation simple, functions are selected with a single rotary switch. High-speed auto ranging automatically selects the correct measurement range and positions the decimal point. Display annunciators clearly indicate range, polarity and any activated features.

According to the company, the new meters have been built to withstand the environmental and electrical abuse common to construction. maintenance, petrochemical and other heavy industries. They are guaranteed to operate in calibration for one year despite drops, shock, vibration, or water and chemical spills. All components are shock-mounted and safeguarded with high-energy fuses and extensive overload protection to prevent mechanical damage or electrical shock. In most overload circumstances replacement of fuses is not even required. Extensive shielding protects the meters from excessive electromagnetic interference.

A new liquid crystal display works at extreme temperatures. Operation is guaranteed from  $-15^{\circ}$ C to  $+55^{\circ}$ C and to  $-40^{\circ}$ C for 20 minutes. Typical continuous operation is from  $-20^{\circ}$ C to  $+60^{\circ}$ C.

The new 20 Series incorporates a new integrated circuit developed by Fluke exclusively for it.

The new chip improves overall accuracy and gives the meters a higher voltage capability with more current ranges. It offers 0.1% dc accuracy, 100  $\mu$ V to 1000 Vac and dc, 0.1  $\mu$ A to 10 A ac and dc; and 0.1 ohm to 32 M resistance. It can also measure up to 10 000 M with a con-

Series 20: The four new meters from Fluke.

ductance function and measure in-circuit resistance without turning on diodes or transistors. A continuity beeper, assists

wiring, diode and transistor checks.

The Software Touch Hold, first introduced with Fluke's 70 Series, has been further improved in the 20 Series. Using Touch Hold, the user can take readings of critical circuitry while watching the probes. Using standard test leads, the meter captures a stable measurement and locks the digital reading in the display for viewing. It automatically beeps and updates when a new stable measurement is established. The analogue bar graph continues to follow the input signal while the digital display is held between measurements.

Fluke has given considerable attention to operator safety for the high-risk environments for which these meters are intended. The 20 Series' built-in safety features include extensive overload protection, high energy fuses and case designed to protect the operator from voltages one thousand volts greater than safety standards require.

The case is also constructed entirely from non-metallic materials with special recessed input jacks to accommodate safety-designed test leads.

For more information contact Elmeasco Instruments, 15 McDonald St, Mortlake NSW 2137. (02)736-2888.

# Fibre optics catalogue

Now available from STC-Cannon Components is the latest Fibre Optics summary catalogue. Contents include sections on optical interconnection design, interconnection components, test instruments and in-

stallation equipment. The Fibre Optic Summary Catalogue is available on request to STC-Cannon Components Pty Ltd, 248 Wickham Road, Moorabbin, Vic 3189.

# NEW EQUIPMENT

# New variable auto-trannies

Electromark has introduced a new series of variable autotransformers suitable for any equipment which is controlled by voltage variation. The SD range covers power ratings from 500 VA to 200 kVA and motor driven models are also available for remote control operation.

The M-types, ranging from 500 VA to 10 KVA have been manufactured in Japan to Electromark specifications for Australian use. Models up to 15A incorporate Australian standard output sockets and are fitted with input cords and plugs. A voltmeter is fitted as standard equipment and the larger models have a circuit breaker in place of the fuse.

For more information contact Electromark, 43 Anderson Rd, Mortdale NSW 2223. (02)570-7287.



regulators nom sapan.

# Programmable spectrum analyzers

A ccording to the company, the new HP 6566B (microwave) and HP 8658B (RF) spectrum analyzers from Hewlett Packard have enhanced functions to process both incoming signals and traces as well as a set of command flow functions to make decisions like a computer.

More than 120 new functions have been added to the B versions of the HP 8566 and 8568 when compared to their A version predecessors. The signalprocessing functions include Fast-Fourier Transforms and power-bandwidth calculation of incoming signals. Trace-processing functions include storing of the minimum value of a trace and finding the standard deviation of trace data.

Users of the new analyzers can define a set of front-panel keystokes for a desired measurement routine. This set then can be stored in a single softkey, accessible either from the front panel or over HP-IB (Hewlett Packard's enhanced implementation of IREE-488). This capability simplifies manual and remote operation.

New functions like IF, THEN, ELSE, ENDIF, REPEAT and UNTIL allow the user to write complete measurement programs on the HP 8566B and 8568B that can be executed without an external computer. For production test, for example, a computer can load a 'command flow' program into several analyzers. The analyzers can then run a program as standalone instruments.

Looking at the Spectrum: Two new spectrum analyzers from H-P with advanced logic.

In an automatic test system, this enhanced internal processing through softkey access of routines and through command flow programs frees the system controller to devote more time to other measurement and processing tasks.

There is 16K of RAM available for storage of user-defined routines. The new analyzers can transfer a CRT display to an X-Y plotter without an external computer. This function can be executed from a front panel or over HP-IB. It can also store and plot more than eight full 1001-point races or 80 compressed traces, also without an external computer.

Programs written for the HP 8566A and 8568A can run on the new B version analyzers. The HPL Pac and BASIC Library for the predecessor analyzers are directly compatible.

For more information contact H-P at 41 Joseph St, Blackburn, Vic 3130. (03)895-2895.

# Low cost function generator

**P**arameters has released a new versatile signal generator that provides all the major functions of a standard generator plus a complete sweep generator capability.

Designated the TFG-4613 and manufactured by Topward Electric, the new generator can produce sine, square, triangle,  $\pm$ ramp,  $\pm$ pulse, am, fm, sweep trigger gate and burst waveforms over eight frequency ranges from 0.1 Hz to 13 MHz. Amplitude modulation frequency range is 0.01 Hz to 10 kHz to a depth of 100%. With an external source this range is extended to dc to 1 MHz.

Frequency modulation can be applied to the main waveform to  $\pm 5\%$  over a frequency range from 0.1 Hz to 10 kHz and, once again, this may be extended from dc to 50 kHz with an external source.

Frequency accuracy of the primary waveform is  $\pm 5\%$  of full scale and distortion is less than 0.5% THD from 10 kHz to 50 kHz. Output is continuously variable from 1 mV p-p to 20 V p-p into an open circuit.

Sweep generator range is 100:1 on any range. The sweep is linear between start and stop frequencies set by the user. Sweep rate is variable from 0.1 Hz to 10 kHz.

The output frequency of the TFG-4613 may be varied by the application of an external voltage over a 1000:1 range. The frequency versus voltage curve is linear with 0.5% over a 100:1 frequency range.

For further information please contact Parameters Pty Ltd, P.O. Box 573, Artarmon NSW 2064. (02)439-3288.



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(02) 20588 ext. 271 (Mr G. Wilson). Application forms may be obtained by phoning the Employment Officer, Sydney Opera House, Bennelong Point, Sydney on (02) 2-0588 and are to be returned to this office by Friday, 1 March, 1985.



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# **'Blue light' is brought to market**



**L**EDs using crystalline semiconductors such as gallium, Dphosphorus and arsenic have provided red, yellow and green light for more than a decade now. LEDs that give off blue light, however, were still in the development stage.

Two years ago, Siemens devised a method of manufacturing 'blue light chips' at a considerably lower price, admittedly without matching the price level of LEDs in the other colours. Having sounded out the market, the company has now decided to include the fourth LED colour in its 1985 range — trial quantities can be indented from early 1985. The new blue LED (SLB 5410) operates at 480 nm and uses silicon carbide (SiC) as starting material.

SiC has emerged as the optimum semiconductor for blue light after years of research. Although it is labor and cost-intensive to produce, it has significant advantages over ZnSe or GaN: the SLB 5410 has a forward voltage of typically 4 V (20 mA), the corresponding figures being 10 V (20 mA) for ZnSe or GaN types.

The purity and reproducibility of the blue LED's 480 nm radiation are unequalled. Further characteristics are a high impulse stability, a narrow spectral bandwidth and a very low aging rate. These features make the new LED suitable for use as a radiation source in spectroscopic, biophysical or medical applications, as a calibration source for TV camera and photographic equipment, and, later on, possibly even as a means for producing the blue luminous dots on flat colour screens.

The blue LED is less suitable for use as a mere on/off indicator than its red, yellow and green counterparts. Apart from higher costs, the angle of radiation and the intensity are lower than in conventional LEDs. Typical values ate 4 mcd (20 mA) measured in the optical centre axis at a half-angle of eight degrees.

The SLB 5410 is mounted in a plastic package (5 mm); other packages are possible on request to Siemens Ltd, 544 Church St, Richmond Vic 3121. (03)429-7111.

# Flash converters

A mpec electronics has announced the release of a range of CMOS flash converters.

These molybdenum gate converters provide high performance and powerful solutions for flash applications.

The 6-bit, MP7682 is a pin compatible, replacement for the RCA CA3300. It's faster (up to 30 MHz), more accurate (linearities to  $\pm \frac{1}{4}$  LSB), lower cost and less demanding on your signal conditioning (1 V reference). Two units in series provide 7-bit resolution, and two units in parallel double the through-put rate.

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power (75 mW), two-step converter with a 10 MHz sampling rate. Its low cost makes it ideal for use in applications where price and power consumption are barriers.

The MP7684 us an 8-bit version. It has parallel architecture, a single 5 V supply, a 1 volt reference, and an overflow bit makes it one of the best available.

A 9-bit version will be available in 1985.

Samples, data sheets, applications boards and applications support are available. Please ring Ampec Electronics, PO Box 112, Fivedock NSW 2046. (02)712-2466.

# IEC cord-type plug

**T**EC-type mains connectors are now fairly widely used on a variety of equipment. Usually a chassis-mounting plug is fitted to the equipment for power input, with a mating cord-type socket used on the end of the power cord. Both such fittings are now stocked by many dealers and retailers, along with complete cords.

However if you've been looking for a cord-type plug to attach to a different monitor than an IBM, Geoff Wood has just obtained limited stocks of a highquality imported plugs suitable for fitting to existing cords, and is offering them for \$2.95 each plus tax and postage if applicable.

Enquiries to Geoff Wood Electronics Pty Ltd, 656A Darling Street, Rozelle NSW 2039. (02)810-6845.



# **Connector** catalogue

STC-Cannon Components has released the "MS standard Circular Connectors for Commercial, Military and Industrial Applications".

The 28 page catalogue details contact arrangements; alternative insert positions; solder/ crimp contacts; high voltage cartridges; RFI shielding; mains power connectors and accessories. Various shell styles, with complete technical details, are listed, i.e. wall mounting receptable, cable connecting plug, box mounting receptacle, straight plug and 90 degree angle plug. The MS Standard Circular Connectors Catalogue" is available on request to STC-Cannon Components, 248 Wickham Rd, Moorabbin, Vic 3189.

# Nectronics — new distributor

A new components distribution company has been formed by Irene Pellett who has had 20 years experience as a sales representative for firms supplying electronics components. She is joined by Lino Giambertone as sales consultant in the venture.

Nectronics Pty Ltd will act as sole agent for Euro Dip, marketing a range of machined IC sockets, as well as for American Research and Engineering, makers of dip switches. Nectronics has also been appointed distributor for the Japanese company NEC. Other products Nectronics will carry include flat ribbon cable, connectors and sockets and other components from Japan, Europe and Hong Kong and Taiwan.

For further information contact Nectronics, 27 Lexton Rd, Box Hill, Vic 3128.

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# Manuals for semiconductors

Imark has again secured a limited quantity of data manuals with information on Japanese semiconductor devices. They include "The Transistor Manual", "The Transistor Substitution Manual", "The Diode Manual", "The FET Manual", "The Opamp Manual" (Parts J and 2) as well as a series of IC manuals, computer manuals and "The Power and Industrial Semiconductor Manual".

The manuals are priced at \$10.95 each plus \$4 p&p 1-13 manuals.

Further information is available from Imark Pty Ltd, 167 Roden St, West Melbourne, Vic 3003.



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# IMPROVED CDI — using shaped pulses

Part 1

## by lan Thomas

Capacitor discharge ignition systems are much more efficient than conventional or transistor assisted systems, and they're also much better at getting maximum performance from today's engines. Unfortunately early CDI systems were also unreliable and plaqued by problems such as engine cross-firing. Here's a design which gets new around all of the problems, but still gives you all of the benefits.

THERE HAS BEEN a lot of criticism of capacitor discharge ignition systems in the technical magazines recently. Some of it was well founded and some was merely relevant to particular designs. From what I've read the principle has often been blamed for problems in certain circuit realizations and so, after some discussion with our beloved editor, I decided to set the record straight.

#### **Desirabilities**

The first thing to get clear is exactly what is desirable in an electronic car ignition system. The basic requirement is to generate a spark to set fire to the air fuel mixture in the engine cylinders at exactly the right time. As most cars today have to meet emission control requirements, this means that the air fuel mixture is leaner than optimum and so the spark should last for as long as possible. The next requirement is that the voltage pulse applied to the spark plugs rises rapidly so that, if the plugs are fouled comparatively little energy is lost in the fouling and most of the energy actually goes into the spark. CDI systems are exceptionally good at this, in fact, they're too good. This means that the pulse applied to one plug will capacitively couple to other plug leads



and possibly fire the fuel in other cylinders at terribly wrong times. This creates two mutually opposed requirements for the ignition system; the firing pulse leading edge should be fast but not too fast. Clearly if we are to design an ideal system the output voltage pulse rising edge should be a controlled parameter.



The third and, in my opinion, the most important requirement is that the system must be reliable. The most incredible whizzbang, mind blowing device ever contrived could be built, but if it only works 90 per cent of the time it's not worth a damn and shouldn't be let anywhere near a vehicle. In the good old days when CDIs first made their appearance it simply wasn't appreciated just how hostile the environment under the bonnet of a car is for electronics. I'm sure that if the Marquis de Sade was alive today and thought about what electronics is subjected to near a running petrol engine he'd get that kinky warm feeling all over. It's stinking hot. There are fierce voltage transients everywhere. The power supply is subject to violent excursions. There are corrosive fluids around. All in all it's a good place not to put sensitive semiconductors. Nonetheless we went blithely ahead and attached our crude systems right next to the coil and cursed everything in sight when they fell over even though we'd used only cheapest commercial components the available.

In these more enlightened times all devices used for automotive operation are usually specified for the full military temperature range of  $-55^{\circ}$ C to  $+125^{\circ}$ C. In Australia the  $-55^{\circ}$  may not be necessary but the  $+125^{\circ}$  is.

Finally, our ideal system should allow for the output pulse to be increased during starting and also it should be controllable so a car burglar alarm can shut it off. It should be triggerable from a Hall effect sensor or existing points. It goes without saying that it should not be possible to damage the device from any terminal by wrong connection or inadvertent shorts.

In the "wouldn't it be nice if . . ." category I thought an ideal system would not be effected by supply variations and would give the same output pulse no matter what the supply voltage.

As a matter of technical nicety it would also be good if the device was as efficient as possible so things didn't get too hot (see above about reliability). As a general rule the reliability of any electronic component halves for every 11°C temperature rise. That is, if a device (say a transistor) is expected to fail once in 10 000 hours at an operating temperature of 55°C then at 66°C it





# Project 342

can be expected to fail once in every 5000 hours. You can see where this leads if we operate the transistor at 125°C. To save you the trouble of digging out your calculator it's a failure each 121 hours to be expected! And we used to bitch when our electronics which was baked to a light golden brown failed!! An excellent rule is cold is good.

#### Problems

Herein lies the problem with some of the newer transistor assisted ignition systems. By the very nature of the way they work the coil has to run too damn hot and the electronics not all that much cooler. The energy to be used in the spark is stored inductively in both ye olde Kettering system and its more modern equivalent transistor assisted ignition. This means many amps and hence entirely too many watts. This was the original appeal of the CDI; the spark energy is stored in a capacitor which is very efficient. However the early CDI systems did have their problems and not all were associated with operating at too high an ambient temperature. A lot of their unreliability was due to design problems.

To illustrate this consider the circuit shown in Figure 1 which represents a fairly typical design. The high voltage to charge the dump capacitor C2 is generated by a conventional push-pull inverter. The output of the inverter is rectified by the diode bridge and applied to one end of C2. The other end of C2 is connected to the coil and the dump silicon controlled rectifier (SCR) is connected to the high voltage side of C2 and when an output pulse is wanted it is triggered on. This causes the coil side of C2 to go negative far and fast! This is all good and fine but it ignores several effects. The first is that saturating push-pull inverters isn't a very good way to charge a capacitor. If we consider what happens when one inverter transistor turns on we can see why. If, say, Q1 is turned on then it will impose the full supply voltage across one side of the primary winding. If the transformer is any



good then the voltage across the output winding is exactly the primary to secondary turns ratio times the supply voltage. If the capacitor is just beginning to charge then a conflict occurs and the voltage across the secondary must be taken up by other com-

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Figure 3. Data for a	typical SCR showing the limit for the rate of rise of	on-state current. (Courtesy of GE Semicondu	uctor Data Book.)	

ponents. Normally this voltage appears across the ignition coil and for the inverter to work correctly the coil must be entirely inductive. If there are large stray parasitic capacitances in the ignition coil primary then the load they present to the inverter may stall it or at best hold the main inverter transistors out of saturation until they're charged. This process is inefficient and makes for a hot inverter. Certainly the push-pull inverter will not tolerate a highly capacitive load.

The next and far more serious effect occurs when the dump SCR is triggered on. The actual works of an SCR consist of a piece of silicon, shown in cross section in Figure 2, along with an equivalent two transistor circuit. Physically the gate lead is connected to a point in the centre of the silicon die. When the SCR is triggered on a positive pulse is applied to the gate in the centre to momentarily turn on the NPN transistor in the equivalent circuit. This in turn turns on the PNP transistor and the very high positive feedback of the two transistors causes the SCR to go to a highly conductive state where it will remain until the current is removed.

However when the trigger pulse is first applied only the region near the gate terminal is turned on. It takes a microsecond or so for the on state to spread across the whole SCR. If the current is allowed to rise through the SCR faster than the on state spreads then the small part of the SCR that has latched on will be overloaded and the SCR will be destroyed! The region of the SCR will have far too high a current density flowing through it and will be burnt out. Figure 3 shows part of the data sheet of an SCR and is the absolute maximum ratings for the device. The line in colour relates to this problem. It shows that the on state current must not be allowed to rise faster than 100 A/µs. If such a circuit is to operate reliably then the current through the SCR must be controlled. This is normally done by inserting an inductor in series with the SCR and will ensure the SCR will survive most forms of abuse but if it's left out the CDI will have almost no resistance to a shorted coil or a highly capacitive coil primary. It doesn't matter how big the SCR is; it's easy to destroy it during the first instants of turn

The choice of capacitor for the dump capacitor is very important too. During the dump cycle the capacitor is discharged in about 50 microseconds (give or take) and this means torrents of amps. The old standby is Philips 'liquorice allsort' capacitors aren't good enough; devices rated for high current must be selected. If the capacitor is rated for 240 Vac operation then it's a good bet it'll be OK for CDI usage too.

Another mistake made in a lot of the earlier designs was to overdo things. Just because it was easy to do, people used to use outrageous voltages on the dump capacitors which in turn generated ridiculous ignition voltages. In my earlier days 1 was guilty of



this sin also. I recall looking in awe under the bonnet of my car in the dark when the engine was running. The whole ignition harness was outlined in blue corona discharge. Gadzooks but it was spectacular!! Not a whole lot of use though.

#### What should be done

This more or less outlines the things to avoid doing, but what should be done? Since CDI was to be used the first decision was the choice of an inverter configuration. One of the most flexible inverter circuits available is the so-called flyback, single ended or ringing choke inverter. The basic circuit is shown in Figure 4 and works as follows. When power is first applied Q1 is biased on through R1 and current flows through the primary winding of L1. Because this imposes the full supply across the pri-mary, the base drive winding generates a voltage equal to the primary-to-base winding turns ratio times  $V_{cc}$ . The base winding is connected with the polarity such that R2 also turns on Q1. As the voltage across L1 is constant (ignoring the saturation voltage of the transistor) the current through it will rise linearly with time and the current is described by:

$$l_{peak} = \frac{V_{cc}t}{L}$$

After a certain time R1 and R2 will not be able to provide enough base current to hold Q1 in saturation and its collector voltage will start to rise. This reduces the base drive voltage for R2 and further pulls the transistor out of saturation. The whole process runs away and results in Q1 being turned hard off. Before Q1 is turned off the current flowing through L1 represents energy stored in the inductor and this energy must go somewhere. Thus when Q1 turns off its collector voltage rises very rapidly and, as the output winding is also on the same core its voltage also rises until diode D1 is turned on. The energy stored in the inductor is dumped out through D1 into the output filter capacitor C1. As soon as all the energy is removed from L1 the voltages across all the windings go to zero (ignoring stray ringing effects) and the whole cycle can repeated. Thus C1 is 'pumped up' until its voltage is sufficient to turn on the zener diode ZD1 and hence Q2. When this happens Q2 steals all the base drive from Q1 and stops the oscillation. In fact if Q2 is removed or fails then the output voltage will rise until something breaks!

This circuit configuration has many advantages. The first is that when the current is being run up on the inductor the output diode is off and vice versa. This results in the output circuit being completely isolated from the input which means that doing bad things to the load doesn't bother the inverter at all. Even shorting out C1 completely doesn't damage anything. All that happens is that the current in L1 runs down to zero very slowly and the whole inverter acts as a constant current generator into the short. As soon as the short is removed the output voltage will rise to its regulated value again.

The second advantage is that the output voltage is determined by R4, R5 and ZD1 only. There is no relationship between the primary supply voltage and output at all. This enables the inverter to generate the same output voltage for a very wide range of input voltages always with (more or less) the same high efficiency. As an example the final inverter used in the ignition system will run off 2 volts to 25 volts in and the final voltage on the dump capacitor is 300 volts over the entire input range. The only thing that varies is the time taken to charge the capacitor.

The third advantage is that the circuit contains very few components and therefore should be very reliable. The only components that have to handle the full output power are Q1, L1 and D1. This compares *most* favourably with the more conventional circuit where there are four diodes and two



9

transistors as well as the transformer. In the final CD1 inverter an extra diode is needed as well as low power components but the overall design still comes out way ahead of the push-pull CD1 system. It even compares well with the components needed for the transistor assisted ignition.

If this type of inverter has all these advantages then why hasn't it been used before? To be quite honest I have to admit I don't know. The only real drawback is that the inverter inductor has to store all the energy being transferred. This makes the inductor somewhat larger than the transformer in a push-pull inverter. Another disadvantage is that the peak current in the inductor is large (in the final design it's about 18 to 20 amps) so the power drawn from the supply is 'lumpy'. This makes supply bypassing more important and a good quality bypass capacitor must be used (but more about this later). The recent upsurge in switchmode power supplies has made both of these problems very easily solved as ferrite cores are easily available to make a suitable inductor and good electrolytic capacitors are also now common.

Since the flyback inverter will do all we want it's a cert for the final design. The next problem to be addressed is that of short spark duration with a CDI system. Referring back to the circuit diagram of a 'conventional' CDI it is instructive to consider what happens during the entire dump cycle. The first thing to happen is that the SCR is triggered on and the voltage across it collapses to zero from the original 300 to 400 volts. As the voltage across a capacitor cannot change instantly the other side of the dump capacitor which is connected to the coil immediately goes to -300 to -400 volts. The voltage rise across the coil is usually of the order of a microsecond or so. The SCR remains on and clamps the inverter end of the dump capacitor so the coil end of the capacitor resonates with the ignition coil primary inductance. Typically the coil inductance is of the order of 10 millihenries and the usual value of dump capacitor is 1µF so the combination resonates at about 1.6 kHz. The waveforms appearing on the coil primary and the coil primary current are both shown in Figure 5. The coil primary voltage is described by a -cos function and the coil current is described by a sine function. The colliderent starts at zero when the SCR first turns on then rapidly rises to a maximum value. It then falls away to zero. As the coil primary, dump capacitor and SCR are all in series the same current flows through all three components. The SCR will only carry current in one direction so when the coil current reaches zero and starts to go negative the SCR will turn off. At this instant the dump capacitor has re-acquired some of its original charge so as the SCR turns off the capacitor is left partly recharged and not all its energy is diverted into the coil. The coil secondary modifies this behaviour a bit by reflecting a comparatively low impedance into the primary circuit which has the effect of shortening the resonant period; but this is the basic cycle for the 'conventional' CDI.

Suppose the dump circuit is modified by adding a diode in parallel with the ignition coil as shown in Figure 6 along with the coil primary voltage and current. The first part of the dump cycle is exactly as for the earlier circuit. The SCR turns on and forces the coil-capacitor node negative. This reverse biases the diode which has no effect on the first quarter cycle of the dump. The coil primary current rises following a sine curve and peaks when the coil voltage passes through zero. The coil secondary voltage rises very (very!) rapidly following the primary voltage until the spark gap breaks down, then collapses to a comparatively low value (only 1000 volts or so). The coil secondary current then rises following a sine curve similar to the primary except at a lower level.

However when the primary voltage attempts to change sign due to resonance with the dump capacitor the clamp diode turns on and shorts out the coil primary. This forces the currents in both the coil primaryclamp diode circuit and secondary-spark gap circuit to decay away due to losses in the two circuits and the dominant loss is the spark gap. Thus all the energy in the dump capacitor is transferred to the coil and thence to the spark gap. This has the effect of lengthening the spark duration exactly as is wanted! Even if the turbulence in the combustion chamber momentarily blows out the arc it is open circuiting an inductor carrying high current so the gap voltage immediately rises to (very) high levels again and restrikes the arc. This would seem to be a pretty good way to have things as in the final design the arc duration is usually about 0.6 to 0.8 milliseconds - about the same as in the newer transistor assisted ignition systems. The only drawback with inserting a clamp diode in a 'conventional' push-pull inverter system is that during the capacitor recharging cycle the inverter must drive into an almost purely capacitive load, as during charging the clamp diode is forward biased and shorts out the coil for the inverter. For reasons already discussed push-pull inverters just won't work into this sort of load. As luck would have it the flyback inverter is perfectly happy working into these conditions (not entirely coincidentally). There is also the further slight advantage that when the inverter is recharging the dump capacitor, the coil is inherently shorted out by the clamp diode and absolutely no charge voltages appear at the coil output. It also means that after the first quarter cycle of the dump the diode is turned on and, if necessary (if you're really in a hurry) capacitor recharging can be allowed to commence, but more of this later.

#### **Rise time & pulses**

This seems to solve all the so-called problems associated with CDI except that of too fast a rise time on the output voltage. To be honest this one had me worried for a while and the CDI which I used with great success for many years had the so called cross firing problem (but never gave a sign of it). The car I had then was a Mazda R100 and in fact there were two CDIs in it as the car had dual ignition. The rotors were fired alternately and it would seem an ideal arrangement for cross firing but then the leads were 🏋 spaced well apart. All in all I have very fond memories of that car - apart from the fact that engines had to be regarded as consumables but no matter. My current iron steed. is a 4.2 litre V8 Torana so cross firing had to be dealt with.

To understand the way the coil primary voltage rise time is controlled it's necessary to look into the design of the flyback inverter in some detail. A decision that was made very early on in the design was that the inverter transistor on time was to be about 20 microseconds. This is about as fast as the inverter can be run before transistor switching losses become a nuisance. For the dump capacitor to be recharged in 2 milliseconds or so the peak inverter current has to be allowed to run up to 15 to 20 amps before turn off. At first this may seem a bit fierce but there's no trouble finding suitable transistors and the main inverter transistor has a much easier life than the one used in a transistor assisted ignition! The high currents make it a good idea to run the inverter at as high a frequency as possible to make bypassing manageable. Given the inverter peak current and runup time the value of inverter primary inductance can be calculated from:

$$Lp = \frac{Vt}{I_{pk}} = \frac{13.6 \times 20 \times 10^{-4}}{20}$$
$$= 13.6 \text{ microhenries}$$

For many and varied reasons the inverter primary to secondary turns ratio needs to be 6 or 7 to 1. This from simple arithmetic means that the inductance seen looking into the inverter transformer secondary will be 36 or 49 times the primary inductance. In the final inverter the turns ratio is 6.4 to 1 so the inverter secondary inductance is about 550 microhenries. So what did I hear you say? Tut! have patience!

It isn't written in words of fire that the in-

66 - ETI February 1985





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# Project 342



verter-rectifier-SCR-coil combination must be as shown in Figure 6. In the final design D1 is the rectifier diode, Q4 is the dump SCR and C2 is the dump capacitor. As long as D1 points the right way (it does) C2 will charge correctly. The only effect of moving the diode is that the secondary winding of the inverter operates at the dc voltage of C2 instead of ground (take note when deciding how much insulation is required). For the moment assume L2 and R16 are shorted out and C5 is removed. After capacitor charging is completed the inverter coil secondary, the anode of Q4 and the cathode of D1 are all at 300 volts. When the SCR, Q4 is triggered on to start the dump cycle it imposes the full 300 volts across the inverter coil secondary and the other side of the secondary starts to drive the dump capacitor negative. Everything proceeds as before except that the output to the ingition coil is driven through 550 microhenries. The very low impedance of the SCR is buffered by the inverter coil secondary. This means we are free to put components in parallel with the ignition coil to shape and tailor the rise time on the coil primary exactly as we desire. In the final design I chose to use a 150 nF capacitor so the rise time for the ignition coil primary is set by the inverter coil secondary and C6. For purposes of calculating output rise time the ignition coil can be treated as an open circuit (it's very large) and the dump capacitor can be treated as a short circuit (it's also very large). The rise time is completely controlled by the inverter secondary resonating with C6. To stop the two ringing a resistor R19 is included in series with C6 to complete the shaping of the rising edge of the voltage applied to the coil. All problems solved!

Moving the SCR to the other side of the inverter secondary has some other desirable features too. If the inverter is still charging the capacitor when the SCR is triggered, then the SCR immediately turns the inverter off as it forces 300 volts across the coil secondary which turns the base winding for the inverter off. This ensures that if there is ever any dispute for control of the inverter coil the SCR wins!

This solution did give a few problems and these are the reason for L2, R16 and C5. Since the C2 side of the inverter secondary



has only the steady state charging voltage on it all the switching voltage of the inverter appears on the anode of Q4 (see Figure 7[a]). This means that the SCR anode has a very large changing voltage applied to it. An SCR, like any other device has stray capacitances between anode and PNP base, and PNP base and NPN base (refer to Figure 2 again). If the anode is driven positive very fast then capacitive currents are induced in both bases of the four layer structure. The SCR cannot distinguish between capacitive currents and gate trigger currents and so if the anode voltage changes too fast then the SCR will turn on. The manufacturers specify this effect as the maximum rate of change of off state voltage or off dv and for most el cheapo SCRs it is usually a mini mum of only 10 V/µs - trouble! RCA (which seems to be pretty good at making power semiconductors) makes a device with an off  $\frac{dV}{dt}$  of 100 V/µs which is the S5800 series. Lo and behold they were available in Australia!

One hundred volts per microsecond still isn't anywhere good enough but it gave me a chance! To completely solve the problem I had to use the old 'saturating inductor snubber trick'. This works as follows. L2 is an inductor wound on a toroid with very high permeability and R16 in parallel with it makes a very low Q inductor assembly. In fact its Q is so low that it forms a critically damped resonant circuit with C5 and the SCR strays (about 100 pF). This means that when the inverter secondary applies a submicrosecond square wave edge to the input of L2 and R16 the SCR only sees a controlled rising edge of (surprise! surprise!) 100 V/ $\mu$ s. Because R16 makes the LCR network critically damped there is no ringing and the SCR is happy (and so, presumably, are we).

But what happens when the SCR is triggered? While it is possible to make quite large inductance values with a toroidal core they are very easy to saturate with direct currents. When the SCR is off no dc can flow and the toroid passes only the small amount of ac needed to charge C5 and the SCR strays. When the SCR is triggered the resultant dc saturates the toroidal core in three or four microseconds and the inductance almost completely disappears. Clever eh? More than that it works just fine! This really completes the discussion of the basic shaped pulse CDI; the design gave me complete control over all parameters of the output spark. The system is completely insensitive to the type of ignition coil used, any sort at all will work well. In general if you have a coil with a ballast resistor for start boost it will work better if the resistor is not included but leave it in if you want. The current rundown is predominantly damped by the secondary spark voltage reflected into the primary.

Once the basic system was working fine I started to consider all the 'bells and whistles' that would be nice. The first to go in was to give the inverter some boost during engine starting. The final charge voltage on the dump capacitor is determined only by R4, R5 and R6 together and the zener diode ZD2. When the cathode of ZD2 is raised to about 10 volts through R4 it turns on Q3 and stops the inverter. R5 and R6 form a potential divider with R4. If R6 is shorted out the input to R4 must go more positive before ZD2 starts to turn on. Q5 does exactly this when the starter voltage is applied through the input network to its base and the output of the inverter goes up to somewhere over 400 volts during starting. This pushes the rating of the S5800D SCR so I don't recommend you use it this way all the time but for short periods it's OK.

The next nicety to be incorporated arose because of the remarkable performance of the final inverter. The final inverter would charge the dump capacitor to 300 volts from an input supply of 1.8 volts. It took a while to get there (see 'Testing') but get there it did! This was wonderful but there weren't enough volts to reliably trigger the SCR. Ridiculous! All those ergs there and no way to get at them! One of the nice things about a flyback inverter is that if several output windings are wound on the coil they all give output voltages related to each other by only their turns ratios. So three quick turns around the core, a five cent diode and a 20c capacitor and a regulated 20 volt rail was available to fire the SCR. This also ensured that an optimum trigger pulse could be applied no matter what the supply voltage.

One very nasty possibility did occur to me. Inside the inverter there is a diode directly across the output to the coil. If the 'earthy' side of the output to the coil was directly connected to ground and some misguided individual connected the hot output to the battery supply then the diode would be destroyed. It's big but not that big. To stop this possibility I connected the output ground to earth through a resistor (R18 on the circuit diagram). This would have almost no effect on the inverter regulation but very nicely prevents any mishaps. Since the secondary and primary sides of the inverter were no longer tied together this required a little thought concerning triggering. As the energy to trigger the SCR is referred to the secondary ground an optocoupler ensured that no matter what was done triggering would always happen when wanted and, better, wouldn't happen if not wanted due to stray currents in R18.

Very little modification is needed to make the circuit operate from a Hall effect sensor. A little performance is lost as the Siemens HKZ101 won't work on less than 4.5 volts but all that's needed is to change R13 from the 47 ohms for points to 300 ohms and replace the 180 ohm R14 with a 22 ohm resistor. Thus when the points input is open circuit (or off for the HKZ101) at least 8 mA flows into the optoccupler at the lowest supply voltage. R20 provides the recommended current limiting for the sensor as per data sheet.

#### **Testing the prototype**

After a certain amount of fiddling around with a rats nest, I built a prototype and had the wonderful experience of having everything work almost perfectly the first time. The board layout had no errors (amazing!) and the whole thing just happened. I had an ignition coil that I'd used on the development of the earlier CDI built some years ago that served just fine for testing. It has a primary resistance of 3 ohms and a turns ratio of near enough to 100:1.

The first thing to do was ensure that all the calculations about arc duration were correct. Photographs were taken with the coil and CDI rigged up on the bench. A rats nest version of a transistor assisted ignition was also built for comparison purposes. Calculations for the transistor assisted ignition said that the rate of rise of primary voltage should be about 15 V/µs and the tests confirmed this exactly. For the CDI I had already done the design for a rate of rise (in this case fall - the pulse is negative for the CDI) of twice this value as there is a compromise between fast voltage change firing dirty plugs and the risk of cross firing. Figure 7(b) shows that, once again the calculations were spot on. The negative going pulse leading edge has a  $\frac{dv}{dt}$  of so near 30 V/µs as not to matter. The photograph also shows that the coil secondary voltage also changes nearly twice as fast for the CDL

The arc duration was measured for the

CDI and the result is shown in Figure 7(c). The timebase for the CRO was changed to 100 microseconds/division and the arc duration was measured at 0.75 ms. This is almost exactly the same as is obtained in the currently fashionable transistor assisted ignition except that the CDI will maintain this arc duration up to 330 pulses per second with no reduction in spark power at all. This corresponds to an engine speed of 5000 rpm in my trusty V8 or 10 000 rpm in a four cylinder car. A transistor assisted ignition with dwell extension can equal this performance but only at the expense of very high coil currents. The pulse shaped CDI draws about 5 amps at these unrealistically high speeds and for normal driving usually draws about one amp.

The next point of interest with the CDI system is the coil secondary voltage and current. Figure 7(d) shows how these parameters vary with time during the spark. The lower trace is the secondary voltage and the upper is the current. It can be seen that before gap breakdown no current flows (reasonable!). After the gap arcs over, the gap voltage drops to about 1 kV and the current rises in a sine wave following the primary resonance of C2 and the coil primary. The secondary current peaks at about 70 mA when D10 turns on and shorts out the primary. After this the coil primary and secondary currents decay away to zero. When the secondary current reaches zero the arc is extinguished and the small amount of energy left in capacitances rings. This confirmed that all was happening as designed for in the system.

To show how the inverter works a photo-graph (Figure 7 [e]) is also given of the collector waveform for Q1. These pictures correspond to an engine speed of about 2600 rpm for a V8 or 5400 rpm for a four cylinder and so are representative. In the test fixture the dwell angle was 50 per cent as a square wave was used to trigger the system. The photograph shows the inverter recharging the capacitor in two milliseconds then shutting down. The rather ragged waveform a millisecond after the inverter shuts down is the CDI triggering, and voltage is due to C2 discharging through the inverter secondary being reflected back into the primary circuit. When Q1 is off its collector voltage sits at the supply voltage. It's interesting to note that the supply rail is being pulled down by the inverter when it's running and takes several milliseconds to recover. This is because rather light leads were used in the test fixture. It illustrates that when the unit is installed in a car good solid supply lines should be used.

A rough estimate of inverter efficiency was made by accurately measuring the value of C2 then measuring the current drawn by the inverter for exactly 100 pulses per second. The power out of the inverter is  $\frac{1}{2}CV^2$ x 100 = 11.8 W. To produce this output 1.4 A was needed from a 12 volt rail which gives an efficiency of 70 per cent but there are many uncertainties in the measurement.



Figure 7(b). Coil primary (lower) and secondary voltage for the new CDI. Time 10 µs/div, prim 50 V/div, sec 2 KV/div.



Figure 7(c). As above, but with 100 µs/div to show arc duration.



Figure 7(d). Coil secondary current (top, 50 mA/div) and voltage (bottom, 2 KV/div).



Figure 7(e). The waveform at Q1's collector (10 V/div, 1 ms/div).

# Project 342



Finally the time taken for the inverter to recharge C2 was measured as a function of the supply voltage and the results are shown in Figure 8. For normal operating voltages of 13.6 volts the capacitor is recharged in two milliseconds but even if the voltage drops to 7 or 8 volts (not unusual during starting in winter) the inverter still gives full spark power after five milliseconds. At 25 volts in (don't ask me how you'd ever get this supply voltage in a 12 volt system) the inverter recharged the capacitor in one millisecond.

The inverter stopped operating at about 1.9 volts and at just over 2 volts was still giving the full 300 volts out. It's hard to imagine how you'll start an engine with the battery this flat but it's nice to know that the ignition system isn't the cause of the engine not starting!

Just to satisfy myself that the design was right I operated the CDI in an oven at 70°C for half an hour. This was a far more severe test than was ever expected in real life as the hot air in the oven wasn't moving and made heat dissipation a major problem for the CDI. Nonetheless the system came through with flying colours. However, I can't emphasise enough that if you build the CDI up it's far better to mount it under the dash or somewhere else that's cool.





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82EF9

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ETI-162



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(Includes humidity sensor \$19.50).

Cat K42560 ET1256 \$29.50 Cat ETI 477



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83MA11

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83MA11



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**FTI-158** 

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(ETI Apr. '83)



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81 MC8



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83118

ETI-163

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# ALL NO.



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\$55.00

ETI-675



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82PS2



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ETI-688

**ETI-147** 

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ETI-164



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82A03A/B

12

ä

# Project 170

# PRECISION CRO CALIBRATOR



Anyone who has used a CRO will know what an incredibly useful instrument it can be — providing you can be sure of its calibrations. This handy low-cost calibrator will let you check and adjust all main settings easily and efficiently. Designed by the Electrical Engineering staff at Sydney University, it is easily built and can also be used to check out a CRO you're considering buying.

WHEN OSCILLOSCOPES or 'CROs' first started to be used in electronics, they were simply used to 'look at' what was going on in a circuit. Just being able to 'see' what was happening to the voltages and currents was a big step forward from what had been possible before, and for a while that was enough.

But nowadays people expect to be able to use a CRO in a much more quantitative fashion, to measure instantaneous voltages, voltage changes and timing details. In order to be used in this more serious way, a CRO naturally needs to be properly calibrated.

The small low-cost calibrator described here has been designed to make calibration of almost any reasonably modern CRO a fairly simple and straightforward job. It produces pulses of known accurate amplitude and timing, whose peak-to-peak voltage and repetition rate can be varied over a

wide range to allow most of a CRO's normal ranges to be checked.

While the calibrator can be used to check and adjust both gain and linearity for both the vertical and horizontal channels of an instrument, in practice most CROs do not provide a means of adjusting the linearity of the vertical amplifier(s) — only the gain. Similarly although you can use the calibrator to check horizontal linearity, most instruments only allow you to adjust the horizontal gain and average timebase sweep speed.

A CRO which was made more than ten years ago (fairly rare in the commercial world, but still common in hobby workplaces) can easily drift a few percent in a period of months. Hence the need for a handy reference like this calibrator.

In addition to its main intended use in keeping your existing CRO or CROs cali-

brated, it is probably also worth taking along when you are looking at new CROs with an eye to purchase. It has surprised the authors to see how badly some otherwise impressive units fared, even brand new.

We will now discuss how the calibrator is used, for those who may not have been exposed to such procedures before. In addition to checking vertical gain accuracy and sweep speed, we will also explain how to check overshoot, timebase non-linearity, the presence of a satisfactory vertical delay line for viewing non-repetitive triggering events, and vertical non-linearity.

Note that we must assume you have access to the operating manual of your CRO, for details regarding the manufacturer's recommended procedures in adjusting the instrument. This varies from instrument to instrument, and it is often important to adjust things like the vertical attenuator and timebase ranges in a particular order.

#### **Vertical calibration**

This is initially fairly simple. Referring to the first CRO screen photograph (represented in Figure 1), the instrument's appropriate present potentiometer is adjusted to obtain precisely the correct deflection. The photograph was taken on a brand new CRO on a medium voltage deflection scale, and so the trace is narrow and sharp. The adjustment should be done on the range(s) specified by the manufacturer, but the checking of calibration should be done on several of the available ranges.

Figure 2 shows the same (brand new) CRO on its most sensitive range. There are two points to note. The first is that the trace was fuzzy, primarily on account of CRO amplifier noise. This requires that the operator effect some estimate of the difference between trace centres. It is best to take one edge (in Figure 2 the lower) and adjust the vertical position so as to set the reference edge precisely on a graticule line. Then the same edge of the trace on the other part of the squarewave cycle gives the deflection.

In Figure 2 the deflection is clearly about 5.2 divisions. The second point to note is that the CRO (correct to the limit of measurement on the 50 mV/div range) is 4 per cent high on the 1 mV/div range, Thus the

need to check performance against specifications on all ranges is very clearly demonstrated.

#### **Horizontal calibration**

Initial sweep calibration should similarly be carried out at the sweep speed specified for the particular CRO, but a check is recommended over a wider range. Figure 3 is of a set of 10 timing pulses arrayed over a CRO screen. With the fine vertical edge of the first pulse set on the first vertical graticule line, the same part of the last pulse should align with the final graticule vertical. Even on the screen of the high quality and fast writing speed CRO used for this the intensity must be set quite high to clearly show the fast vertical edges.

#### **Timebase Linearity**

Regrettably for readers, but fortunately for ETI, we were unable to locate a CRO with bad linearity. Referring still to Figure 3, had there been a non-linearity in the sweep, it would have been evident as one or more vertical pulses not being aligned with their respective graticule lines after the horizontal calibration had set the end pulses on their respective markers. We have observed such problems even on new CROs. The calibrator prevented a poor purchase in that case!

#### Overshoot

This phenomenon is becoming rarer on CROs these days. The only CRO we could locate with significant overshoot proved to have neither graticule illumination for clear illustrations nor the ability to give a good look for single edge blow up.

We settled for using a 10 year old CRO — also with no graticule illumination. Figure 4 shows overshoot on the edges of the inverted timing waveforms, rather than the voltage waveforms. These were easier to trigger on than the relatively infrequent edges of the voltage calibration squarewave. The overshoot is the 'wiggle' seen at the beginning of the clear horizontal parts of the trace.

#### **Delay Line**

Figure 5 shows the rising edge of the voltage calibration waveform on a high quality oscilloscope. The trace was rather dim, because the sweep occurs only at 1 kHz, despite the sweep speed of 10 ns/div. The same edge that triggered the timebase can be seen, because the CRO contains a delay line, so that the signal is delayed for long enough (some tens of nanoseconds usually) to allow the sweep to commence before the signal is applied to the vertical deflection plates. A view such as this is impossible otherwise.

The edge is not perfect. The salient feature is a minor step-like appearance at the top of the edge. This is in fact not CRO



Figure 1. Vertical calibration. New Trio CS-1022 set to 0.2 ms/div, 50 mV/div.

induced overshoot, but the result of mismatch in the connection from the calibrator via a 50 ohms cable to the 1 m/50 pF input impedance of the CRO. Such is the typical situation which will be encountered.

#### **Construction & setting up**

The construction is basically a case of duplicating the prototype shown here. Initially drill the appropriate holes in the panel of the case being used. All the electronics is attached to the panel along with the switches and conflectors, so the shape of the rear of the container is not important.

Once drilled, the various holes on the front label can be lined up with the holes in the panel and the label attached. After adhering the label, fit the components and their knobs where applicable. Next assemble the pc board, exercising the usual care to ensure that all the polarised components are orientated correctly. Also take care to leave the solder joints clean and free of blobs which might short tracks together. Remember that only resistors and capacitors without polarity markings can be put in either way around, because everything else is polarised in some way!

Next attach all the components which are mounted to the rear of the panel controls. These components are fixed to the panel controls to reduce the number of flying leads. Be careful to get all of them in their correct circuit locations, as reversal of components here can lead to 'satisfactory' but uncalibrated operation.

Finally connect the pc board to the panel with short lengths of hookup wire. The leads should be as short as is practicable while leaving the board accessible for alignment, servicing, etc. Long leads can give rise to unwanted coupling between the circuit sections, and may also permit the radiation of unwanted EMI (Electro-Magnetic Interference), even up to VHF frequencies, because of the sharp edges generated by the driver circuits.

With a battery connected the unit should now function. It remains only to adjust the supply to its correct voltage. This of course



Figure 2. The same CRO as in Figure 1, at 1 mV/ div.



Figure 3. A seven year old Tektronix 465 is shown at 1 ms/div. Its last calibration date was not recorded, if it happened at all!



Figure 4. A BWD 511 of unguessable age. CRO calibrator set to 1µs timing pulses, CRO at 1 V/div and 0.2 µs/div. No graticule illumination available.



Figure 5. Tek 465 again, 20 mV/div and 10 ns/div, with the calibrator on 100 mV range.

#### HOW IT WORKS --- ETI-170

The CRO calibrator circuit divides into six parts. These are a power supply section, a 1 MHz clock, a 10 MHz clock, a divider chain, a voltage output section and a timing pulse output section. Each will be discussed in its turn.

#### **Power supply**

The power supply consists of ICs 1, 2 and part of IC3, with their surrounding components. Q1 is used as a saturating pass element, to connect and disconnect the battery from those parts of the device which are not continuously powered. IC1 is the only IC connected all the time to the supply. Initially consider that C1 is discharged. IC1(b) and (c) have their inputs held iow, so that their outputs are high. These gates are connected in parallel to increase the current delivering capacity. Because the outputs are high, Q1 is held off.

In this condition the current drain from the battery is so small that it will last its shelf life, and it may be considered unconnected.

When PB1 is depressed to turn on the instrument, C1 is charged up rapidly via R1. The inputs of IC1(b) and (c) and taken high and the outputs are thus driven low, turning on Q1. This applies almost the full battery potential to IC2, a precision supply regulator IC, via the external supply jack. Capacitors C3 and C4 decouple the rail to prevent instability problems arising from the source impedance of the battery. RV1 is used to trim the regulated supply voltage to 5.05 volts, while C5, C6 and C7 provide further decoupling and compensation.

C1 will slowly discharge via R3. When it reaches about half rail potential, IC1(b) and (c) will once again turn Q1 off, returning the instrument to the quiescent (off) condition. Thus automatic turnoff is effected. Should PB2 be fitted, it may be used to manually turn off the supply.

If the unregulated supply falls below a level where good regulation of the 5.05 volt supply by IC2 is not possible (when the battery goes flat, for example), IC3(a) and IC1(a) and (d) are wired to immediately turn the supply off. Note that IC3 is powered from the 5.05 volt rall. When the input of IC3(a) falls to below about half its supply rall, which is about 5.05 volts, it charges C2 via R5. This causes IC1(a) and (d) to discharge C1 via D1 and R2, which shuts off the supply.

Resistors R6 and R7 set the potential on the collector of Q1 which is required to initiate this automatic shutdown. This mechanism prevents any attempt to use the device when it is potentially operating incorrectly due to a low battery condition.

#### **1 MHz Clock**

The 1 MHz clock is crystal controlled and provides the timing reference for all (but the 10 MHz) timing signals and the voltage callbration squarewave. The oscillator consists of IC4(b) and surrounding components. It runs off the 5.05 volt supply, and operates continuously when the unit is in the on state.

#### **Divider Chain**

The divider chain consists of six decade counters. These provide signals divided by 10, 100, etc, up to 1 million, giving 10 per cent duty-cycle pulses of from 100 kHz to 1 Hz, all derived from the 1 MHz reference. The 'on' indicator is driven from one of these dividers, giving fast flashes with low duty cycle to give indication without wasting power.

#### **Timing Pulse Output**

iC3(b/d) and iC11 with surrounding components form the timing pulse output section. This section, when enabled, shapes the timing pulse train and delivers it to the output with 50 ohm impedance. With SW1(a) in the 'time' position, IC3(d) is enabled. The disabling system prevents the gates consuming power when not required. SW2(a) selects the timing train with the required frequency.

SW2(b) selects the capacitor which defines the shape of the output pulse. This mechanism is incorporated to provide pulses whose edges are conveniently visible on all frequency ranges.

On the 1µs range, the puises are not modified by a capacitive load on IC11(c/d/e), but are shaped by the driving network formed by the sections of IC4 not used in the oscillator. Not only does this provide neat, visible pulses at this frequency, but it makes use of the gates in the package already handling 1 MHz signals. This reduces the chances of crosstalk between these signals, with components in very high frequency ranges, and the slower timing signals. Such crosstalk can upset triggering and interfere with the waveform being displayed.

#### **10 MHz Clock**

The 10 MHz clock circuit is built around IC12. It differs somewhat from the 1 MHz clock in order to allow clean operation at this high frequency. R39 and C18 with IC12(c/d) form the output wave of the oscillator into a pulse string for easy alignment against graticule timing marks. Note that the circuit is separately decoupled by C19, and is only powered by completing the earth return when required, as selected by the range switch SW2(b).

This circuit consumes considerable power in relation to the other parts of the calibrator, and so is not activated when not required. Its output is fed to a different connector. If it is not required, this whole section can be deleted, removing the 10 MHz 100 ns range with commensurate cost saving. If no fast CROs are likely to be encountered, this would not be a problem.

#### **Output Section**

The voltage output section, when enabled by SW1(a), produces calibrated output squarewaves at 1 kHz. The voltage level is selected by SW3. When not enabled Q2 remains off. When enabled it is switched into and out of saturation by IC11(b). Capacitor C16 is selected to precisely pull Q2 out of saturation as quickly as possible.

When fully saturated, Q2 has approximately 0.05 volts drop across it — hence the 5.05 volt supply. R16 through R36 provide a precise 1-2-5 sequence attenuator using only E12 series preferred resistor values. Signals of 1 mV p-p to 5 V p-p are developed. The output impedance is at most a few hundred ohms. Because no buffer is provided, the CRO input must not be terminated in 50 ohms.

#### WHILE WE'RE TALKING ABOUT CROs . . .

While we're thinking about CROs and their performance, let's consider for a moment a few general aspects of oscilloscope or 'CRO' performance which are often taken for granted.

Nowadays there are quite a few CROs available for around the same price as a humble (humble?) audio amplifier. But we expect rather different things from the two types of equipment. For example we only expect amplifiers to have a bandwidth of from 20 Hz to 20 kHz — yet for the same money we expect a CRO to have a bandwidth of dc to 20 MHz! This is a very big difference, yet some of the better CROs go a great deal further, to 200 MHz or even 500 MHz.

Astute readers will guess at once that something is traded for this wide range. But perhaps few of you may have realised just what it is that is missing from the CRO's performance.

One of the things usually noted in the 'fine print' of CRO specifications is linearity. A vertical deflection linearity error of one or two per cent is typical, probably specified for a deflection of say six or seven major divisions.

in an audio amplifier this would be called distortion, and if it rose much above say .01%, today's audio enthusiast would be most unhappy. By paying more money, you can get an amplifier with ten times lower distortion again. In addition, the audio amp is expected to maintain this low level of distortion even at power output levels of 100 watts or so — a far cry from the few tens of volts, at low currents, expected from the CRO deflection amps.

The CRO can of course get away with this relatively poor linearity because the human eye can't perceive less than about one per cent distortion, whereas our ears most certainly can — particularly on the right kind of programme material. Round two to the audio amplifier.

In most applications where a CRO is used, the user expects to be able to resolve the position of the trace to within 0.1 divisions, on a screen with eight vertical and 10 horizontal divisions. This represents a dynamic range of about 38 dB in the vertical direction. Compare this with the 50 dB or more you get from your cassette player, or the 96 dB from a compact disc.

The upshot of this is that the signal-tonoise ratio of a CRO can be relatively poor, and the spot position uncertainty fairly large. Round three to the audio amplifier.







# precision CRO calibrator

PARTS LIST — ETI-170 CRO CALIBRATOR					
Resistorsall 1/4W, 2% uni	ess noted				
R1, R2, R381k					
R310M					
R4					
R5, 7, 10, 11, 12,					
411M R61M8					
R8					
R9					
R13, R40					
R1410k					
R15, R37					
R16, 17, 28, 29330R 1%					
R18, 19, 20, 30, 31,					
32					
R23, 24, 25, 35,					
36					
R26					
R27150R 1%					
R37					
R39					
R42, R43 100R					
Capacitors C147µ/16VW RB	alactro				
C2	poly				
C3 5 7					
19-27, 29 10n ceramic mo	onolithic				
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C6					
C815p NPO ceran	nic				
C9 10p NPO ceran	NIC .				
C102n2 ceramic mo C1122n ceramic mo	onolithic				
C12	OHOIMINE				
C13					
C14	tant.				
C15	ectro.				
C16 100p ceramic	1.77				
C17, C18	nic				
C28	d tant.				
Semiconductors	. oimilar				
D1	r similar				
IC1, 3, 4	uad NOR				
IC2LM305 376 rec	ulator				
IC5, 6, 7, 8, 9, 10 4017 CMOS de	ecade				
divider					
IC11	ex inverter				
IC12	NOR				
Q1, Q2BC559 or simil	ar				
Miscellaneous	and a second				
SW1	n. toggle				
SW2	rotary				
PB1 Mom. contact	sub-min.				
pushbutton					
SK1 3.5 mm sub-m	in jack				
socket with sw	1.				
SK2, SK3Panel-type BN	C socket				
J1, J2Tip jacks X11 MHz crystal,	nar res				
with 30p	pai. 105.				
X2	I, par. res.				
with 30p					
ETI-270 pc board and Scotchcal lab	el; 196 x 1				

ETI-270 pc board and Scotchcal label; 196 x 112 x 60 mm zippy box; 2 small knobs; 216-type 9 V battery; battery clip lead; 2 x 45 mm tapped spacers for pcb mounting; hookup wire, nuts, bolts, etc.

Estimated price: \$60

5



Mounting the pc board to the panel. The leads should be as short as possible to avoid coupling between circuit sections and to prevent unwanted radiation.



Inside the case. Apart from the ICs and other components mounted on the PCB there are few other components required and these are mounted directly on the switches. Note the battery clamp bracket, attached to the front panel by the 10 MHz output socket.

requires a known accurate reference. We used a  $3\frac{1}{2}$ -digit multimeter, which is adequate. It should be attached to the regulated supply rail, and RV1 adjusted to give the specified 5.05 volts. This gives 1% accuracy to the unit. As most CROs are good only to 2% or so, this is reasonable.

Further accuracy can be obtained if required by using the following alignment procedure. Switch the circuit to volts. Attach a  $4\frac{1}{2}$ -digit meter to the volts output. Short 1C3 pin 10 to rail momentarily, and set RV1 for 5 volts on the 5 V range. This gives accuracy limited only by the attenuator chain resistor tolerance.

Artwork: Front panel artwork and drilling diagram are available on request to ET1, PO Box 227, Waterloo, NSW 2017.

# AM Stereo Signal Generator VP-8253A



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A technique called constrained or channel coding, explored for some time at IBM Research, makes possible faster, more error-free transmission and recording of digital data.

# **KEEPING THE** MESSAGE CLEAR T. Murphy



[1,7].

STORIES IN THE popular press covering advances in computer technology often report on the contributions of physicists, engineers, and computer scientists. But the efforts of mathematicians and information theorists often remain hidden behind a jumble of blackboard scratchings and talk of 'sets', 'sequences', and 'mappings' that defy the comprehension of the outsider.

A case in point is channel or constrained coding. The increased capacities of today's high-end disk and tape storage systems have, in part, been made possible by the advances of researchers in this area of applied mathematics. Peter Franaszek, Roy Adler, and Martin Hassner are among those, at IBM Research and elsewhere, who have examined the problems of constrained coding theoretically, and have evolved algorithms (which can be computerized) for the design of optimised solutions and of actual hardware for coding and decoding.

"IBM is really the place where this was done," says Franaszek, who was responsi-ble for some of the earliest work in the theoretical treatment of the constrained coding problem. "Previous approaches to this problem have been largely replaced in the industry by techniques often developed within IBM."

But at first glance it may not be obvious why there would be any particular problem with putting digital information onto a medium, or 'channel', such as magnetic tape or disk. The best way to store computer data might seem the straightforward method of taking the sequence of 1s and 0s and representing them directly. Following such a theory, a sequence such as 100110100110 might best be represented in the storage channel by a series of magnetizations of opposite polarity that correspond directly - one-to-one - with the elements of the sequence.

This approach, however, presents a number of practical difficulties in a real storage system. For example, intersymbol interference, i.e: the interference of adjacent 1s and Os. This interference can occur because a channel is a physical system, and no physical system reponds instantaneously and with

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# TECHNIQUES

perfect sharpness. There is always a degree of uncertainty. In the data sequence above, this could result in '11' being read as '1' — resulting in the wrong number being recorded. In transmission channels there is an analogous problem called 'spectral shaping'. And, in both storage and transmission, the internal 'clock' of the system can also be disrupted by the input of arbitrary streams of data.

These kinds of problems are not particular to computers, or even to the 20th century. Messages sent in Morse code could not be understood if they were transmitted as continuous strings of *dits* and *dahs* with no pauses. No one could read a book or business letter or a newspaper if the words had no spaces between them. The information would be there in each case, but it would be unusable.

Pauses in the Morse code and spacing in the written word have parallels in the world of computer storage and transmission. The binary form of information to be saved, encoded as a data sequence of 1s and 0s, must undergo a second-level encoding process, called *constrained* or *channel* coding.

#### **Constrained** coding

In magnetic recording, for instance, the most common means of avoiding intersymbol interference and maintaining the internal clock is to place restrictions on the minimum and maximum number of 0s that can occur between consecutive 1s. Stated in formal terms, these codes required at least d but no more than k 0s between consecutive 1s. The values of d and k depend upon the physical properties of the particular channel in question; that is, how close consecutive Is can be before they might interfere with one another, and how far apart they can be before the internal clock might be disrupted. The resulting codes are called (d,k) constrained. They function both to prevent mistakes from occurring and to limit the effect of those mistakes that do occur.

There may be many codes that meet any particular set of (d,k) constraints. Once the (d,k) constraints for a channel have been determined, the next step is to choose the best code from this 'family' of acceptable codes. It is possible to determine mathematically a channel capacity; i.e. the maximum amount of information that can possibly be represented by codes in the acceptable family. Picking the best code (the one that packs the maximum amount of information code) is then the same as producing a code that most closely approaches the channel capacity.

Early formal approaches to this problem were largely confined to mixed block codes: assigning fixed coder outputs to each possible input. Here, every possible output must be compatible with every other output so as to form a complete sequence meeting the (d,k) constraints. A major drawback of such methods was that obtaining a code closely approaching the channel capacity would have required, in many cases, coders of impractical complexity. A consequence of this was that the recording or transmission system often operated well below its potential.

#### Solving the problem

36

Perhaps the earliest systematic methods for overcoming this problem were Franaszek's results during the 1960s that avoided the use of fixed blocks and made use of the notion of codes based on finite state descriptions. Finite state descriptions are produced by abstract mathematical structures known as finite state machines. These descriptions enable each segment of information to be coded while taking into account how both the incoming bit stream and neighbouring segments of information were coded. The resulting codes allowed efficient computer optimization, and generally produced large reductions in coder complexity. One of the many codes obtained is the (2,7) code implemented in IBM high-capacity disk drives. This code has been credited with increasing the storage density substantially over what was possible with earlier codes.

Further work was done in this area by a number of investigators, with good results. However, instances could still arise in which the densest coding possible for a given channel would require coders of unbounded complexity. This suggested the need for more general forms of codes. Two approaches leading to such solutions have been obtained by Franaszek, and by Adler and Hassner.

Franaszek's method is a generalisation of his earlier work, and is likewise based on a knowledge of the information stream to be encoded (usually called *look-ahead* in coding parlance), and the finite state structure of the channel. He obtained algorithms that yield coder structures that use a minimal amount of look-ahead.

Adler and Hassner provided solutions to this class of problems through a different mathematical approach. They discovered and developed a relationship betwen constrained coding and a field of mathematics called *symbolic dynamics*. Their results in this field include techniques that have been adapted and extended to the constrained coding problem by a group including Don Coppersmith, John Moussouris and themselves.

These new developments have not only advanced the theory of constrained codes but in important cases they have also produced improved coders and decoders. Numerous questions remain, however. For example, the techniques yield many possibilities for any single set of constraints. It is not known how to find an optimal code, except largely through a process of trial and error. In addition, the question of how such coding could be extended to include redundancy for error detection remains largely unexplored. Today this is generally done by a separate coder/decoder. These and other issues suggest that this field will remain a lively area for investigation.

Project 742

# A 'speech-tailored' COMMUNICATIONS SPEAKER

# Robert Irwin Roger Harrison VK2ZTB

IN ANY COMMUNICATIONS SYSTEM where speech is used, 'intelligibility' is enhanced by restricting the frequency response, over and above simple channel bandwidth considerations. Restricting the voice frequency spectrum to a range from about 100 Hz to about 3 kHz removes many of the characteristic components generated during speech but does not reduce intelligibility. Telecom employs a frequency range of 300 Hz to 3 kHz in the telephone system, for example.

In amateur radio, the emphasis is on speech intelligibility. Any unnecessary speech spectrum components present will generally only degrade the useful information. For this reason, most transmitting equipment these days incorporates speech response filtering in the audio system.

But this is not always the case with the receiver. The audio amp may have a little bottom-end rolloff and some simple highcut response tailoring. Problem is, the signal coming out of the detector will contain components spanning right across the audio spectrum. When receiving speech, low audio frequency noise will 'muddy' the signal, reducing intelligibility, making it hard to 'copy'. Likewise, higher audio frequency noise (spits and crackles) can make the signal difficult to copy as the ear is quite sensitive to this.

The internal speakers in communications receivers and transceivers generally have what can best be described as an 'indifferent' response. Many have a 'peaky' response at the higher audio frequencies, about 4 to 10 kHz, and equipment cabinets make the bottom end response 'lumpy'.

A nondescript speaker in any sort of box,



After all the care taken with the design and performance of modern communications receivers and transceivers, they're virtually all let down right at the end of the chain — the loudspeaker. Speech 'intelligence' can be marred by various deficiencies in the audio amp and speaker response, making the signal sound 'muddy' or just 'hard to copy', despite good signal strength. With this speaker it's 'Q5 all the way'.

# Project 742

or just on a baffle, can remarkably improve the sound from a receiver or transceiver apart from throwing the sound in the right direction (i.e: at you!). But, by 'tailoring' the response to remove the unwanted audio spectrum components coming out of the receiver, the result can be dramatic indeed.

This project employs a number of little 'tricks' to obtain the appropriate tailored response for improved intelligibility. It involves choosing a driver that partly has a natural response approaching what we want and then using L and C filter elements to finish the job.

The ET1-742 Communications Speaker is designed to plug directly into the extension speaker outlet on a receiver. Its power handling capability, at 10 watts, is more than ample for the application and it should make not just a useful adjunct to any 'shack', but a decorative one as well.

#### The speaker

Because of the small frequency range that needs to be covered there are no exacting demands on the speaker to be considered. Rather than use a filter to tailor the bass end of the range, a speaker which naturally rolled off the bottom end could be used. The only demand on the top-end performance of the speaker is that it extends beyond 3 kHz or so. The speaker we selected was a Pioneer 4 ohm, 6" (150 mm) car speaker sold by Jaycar (type H16CP60-02C). These speakers have a free-air resonance of around 150 Hz, are rated to handle 10 W RMS and have the advantage of being very cheap.

After running a frequency response we found that this speaker had one other unex-

#### **COIL WINDING DETAILS**



Figure 1. Low pass LC filter.

The coil on the prototype was wound on a piece of 50 mm internal diameter, PVC pipe which can be obtained from most hardware or plumbing supply stores. Cut off a 90 mm length of the pipe and drill a pair of 1.2 mm holes about 5 mm from edge on both ends of the pipe. These holes will be to secure the coil wire. The coil was wound from 18 B&S, 20 swg enamelled copper wire. Start by threading the wire in one hole and out the other on one end of the pipe. Leave about 100 mm protruding as a connection lead. Wind a layer of about 60 turns on to the pipe making sure that the turns are close wound and neat. When you have completed the first layer you can secure the turns with a bit of sticky tape and wind another layer back the other way on top of the first. Repeat this once again so that you end up with a coil of three, 60 turn layers (180 turns in all). The end of the coil should be at the opposite end to which you started. The wire can be secured by threading it through the two holes at this end once again leaving bout 100 mm as a connection lead. Note that before soldering the leads of the coil to anything you will have to scrape the enamel off the ends





communications speaker

pected advantage. There was a dip in the response around the 300 Hz region. You may think that uneveness in the response would be the last thing to praise in a speaker, but in this case it is very useful indeed as the region around 200 to 300 Hz often makes speech sound 'muddy' through an amplifiction system.

Often, in speech reinforcement systems, a parallel resonant circuit in series with the driver is used to artificially create a dip in the response in this region.

Although the driver is a 4 ohm type, the majority of modern (and not-so-modern) receiver audio amps will drive this impedance without complaining (but check to be sure).

### **Tailoring the response**

The first step in adjusting the frequency re-

sponse is to roll off the top end of the speaker, Figure 1 shows the circuit of an LC low pass filter network. I happened to have a 1 mH coil lying around from a previous crossover design so I conveniently let L1 be 1 mH (see 'Coil Winding Details'). The corner frequency of the low pass network is given by:

$$2\pi f = 1/\sqrt{(LC)}$$

If we let the rolloff frequency be 2 kHz, then the value of C is  $6.8 \,\mu$ F. It was found that, due to slight peakiness in the driver's response around 2 kHz, it was better to start rolling off a bit earlier so a value for C1 of 10  $\mu$ F was used.

The frequency response was reviewed again and it was found that the top-end response rolled off quite nicely, so now for the







PARTS LIST	— ETI-742
Speaker	
SP1	150 mm 4 ohm car speaker
C1	
	electrolytic
C2	
Inductors	electrolytic
L1	1 mH air core inductor (see
	coil winding details)
Miscellaneous	
2-way spring ter	minal block; 5-way tag strip; 300
x 0.2 mm hook	up wire: 300 x 900 x 13 mm
panicieboard; 80	00 x 13 x 13 mm beading; wood
giue, sciews, na	ils; non-hardening sealant.
Price	estimate: \$10-\$12

# Project 742

bottom end. The speaker resonance gave quite a peak at 200 Hz so a series capacitor was called for. This cuts back the peak at 200 Hz and also rolls the bottom end off at a steeper rate. Because the two poles of the combined network interact with one another to some extent, a certain amount of experimentation was required to find a value for this capacitor that would bring the peak down to a suitable level. It was found that a value of about 22  $\mu$ F gave the appropriate response (see Figure 2).

#### Construction

The speaker and filter were mounted in a 225 x 225 x 150 mm chipboard enclosure.

There is no particular problem with the construction of this box as all the joints are butt joints (about as far as my woodworking abilities go) and there are no nasty ports or braces to confuse the would-be woodworker.

The enclosure itself is not all that critical, as long as it is roughly the same size. The prototype was made from 13 mm thick chipboard. You will need a piece about 300 x 900 mm. If you have some off-cuts that will be suitable then use them, otherwise Figure 6 shows how to cut the box from a 300 x 900 mm sheet which is a standard size sold in hardware stores.

Once you have cut all the pieces, sandpa-





Figure 5. How the box fits together. Note that the sides butt against the top and the bottom. Clears are used to mount the front and back panels on. NB: The rear cleats are set in 13 mm in from the rear of the box. The face board is set in 5 mm in front of the box.

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# communications speaker

per down all the rough edges. Care should be taken when cutting the hole for the speaker in the front baffle. Be sure that you do not make it too big otherwise there may not be enough 'meat' left around the edges to screw the speaker on to.

Assemble the top, bottom, sides and front baffle together. All the joints should be glued and then screwed or nailed together (if you are using screws then drill small pilot holes first to ensure that the chipboard doesn't split).

The front baffle should be mounted about 5 mm back from the front edge so that a grille can be mounted if desired. To ensure that the back of the box will sit properly, cleats made from 13 mm square beading should be nailed 13 mm in from the back edge.

To complete the woodwork, two holes should be drilled in the bottom of the back panel for the speaker terminals.

The box can now be finished off in any way you please. The prototype was finished with an iron-on wood laminate that can be bought from most hardware stores. The laminate is cut to size with a pair of scissors and placed in position. A hot iron (clothes iron not soldering iron! Ask your mother how it works if you've never used one!) is then run over the top of the laminate thus melting the glue on the underside and bonding the laminate to the surface. The front baffle was painted matte black.

Once the box is finished you can mount the speaker to the front baffle. Use some silastic sealant around the edge of the speaker to form an air-tight seal. The coil was mounted next. It is positioned on the floor of the box and to one side. I secured it with Araldite. A small piece of tagstrip was screwed to the floor also. The components can now be wired up according to the wiring diagram. Note that C1 is wired directly across the speaker terminals. Finally, wire up and mount the speaker spring terminals on the back panel. Before closing up the back, some acoustic stuffing should be placed inside the box to cut down on box resonances. The back can now be screwed on.



Figure 7. The inside story. A picture showing the positioning of the coil inside the box. The coll was held in position with Araldite.

#### Listening

All that's left to do now is to hook the speaker up to your receiver and find someone to listen to. If your receiver doesn't have an extension speaker outlet then you can disconnect the internal speaker and connect up a socket here. We tested the speaker out on an Icom IC-R71A communications receiver and found that it gave better clarity than the internal speaker, apart from the advantage of throwing the sound forward.

"Cuts out all the crud on the bottom end," was the knowledgeable opinion of our humble editor. He was right. I could actually hear what was being said on 3565 kHz without all that rumble and mud. (Judging by the conversation ft may have been better if all the crud had been left in!). Happy listening!



# Intelligent digital communications interface

G FS Electronic Imports has undertaken the manufacture of a new digital communications interface unit, the model CPU-100. It's designed for interfacing a dumb ASCII terminal to a radio transceiver via a modem, which GFS can also supply.

Using a 6809 microprocessor, the CPU-100, via ROM software, is able to perform a wide variety of functions. These include transmission of both BAUDOT and ASCII at user definable speeds, as well as digital selective calling. SITOR transmission may also be possible given the appropriate software.

Because of its built in intelligence the CPU-100 is extremely flexible. For example it may be configured for either simplex or full duplex operation depending on its end user's requirements.

A wide variety of applications exist. These include accessing a

mainframe computer from a vehicle or aircraft and radio teletype operation where a number of stations operate on the one frequency, and the calling station wishes to address each one separately via selcal.

GFS is able to supply the CPU-100 as a pre-wired and tested printed circuit board, or as a fully housed unit including its MDK-17 modem. Special applications software can also be made available.

For further details contact the manufacturer, GFS Electronic Imports, 17 McKeon Rd, Mitcham, Vic 3132. (03)873-3777.



The CPU-100 Interface unit. It interfaces a mainframe to a radio transceiver.

# Non-metallic guy wire

GFS Electronic Imports renative guy wire manufactured using continuous yarn fibreglass filament and vinyl chloride sheath.

Known as Debeglass Wire, GFS claims it offers its users a number of advantages over using steel as a guy wire, such as, a higher tensile strength than steel wire of the same diameter, no corrosion, as well as lighter weight than an equivalent diameter of steel wire.

As Debeglass wire is not a conductor there is no requirement to use insulators over its length as there is with steel. Additionally, unlike steel, it exhibits extremely low elongation. Debeglass is available in sizes,

4 mm, 5 mm and 6 mm diameter. The tensile strength of DB-4 is 430 kg, DB-5 is 560 kg while DB-6 is 970 kg.

Applications exist for Debeglass wire in many areas from the guying of radio masts to the supporting of HF antenna arrays. It is also uniquely suited to use in marine and other highly corrosive environments.

GFS Electronic Imports advises that it keeps stocks of the DB-4 and DB-5 while the DB-6 may be obtained on special order. DB-4 is priced at 48 cents per metre with DB-5 at 68 cents per metre.

For further details contact GFS Electronic Imports, 17 McKeon Road, Mitcham Vic 3132, (03)873-3777.

# Computer talk by 2-way radio

West Australian mining companies are using portable computers to improve twoway radio communications.

Radio communications firm T R Barrett of Spearwood, South of Perth, has helped the companies set up communications networks using its commercial two-way radios with Commodore computers.

Transmission can be via HF (3 to 30 MHz) single sidebank or VHF and UHF FM radio links. It is also possible to use the computers and its AMTOR units to communicate via regular telephone lines.

The field units are using Commodore SX 64 'Executive Portable' machines, while most base stations are connected through a Commodore 64 personal computer. The radios and computers are wired into an AMTOR unit, a sophisticated electronic black box which sends and receives text and data with full errorcorrection. The AMTOR unit (which is normally used for advanced Amateur Radio communications) continuously checks each character as it is received, and transmits signals back every split second so that the sending station's computer knows the message has been received properly.

Each sequence takes only 450 milliseconds: 250 ms is used to transmit, 70 ms for verification, and the rest in sending the signal from one station to the other at the speed of light. If the text gets garbled, the sending station just keeps repeating it until the two computers agree.

This type of computerised radio communication is fast replacing radio teletype services because of its speed and reliability. It also offers a higher degree of security.

Further information on the system is available from T R Barrett Radio, P.O. Box 198, Hamilton Hill, WA 6163. (09)418-4141.

# New mm wave detector series

Four new millimetre frequency detectors, offering excellent tangential sensitivity for use in many applications, have just been announced by Alpha Industries.

The frequency range 18 GHz to 220 GHz is covered in nine bands. For example the fixed tuned units have -55 dBm sensitivity at 35 GHz and biased type detectors have -68 dBm sensitivity at 35 GHz.

For applications in test sets and integrated subsystems the 971/925 series of self-biased detectors operate over the full waveguide band without tuning. At 50 GHz and above, beam lead GaAs Schottky barrier diodes are used. Series 973 detectors provide flat, self-biased response with VSWR ranging from 1.6:1 to 2:1, over full waveguide bands up to 60 GHz. The detector output is typically 200 mv/mw at 35 GHz.

The 965D series of detectors has the highest tangential sensitivity, ranging from -48 dBm to -68 dBm depending on frequency. These detectors feature a field replaceable wafer with a GaAs Schottky barrier diode. Provided as part of this detector assembly is a battery bias supply.

For information contact Benmar International, P.O. Box 4048, Sydney NSW 2011. (02)2233-7939.

Tell them you read it in ETI

Tara Patch. A connection system for telephone and radio.

# Phone-radio interface

A fter three years of planning and development Australia's only phone-patch unit specifically designed for radio amateurs and CB operators is now available.

TARA PATCH

Marketing manager of Tara Systems, Neil Parkinson said it was the latest model in a range of Tara radio/telephone interconnection units in use throughout Australia by emergency services, government agencies, and business enterprises.

"It's not only a phone-patch but can be used as an interface between up to three different base radio sets. This enables the user to engage in cross-band operations at the flick of a switch, or provides an easy way to record all communications by plugging in a cheap cassette recorder."

The basic unit will do everything normally expected of a phone-patch for radio amateurs and CB operators but it can be adapted to suit individual special requirements.

The only external adjustment, a slider volume control, adjusts output from the monitor speaker.

All radio signal levels are internally preset but can be adjusted to suit individual needs.

For further information contact Tara Systems, 6 Malvern St, Bayswater Vic 3153. (03)729-0118.



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TROUBL

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# Project 666

# PARALLEL PRINTER SWITCH

Tired of plug swapping whenever you want to change from one printer to another? This low-cost project should suit you down to the ground. It lets you have two Centronics-type printers connected up permanently, so that you can select one or the other at the flick of a switch.

## **Geoff Nicholls**

IF YOU HAVE both dot matrix and daisywheel printers to go with your computer, you probably find changing from one to the other a hassle with a modern computer system. With both serial and parallel ports you might be able to run one printer on each, but what if you have a modem as well?

Fear not, I have a solution! This project switches the 12 most critical Centronics signals from one printer port to two printers. It is really nothing more than an electronic 12 pole two position switch but it has one advantage over a massive switch— the switching can be optionally controlled by a single TTL level signal. In the prototype I have used a small toggle switch to select the printer, but you may use any method you want.

By now some readers are probably wondering why a software switching system was not used, for example, detecting an 'escape' code sequence to select the printer through the data being transferred. There are a number of traps in that approach, one of the hardest to overcome being the problem of bit image graphics where a psuedo escape sequence can occur in the middle of a graphics data dump.

To explain further, when a printer is being sent normal text there is a number of non-printing control codes used to select printer options like enlarged printing, different line spacing, subscripts etc. Most of these are done in so-called escape sequences, where the ESC character code (27 decimal) is first sent and then the next two characters received are not printed, but used to define a unique control code sequence. This system works because the ESC code is not used for any other purpose in ASCII text. When graphics data is being sent, any 8-bit code can occur however, and it is likely that the number used for ESC will come up sooner or later. This does not cause problems for the printer because the protocol for graphics data transfers is rigidly defined — the printer 'knows' that the data is for graphics and does not interpret any pseudo control codes.

But if the printer switch was to use an escape sequence to change printers, it too would have to 'know' when a graphics dump was occurring and ignore the data until the dump was over. This means the project would need to have inbuilt tables for the various types of printers on the market, and would almost certainly require a microprocessor. Once you have a micro on board there is a whole host of extra features that could be included such as 'spooling' or providing a buffer memory, conversion from one data format/baud rate to another etc. However, as this seems to be getting away from the concept of a printer switch, the simple approach was adopted for this project.

#### Construction

The overall cost of the project is largely determined by the choice of connectors for

the Centronics ports. The pc board has been laid out to take 26-way pin headers with two rows of 13 pins spaced 1/10 inch between rows and pins. These can connect to insulation displacement (ID) transition connectors and thence to ID Centronics plugs and sockets, via ribbon cable. This method was used for the prototype, although many readers will want to save some money by using solder-type Centronics connectors and soldering the wires straight to the pc board.

If you use pin headers it will be necessary to cut some pins off the printer 2 header and the computer header. You can check the header against the pc board to see which pins to cut — they are the ground pins numbered 20 to 30 on the Centronics port. (There was no way to get the signal tracks through if the ground pins were left, without going to a double sided pc board or a lot of extra links.)

The only ground to the pc board from the computer port will be through Centronics pin 19 if the ID construction method is used. I had trouble with one supposed 'Centronics' interface that did not connect pin 19, so be warned! You could run a wire along the upper side of the pin header to connect pins 20 to 30 to pin 19, but if you do, try to keep all solder blobs to a minimum size so the transition connector has plenty of pin length left to contact the socket.

If you are using ID connectors you will need 26-way ribbon cable between the headers and Centronics plugs/socket. Crimp the ribbon down at the pin 1 end of the Centronics connectors. Constructors using solder-type Centronics connectors should connect pins 19 to 30 together with a wire on the back of the connectors, thence to the pc board.

Start the pc board by soldering in the 10 links. If you use uninsulated wire be careful to pull the wire tight to avoid shorts. The resistors can be wired in next, followed by the two capacitors and the transistor. The five ICs can go in now; all DIL types mount with the same orientation (most of them

parallel printer switch

HOW IT WORKS - ETI-666



SE ECT

R.N. 75 12 3

eti 666 PRINTER SWITCH

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The circuit uses tristate buffers to Interface the 12 Centronics signals from the computer to the two printers. Nine of the signals go from the computer to the printer (D0 to D7 and STB-BAR) while the remaining three signals come from the printer to the computer (SLCT, BUSY/READY-BAR and ACK-BAR).

When printer 1 is selected, the tristate outputs of IC1 and IC2 are enabled while the outputs of IC3 and IC4 go into the high impedance state. The reverse happens for printer 2. The printer selection switch directly controls the output enables for IC1 and IC2, while transistor Q1 inverts the switch level to control IC3 and IC4.

This ensures that each computer output line drives only one line at once. Similarly only one set of printer return signals is connected back to the computer, from the selected printer.

IC5 regulates the dc input voltage from the plug pack to provide the 5 V supply required for the TTL buffers.



# Circuit-Board-Design Without the Tedium

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- □ Microsoft Mouse (Optional).



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# parallel printer switch



The Inside view. Note the position of the switch and socket. The ID cables have been removed.

SW1

have pin 1 marked with a dot on the pc board). Remember that IC5 is the regulator in the TO-220 package. I mounted the printer

I mounted the printer selector switch along the long axis of the box lid, 48 mm from the end where the printer 1 cable comes out. If you are using a different box, be careful to avoid fouling the back of the switch on one of the pin headers. Connect two pairs of hookup wire for the leads to the power supply and the printer selector switch. The power supply input should be wired to a socket suitable for the	6 TO 12V DC FROM PLUG PACK SOCKET
PARTS LIST - ETI-666	
Resistors        R1, R2      1k0 5% 1/4 W        R2      10k 5% 1/4 W        Capacitors      1/4 W	PRINTER 2
C1, C2 100 nF ceramic bypass	COMPUTER
Caps Semiconductors	Close up
IC1, IC3	of the pc board. ALL 26 WIRE RIBBON CABLE WITH OPTIONAL SOCKETS.
buffer IC2, IC4	ALL 20 WIRE RIDDOW CADLE WITH OPTIONAL SUCKETS.
buffor	a second designed to provide the providence of the second second
IC5	
Q1BC548 etc NPN transistor Connectors	
Either insulation	The second of the second secon
displacement types3 x 26-way pin headers	AND
3 x 26-way ID transition sockets	
2 x Centronics ID plugs	
1 x Centronics ID sockets or solder types2 x Centronics solder plugs	
1 x Centronics solder	
socket	
Miscellaneous ETI-666 printed circuit board; single pole single	the second se
throw switch; 6 to 12 V dc plug pack; socket to	
suit plug pack; plastic case to take 60 x 90 mm	
pc board; 26-way ribbon cable; hookup wire, link wire, solder etc.	
Price estimate: \$25	
(not including connectors)	
\$50-\$70	And a state of the second
(including connectors)	A Republic to the second s

IC5

# Project 666

plug pack to be used. Watch the polarity and note that the plug pack must produce a dc voltage between 6 and 12 V.

I filed away part of the box to allow the ribbon cables to pass under the mounted lid. Two of the cables pass out one end, with the remaining one out the other.

#### Testing

Carefully check all wiring and look over the pc board for solder blobs, especially where a track runs between IC pads. When all seems well, connect a multimeter across C2 and power up. The voltage reading should be within 200 mV of 5.0 V. If it isn't, stop and find the fault before connecting the printers or computer to the project.

Once the voltage is correct you can plug the computer and printers in and try printing on each printer in turn. If nothing happens at first, try resetting the computer then switch each printer off-line then back online and try again. If there is still no action then the handshake lines ACK-BAR, STROBE-BAR or BUSY/READY-BAR may be stuck high or low. If the printers work but the print-out is garbled then one or more data lines could be stuck or perhaps shorted together. Look for solder blobs joining pc board tracks or insufficiently crimped ID connectors.



# Master the Microprocessor

#### The Purpose of this Course

There is a considerable, expanding and world-wide demand for people with a real knowledge of microprocessors and general computer echnology. Such people are needed to design and evaluate systems and to assess and develop the enormous range of possible applications, both present and future. of microprocessors and to

understand the installation and servicing of the main types of equipment of which they may form the most vital component. (A microcomputer has

already been produced to replace the mechanical programmer on a domestic washing machine, for example.)

This Course provides the necessary basic information to enable a student to really understand the functioning of microprocessors and their supporting circuitry,

usually referred to as the "hardware". This is backed up by showing how to program a microcomputer (or produce its "software") in the most fundamental form of computer language called "machine code", No previous knowledge of computers is necessary. though a little basic knowledge of electronics

plus digital and logic circuits will be found heipful A special introductory short course is available to provide this back-ground information, if required by an individual student on the course without extra fee

#### Student-Tutor Contact

A qualified Tutor is available to every Student throughout this Course in order to deal with any queries which may arise and to assess certain questionnaires which are issued to Students throughout the period of training

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completing the Course successfully. Course covers main requirements of the City and Guilds Certificates in Computers Practical Self



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Learn how Microprocessors really work

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The basis for the practical work in the Course is the Microcomputer. This is supplied completely assembled and ready to use

The Course text is carefully arranged in sequence so that each new section follows logically from previous work. Hardware description and programming technique progress together, so that the Student is discouraged from treating them as distinctly separate subjects. Following each section of descriptive text, detailed instructions are given in order to use the Microcomputer to provide a practical demonstration of each new function or technique. This provides a very powerful way of learning precisely how the system operates, and enables any possible ambiguities in the Student's mind to be quickly resolved.

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Univolt's top-of-the line handheld LCD multimeter. It features 30 measurement ranges, including dc volts, ac volts, dc and ac current, transistor  $h_{FE}$  (gain) plus diode check and audible continuity check (beeper). Range selection Is via the single large rotary switch. The case features a prop-up stand and the whole unit comes with carrying case, probes and  $h_{FE}$  clip-probe for just: \$73.40 (tax exempt); \$95 tax paid. 3½-dlgit LCD readout (1999

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NEW! UNIVOLT PD-1800 DIGITAL PROBE TESTER

Wow! — a tiny digital volt-ohm-continuity probe with the 3½-digit LCD readout built-in! Great for the toolkit, great for the beach! It features autoparty indication. A beeper sounds in the continuity test mode. There are four dc and ac voltage ranges and four resistance ranges. It measures a tiny 163 mm long, 28 mm high and 19 mm deep! How much? \$50 (tax exempt); \$65 tax paid.

 $3\frac{1}{2}$ -dlglt LCD readout (1999 max.); ac and dc voltage ranges — 2, 20, 200, 500 V, basic accuracy 0.5% dc, 1% ac; resistance ranges — 2k, 20k, 200k, 2M, basic accuracy 0.7% (in-circult test capability — uses 0.3 V on ohms range); input impedance — above 10M; power supply — 2 x 1.5 V (LR-44) cells (inc.).

## **IDEAS FOR EXPERIMENTERS**



### Capacitance tester

A. Glover of Cootamundra NSW 2590 sent us this idea for a simple, ultra low cost capacitance tester. It works by comparing the value of a known capacitor with one under test. The amount of time the voltage on pin 3 of the 555 is high is proportional to the value of the capacitance hanging off pin 6 and 7. In the circuit shown, when the two capacitors are of the same value the LEDs will be on for the same length of time and the buzzer will not sound. Any discrepancy in value will result in one LED being on for longer than the other and the buzzer sounding.

#### Flasher

M. Howe of Mitcham, Vic used this variation on the common LED flasher theme in which daylight controls the flasher.

During daylight hours RV2 is adjusted to keep voltage at pins 2 and 6 of ICI just below 2/3 Vcc. As darkness falls the resistance across the LDR increases until pin 6 sees 2/3 rail voltage and the 555 starts to operate, flashing the light via Q1.

When the sun rises the opposite occurs and C1 is effectively shorted out, stopping the 555. The prototype, using a 6 V battery, flashes every 1-2 seconds. Current consumption in daylight is 2.4 mA. Operating current depends on the globe used. In daylight RV2 holds pins 2 and 6 at 3.8 V.

I used a BD140 as Q1 because I had a few to spare.

All components except the LDR and light-bulb will fit on a pc board only 25 mm square. The LDR is not affected by the operation of the globe and they can be mounted side by side.





# Nine position joystick

This simple circuit was sent to us by **R. Howie of Mansfield Qld.** It should work with any computer running the Z80 processor and equipped with an expansion port. The two joystick pots are used as voltage dividers for the 74LS367. Gating signals are supplied from the computer via the 74LS30. This circuit is cheap and very easy to build.

## **IDEA OF THE MONTH**

### Headlight Warning

#### Jeff Lefaro, Mulgrave Vic 3170

To help prevent leaving the car lights on after switching off the engine, and thus flattening the battery, a simple circuit such as

#### this one can be implemented.

The circuit consists of an oscillator which can be enabled or frozen with respect to the inputs on IC1b. If both these inputs are high then there will be no current passing through D1 and the oscillator can produce the desired tone.

The frequency output is proportional to R4 and C1, and can be varied to suit, although R4 should not be reduced to less than 5k. The value of C1 should not be changed drastically as the output many become inaudible.



#### **Contact material**

Further to the article which appeared in the February 84 edition of ETI, Donald Sutherland of Wanganui, NZ has written to us noting a method of determining the materials of which a contact is made, at least for the BPO 3000 and 600 types. The shape of the spring tips indicates the contact material.



#### **'IDEA OF THE MONTH' CONTEST**

#### COUPON

#### Cut and send to: Scope/ETI 'Idea of the Month' Contest, ETI Magazine, P.O. Box 227, Waterloo NSW 2017.

"I agree to the above terms and grant *Electronics Today* International all rights to publish my idea In ETI Magazine or other publications produced by it. I declare that the attached idea is my own original material, that it has not previously been published and that its publication does not violate any other copyright."

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Title of idea	
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The winning entry will be judged by the Editor of ETI Magazine, whose decision will be final. No correspondence

The winner will be advised by telegram the same day the result is declared. The name of the winner, together

with the winning idea, will be published in the next possible

where indicated on each entry form. Photostats or clearly

Contestants must enter their names and addresses

can be entered into regarding the decision.

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the last day of the month

issue of ETI Magazine.

Scope pc board Work Centre

Scope Laboratories, which manufactures and distributes soldering irons and accessory tools, is sponsoring this contest with a prize given away every month for the best item submitted for publication in the 'Ideas for Experimenters' column — one of the most consistently popular features in ETI Magazine. Each month we will be giving away a pc board Work Centre consisting of the Model 315 adjustable pc board holder with capacity to accept 300 mm boards, Model 300 180° swivel and lock base which can be attached to the Model 312 tray base with wet sponge receptacle, Model 371 solder spool holder and Model STS 3 soldering iron safety stand. Please note prize does not include solder or scope TC60 temperature controlled iron shown above. The prize is worth \$123!

Selections will be made at the sole discretion of the editorial staff of ETI Magazine. Apart from the prize, each winner will be paid S10 for the item published. You must submit original ideas of circuits which have not previously been published. You may send as many entries as you wish.

#### RULES

This contest is open to all persons normally resident in Australia, with the exception of members of the staff of Scope Laboratories. The Federal Publishing Company Pty Limited, ESN, The Litho Centre and/or associated companies.

Closing date for each issue is the last day of the month. Entries received within seven days of that date will be accepted if postmarked prior to and including the date of written copies will be accepted but it sending copies you must cut out and include with each entry the month and page number from the bottom of the page of the contest. In other words, you can send in multiple entries but you will need extra copies of the magazine so that you send an original page number with each entry. This contest is invalid in states where local laws prohibit

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## TECHNIQUES

# COMPUTERISED CIRCUIT ANALYSIS

Designing amplifier circuits? This article shows how to design programs to take the sweat and guesswork out of the operation.

Lance Wilson Jon Fairall

MOST READERS of this magazine will, sooner or later, find themselves in a position where they have to design basic amplifier circuits. This need not be a tedious and time-consuming task if you develop some of the ideas presented in this article. We have included a demonstration program for the sake of interest, but the object of this exercise is to show you how to go about the problem of designing with a computer. You can write your own program to suit your own computer and your own design.

The fundamental circuit for a Class A amplifier is given in Figure 1. The first step in analysing a transistor circuit is to establish the biasing, since it is this that sets up the effective gain of the amplifier. For the circuit in Figure 1 the first step is to establish the base voltage, Vb. A standard but simplified equation which allows a quick solution is:

$$Vb = \frac{Rb2 \times Vcc}{Rb1 + Rb2}$$

From this we can determine Ve very quickly if we assume that there will be a drop of about 0.6 V across the base-emitter junction of the transistor:

$$Ve = Vb - 0.6$$

Figure 1. General form of the Class A voltage amp



We now have access to the current flowing in the emitter resistor from Ohm's law, since we know the voltage across the resistor and its value:

$$Ie = \frac{Ve}{Re}$$

where Re = Re1 + Re2.

Since we also know that the emitter current must be more or less the same as the collector current we can also work out the collector voltage:

Vc = Vcc - Rclc

#### **AC response**

With this series of simple steps we have worked out all the voltages around the transistor plus the current flowing between the collector and the emitter. We are now in a position to begin an examination of the circuit's response to an input signal, i.e: its ac response.

The gain of an amplifier is given by:

 $Av = \frac{\text{collector load}}{\text{emitter load}}$ 

Bear in mind that these values apply to ac conditions only. The collector load includes all the resistances that tie the collector to either the ground or supply rails. (Supply is ac-shorted to ground through the power supply.) It includes at least the collector resistor Rc, the load resistance R1 and the collector-emitter leakage resistance. This latter is usually so high that it can be ignored in low frequency, small signal applications.

The emitter load, likewise, includes all the resistances between the emitter of the transistor and either rail. In practice this will mean the unbypassed emitter resistor Re1, but not Re2. Remember we are talking about ac and assuming that all the capacitors are short circuits, so Re2 is effectively shorted. It also includes the baseemitter resistance,  $r_e$ , which is given by 30/Ic.

The result of this is that we can establish a gain equation for the circuit of Figure 1:

$$Av = \frac{Rc}{r_e + Re1}$$



Obviously, different configurations will have different equations, but the principle remains the same, so you can work out the relevant equation for your particular application.

So far, we have sufficient information to generate a program that will predict certain elements of the performance of an amplifier given the circuit. If you input the values of the resistors the program should come back at you with the gain. If you go to a textbook you should be able to extract equations to give you input and output resistance as well.

The question not answered, and the one we would like to know, is whether the combination of resistors we have chosen is an optimum. The classic method of doing this is with the *load line*.

#### **Load Lines**

Load line analysis involves drawing a pair of straight lines corresponding to the ac and dc loads on the transistor. It is actually a graph of Ic and against Vce. The load line is thus all the possible combinations of Ic and Vce that can exist at the collector of the particular amplifier under consideration.

We can determine the dc line quite easily (see Figure 2). When no current flows i.e: Ic=0, then Vce=Vcc. This defines the bottom point of the line; i.e: the intersection with the horizontal axis. At the other end of the line, when Vce is at a minimum, Ic is

ETI February 1985 - 103



# TECHNIQUES

determined by the value of the resistors through which it flows (Vce is assumed to be zero). The dc load line is just a straight line between these two points.

The operating point, Q, at which the amplifier is biased, must lie somewhere on this line. The ac line intersects the dc line at this point. It is drawn with slope equal to the inverse of the ac collector load. For an unloaded amplifier this will just be Rx, with the result that the ac slope will be the same as the dc slope. However, when the amplifier is loaded the slope of the line will tend to increase as the total collector resistance decreases.

As with the dc load line, the ac line determines the combination of values of Ic and Vce that can exist in the amplifier with a **>** 

The plot quickens. Screen dumps of load lines plotted by the program over the page. The beauty of the technique is its ability to show the results of any variation quite quickly.



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### **PROGRAM LISTING**



specific load. We are now in a position to ask the question about the amplifier we have designed, namely: what is the maximum output voltage I can get from this amplifier without distortion? In most applications the aim of the exercise, after all, is to magnify the input as much as possible while distorting it as little as possible.

#### **Non-linearities**

Non-linearities will occur whenever the output gets close to either end of the load line. Clipping will occur if you try to push the output past it. The idea then is to arrange your gain for a given input such that it can drive the output close to, and not right to, the end of the line. You also want to arrange things such that both positive and negative voltage excursions begin to slip at the same time. There is no point building something that will leave the positive side of the wave unclipped while distorting the negative wave badly. This state of affairs will come about when the Q point, i.e: the quiescent voltage of the transistor collector

11 REM\* THIS PROGRAM PLOTS A SET OF 12 REM\*TRANSISTOR CHARACTERISTICS WHEN\* 13 REM\*CERTAIN PARAMETERS ARE ENTERED. \* 14 REMATHEN LOAD LINES ARE PLOTTED FOR\* 15 REMAVARIOUS VALUES OF COMPONENTS. 35 PAPER 1: INK 7 39 PRINT "\* 40 PRINT "# # \* \* 41 PRINT "# \* \* \* \* \* \* \* 4 \*\*\* \* \*\* 42 PRINT "\* \* \* \* \*\*\* \* \* \* \* \*\*\* \*\*\*" 43 PRINT "\* \* \* \* \* \* \* \* \* \*\*\* \* \* \* 44 PRINT \*\* \*\* \* \* \* \*\* \* \* \* \*\*\*\* \*\* 45 PRINT "# 4 46 PRINT \*\* LANCE WILSON, 1984 \*\* 47 PRINT \*\* 49 PAUSE 2000 50 CLS 55 REM\*\*WRITTEN FOR THE MEMOTECH\*\*\*\*\* 56 RENX MTX500 \*\*\*\*\* 57 REM\*\*GRAPHICS DUMP FOR CP80 PRINTER\* 58 REMA 65 REMATHESE LINES SET VIRTUAL SCREENS. 66 REM#IST FOR TEXT, THEN GRAPHICS. 70 CRVS 2,0,3,0,36,5,40 75 VS 2: CLS : PAPER 6: INK 7 80 CSR 4,0: INPUT "COMPLEX: Y/N?" ;As 81 LET PMAX=200 82 LET VSF=2: LET ICMAX=20 83 LET BVCED=30: LET HFE=100 84 IF AS="N" THEN GOTO 100 85 CLS : CSR 4,0: INPUT "HFE:?" HFE 86 CSR 4,1: INPUT "MAX PC:?"; PMAX 88 CSR 4,2: INPUT "ICmax:?mA"; ICMAX 90 CSR 4,3: INPUT "BVCED: ?"; BVCED 92 LET VSF=INT(ICMAX/10) 100 CLS 102 CSR 4,0: INPUT \* ENTER STEP"; STP 104 CLS : CSR 4,0: INPUT " ENTER VEC"; VCC 105 CSR 4,0: INPUT " EMITTER&COLLECTOR R"; RE, RC 106 CSR 4,3: INPUT "RB1,RB2= ?";RB1,RB2 107 LET ID=VCC/(RB1+RB2) 108 VS 4: CLS : COLOUR 2,11: COLOUR 3,4 109 COLOUR 0,1: COLOUR 1,15: COLOUR 4,6 110 REM\*\*SETS UP AXES AND SCALES\*\*\*\*\*\*\* 111 LINE 20, 12, 255, 12: LINE 20, 12, 20, 190 112 CSR 5,22: PRINT "05 10 15 20 25 30 35" 113 CSR 22,21: PRINT "volts Vce" 114 FOR I=0 TO 11 STEP 1 115 CSR 0, (11-1)#2: PRINT I#VSF 117 NEXT I 118 REM##DRAWS CHAR. CURVES \*\*\*\*\*\*\* 119 LET X=20: LET Y=10 120 LET NEWX=X+STP/10: LET NEWY=Y+STP 122 LINE X, Y, NEWX, NEWY 123 LINE NEWX, NEWY, 250-. 8\*Y, NEWY\*1.11 125 CSR 30-Y/12, 21-Y/7: PRINT Y+10 126 LET X=NEWX: LET Y=NEWY 127 IF Y>150 THEN GOTO 130 128 GOTO 120 129 REN#AFTER TOP CURVE, PLOT PCMAX 130 FOR I=1 TO 230 135 LET YP=PMAX#100/(VSF#I) 140 IF YP>175 THEN GOTO 150 142 COLOUR 3,6 145 PLOT 20+1, YP+12 150 NEXT I 151 CSR 16,10: PRINT "Pc="; PMAX 153 REM\*\*\*\*\*\*UPPER IC & VCE LIMITS\*\*\*\*\* 154 LET ILIM=ICMAX#16/VSF+12: IF ILIM>190 THEN GOTO 990 155 LINE 20, ILIM, 80, ILIM 156 CSR 10,22-ILIM/9: PRINT "Imax="; ICMAX 157 LET VLIM=BVCE0#6.2+20: IF VLIM>250 THEN GOTO 990 158 LINE VLIM, 12, VLIM, 80: CSR VLIM/9-4, 18: PRINT "BVCEO="; BVCEO 159 REM####NEXT CALCS. FOR OPT. AND####

10 REM\*

## TECHNIQUES

162 LET IBQ=ICQ/HFE 163 IF IBR#6>ID THEN GOTO 300 165 LET VCEQ=VCC-ICQ\*(RC+RE) 170 LET IQX=ICQ#16/VSF+12 175 LET VQX=VCEQ+6.2+20 177 IF VOX>250 OR 10x>190 THEN GOTO 850 179 IF VCEQ(O THEN GOTO 2000 180 CIRCLE VQX, IQX, 2 185 CSR .8\*VCEQ+1,22-1.65\*ICQ/VSF: PRINT "Q" 190 CSR 22,0: PRINT 'ICG '; INT(ICG\*10)/10 191 CSR 22,1: PRINT "VCEQ '; INT(10\*VCEG)/10 192 CSR 24,2: PRINT "RE ";RE 193 CSR 24,3: PRINT "RC "IRC 194 CSR 4,0: PRINT "RB1&2"; RB1; RB2 199 GOTO 800 300 REM## 305 VS 2: CLS 308 LET S=ID/IBQ 310 CSR 6,10: PRINT "Id/Ib Ratio is "15 311 CSR 8,12: PRINT ":rather low!" 315 PAUSE 8888 320 GOTO 106 405 VS 2: CLS 410 INPUT "RE BYPASSED?"; AS 420 INPUT "RL=? ";RL 425 LET RAC= (RC\*RL) / (RC+RL) 430 LET VPK=ICQ\*RAC 435 LET VPX= (VCEQ+VPK) #6.2+20 440 VS 4 443 CSR 24,4: PRINT "RL "IRL 445 LINE VPX, 12, VQX, IQX 450 LET IPK=ICQ+VCEQ/VPK+ICQ 455 LET IPX=IPK#16/VSF+12 457 IF IPX>192 THEN GOTO 480 460 LINE VQX, IQX, 20, IPX 470 PAUSE 7777 475 GOTO 1000 479 REM#\*\*\*\* LIMIT CONDITIONS \*\*\*\*\*\*\* 480 LET IPX=190: LET IPK=(IPX-12)\*VSF/16 482 LET VA=VCEQ-RAC\* (IPK-ICQ) 483 LET VAX=VA#6.2+20 484 LINE VQX, IQX, VAX, IPX 488 PAUSE 7777 489 GOTO 1000 800 REM 810 LET IX=VCC/(RE+RC) \*16/VSF+12 812 LET VX=VCC+6.2+20: REM SCALEUNITS 814 IF IX>190 THEN GOTO 900 815 LINE 20, IX, VX, 12 820 PAUSE 8888 830 GOTO 400 855 CSR 15,10: PRINT "OPT OFF SCREEN" 360 GOTO 190 900 REM\*TO COVER OFF-SCALE LOADLINE 905 LET IC=ICMAX 906 LET IX=IC+16/VSF+12 910 LET VX= (VCC-IC\*(RC+RE)) \*6.2+20 916 PAUSE 9000 920 LINE VCC+6. 2+20, 12, VX, IX 930 GOTO 830 990 CSR 5,2: PRINT "ICMAX OFF SCREEN" 998 STOP 1000 LPRINT CHR\$ (27); "A"; CHR\$ (8) ; 1010 FOR J=191 TO 0.STEP -8 1020 LPRINT CHR\$(13); CHR\$(10); 1030 LPRINT CHR\$(27); "K"; CHR\$(254); CHR\$(1); 1040 FOR I=1 TO 255 1050 LET R\$=GR\$(I, J, 8) 1060 LPRINT R\$;: LPRINT R\$; 1070 NEXT I 1060 NEXT J 2000 VS 2: CLS 2001 PRINT "NEGATIVE COLLECTOR VOLTAGE" 2002 PRINT "NOT ALLOWED" 2004 PAUSE 7777 2005 GOTO 104

is midway between the maximum voltage excursions.

#### Departures

To get this far in the analysis we have made certain assumptions about the circuit which are not strictly true. Whether they are significant or not depends on the individual case. It is important to realise they are there, however, so that if you start getting results that are not as predicted you know where to look.

The first problem is that the transistor has a saturation voltage that depends primarily on the current. Saturation voltage is drawn on a load line diagram as part of the transistor collector characteristics. Usually these are drawn as a family of curves indicating the relationship between collector current and voltage for a given base current. These curves will be more or less flat in the linear operating region of the transistor, falling off on the left-hand side as the transistor goes into saturation.

In order to achieve a really accurate determination of the transistor characteristic you ideally need to make a plot for each individual transistor. Failing that, manufacturers' data is a good source for typical figures. However, for our purposes it is probably just as useful if you think of the transistor characteristic as a line passing through the origin. The slope is set by at least one typical combination of current and voltage supplied from manufacturer's data. If you don't have access to this information then a value of 0.3 V at 2 mA is typical for small signal transistors.

A second source of errors is likely to be the assumption that all the capacitors are short circuits and that stray capacitances around the circuit are negligible. As frequency goes up this will become more and more of a problem.

So far, we have thought through this problem as a simple linear process, a not very difficult programming exercise involving a few calculations and the ability to draw some lines. This doesn't really explore the potential of the computer in this regard, though. Its biggest advantage is the fact that you can very quickly see what happens to a host of different parameters of the amplifier if you change values of any of the biasing resistors, or indeed if you change resistor configurations.

We have included a flow chart that should give you some idea of how to go about writing the program. It includes a menu for making individual changes to resistors and then re-running to see the effect. We have also included a listing of a BASIC program that draws a load line diagram complete with transistor characteristics and both load lines. This is written for the Memotech computer and so will need to be rewritten by anyone using a different type of machine, but close study of how it works will be instructive. Good luck!

ETI February 1985 - 107

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#### COMPUTING NEWS

## JED-STD/800, an Australian CMOS single board computer

The new JED-STD/800 CMOS single board computer now in production is a completely CMOS design on the STD buss. Using the National NSC800 CPU, it takes advantage of the Z80 machine code (directly, or via any language where Z80 compilers exist), or in XTBASIC, a version of BASIC generated by JED and tailored for control applications.

According to the manufacturers the board is proving popular in applications where low power consumption and small physical demand an unusual size approach. The CMOS logic provides the low power consumption (less than 0.5 of a watt). The board is very cost competitive with other N-MOS Z80 boards because so many functions (which normally need extra boards) are packed onto one board. It can be only partly loaded, if required, for even lower cost systems (one advantage of production in this part of the world).

The JED STD/800 card puts up to 56K of memory on the STD CPU card, as well as featuring an RS232 serial I/O, 30 bits of parallel I/O, two 16-bit counter/timers, eight analogue inputs (8- or 10-bit) and a realtime clock. The possibility of using only one card in the system, thus eliminates motherboards and racks.

To make programming easy, the board comes as standard with XTBASIC, a fast BASIC interpreter with many control oriented commands and functions built in (allowing, for example, direct control of the analogue system and real-time clock from BASIC).

JMON, a powerful monitor program is also included. It provides facilities for machine code checkout (including hardware single-step of NSC800 machine instructions), and is able to exchange programs and data with CP/M systems using the serial interfaces and a 'talk-though' mode.

Source listings of JMON and the XTBASIC are automatically supplied in the manufacturer's belief that customers are much more able to understand the workings of hardware/software systems if the software sources are readily accessible.

To develop 'on-card' a BASIC control or applications program, a user simply plugs in a terminal and types in the program, using the built-in editor. The program is tested in the final hardware situation, and, when working properly, a low-cost PROM programmer (available from JED) is plugged into the parallel connector at the top of the board, and the program transferred to PROM memory. The PROMs are then plugged into the board.

There is space on-board for 32K bytes of user program. As standard, there are 8K bytes of RAM and the BASIC/Monitor PROM is in high memory.

There are links on the board and data storage locations in the built-in EEPROM which control startup and direct it to the user's program automatically. The EEPROM also controls I/O device driver selection and baud rates. Extra space in the EEPROM is available for users.

In most applications, all the I/O needed is already on the card. The parallel I/O includes 8 or 12 power FETs as relay or opto-osolator drivers, and software to interface directly to a Centronics printer or plotter. If more is needed, it can either be added by calling on other STD buss cards on the market from JED or someone else, or it can be put onto a JED wire-wrap or point-to-point prototyping card; the only overhead with the STD buss is a handful of low-cost logic parts.

Direct serial interface is provided to both LED (or LCD) multi-digit displays and to the JED DataSafe, a portable oneinch by two-inch by four-inch diecast metal box containing up to 64K of CMOS battery backed RAM, designed for data capture in harsh environments.

The current consumption of this card is approximately 100 milliamps total, (depending on memory configuration), and it needs only a single 5 volt supply. A dc/dc converter on board generates -5 volts for RS232 output.

A plug-in card, the JED-STD/805, adds a second story to the STD/800, with two CMOS UARTs, adding two RS232C serial channels, or a single RS232C channel and the choice of an RS422 channel or a current loop with opto-isolators, designed for use around a factory environment. The manufacturer claims it has already found many applications in Australian industry, research, and educational institutions, finding control, field data logging, and teaching uses.

JED also manufactures other STD buss cards and accessories. One of the most popular has been the low-cost battery backed RAM/PROM card. This can hold up to 56K of RAM, or any combination of RAM or PROM. It sells for about half the cost of imported cards of smaller capacity. Other items include a TTL or CMOS I/O board, the JED-STD/820 handles I/O to the OPTO-22 power I/O rack, and adds counter/timers and extra RS232C I/O to an equivalent to the Prolog 7507 or 75C07 boards; an 8-channel, 12-bit analogue output card is also in production. Low cost cabinets are also locally made.

Full data sheets, and any JED products are available from JED Microprocessors, 28 Anderson St, Boronia Vic 3155. (03)762-3588.



#### COMPUTING NEWS

#### Laser unit offers two gigabytes of data storage

New technology from France problem of accumulating libraries of data disks and tapes for Australian computer users.

Distributed in Australia by Pulsar Electronics, the new Gigadisc uses solid-state laser technology and will store 1000 megabytes (one gigabyte) of data on each single or doublesided disk cassette.

Gigadisc can be connected to almost any computer via the Small Computer System Interface (SCSI), and allows the direct accessing of any record.

The unit is attracting major interest as a low-cost storage medium for the holding of large banks of data such as continuous satellite data, says Pulsar's Managing Director, Mr Phillip Delacretaz. Other areas of high interest included TV stations storing multiple video images, CAD/CAM users, medical and scientific institutes and companies with large data bases.

"We have even had an inquiry from a large law firm seeking ways of storing large volumes of important correspondence," Mr Delacretaz said. "The low storage maintenance costs of Gigadisc make it extremely competitive with tapes, which need to be rewound every two years."

Developed by the French company, Thomson-CSF, and distributed nationally by Pulsar, the computer peripheral can record and store data, text, digital image and speech over a period of at least 10 years.

"The viable storage life of recorded magnetic media remains limited to two to three years, even in a controlled environment," Phillip Delacretaz said.

The read/write unit of Gigadisc includes a laser and a photo/detector, and the data can be accessed by three methods, random, optomised random and sequential.

À computer interface controller can be integrated into the Gigadisc unit, which can handle up to eight Gigadisc units. Associated with this controller is an automatic error detection and correction module.

The Gigadisc is supplied in a cassette which simplifies all



handling, storage, loading and unloading operations. It also provides an additional level of environmental protection.

The disks supplied by Pulsar Electronics are preformatted into sectors and tracks and each sector is directly addressable by its logical address. Spiral organisation of the tracks allows for continuous reading and writing of streams of information.

The read-write unit is supplied either as a rack-mounted chassis or as a small stand-alone unit capable of being installed in an office environment.

With the use of the innovative laser technology, writing on the Gigadisc is achieved by the thermal alteration of a metallic film on the disc while reading is done by a laser diffraction technique.

The inner surface of the disk is coated with a layer of polymer and a thin metallic film and while writing, the laser beam evaporates the polymer layer, causing thermal alteration ("bubbling") of the metallic film. According to Pulsar Electronics, this type of laser technology took more than seven years to develop.

Pulsar will retail the basic Gigadisc model in Australia, for approximately \$20 000. Extra two Gigabyte cassettes will be approximately \$500.

For additional information contact Mr Phillip Delacretaz, Pulsar Electronics, Catalina Drive, Tullamarine, Vic. (03)330-2555.

#### Low-cost line conditioning

A rlec Pty Ltd has just introduced the first of a range of line conditioners designed and manufactured in Australia for use with microcomputers and other types of voltage sensitive electronic equipment. Rated at 250 Vac, the 80250 is

Rated at 250 Vac, the 80250 is designed for operation on 50 Hz only and supplies a stable 240 volt RMS sine wave output, within close voltage limits for varying mains inputs extending from 190 V to 260 V.

According to the manufacturer, input voltage fluctuations of up to 15% are regulated to within 3% at the output ensuring a constant supply with less than 5% harmonic distortion.

This performance has been achieved by means of an entirely new core structure which is the subject of a patent application. An electrostatic shield is also incorporated for further noise reduction.

The 80250 is inherently short circuit protected and does not need any protective devices. It will operate continuously at 275 V input and take transients up to 1 kV peak without damage. In common with all Arlec products it is guaranteed for two years.

Further information is available from Arlec Pty Ltd, 30 Lexton Road, Box Hill, Vic 3128. (03)895-0222.



Tell them you read it in ETI

#### Stepper motors

A range of four Stepping motors, manufactured by the diversified Japanese conglomerate Toyo Corporation, has just been released. The units are designed for floppy disk drives, printers, typewriters or any other application requiring precision control.

Two of the four units utilise permanent magnet motors with four phases and offer a choice of 6 V or 12 V. Step angle in both cases is  $7.5^{\circ}$  (48 steps per revolution). Holding forque is 1100 g-cm (12 V model) or 2600 g-cm (6 V model).

The other two units utilise a hybrid type motor with 4 phases, both rated at 12 V. There is a choice of step angles:  $3.75^{\circ}$  (96 steps) or  $3.6^{\circ}$  (100 steps per revolution). Holding torque is 540 g-cm minimum.

For further information, please contact Amtex Electronics, 36 Lisbon St, Fairfield NSW 2165. (02)728-2121.

#### CLUB CALL

The Newcastle Microcomputer Club meets on the second and fourth Monday of each month In room G12 of the Physics Building, University of Newcastle at 7.30 pm. The club which publishes a monthly news letter is not formed around any specific brand of computer. Interested parties should contact Angus Bliss on (049)67-2433 extn 326 (office hours) or Tony Nicholson on (049)52-6017 (after hours weekdays).

Microbug Australia is the Melbourne based Microbee users group. The club meets on the second Wednesday of the month at the Victorian State College, Burwood campus, 221 Burwood Highway, Burwood, Vic, starting at 7 pm. The Vice President Is A. S. Reld, phone (03)873-4455.

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- INTERNAL SHORT CIRCUIT CURRENT LIMIT
- . LOW DROPOUT VOLTAGE (TYPICALLY 2.5 V @ 10 A)
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- PIN-FOR-PIN COMPATIBLE WITH THE #A78H12
- STEEL TO-3 PACKAGE





## 10 AMP ADJUSTABLE uA78PG \$16.83

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- . INTERNAL CURRENT AND THERMAL LIMITING
- INTERNAL SHORT CIRCUIT CURRENT LIMIT
- . LOW DROPOUT VOLTAGE (TYPICALLY 2.5 V @ 10 A)
- 70 W POWER DISSIPATION
- ELECTRICALLY NEUTRAL CASE
- STEEL TO-3 PACKAGE
- ALL PIN-FOR-PIN COMPATIBLE WITH #A78HGA







#### VICDUMP

#### L. Murakami, West Beach SA 5024

This program may be of interest to readers who persevere with using an RS-232 printer with the VIC-20. (Real men don't use Commodore peripherals!) The purpose of the program is to enable one to dump a block of memory (in hex) to an RS-232 printer. It was developed because VIC-20 machine code monitors such as VICMON and HESMON do not support RS-232 printers.

Program structures is as follows:

 Line 40 opens the RS-232 channel at a data rate of 300 baud. The baud rate can be readily changed by changing the CHR\$ value e.g: CHR\$(165) for 150 baud and CHR\$(167) for 600 baud.

 Lines 70-160 gets from the user the desired printer column width from the options of 20, 40, or 80 columns.

. Lines 180-240 Inform the printer as to the column width. The program was developed using an Axiom EX800 series printer which uses the ASCII character codes GS, RS and US for setting the column width. Other printers may require the CHR\$ values in lines 220-240 to be changed.

• Lines 260-410 get from the user the required address range (in hex) of the memory block to be dumped.

Lines 430-580 dump the data to the printer.

. Lines 600-610 are the program exit point whereby the RS-232 channel is closed so that other programs can be run.

· Lines 640-720 perform a hexadecimal to decimal conversion while lines 740-870 perform the opposite function

. Lines 890-1010 get the desired character string from the keyboard.

 Lines 1030-1110 are used to instruct the user as to the format that is to be used for entering the column width and address range.

The program is suitable for any version of the VIC-20.

#### ERRATA

Mr C. Morris has advised that his C64 program hints (September 1984) are not as useful as he first thought. If you fit a 'momentary-on' switch between reset and ground you might damage some ICs. This circuit should perform the reset function without destroying your computer.



#### **ROCKY RALLY** >

#### D. Simpson, Armidale NSW 2350

This game is written for the VIC-20 in the standard 5K. The car is driven using the two cursor keys or the joystick to avoid the rocks and the barriers. There are three stages accompanied by variations in colour and sound.

123456789 610 100 110 120 130 130 150 150 150 150 190 210 210 30 498 500 510 520 520 530 540 550 560 NEXTJ FORI=1TOWID:PRINT#128,DAT\$(1);:NEXTI PRINT#128 1 GOT013000 2 POKE 36879,95: PRINT "" : S=0: FORX=0T021: POKE38884+X,0: NEXTX: CC=2: VC=0 3 POKE650,128 Q=7933:GOSU812000 5 6 DIMIS(2.2): POKE37139.0: DD=37154: PA=37137: PB=37152 7 FORI=0T02:FORJ=0T02:READJS(J,I):NEXTJ,I

9 FORX=0T04:POKE8164+X,8:NEXTX:FORX=0T06:POKE8164+X+15,8:NEXTX

RETURN

\*\*\*\*CLOSE RS-232 PORT\*\*\*\*\*\*\*

REIM\*\*\*\*\*CONVERT HEX TO DECIMPL\*\*\*\*\*\* DEC=0 FORI=ITOLGH:SUB=48 CODE=ASC(MID4(HEX\$,I.1)) IFCODE(SBTHEN690 SUB=55

CURTESTIQUE (CONTRACTOR OF CONTRACT ON CONTRACT OF CONTRACT ON CONTRACT OF CONTRACT ON CONTRACT OF CONTRACT ON CON

- 10 K=INT(RND(1)+8):POKEK+8170,25:POKEK+38890.CC:POKE8169. 8: POKE8178 .8: PRINT
- 11 GOSUB18:S=S+5
- 12 PRINT " # : ROGO OGO OGO OGO OGO OGO OGO OF ECORE : "S
- 13 IFS(250THENFORX=1T090:NEXT:POKE36874,128
- 14 IFS>750ANDS(2000THENPOKEINT(RND(1)+8)+1+8164+6,25:POKE36877,128:
- PDKE36879,40: CC=0
- 15 IFS=250THENPOKE36875,128:POKE36879,108:POKE36874,0:CC=3:VC=0
- 16 IFS>1700THENPOKEINT( RND( 1) +8)+1+8164+6,25
- 17 GOTO10
- 18 GOSU89000; IFS2=-1THEN27
- 19 IFS3=1THEN30
- 20 GETCC#: IFCC#= "M"THEN27
- 21. IFCC\$="1"THEN30
- 22 IFPEEK(Q)=250RPEEK(Q)=8THEN740
- 23 POKEQ-7922+38642.0
- 26 POKEQ-22,32:POKEQ,17:RETURN 27 POKEQ,32
- 28 Q=Q-1
- 29 GOTO20
- 30 POKEQ.32
- 31 0=0+1:607020
- 740 PRINT " SUBSULATION DEPENDENCE
- 741 POKE36878,15:POKE36874,0:POKE36875,0:POKE36877,0:POKEQ-22,0:POKEQ -21,60:POKE0,61:POKE0+1,62
  - 742 POKE36877.220
  - 750 FORR=1T0200 + NEXTR

  - 765 POKE36877,0
  - 769 PRINT "
  - 770 PRINT BEDESCORE : S
  - 780 FORR=1101500:NEXTR
- 800 GETK#: IFK#= " "THEN800
- 805 S=0:C1=0
- 810 IFK\$="Y"THEN:CLR:GOTO2



#### **COMMODORE COLUMN**



FROGRAM DAY 00100 REM FROGRAM DAY 00110 REM This program, when given any date after 1582 will 00110 REM This program, while give in due to the week. 00130 REM This can give Granny quite a surprise.. 00140 REM Written by David S Rapson S. Australia 5050 11/3/84 00150 CL9 00160 INPUT "Enter date: dd.mm./yyy ":D1,M1.Y1 00170 PPINT 001/0 PPINI 00180 IF DI(1 0R D1)21 THEN 160 00200 IF M1(1 0R M1)12 THEN 160 00200 IF M141582 THEN 160 :REM Gregorian calendar only! 00210 IF M1=1 DR M1=2 THEN LET F1=365#Y1+D1+51#(M1-1)+FLT(INT((Y1-1)/4))-FLT(INT (0.75\*FLT(INT(((Y1-1)7100)+1))) 00220 IF M1'2 THEN LET F1=365\*'1+D1+31\*(M1-1)-FLT(INT(0.4\*M1+2.3))+FLT(INT(Y1/4) 1-FLT(INT(0.75\*FLT(INT((":/100)+1)))) 00230 F2=-F1/7 :REM 7 days in one week 00240 A1=F2/10 :REM cant have integer)65534, need special routine as f1 is about 720.000 depending on the date. 00250 42=FPACT(A1) 00260 A2=FL\*(IN\*(A2#10)) 00270 F2=FLT(INT(A1))#10+AZ :REM end of integerising section !! 00280 D2=F1+(F2#7) 00290 PRINT "That's a 00300 IF D2=0 THEN PRINT"Saturday" 00310 IF D2=1 THEN PRINT"Sunday" CO320 IF D2=2 THEN PRINT Monday 00320 IF D2=2 THEN PRINT Monday '00330 IF D2=3 THEN PRINT Tuesday" 00340 IF D2=4 THEN PRINT Wednesday 00350 IF D2=5 THEN PRINT Thursday 00350 IF D2=5 THEN PRINT Indesday 00350 IF D2=6 THEN PRINT"Friday" 00370 PRINT: PRINT"Another date? "i 00320 Y1588EY5 00390 IF Y1\$="Y" OR Y1\$="y" THEN 150 00400 IF Y1\$="N" OR Y1\$="n" THEN END

 O0100
 REM
 Program SUN

 00110
 REM This program will calculate the time of Sunrise and

 00120
 REM Sunset for any place in the world.

 00130
 REM The program is based on Adelaide, but given the decimal

 00140
 REM latitudet- for south, longitude(- for east), and the

 00150
 REM any other place may be calculated.

 00170
 REM any other place may be calculated.

 00180
 REM Written by David S Rapson

 00120
 PRINT\*Are you in Adelaide? ";

 00210
 PIs=\*\*\* OR YI\$\*\*\*\*

 00220
 IF YI\$\*\*\*\*\* OR YI\$\*\*\*\*\*

 00230
 IF YI\$\*\*\*\*\* OR YI\$\*\*\*\*\*\*

 00240
 GOTO 210

 00250
 L1=-35

 1=-35
 L2=-135.58

 1=-35
 L2=-135.58

00260 PRINT : 6070 310 00270 PRINT "No" : PRINT 002/0 PRINT "No" : PRINT 00280 INPUT"What (s the town's Latitude;(\* for N,= for S) ";L1 00290 INPUT"What is the town's Longitude;(\* for West,- for East) ";L2 00300 INPUT"And it's GMT difference;(\* for East of Greenwich) ";G5 00310 PRINT : PRINT 00320 INPUT Enter date; eg. dd,mm "iD1.M1 00330 IF Diki OR D1331 THEN 310 00340 IF M1ki OR M1312 THEN 310 00340 IF MIGI UR MIDIZ THEN 310 00350 SI=0 :REM Correction for Daylight saving, Nov-Mar imc. 00360 IF Missiw OR Yis="" AND LIDO THEN 390 00370 IF MIGZ THEN LET SI=1 00380 IF MIDIO THEN LET SI=1 00390 C1=0.01745329 00400 T1=3. 988\*(D1+30.3\*(M1-1)) 00410 H1=23.5\*(COS((T1+10)\*C1)) 00420 E1=0,123\*(COS((T1+87)\*C1)) - (SIN(2\*(T1+10)\*C1))/6 00430 X1=SIN(H1&C1)/COS(H1&C1)&CIN(L1&C1)/COS(L1&C1) 00430 X1=SIN(H1&C1)/COS(H1&C1)&CIN(L1&C1)/COS(L1&C1) 00440 C2=(1.570796-ATAN(X1/SOR(1-X1&X1)))/C1:REM aFCCOS(X14/C1 00450 G1=12-E1\*(L2-C2)/15 00460 G2=12-E1+(L2+C2)/15 00470 91=01+65 10420 87=62+65 00490 IF R2)12 THEN LET R2=R2-12 :REM keeps 12 hour time 00500 Bi=(RI-FLT(INT(RI)))\*60/100 :REM converts decimal to mins 00510 B2=(R2-FLT(INT(R2)))#60/100 00520 U1=FLT(INT(R1))+B1+S1 00520 012=F1T(1MT(R2))+B2+S1 00540 PRINT: PLAY 24,1 00550 PRINT-Sub-15e ":(F5.2 U1)1" am" 00560 PRINT-Sub-15e ":(F5.2 U2);" pm" 00570 PRINT: PRINT "Another date P"1 00500 Z1\$=KEY\$ 00500 IF Z1\$="Y" OR Z1\$="y" THEN 310 00600 IF Z1\$="N" OR Z1\$="n" THEN END 00410 G070 580

#### DAY

#### David Rapson, Bellevue Heights, SA 5050

This program can be used to calculate the day of the week for any date after 1582.

#### SUN

#### David Rapson, Bellevue Heights, SA 5050

This program will calculate the time of sunrise and sunset for any place in the world. The program is based on Adelaide, but given the decimal latitude (+ for North, - for South) and longitude (- for West, + for East) any other place can be calculated.

#### THE CONVERTER

#### Jon Barnett, Northmead, NSW 2152

The Converter converts an assembly file llsting into a BASIC listing. To use it, assemble and save it to tape. Load the assembly listing you wish to convert, making sure it does not clash with the conversion program. Load the Converter In and run it. It will warm-start to BASIC once the conversion process has finished. Next It must be saved to tape by typing: "OUT 2:LIST:PRINT CHR\$(26):OUT 0". Cold-boot to BASIC and reload the program by entering IN 2.

Now replace the characters which indicate tab spacing with space characters to achieve proper spacing like that of an assembly listing. Save the list-

00010			*********	********************	
	1				
00030	1		**********		
00040	1			ILL DECODE ANY ASSEMBL	
		81913	S PRUSRAM W	TEL DECODE ANT HOSENDL	E e
00050	3	#FILI	E AND CONVER	T IT INTO A BASIC FIL	6.8
00060	8		LASSESSESSES.	J.L. BARNETTERERERE	***
00070	1	ADE S	IGNED FOR TH	E MICROBEE V5.1 SERIE	5.8
08000	3			THE 10TH.FEBRUARY 198	
00090	8				***
00100	8				
00110	8				
00120	1				
00130	3				
00140	IPROGRA		RTS AT 7400	н	
00150		ORG	TAOOH		
00160	DCOSPR	EDU		LOCATION OF BASIC PRI	RT ROUTINE
00170	START	LD	8.12		
00180		CALL	DCOSPR		
00190		LD	C+14		
00200		LD	HL . MESSAG		
00210		CALL	PRINT		
00220		LD	BC.(10BH)		
00230		CALL	ADDRES		D.D.D.C.C.A.M
00240		PUSH		ISAVES THE LOCATION OF	PRUGRAM
00250		EX	DE . HL		
00260	ITHE EP	ND OF	THE FILE IS	SEARCHED FOR	
	SEARCH		8.2		
	FINDEF	LD	A+CHL)		
00290		INC	H		
		CP	255		
00300					
00310		plR	NZ . SEARCH		
00320		DJMZ	FINDEF		
00330		DEC	HL		
00340		LD	A+127		
00350		POP	DE		
00360		CP	н	ICHECKS IF HL POINTS	TO ROM AREA
00370		JR	NC . CONTIN		
00380	10000			D AND DELAY GENERATED	
		LD	HL . ERRORN		
00390			CIR		
00400		LD			
00410		LD	B+12		
00420		CALL	DCOSPE		
00430	)	LD	R.10		
00440	)	CALL	DGOS PE		
00450	)	CALL	PRINT		
0046	)	CALL	OAAF 8H	IDELAY ROUTINE FROM E	ASIC
0047		JR	START		
	CONTIN		HL + 900H		
				LER FORMAT IS REVERSES	
0047	A AND T	NUMPE	CONCO THE	CORRECT FORM FOR HASIC	
	ANU 1	H12 R	ECOMES THE	CURRELI FURN FUR PRSIC	
0051	HL IS	THE	NOTHIER ID	SOURCE CODE AND DE IS	
				NERATED BASIC PROGRAM.	
0053	LINEM.	LD	Ar(DE)		
0054	0	LD	CIA		
0055	D	INC	DE		
0056		LD	A.CTE )		
0057		LD	(HL)+A		
0050		INC	HL.		
0059		LD	(HL)+C		
			255	HOOKS FOR END OF FIL	E MARKERS
0060		CP			Be Charles and
0061		JR	NZONOTEND		
0062	0	CP	C	3.	
0063	0	JR	Z.BASIC		
	O NOTENI	DINC	HL		
0065		THC	DE		
0066		LD	A+( DE )		
0067		LD	B.A		
			A		
0068		INC		HAVE CONTAINING THE	I THE LENCTH
0069		LD	(HL)+A	IBYTE CONTAINING THE	PINE CENCIN
0070	a	INC	DE		

00410 GOTE 360

#### **MICROBEE COLUMN**

#### BANISH THAT 'BAD LOAD' ETI, September 1984.

Max Maughan, Engineering Manager of Applied Technology, (designer and manufacturer of Microbees) has advised that the Idea of the September Issue "Banish that 'Bad Load'," is good for microphone input on a standard cassette recorder, but under test by him on the Micron data cassette the signal was very weak; it would reload on the Micron cassette but not on a standard recorder.

Applied Technology is presently selling a Datatree computer cassette data unit which will not work with the circuit added and this is a reason why the output from the Microbee cannot be set to suit a microphone input only.

Also, with the Datatree the cassette load or input circuit in the Microbee is not needed. The Datatree cassette has a TTL level output and cannot be connected directly to pin 27 on the PIO. The circuit cannot be changed to sult the one application, advises Max.

## ing to tape as a BASIC program using SAVE. The

reason I wrote this program was that I have an assembler but no printer and my friend has a printer but no assembler. Therefore to get any listings required of me to change assembler to BASIC.

00710 INC 00710 INC HL 00720 LD (HL)-32 FINSERT A SPACE AFTER LINE MUMBER 00730 INC HL 00740 LD C-91 00750 ITHE REST OF THE LIME-COMTAINING THE ACTUAL SOURCE CODE 00750 ITHE REST OF THE LIME-COMTAINING THE ACTUAL SOURCE CODE 00750 IST CHARACTERS, THE FIRST THAN IN A LINE WILL BE KERL-00760 IACED BY L-THE SECONU BY \THE THIED BY JITHE FOURTH BY 00790 IACED BY L-THE SECONU BY \THE THIED BY JITHE FOURTH BY 00790 IACED BY L-THE SECONU BY \THE THIED BY JITHE FOURTH BY 00790 IACED BY L-THE SECONU BY \THE THIED BY JITHE FOURTH BY 00790 IACED BY L-THE SECONU BY \THE THIED BY JITHE FOURTH BY 00790 IACED BY L-THE SECONU BY \THE BASIC CX COMMAND. 9 ICHECKS FOR TAB CHARACTER 00810 CONTEN LD As(DE) 9 00820 CP 00830 JR 00840 A+C C 00850 INC 00850 IMC 00860 NDTTAR LD 00870 IMC 00880 INC 00890 DJNZ C (HL )+A HL DE CONTEN (HL )+13 FCARRIAGE RETURN INSENTED 00900 LD 00910 HL 00920 00920 JR LINERU 00930 JBASIC POINTERS ARE SET AND MACHINE CODE PROTECTED 00940 BASIC LD (2258),HL JPDINTER TO PROGRAM END 00950 LD NL+32512 00960 LD (1758) HL IVARIABLES AREA POINTER 
 00%0
 LD
 (1758)+ML
 FVARIABLES
 AREA
 PDINTER

 00%0
 LD
 ML
 FV90H)
 NUM
 01070 DEC N7.PRINT 01080 IR 01090 RET 01100 JGETS THE START LOCATION IN HEX AND STORES IT IN DE 01100 JGETS THE START 0 01110 ADDRES CALL 944 01120 LD D+E 01130 CALL KEY 01140 RLCA 01150 RLCA 01150 RLCA RLCA 01170 01180 01190 LD E.A CALL KEY OR E LD E.A RET 01200 01210 01220 RET 01230 IKEYBOARD TRAP ROUTINE.BC HAS LOCATION ON SCREEN 01240 KEY 01250 KEY1 INC BC CALL BODAN 01260 LD (BC).A SUB 01270 C.KEYI 01280 JR 01290 01300 01310 01320 01330 01340 01350 10 RET 01300 RET 01310 SUB 01320 CP 01330 JR 01340 CP 01350 JR 01360 RET 01370 MESSAG DEFM 01380 ERRORM DEFM 01390 DB C.KEY1 16 NC+KEY1 START ADDRESS: 01390 DB 01400 END

#### **GRAPHICS CREATOR**

#### S. Robiohns, Somerton Park, SA 5044

This program is written for the Microbee Personal Computer and allows you to draw shapes on the computer screen in HIRES and LORES graphics. A cursor can be moved over the screen area and lines drawn between selected points as determined by the user. The co-ordinates of these points are retained and printed out so that they can be used in PLOT statements in later programs to recreate the shapes.

The command keys which control the cursor are: 'E' - to move cursor up, 'X' - to move cursor down, 'S' - to move cursor left, 'D' - to move cursor right, 'M' - to save cursor position, 'I' - to start another trace, 'L' - to end graphic and print coordinates.

Each graphic can comprise a maximum of 30 Independent shapes each containing up to 100 lines. to halt the output if necessary.

These values may be changed in lines 130 and 410 to suit other memory capacities. When using HIRES graphics you are limited to 128 PCG characters. This number is continually monitored and displayed in the top left corner of the screen. Also directly below this number appears a letter which corresponds to the last command given from the keyboard. This can be useful when you are not sure whether you have entered the 'M' or 'l' commands.

In the program as presented, it is assumed a printer is connected to receive the printout of the point co-ordinates. If this is not the case then the following lines should be changed to:

480 SPEED 50

600 SPEED 0

This will allow time to use the 'CTRL S' command

	The second
	OgioO REM HIRES/LORES - Graphic Creator
	JOIIO REM S.L. Fobjohns
	30120 CLS
	00130 DIM A(101,31),B(101,71),M(31)
	00140 CURS17, 5: UNDERLINE: PRINT "HIRES/LORES Graphic Creator ":NORMAL
	00150 CURS13, S:FRINT"Enter 'L' FOR LORES : 'H' FOR HIRES";:CURS0
	00160 K0%=KEY%: IF K0%=** THEN 160
R	00170 IF KO\$="H" OR HO\$="h" THEN HIRES:CO\$="HIRES :":X=256:7=102:Q=511:R=208:GOT
	0 190
	00180 IF KO\$="L" OF KO\$="1" THEN CLS:LORES:CO\$="LORES :":X=64:Y=19:Q=127:R=\$3:GO
	TO 190 ELSE 160
	00190 FOR Z=1 TO 64:CURSZ, 3:FRINT""" INEXT Z
	00200 CURS5:PRINT"1":CURS69:FFINT"1"
	00210 CURSS:PRINT"Cursor Movement : E - up X - down S - left D - right"
	00220 CUR372: PRINT "M - save point I - another trace L - finish graphic"
	00230 SET X,Y
	00240 C=0:I=0:J=0:K=1:W=0:T=0:K1\$=" "
	00250 CURSO: PRINT USED
	00260 ON ERROR GOTO 620
	00270 CURS65: PRINT K1#
	00280 IF X=0 THEN RESET X, Y:X=1:SET X, Y:GOTO 320
	00290 IF X=0 THEN RESET X, Y:X=Q-1:SET X, Y:GOTO 320
	00300 IF Y=0 THEN RESET X, Y:Y=1:SET X, Y:GOTO 320
	00310 IF Y=R THEN RESET X, Y:Y=R-1:SET X, Y
	00320 K15=KEY5
	00330 IF KIS="E" OR KIS="e" THEN LET E=X:F=Y:Y=Y+1:SET X,Y:RESET E,F:GOTO 370
	00340 IF KIS="X" OR KIS="x" THEN LET E=X:F=Y:Y=Y-1:SET X,Y:RESET E,F:GOTO 370
	00350 IF KIS="S" OR KIS="S" THEN LET E=X:F=Y:X=X-1:SET X,Y:RESET E,F:GOTO 370
	00360 IF KIS="D" OR KIS="d" THEN LET E=X:F=Y:X=X+1:SET X,Y:RESET E,F
	00370 IF W=1 THEN SET U.V
	00380 IF KIS-M" OR KIS-"M" THEN LET W=1: U=X: V=Y: T=0: GOTO 430
	00390 IF Kis="L" OR Kis="1" THEN LET I=I+1:M(1)=J:GOTO 470
	00400 IF K15="I" OR K15="1" THEN LET I=1+1:M(]=J:J=0:K=K+1:T=1
	00410 IF I=30 OR J=101 THEN CURS16,4:PRINT"Maximum number of shapes or lines":GO
	TO 650
	00420 0010 250
	00430 J=J+1:A(J,K)=X:B(J,K)=Y
	00440 IF J(2 THEN 250
	00450 PLOT A(J,K), B(J,K) TO A(J-1,K), B(J-1,K)
	00460 6010 250
	00470 CLS
	00480 CURS16,7: INPUT Ready printer, then press RETURN \$ 1435: CLS: OUT#1 ON: REM SPEE
	D 50 (without printer)
	00490 PRINT:PRINT COS:PRINT
	00500 IF T=1 THEN LET I=1-1
	OOSIO FOR P=1 TO I
	00520 PRINT"PLOT (")
	00530 IF M(P) (=0 THEN LET M(P)=1
	00540 FOR G=1 TO M(P)
	00350 PRINT A(G,P)1 * , * (B(G,P))
	00560 IF G(M(P) THEN PRINT" ) TO (";
	00570 NEXT G
	00580 PRINT ) *: PRINT
	00590 NEXT P
	00600 OUTHO:REM SPEED 0 (without printer)
	ODGIO END
	00620 IF ERROR C = 16 THEN LET C=1:GOTO 640
	00630 CLS:PRINT"Error ";ERROR C;" in Line ";ERROR L:END
	00640 CURSO: PRINT USED: CURS14, 4: PRINT You have used all the PCG characters"
	00650 CURSIZ 16:PRINT Do you wish to save these coordinates?"
	00600 K2\$=KEY\$: IF K2\$="" THEN 660
	00670 IF K25="Y" OR K25="Y" THEN LET I=I+1:M(I)=J-C:GOTO 470
	OOGO IF K25="N" OR K25="n" THEN CLEAR GOTO 120 ELSE 660

#### IAN J. TRUSCOTTS

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#### SHOP AROUND

The information in this section is to assist readers in the continual search for components, kits, printed circuit boards and other parts for ETI projects and circuits.

#### ETI-342 Pulse-shaped CDI

Most of the components for this project should be readily available from the majority of electronics suppliers. You may have to shop around for the potcore for L1 and toroid for L2, however; we'd suggest you try places like Geoff Wood Electronics or Radio Despatch Service in Sydney, or All Electronic Components in Melbourne. The same places should be able to help with the more esoteric capacitors: C1, C2 and C6, and with the less common semiconductors D1, D10, Q4 and IC1. If you're after a complete kit, try Altronics in Perth. Jaycar in Sydney and Rod Irving in Melbourne. Dick Smith Electronics has advised that it may also be producing a kit.

#### **ETI-170 CRO** calibrator

The only parts in this project that are likely to cause any availability problems are the rotary switches, SW2 and SW3. These aren't as readily available nowadays as they were, although most suppliers should be able to get them for you even if they don't normally carry stocks. If you have trouble, suitable switches from the 'Lorlin' range (type RA for SW2, type CK for SW3) are available from C& K Electronics Pty Ltd, at PO Box 229, Parramatta, NSW 2150 or 15 Cowper Street, Harris Park. Phone (02)635-0799. For complete kits, try *Altronics*, *Jaycar* or *All Electronic Components*.

#### ETI-742 Speech-tailored speaker

The 6-inch speaker used in our prototype was from *Jaycar* in Sydney, type H16CP60-02C. Low in cost, it's ideal for the job; in fact the design is based on this speaker, so it would be unwise to make any substitutions. Everything else should be readily available.

#### ETI-666 Printer switch

The only parts in this project that may involve a little shopping around are the insulation-displacement connectors. You should be able to get these from *Geoff Wood Electronics, Radio Despatch Service*, or *Sheridan Electronics* in Sydney and from *All Electronic Components* or *Rod Irving Electronics* in Melbourne. *Jaycar* and *Altronics* would also be worth trying. If all else fails, or if you want to save money, solder-type Centronics plugs and sockets will be available from an even wider range of suppliers.

#### Artwork

Here are prices for this month's projects:

ETI-342: pcb — \$3.30 ETI-170: pcb — \$3.60; panel — \$5.10 ETI-666: pcb — \$2.15; panel — \$1.60



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#### **COMPUTER REVIEW**

**Mark Lingane** 

## COMMODORE PLUS/4



Commodore goes for the low-end business market! This powerful little package comes with built in business software designed to be really user friendly as well as 'user useful'. It's packed with features not offered elsewhere, so Commodore might be onto a winner.

THE NEW Commodore Plus/4 is the next generation 64. In brief, it can be described as having everything the model 64 should have had, but didn't.

The Plus/4 is something of a 'different' beast for Commodore. It is so-named because of the four applications programs included in read-only memory in the machine. Quite a departure for many computer companies, Commodore not excepted. The four programs are the sort of thing a small business person or freelancer could usefully use in day-to-day business: word processing, file management, spreadsheets and graphics. The four programs can share and exchange information and the Plus/4 has a 'split screen' or 'windowing' facility which allows viewing of the operating parts of two of the packages simultaneously.

For the record, the hardware has 64K of RAM (60K available to the user), a 'help' key (which all newcomers will make much use of) and an advanced version of BASIC for those who want to get into programming. It will work in conjuction with many of the VIC-20 and Commodore 64 peripherals. It costs only around \$599 and, while aimed initially at the smaller business market, the Plus/4 is expected to appeal to home computer buffs, too.

Commodore Australia is no 'johnnycome-lately' on the computer scene. They've been operating here for four years and have sold over 200 000 computers, they claim, during that time. They have an Australia-wide network of outlets and a corporate structure to support increasing sales here, having undergone a bit of a rearrangement a few months back.

#### **First impressions**

The first thing you notice about the Plus/4 is its bold new look. It is charcoal black with white keys which gives a 'space' look to the whole machine. However, the keyboard has a 'clackety' feel about it which gives the impression of a cheap set of keys. All the graphics characters, and colour functions etc are printed on the keys giving the keyboard a cluttered look. The keys are easy to read though.

At last Commodore has included all the colour functions on the keys; something that was missed on the 64. Instead of remembering that to get the colour orange you need to press the Commodore key and the 2, now you just look at the key and it has the letters 'ORG' on it, indicating the colour orange.

#### **New features**

A new feature includes a flashing characters feature. Press the 'Flash' key and whatever you type will flash on and off. Ideal for warnings.

One of the most important new features is that of window capability. Not quite the same as Apple's Macintosh but still, it's a start. On the 40 characters by 25 lines you can define your own screen, for example, a 20 character by 10 line screen in the middle of the screen or a 40-by-5 at the bottom of the screen as a control window. Possibilities are limited only by your ingenuity.

The cursor keys are now arranged in a cross. That is, there are now four cursor

keys, each one pointing in the appropriate direction. A great idea which should have been incorporated in the Commodore machines much earlier.

Another new feature included (as against omitted on the 64) is the Disk Operating System. Useful commands such as DIREC-TORY, DLOAD, DSAVE, SCRATCH (deletes files), RENAME, BACKUP and many others are available. This feature is about the best one that could have been included on the Plus/4. The time that it will save and the ease of use is immeasurable, I think.

#### **BASIC** etc

Commodore has also included a decent BASIC this time round. It features a massive 75-word command/function vocabulary, including DRAW, SOUND, COLOR and many others. Commands are included for graphics, sound, editing and debugging. The Plus/4 is a real breeze to program without delving into the murky depths of machine code.

Speaking of machine code, the Plus/4, like the Commodore 16, has a built-in machine language monitor. This includes 12 powerful commands such as Assemble, Disassemble, Transfer and others which will make Commodore 64 owners sick with envy. What will make the 64 owners even more jealous is the fact that when they bought their 64 it boasted 64K of RAM with only 39K available, whereas on the Plus/4 the power-up message states that there is over 60K free for BASIC programming! I can see the previous 64 owners

#### COMPUTER REVIEW

reaching for the bank book now. But still the Plus/4 has more features.

#### **Plusses for the Plus**

The function keys are predefined. This means that when you press them something happens; not like the 64 where they must be programmed. The keys can be changed by simply typing the word 'KEY' and all the functions that the keys are doing are dis played and can be altered.

The function keys and the powerful BASIC included means that you don't need to be a programming genius to use the machine to its full potential. Quite a different machine to the 64 in that way. This change may be caused by the fact that the Plus/4 is aimed at the business market and not the home user or computer boffin, and the business people who cannot always afford the time to learn how to program the machine to make it do simple tasks.

For example, to clear the high resolution screen on the 64 in BASIC took about two minutes and a lot of number remembering. On the Plus/4 one statement is required, SCNCLR, and is instantaneous. Which would you prefer?

Another business plus is that the Plus/4 fits into a brief-case very easily as it is about three quarters the size of the Commodore 64. The keyboard is the right height to minimise wrist fatigue, unlike the 64 which was too high off the table surface to facilitate hours of constant typing.

#### Graphics

The graphics have been improved greatly. Impossible, I hear you say! No so. Now, instead of having 16 colours to choose from you have 128. That is, 16 colours with eight shades each (though I cannot see how you gan have eight shades of black).

The graphics can almost equal the supreme graphics of the Atari home computers. The high resolution screen has not changed much from the 64. It is still 320 by 200 pixels or 160 by 200 in multicolour mode (more than one colour on the screen at a time).

With the command GRAPHIC you can jump into high resolution mode. Following the command with the number two gives you four lines of text at the bottom of the screen, a feature which is fairly common to most forty character screens.

#### Sound

With the sound it looks as though Commodore has taken a backward step as they have dropped the powerful programmable SID chip and replaced it with a more common tone generator. This step was most probably done with business use in mind. Sound enveloping and programming ADSR might seem a bit excessive if you only want to produce simple monotonic sounds.

#### **On-board software**

The real strength behind the Plus/4 is what comes on board and how its name is derived. On board there are four business packages; word processing, spreadsheet, database and a graphics package. This makes the Plus/4 a sort of 'Super-Adam' (Colecovision's home/business computer system).

Each of these programmes can be accessed by pressing just one key. The ultimate for business? — a complete office at the touch of a button! But if Commodore wishes to pursue the lower end business market they definitely need an 80 column alternative. This I think is the only weak point of the Plus/4.

#### Summary

The Plus 4 has many plusses and very few minusses. I am almost tempted to say that it has everything that a computer user might want. But that would mean that the Commodore Plus/4 is the perfect machine. I don't think we are at that stage of the ultimate computer, but the Plus/4 takes us that one step closer.

#### COMMUNICATIONS

MINI MART

WANTED: FOR COLLINS R390A — modules, particularly if strip and aerial C/O. Also Collins handbook, purchase or Ioan. D. Bruce (03)587-1593.

#### COMPUTERS

FOR SALE: ZX-SPECTRUM 48K as new with  $\pm 250$  programs \$450 ono. Will separate. Write to J. Crest, 175 Trafalgar St, Stanmore 2048.

MICROBEE unwanted original software. Ztrek, Chess, Robotman, Pilot, Escape from Colditz — \$6 each. Chopper, Kilopede, Sydney Approach \$8 each. J. Arnold (02)625-8950.

FOR SALE: SUPER 80 Disassembler, cassette \$9; teleprinter \$40; vintage valves, new from \$1, include 6L6-G, 809, CV4060. Send SSAE for the price list. R. Vowels, 93 Park Dve, Parkville 3052.

DISK DRIVES, Siemens FDD 100-8, 8", single sided, double density, IBM 3740 format compat, all power connectors, manual, Shugant 801R compatible, brand new, \$325 each. (02)929-6497 (ah).

VZ200 SOFTWARE, cash book ledger, assembler, utilities, hardware tips etc. Send SAE to Mr J. C. E. D'Alton, 39 Agnes St, Toowong, Qid 4066.

\*

FOR SALE: COMPUTERS — Arcade game board "Warlords" includes: 6502 CPU, 16K EPROM (8 x 2716) wiring diagram, harness, artwork, fully working. Can be used in Arcade or experimented on. \$100. (09)447-8819.

FOR SALE: SINCLAIR ZX81 — 16K RAM, full size keyboard, green phosphor monitor, cassette recorder, plenty software, mags. Package \$400. Will sell Individually. (03)49-5587.

FOR SALE: MICROBEE 32K+ with Wordbee, Edasm, green screen monitor, DataTree tape recorder, joystick and over \$200 software. The lot for \$600. (02)869-1113.

FOR SALE: Computer Biorhythm Printout for nine months plus notes \$4 posted. Send date of birth with name to Comprint 'Baliambee', Cassills Road, Mudgee 2850.

FOR SALE: S100 boards DGZ80 CPU, DG640 VDU, ETI-681 PCG, 2 x 16K RAM, and mother board, all working order. Any offers? C. Cogzeli, 28 Stuart St, Toowoomba 4350. (07)32-1832 ah.

FOR SALE: S100 ENTHUSIASTS — 4 boards DGZ80, ETI-640 VDU, 16K RAM, I/O port board as new. Plus 10 slot mother board, power supply. Manuals Incl. Only \$500 ono. G. Knott, Newcastle (049)57-5421.

FOR SALE: SYSTEM 80/TRS 80 programs. For Information contact Raymond Schatz, 53 Yass St, NSW 2594.

FOR SALE: ACT VIC-20 bimonthly magazine. Many interesting articles and programs. December issue \$2. Bimonthly \$12 per year. Write to Chris Groenhout, 25 Kerferd St, Watson 2602.

FOR SALE: BATTLESHIP: TRS80, 1, 2, 4. Requires 2 computers or modem. Send SASE for info to C. Cranstone, 17 Helen St, Christie Downs 5164.

#### **AUDIO**

FOR SALE: REALISTIC ST/500, SA/500, SCT/500 tuner, amplifier, deck, cost \$879, sell for \$479; plus Bearcat 150 FB scanner, cost \$285, sell \$199; plus an ac intercom, cost \$119, sell \$75 or \$575 the lot. Nino Paradiso, 12/12 De Munska St, Windsor 3181.

#### **MISCELLANEOUS**

FOR REPAIRS, modifications or building of ETI, EA projects, even custom designs. Contact Andrew Newcombe, 66 Gellibrand St, Campbell ACT. (062)47-7601.

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6



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#### DREGS



#### Ants

We received the picture above from an outfit calling itself British Telecom. I take it this is the pommy version of our own dear phone company. Anyway, this go ahead organisation has designed a chip configured as a laser to project pulses of light through hair thin optical fibre.

The electron microscope pic shows just how small the chip is — that's an ant's head it's sitting on.

Questions arise. Why is it so small? What is it doing on an ant's head? Is it really a chip at all?

At a recent lunchtime brain storming session, held at an establishment not terribly far from our office, ETI staffers tried to solve this and other mysteries of the universe. Numbers between 41 and 43 were not well received. Neither were references to flea brains.

One thought held that this was the British answer to threats to the empire. The idea being that the chip is actually an extremely clever mind control device embedded in the ant's brain. By judicious selection of ant types it should be possible to find animals that will eat just about anything. So it becomes possible to direct legion after legion of ants to attack the enemy telecommunications and energy supplies, eat his bridges and buildings, destroy tapes and magnetic recording media. In short, to really screw things up.

The idea is not new. For a while the CIA (why does every Australian wind up talking about the CIA sooner or later?) was training dolphins to carry explosives to enemy shipping. They gave up on the idea when someone pointed out that the dolphins might get such a kick out of blowing up friendly ones as well. (Of course, the mathematics of this situation start to look very different if you come from a country without much of a navy...).

Other thought provoking military applications were suggested. For instance, the ants could be liberated in enemy territory, each with this little laser cap on its head.

When approached by the enemy the laser can be used to zap them. It's a sort of cheap star wars scenario. (And hopefully about as practical.)

Non military uses? Well, what about a radio transmitter so that entomologists can keep track of bugs and other small animals. What about medical uses? As long as doctors like sticking things into, up, down and through our collective bodies, there should be a place for a device like this. In fact, if you think about it, it really gives new meaning to the phrase: "ants in your pants".

#### Going ... going ...

Several pubs in the Waterloo, Zetland area of Sydney have reported a significant downturn in profitability since the demise of a large bearded person with a raucous laugh from this area. He was last seen staggering from the Doncaster Hotel in the direction of greener pastures. It is not known whether the pubs will survive this trauma.

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