

MODEL PCL-505 (890-960 MHz)

### MONAURAL

**5** 

I

0

### AURAL

### STUDIO-TO-TRANSMITTER LINK

MOSELEY ASSOCIATES, INC. Santa Barbara Research Park 111 Castilian Drive Goleta, California 93017

> Revised March 1978

(805) 968-9621



MOSELEY ASSOCIATES, INC.

e su Na

.

### FINAL TEST DATA

MODEL PCL-505

	Date Order <u>#</u> Technician	17 Aug. 19 3392 Conrad	83	Customer Tx Serial Rx Serial Frequency	KHYX # 39225 # 40699 950.125 MHz
_	Transmi	tter Meter I	Readings	Receiver Me	ter Readings
	Program MPX Chan. 1 @ Chan. 2 @ AFC FRD PWR 6.0 RFL PWR +VDC Reference Oscil H.F. Divider I.P.A. Drive Final Current	<pre> 26 kHz 67 kHz Watts lator 2 amp max</pre>	$\begin{array}{c c} 0 & dB \text{ top} \\ \hline 10 & bottom \\ \hline 15 & bottom \\ \hline 15 & bottom \\ \hline 0 & top -0.5 \\ \hline 0 & bottom 0 \\ \hline 12.5 & bottom 12.5 \\ \hline 14.5 & bottom 14.5 \\ \hline 13.0 & bottom 13.0 \\ \hline 20.5 & bottom 14.0 \\ \hline 11.5 & bottom 14.2 \\ \hline \end{array}$	+VDC Signal (no input) Program @ 100% mod MPX 26 kHz 67 kHz Level for 45 dB SNR: <u>RF P<sub>o</sub> 1</u> FMO MULT-DRIV FINAL AMP	$\frac{12.5}{\text{bottom}}$ $\frac{12.5}{\text{bottom}}$ $\frac{12.5}{\text{bottom}}$ $\frac{18}{10}$ $\frac{10}{15.9}$ $\frac{15.9}{\text{bottom}}$ $\frac{-90}{\text{dBm}}$ $\frac{120}{\text{Bm}}$ $\frac{25}{15}$ $\frac{15}{120}$ $\frac{180}{5}$ $\frac{120}{5}$ $\frac{10}{5}$
1	Power Supply to Transmitte: Receiver	be set u <u>s</u> in; r	g a DVM <u>12.5</u> VDC <u>12.5</u> VDC	Receiver Signal Met	ter Calibration
-	PCL-505 S	ystem Perfe	ormance	Microvolts	
-	Freq. (Hz) R	lesponse	Distortion (%)	5	1.8
-	30	<u>4</u> dB		10	1.9
	50	<u>4</u> dB	.13	20	2.5
-	400	0dB	.07	50	7.0
	1,000	0ref	.05	100	9.1
٦	5,000	0dB	.07	200	11.0
	10,000	+ .3 dB	.06	500	13.0
	15,000	0dB	.08	1,000	14.0
-	Sys	tem Noise		1,500	14.0
د د د ه	Ultimate SNR: SNR: Level for 60 dB Squelch set betw These readings for reference pu adjustment, RF Rev. 12 May 198 ph	74 dB with not SNR: -82 ween 15-20 were noted of rposes. Re termination: 3	dB ise reduction circui dBm μV during final electric adings may vary wi s, equipment instal	it active al test of the equipme th component replaces lation, or path conditi	nt and are intended ment or aging, ons.
• •• •	pu			. 1	

### REALEST ADDRESS TELEPIN

### PIERAL PESS DATA

201-108 d3010

MOSELEY	ASSOCIATES,	INC.
---------	-------------	------

FINAL TEST DATA

MODEL PCL-505

_	Date	17 Aug. 1983
71	Order #	3392
	Technician	Conrad
-		

Customer	KHYX	
Tx Serial $\#$	39226	
Rx Serial #	40700	
Frequency	949.875	MHz

Receiver Meter Readings

### Transmitter Meter Readings

	Program MPX Chan. 1 Chan. 2 AFC FRD PWR <u>6</u> RFL PWR +VDC	@ 26 kH @ 67 kH Wat	Iz Iz ts	$\begin{array}{c c} 0 & dB \text{ top} \\ \hline 10 & bottom \\ \hline 15 & bottom \\ \hline 15 & bottom \\ \hline 0 & top & 0 \\ \hline 0.5 & bottom \\ \hline 12.5 & bottom   1 \\ \end{array}$	+VDC Signal (no inpu Program @ 10 MPX 26 kHz 67 kHz Level for 45 d	ut) 0% mod. B SNR:	12.5bottom1.8bottom0dB10bottom13.1bottom-88dBm
	Reference Osci H.F. Divider I.P.A. Drive Final Current	illator 2 amp	max	15.0         bottom K.º           13.0         bottom B.º           19.0         bottom b.º           8.5         bottom G.S	FMO MULT-DRIV FINAL AMP	RF P <u>o Levels</u> <u>18</u> <u>180</u> <u>6</u>	15 MW min 120 MW min .0 5 W min
	Power Supply t Transmitt Receiver	o be se er	t usin	g = DVM $12.5 VDC$ $12.5 VDC$	<u>Receiver Sig</u> Microvolts	nal Meter Ca	libration
	PCL-505	System	<u>i ren</u>		5		-
	Freq. (Hz)	Respon	ise	Distortion (%)	2		5
<sup></sup> 1	30	- 4	_dB	.10	10	Z .	
_!	50	2	_dB	.05	20	6.	5
-1	400	0	_dB	.05	50	9.	5
_1	1,000	0	ref	.04	100	11.	1
1	5,000	0	dB	.07	200	12.	7
	10,000	+ .2	dB	.06	500	_14.	0
-	15,000	0	dB	.09	1,000	14.	2
_					1,500	_14.	. 5
	<u>S</u>	ystem ]	Noise				
	Ultimate SNR:		-	74 dB			
1	SNR: 82	dB ·	with n	oise reduction circu	uit active		

Level for 60 dB SNR: \_\_\_83 dBm

Squelch set between 15-20 20  $\mu V$ 

These readings were noted during final electrical test of the equipment and are intended for reference purposes. Readings may vary with component replacement or aging, adjustment, RF terminations, equipment installation, or path conditions.

1

Rev. 12 May 1983

ph



.

Ţ

TABLE OF CONTENTS

					Page
1.	INTI	ODUCTION			1
2.	SPE	CIFICATIONS			2
	2.1 2.2 2.3	System Transmitter Receiver			2 3 4
3.	UNP	ACKING			5
4.	INST	ALLATION			5
5.	OPE	RATION			15
6.	CIRC	UIT DESCRIP	TION		15
	6.1	Transmitter         6.1.1       Input         6.1.2       Modul         6.1.3       Freque         6.1.4       Power         6.1.5       (This         6.1.6       High-F         6.1.7       AFC         6.1.8       Power	Interface ated Oscillator ency Multiplier • Amplifier section deleted 2/76) Frequency Buffer and • Supply	Divider	15 20 22 25 25 27 27
	6.2	Receiver 6.2.1 Input 6.2.2 RF Pr 6.2.3 Balance 6.2.4 Local 6.2.5 74 - 1 6.2.6 FM De 6.2.7 Meters 6.2.8 Program	Bandpass Filter reamplifier ced Mixer Oscillator 10.7 MHz Converter emodulator and Meter ing and Muting am Amplifier	• Amplifier	31 31 31 36 36 36 36 41

PCL-505, PCL-505/C (Rev. 2/76)

-i-



### Page

61

62

OPE	RATIONAL SUGGESTIONS	44
7,1	Recommended Standards and Data	44
7,2	Program Levels	45
7.3	Subcarrier Levels	46
7.4	Proof of Performance - PCL-505	47
7.5	Proof of Performance - PCL-505/C	50
7.6	Cross Talk into Subcarrier	52
7.7	Composite Receiver to Exciter Interface	52
7.8	Remote Control of the STL Transmitter	53
7.9	Adjustment Guides	53
	OPE 7.1 7.2 7.3 7.4 7.5 7.6 7.7 7.8 7.9	<ul> <li>OPERATIONAL SUGGESTIONS</li> <li>7.1 Recommended Standards and Data</li> <li>7.2 Program Levels</li> <li>7.3 Subcarrier Levels</li> <li>7.4 Proof of Performance - PCL-505</li> <li>7.5 Proof of Performance - PCL-505/C</li> <li>7.6 Cross Talk into Subcarrier</li> <li>7.7 Composite Receiver to Exciter Interface</li> <li>7.8 Remote Control of the STL Transmitter</li> <li>7.9 Adjustment Guides</li> </ul>

Field Changes

P

1

Final Test Data

PCL-505, PCL-505/C (Rev. 10/75)

														(==)				E
--	--	--	--	--	--	--	--	--	--	--	--	--	--	------	--	--	--	---

·

### INSTRUCTION MANUAL

### MODELS PCL-505 AND PCL-505/C

### AURAL STUDIO-TO-TRANSMITTER LINKS

### 1. INTRODUCTION

The Models PCL-505 and PCL-505/C Studio-to-Transmitter Links (STL) were designed to convey high-quality aural program material from a studio site to a transmitter site. Control and secondary programming subcarriers may also be simultaneously carried by the PCL-505. The wide-band "composite" version of this equipment, designated the PCL-505/C, allows the transmission of the complete composite FM stereo broadcast signal over only one Two PCL-505 units can be operated in the dual-link conlink. figuration to carry Left and Right audio channels for stereo operation. This equipment may also be used in intercity relay The environment in which this equipment must operate service. and the operators using it have both been carefully considered. Attention to design details and quality in construction distinguish the PCL-505.

PCL-505, PCL-505/C (Rev. 10/75)

Ę

-1-

### 2. SPECIFICATIONS

### 2.1 System

Monaural (PCL-505):	
Audio Response	±0.4 dB, 30 Hz to 15 kHz
Audio Distortion	Less than 0.4%, 30 Hz to 15 kHz
Signal-to-Noise Ratio	Better than 68 dB
Emission	110F3 (no subcarrier) 110F9 (26 kHz control subcarrier) 230F9 (67 kHz program subcarrier)
Composite (PCL-505/C):	
Wide-band Response	±0.2 dB, 30 Hz to 60 kHz ±0.5 dB, 30 Hz to 75 kHz
Wide-band Distortion	Less than 0.4%, 30 Hz to 60 kHz
De-emphasized wide-band output ultimate SNR	Better than 65 dB
Stereo Separation	Better than 35 dB (assuming stereo generator is better than 38 dB)
Stereo Cross Talk	Better than 43 dB, linear and nonlinear combined
Emission	226F9 (no subcarrier) 270F9 (67 kHz program subcarrier) 340F9 (110 kHz control subcarrier) 490F9 (185 kHz program subcarrier)
RF Frequency Ranges	148-174 MHz, 215-240 MHz, 300-330 MHz, 450-470 MHz, 890-960 MHz
Temperature Range	-20°C to +60°C

PCL-505, PCL-505/C (Rev. 6/76)

.

-2-

ü J 

li

Ľ

,

### AURAL STUDIO-TRANSMITTER LINKS

Bulletin 278 A



## PCL-606 and PCL-606/C



SEH

HY

FLOW GENERALIS GOMPANY

FOR 148-174 MHz 215-240 MHz 300-330 MHz 450-470 MHz 890-960 MHz

**FES, INC.** 

# PCL-606 and PCL-606/C



### WHY STL?

Studio-transmitter link (STL) systems have traditionally offered broadcasters an alternative to leased telephone lines for conveying program information from the studios to a remote transmitter location. Telephone line charges have increased dramatically over the past few years, while line reliability and ultimate audio quality have either degraded or stayed the same. Studio-transmitter links offer the broadcaster complete control over program carriage with excellent reliability, two factors very important in today's broadcasting. Studio-transmitter links will also convey a program subcarrier, such as an SCA feed, as well as remote control information over the same economical link.

### WHY PCL-606?

The PCL-606 and PCL-606/C Studio-Transmitter Links provide the broadcaster and industrial user alike with the highest quality program conveyance service currently available in equipment of this type. By the use of the latest technology available in today's market, significantly improved specifications and performance are achieved, even in areas overly congested in STL service or in areas presenting high density RF environments. The PCL-606, designed for highest quality monaural audio service, may be used in a dual configuration for stereo service where composite stereo is not desired. The PCL-606/C. the composite stereo version, conveys the composite stereo waveform with virtually no degradation, neither adding to nor taking away from the stereo waveform.

The PCL-606 and PCL-606/C Transmitter and Receiver are of an all-new design, using techniques and components heretofore unavailable. Extensive field testing was done on this new STL system to ensure highest performance in hostile RF environments. Enclosed module construction is used to reduce the possibility of RFI as well as allow easy service access to each printed circuit board. All normal service tuning adjustments are easily, yet securely, accessible through the tops of the modules and unit top covers, while extensive internal metering capabilities are standard in both the Transmitter and Receiver.



## PCL-606 and PCL-606/C



### TRANSMITTER

The PCL-606 and PCL-606/C Transmitters employ a direct FM modulation concept never used before in STL equipment. A synthesized reference oscillator is used for frequency and phase control of the direct FM oscillator. Transmitter FM modulated oscillator frequency conversion is done via a double balanced mixer, instead of employing the usual frequency multiplication of the modulated RF signal.

The transmitter includes a front panel meter with step-switch input selection to allow the metering of important parameters, such as RF forward output, RF reflected output, input levels, and AFC voltage. The metering system even includes built-in absolute value peak responding voltmeter capability, with internal LEDs to indicate DC polarity.

### RECEIVER

The PCL-606 and PCL-606/C Receiver designs incorporate several performance and user-controlled features never before seen in point-to-point audio distribution equipment of this type. A PIN diode attenuator circuit is supplied for user adjustment of overall system signal to noise ratio. The PIN diode attenuator circuit reduces adjacent signal intermodulation products caused by input signal overloads.

The receiver IF bandwidth may be changed by the user to optimize the tradeoff between distortion and selectivity. All specifications shown are with the IF system in the "narrow" position, providing maximum selectivity.

The receiver demodulator is of an all-new design, offering extremely low distortion and noise characteristics. The demodulator is broadband and adjustment free, using digital pulse counting techniques for maximum fidelity.

The receiver includes a front panel meter with stepswitch input selection to allow the metering of several parameters, including audio output level, subcarrier level, and RF input level in microvolts. The metering system includes built-in absolute value peak responding voltmeter capability with polarity indication. The metering circuit output appears on a back panel connector for remote metering.

Built-in transfer circuitry is standard in the PCL-606 and PCL-606/C Receivers to allow automatic changeover to a standby receiver in the event of a detected malfunction.

## SPECIFICATIONS

PCL-606	SYSTEM	PCL-606/C
148-174 MHz, 215-240 MHz, 300-330 MHz 450-470 MHz, 890-960 MHz Specify exact operating frequency	Frequency Range	148-174 MHz, 215-240 MHz 300-330 MHz 450-470 MHz, 890-960 MHz Specify exact operating frequency
Monophonic audio: ±0.25 dB or better 30 Hz to 15 kHz	Frequency Response	Composite: ±0.1 dB or better 30 Hz to 53 kHz, ±0.3 dB or better 53 kHz to 73 kHz
0.20% or less 30 Hz to 15 Hz (typically better than 0.1% at 1 kHz)	THD & IMD Distortion: Narrow (Wide) I.F. Filter	0.3% (0.2%) or less 30 Hz to 53 kHz, typically •better than 0.1% (0.07%) at 1 kHz
Not applicable	Stereo Separation	48 dB or better, 50 Hz to 15 kHz (typically 50 dB or better)
Not applicable	Nonlinear Crosstalk, Subchannel to Main Channel: Narrow (Wide) to I.F. Filter	50 dB (54 dB) or better
Not applicable	Nonlinear Crosstalk, Main Channel to Subchannel: Narrow (Wide) I.F. Filter	50 dB (54 dB) or better
72 dB or better (typically 75 dB) below 100 % modulation	Signal-to-Noise Ratio	72 dB or better (typically 75 dB) below 100 % modulation, demodulated, de-emphasized left or right
3.5" (8.9 cm) high, 19" (48.3 cm) wide, 16" (40.6 cm) deep 0°-50°C	Dimensions, Operating Temperature Range: Transmitter and Receiver	3.5" (8.9 cm) high, 19" (48.3 cm) wide, 16" (40.6 cm) deep 0°-50°C
	TRANSMITTER	
10 Watts maximum, 5 Watts minimum 15 Watts maximum, 10 Watts minimum	RF Power Output 890-960 MHz 148-470 MHz	10 Watts maximum, 5 Watts minimum 15 Watts maximum, 10 Watts minimum
Type N Female, 50 ohm	RF Output Connector	Type N Female, 50 ohm
±40 kHz	Deviation for 100% Modulation	±50 kHz
Better than 0.00025% 0°C to 50°C	Frequency Stability	Better than 0.00025%, 0°C to 50°C
More than 60 dB below carrier level	Spurious and Harmonic Emission	More than 60 dB below carrier level
One Program and Two Subcarrier Channels	Modulation Capability	One Program and Two Subcarrier Channels
Monophonic: +10 dBm, 600 ohm, balanced, floating, barrier strip screw input. Multiplex: 1.5 V peak-to-peak: 4 K ohms unbalanced, type BNC female connectors (2), frequency range 22-85 kHz	Modulation Inputs	Composite: 3.5 V peak-to-peak, 6 K ohms unbalanced, type BNC female connector Multiplex: 1.5 V peak-to-peak, 4 K ohms unbalanced, type BNC female connectors (2), frequency range 110-185 kHz
100/120/220/240 VAC ±10%, 50/60 Hz, 70 Watts	Power Source	100/120/220/240 VAC ±10%, 50/60 Hz, 70 Watts
	RECEIVER	
Type N female, 50 ohm	RF Input Connector	Type N Female, 50 ohm
20 $\mu$ V or less required for 60 dB SNR	Sensitivity	150 μV or less 60 dB SNR required for left or right channel de-emphasized demodulated,
3 dB I.F. bandwidth ±90 kHz 60 dB I.F. bandwidth ±400 kHz 80 dB I.F. bandwidth ±1 MHz	Selectivity: Narrow (Wide) 1.F. Filter	3 dB I.F. bandwidth ±100 kHz (±150 kHz) 60 dB I.F. bandwidth ±450 kHz (±850 kHz) 80 dB I.F. bandwidth ±1 MHz (±2 MHz)
Monophonic: +10 dBm, 600 ohm, balanced, floating, barrier strip screw output. Multiplex: 1.5 V peak-to-peak, 100 ohms, unbalanced, type BNC female connectors (2)	Modulation Outputs	Composite: 3.5 V peak-to-peak, 100 ohm, unbalanced, type BNC female connector. Multiplex: 1.5 V peak-to-peak, 100 ohms unbalanced, type BNC female connectors (2)
100/120/220/240 VAC ±10%, 50/60 Hz, 30 Watts	Power Source	100 120/220 240 VAC ±10%, 50 60 Hz, 30 Watts

### FOR FURTHER INFORMATION PLEASE CONTACT OUR MARKETING DEPARTMENT

PRINTED IN U.S.A. 4/82 SPECIFICATIONS SUBJECT TO CHANGE WITHOUT NOTICE



C. Park 3117 LEY

### 2.2 Transmitter

Type All solid-state, direct FM, indirect crystal control RF Output 7 watts maximum, 5 watts nominal; Type N female connector Frequency Stability  $\pm 0.0005\%$ Frequency Deviation Monaural (PCL-505) 40 kHz peak for 100% modulation (75 µsec pre-emphasis used in PCL-505) Composite (PCL-505/C) 60 kHz peak for 100% modulation Harmonic suppression Better than 60 dB below carrier Spurious emissions Better than 65 dB below carrier AM Noise Better than 70 dB below carrier Modulation inputs Monaural (PCL-505) +10 dBm,  $600\Omega$  resistive, balanced. floating, barrier-strip connector Composite (PCL-505/C) 3.5 V P-P. 12,000 $\Omega$ , resistive, unbalanced, Type BNC connector 1.5 V P-P, 2000 $\Omega$ , resistive, Multiplex unbalanced, Type BNC connector Power Requirement 120/240 VAC  $\pm 10\%$ , 50-60 Hz. 60 watts Dimensions 8.9 cm (3.5") high, 48.4 cm (19") wide, 40.6 cm (16") deep Shipping Weight (domestic) 11 kg (25 lbs.)

PCL-505, PCL-505/C Rev. 14 Sept. 1981

### 2.3 Receiver

Monaural (PCL-505)					
Program Output	+10 dBm, 600Ω, balanced, floating, barrier-strip connector				
Multiplex Outputs	22-85 kHz, 1.5 V P-P behind $1000\Omega$ , unbalanced, Type BNC connectors				
Sensitivity	30 dB signal-to-noise ratio, -100 dBm 60 dB signal-to-noise ratio, -81 dBm (program output, de-emphasized)				
3 dB I.F. Bandwidth	180 kHz				
80 dB I.F. Bandwidth	2.5 MHz				
Composite (PCL-505/C)					
Wide-band Output	3.5 V P-P behind 1000Ω, unbalanced, Type BNC connector				
Multiplex Outputs	100-240 kHz, 1.5 V P-P behind 1000Ω, unbalanced, Type BNC connectors				
Sensitivity	30 dB signal-to-noise ratio, -100 dBm 60 dB signal-to-noise ratio, -73 dBm (wide-band output, de-emphasized)				
3 dB I.F. Bandwidth	330 kHz				
80 dB I.F. Bandwidth	3 MHz				
Power Requirement	120/240 VAC ±10%, 50-60 Hz, 12 watts; 13.5 ±1 VDC, 0.2A				
Dimensions	4.5 cm (1.75") high, 48.4 cm (19") wide, 34.9 cm (13.75") deep				
Shipping Weight (domestic)	7 kg (15 lbs.)				

PCL-505, PCL-505/C (Rev. 10/75)

]| ]] -

### 3. UNPACKING

The PCL-505 transmitter and receiver should be carefully unpacked and inspected for any shipping damage. Keep all packing material until performance is confirmed. Should inspection reveal shipping damage, or should hidden damage be revealed, immediately file a claim with the carrier.

It is recommended that the top covers on both the transmitter and receiver be removed for a brief superficial inspection. There are two screws that are used to hold the FMO in place during shipment. They should be removed from the bottom of the transmitter chassis before installation. Retain these screws and reinstall them if the transmitter is to be moved. This will prevent damage to the modules inside.

NOTE: DO NOT REMOVE THE COVERS ON THE TRANS-MITTER RF POWER AMPLIFIER ASSEMBLY. DO NOT ATTEMPT DISASSEMBLY OR INSPECTION OF THE RECEIVER INPUT BANDPASS ASSEMBLY. DO NOT MAKE ANY ADJUSTMENTS OF ANY KIND TO THE EQUIP-MENT. DO NOT APPLY POWER UNTIL SPECIFICALLY INSTRUCTED TO DO SO LATER IN THIS MANUAL.

The inspection should ascertain that the various modules, assemblies, and components are mechanically secure. After the inspection, replace the covers.

### 4. INSTALLATION

Although the PCL-505 is intended to provide a wireless equivalent to a wire-line interconnection between a studio and a transmitter site, there are some basic differences:

- a) If the audio level applied to the transmitter is excessive, distortion will result and occupied RF bandwidth will increase.
- b) Undermodulation or operation with lossy feedlines or operation over extremely long distances may result in degradation of the signal-to-noise ratio of the received signal.

PCL-505, PCL-505/C (Rev. 2/76)

- 5-

- c) The PCL-505 incorporates pre-emphasis (treble boost) in the transmitter and de-emphasis (treble cut) in the receiver to enhance the signal-to-noise ratio. A byproduct of this process is an increased susceptibility to overload by higher audio modulating frequencies.
- d) The PCL-505 has the bandwidth and linearity to carry control tones and secondary program material in the form of subcarriers.
- e) The PCL-505/C uses special circuitry to allow the transmission of the composite stereo waveform (as well as control and program subcarriers) over a single link.

Drawings illustrating the various connections to be made to the PCL-505 transmitter and receiver are shown in Figures 1 through 6.

The PCL-505 equipments should be mounted in a standard rack, preferably between waist and shoulder height. The associated antenna should be mounted at a height such that a reasonably clear path is available between the transmitter and receiver sites. A path having 0.6 Fresnel zone clearance is recommended. Either vertical or horizontal polarization may be used, but the polarization must be the same at each end of the path. Generally, vertical polarization is employed.

Interconnections between the transmitter and its antenna (and between the receiver and its antenna) should be made using coaxial cable whose loss characteristics have been determined by an engineering study. For example, half-inch diameter foam-filled coaxial cable (with a loss of 3 dB per hundred feet) will have a loss of 9 dB if the length of line totals 300 feet. Such a loss will generally be tolerable if the path is short, for example five to ten miles. It might prove disastrous on grazing or long paths. The gain of the transmitting and receiving antennas must also be considered.

Noting that the better feedlines are relatively inflexible, Moseley Associates has made available short "pigtail" assemblies. These are to be attached to the ends of the actual feedlines, and they enable movement of the equipment with less chance of harm to the

PCL-505, PCL-505/C (Rev. 10/75) -6-



### PCL-505 TRANSMITTER R F CONNECTIONS

1

. .

1





FIGURE 2





PROGRAM AND MULTIPLEX CONNECTIONS

PCL-505, PCL-505/C

il

į ]

. .

j

C

-9-





HOTES:

1

L STERED GENERATOR MAY BE MODEL SCS-S OR EQUIVALENT. 2. PROGRAM SUBCARRIER GENERATOR MAY BE MODEL SCS-S OR EQUIVALENT. 3. Control Subcarrier Generator May be part of remote control system. 4. COAX CABLE IS RE 58 A/U OR EQUAL.

PCL-505/C TRANSMITTER ( COMPOSITE )

PROGRAM AND MULTIPLEX CONNECTIONS

FIGURE 5



FIGURE 6

PCL-505, PCL-505/C

equipment or feedline. These assemblies carry Moseley Associates part number KTL-(). As an example, use KTL-4 assemblies for Andrew foam-dielectric half-inch line. Each KTL-() kit consists of two pigtails with connectors attached, and two individual Type N Female coaxial connectors. Each such kit is sufficient for installation of one end of a link. Two kits would be needed if pigtails are desired at each end of a link.

Should it be desired to mount the receiving antenna on a seriesfed Standard Broadcast tower, the required isolation may be accomplished as illustrated in Figure 2. At the base of the tower, a Moseley Associates Isocoupler is used to allow passage of the STL RF signal while introducing no particular change in the tower base impedance. Isolation at Standard Broadcast frequencies is very high, and the Isocoupler introduces a minimal loss to the STL RF signal.

For monaural operation, the output of the program limiter is applied to the program input on the PCL-505 transmitter. Applied to the transmitter multiplex inputs are subcarrier signals (if applicable) for control and secondary programming purposes.

For dual-link stereo operation, the outputs of the program limiters are applied to the program inputs on the pair of PCL-505 transmitters. Choose one link arbitrarily and (if applicable) connect the control subcarrier generator output to a multiplex input on that transmitter. Use the other link (if applicable) for the program subcarrier; connect the program subcarrier generator output to a multiplex input on that transmitter.

The program lines use barrier-strip connections and operate at +10 dBm at <u>low</u> audio frequencies. The multiplex inputs operate at 1.5 volts peak-to-peak and use Type BNC connectors.

For composite (single-link stereo) operation, the output of the program limiters are applied to the stereo generator, such as the Moseley Associates Model SCG-9. The output of the stereo generator is then applied to the wide-band input on the PCL-505/C transmitter. Also applied to the transmitter multiplex input connectors are subcarriers (if used) for control and secondary programming purposes. Multiplex Channel #1 should be used for the control subcarrier while Multiplex Channel #2 should be used for secondary programming.

PCL-505, PCL-505/C -13-

1

1.

1 1.

The program inputs to the SCG-9 Stereo Generator use barrierstrip connections and operate at +10 dBm at low audio frequencies. The output from the stereo generator is 3.5 volts peak-to-peak for full modulation and uses a Type BNC connector.

At the receiver site, the PCL-505 monaural (or dual-link stereo) receiver program output is applied to the program input(s) of the exciter or stereo generator. Also available from the receiver multiplex outputs are the subcarrier outputs to operate subcarrier equipment such as remote control and secondary program demodulators.

The program line uses barrier-strip connections and delivers +10 dBm at low audio frequencies. The multiplex outputs deliver 1.5 volts peak-to-peak and use Type BNC connectors.

The wide-band output from the PCL-505/C composite receiver is applied to the wide-band or composite input of the direct-FM exciter. The receiver multiplex outputs are also available to operate remote control and secondary programming demodulators.

The wide-band output from the receiver is 3.5 volts peak-to-peak behind  $100\Omega$  and uses a Type BNC connector. The multiplex outputs deliver 1.5 volts peak-to-peak and also use Type BNC connectors.

If either the transmitter or receiver is to be operated from 240 VAC, refer to the schematics for rewiring information.

With the transmitter properly terminated, power may now be applied to both the transmitter and receiver. At this time, system performance may be checked on a back-to-back basis.

Each transmitter metering position should be checked. The readings may be compared with the values shown in the final factory test data. It would be wise to record these readings for future reference. Note that the forward power (and the reflected power, in particular) may deviate somewhat from the final test values due to possible VSWR of the load.

Now the PCL-505 may be installed in the operating configuration and a skeleton proof of performance run. See paragraph 7.4 or 7.5 as applicable.

World Radio History

PCL-505, PCL-505/C -14-

Rev. 3/78

### 5. OPERATION

Routine operation of the PCL-505 system is very simple. Power should be applied to both the transmitter and the receiver at all times.

NOTE: USER MUST COMPLY WITH APPLICABLE OPERATING REQUIREMENTS OF GOVERNING REGU-LATIONS.

The transmitter unit may be remotely controlled and metered. Refer to Paragraph 7.8 on remote control of the transmitter. When no other meter readings are being taken, it is suggested that the program position be used to continuously monitor program modulation. Table 1 on the following two pages discusses frontpanel controls and switches for both the receiver and the transmitter.

### 6. CIRCUIT DESCRIPTION

### 6.1 Transmitter

The block diagram of the PCL-505 transmitter is shown in Figure 7. Individual module block diagrams are shown in Figures 8 through 15.

### 6.1.1 Input Interface

Modulation input to the transmitter is applied to the modulation circuitry via an Input-Interface module (see Figure 8). The monaural version of this module terminates the program input with a pi-type attenuator. This assures a resistive input and allows various nominal input levels to be accommodated.

The output from the pad is routed to the input isolation transformer and then back to the board. At this point, there is an active pre-emphasis system with an amplifier whose gain is adjustable.

Following this amplifier is a 16 kHz low-pass filter to prohibit program components from interfering with any subcarriers which may also be applied to the link. The output of the filter is applied to an active summing amplifier.

The composite version of this module, used in the PCL-505/C, is similar except that it contains no input pad, has no preemphasis, and does not contain a low-pass filter.

PCL-505, PCL-505/C (Rev. 3/78)

-15-

### TABLE 1

MODEL PCL-505 FRONT-PANEL CONTROLS AND SWITCHES

### TRANSMITTER

RADIATE	Turns power on or off to multiplier driver module thus carrier is on or off.	
Metering Switch Positions		
PROGRAM	Meters main program applied to modulator. The "0" on the top meter scale represents 100% modulation.	
MPX.	Meters subcarriers applied to modulator. Percent injection is read on lower scale.	
AFC	Meters DC level of AFC system. (See AFC ADJUST - next page)	
FWD. PWR.	Meters forward RF power to antenna.	
RFL. PWR.	Meters reflected RF power from antenna.	
+VDC	Meters +13.5 VDC power supply on the bottom meter scale.	
REF. OSC.	Meters reference oscillator and associated circuitry. Normal is between 10 and 20 on the bottom meter scale.	
H.F. DIVIDER	Meters FMO, H.F. Buffer and Divider, and associated circuitry. Normal is between 10 and 20 on the bottom meter scale.	
IPA DRIVE	Meters output of multiplier module driving power amplifier. Normal is 15 or more on the bottom meter scale.	

PCL-505, PCL-505/C (Rev. 2/76)

FINAL CURRENT Meters current of final power amplifier stage. Bottom scale is actual current used by the output transistor.

AFC ADJUST Screwdriver adjustment to set AFC to center of AFC range. Shown on meter in AFC position.

POWERGreen L.E.D. is illuminated when primarypower is applied to the transmitter.

RECEIVER

Metering Switch Positions

+VDC	Meters +13.5 VDC power supply on the bottom meter scale.
SIGNAL	Meters relative received signal strength. (See Final Test Data)
PROGRAM	Meters received program level. The "0" on the top meter scale represents 100% program modulation of received signal.
MPX.	Meters received subcarrier levels. This reading is relative and should be noted for

future reference.

POWERGreen L.E.D. is illuminated when primarypower is applied to receiver.

PCL-505, PCL-505/C (Rev. 2/76)

-17-



BLOCK DIAGRAM PCL-505 TRANSMITTER (MONAURAL OR COMPOSITE) FIGURE 7



-18-





PCL-505, PCL-505/C

.

ļį

K

]

1

11

-19-

Both the monaural module and the composite module contain identical subcarrier-processing circuitry. Subcarrier inputs are terminated with individual level-setting potentiometers, whose outputs are applied to the active summer. The potentiometer outputs are also applied to a separate buffer amplifier for subsequent application to the metering system.

Also included on the Input-Interface module is a series of metering amplifiers. One metering amplifier processes the selected sample in a peak-sensitive fashion for reading either program modulation or subcarrier injection. This amplifier has a calibration control, a phase-splitter, and a full-wave peak-sensitive rectifier. The rectifier output is applied to a DC amplifier for application to the panel meter. Adjustable meter acceleration and zeroing controls are included in this amplifier.

Π

Π

Π

A second metering amplifier buffers the AFC voltage for application to an external meter movement. A third amplifier buffers the forward power sample. A fourth amplifier buffers this same sample and allows its application to an external meter.

The external meters referred to here are those involved in remotely controlling the PCL-505 transmitter. For further information on this, see the section headed "Remote Control of the STL Transmitter."

### 6.1.2 Modulated Oscillator

The total modulation output from the summing amplifier on the Input-Interface module is applied to the frequency-modulated oscillator, shown in block diagram form in Figure 9.

A low-noise oscillator is frequency-modulated by a pair of varactor diodes. One of these modulators is used for frequency control and the other is used for program modulation. Subcarrier modulation, if used, is merely summed with the program modulation.

PCL-505, PCL-505/C (Rev. 2/76)

-20-



### PCL-505 TRANSMITTER FREQUENCY-MODULATED OSCILLATOR

( MONAURAL OR COMPOSITE )

FIGURE 9

PCL-505, PCL-505/C (Rev. 12/75)

1

3

-21-
The output of the oscillator is applied to an amplifier. The buffer amplifier operates from 13.5 volts while the oscillator and program modulator bias operate from a regulator whose output is 9 volts. The primary purpose of this regulator is noise reduction.

The output of this module, in the region of 80 MHz and a power level of about 12 milliwatts, is applied to the frequency multiplier.

#### 6.1.3 Frequency Multiplier

The output of the modulated oscillator is applied to the frequency multiplier, shown in block diagram form in Figure 10.

The first stage of this module is a buffer, followed by a doubler, tripler driver, and finally a power amplifier.

The RF output from this module is in the 485 MHz region at a typical power level of 120 milliwatts. It is applied to the power amplifier module. The RF output is also rectified by a diode and applied to the front-panel meter in the IPA position.

The input drive signal to the frequency multiplier is sampled and provides excitation to the AFC system.

## 6.1.4 Power Amplifier (890-960 MHz)

The transmitter power amplifier (see Figure 11) accepts the 100 milliwatt 445-480 MHz signal from the buffer multiplier, doubles and amplifies it to a nominal 5 watts. The first stage is a doubler with input and output filtering and matching. The second and final stages are 890-960 MHz amplifiers. A nine section lowpass filter follows the final transistor to reduce all harmonics. Following the final RF amplifier filter is a dual directional coupler used to assist in the tune-up of the amplifier assembly and to provide drive to the panel meter.

PCL-505, PCL-505/C

-22-



# PCL-505 TRANSMITTER MULTIPLIER (MONAURAL OR COMPOSITE)

FIGURE IO

PCL-505, PCL-505/C (Rev. 4/76)

2 • •

•

1

1

ß

1.

2

-23-



iP

## PCL-505 R F POWER AMPLIFIER

( MONAURAL OR COMPOSITE )

FIGURE II

World Radio History

,

Section 6.1.5 deleted from text

## 6.1.6 High-Frequency Buffer and Divider

A sample of the modulated oscillator signal is taken from the input of the frequency multiplier and is applied to the High-Frequency Buffer and Divider module. The block diagram of this module is shown in Figure 12.

The first three stages of this module provide amplification and shaping to drive a high-frequency, integrated-circuit frequency divider. This divider accepts the amplified and shaped, modulatedoscillator signal and divides it by a factor of 16 down to the 5 MHz region.

This signal is applied to an amplifier which interfaces the highfrequency signal to a form acceptable to another divider, for further division down to the 1200 Hz region.

The output of this divider is applied to an output buffer amplifier which both drives the next stage of the AFC system and provides a metering sample.

PCL-505, PCL-505/C (Rev. 2/76)

-25-



j.

PCL-505 HIGH-FREQUENCY BUFFER AND DIVIDER (MONAURAL OR COMPOSITE)

FIGURE 12

-26-

## 6.1.7 AFC

The AFC module (see Figure 13) generates a stable reference signal in the 5 MHz region (carrier frequency divided by a factor of 192). The oscillator is crystal-controlled, with the crystal located in an oven which is proportionally controlled for best stability.

The reference signal is applied to an integrated-circuit frequency divider for division down to the 1200 Hz region. This 1200 Hz signal, along with the 1200 Hz signal from the divided-down modulated oscillator, is applied to an integrated-circuit phase detector. The output of the phase detector is applied to a 30 Hz low-pass filter to remove the 1200 Hz component present in its output, leaving only the DC frequency-correcting voltage. This voltage is applied to an active lag-compensation circuit for processing prior to application to the frequency-modulated oscillator. The output of the lag compensator is applied to the AFC input on the modulated oscillator for frequency stabilization.

Note that the center frequency stability of the transmitter is determined by the 5 MHz oscillator; if the modulated oscillator should attempt to drift, the only effect will be a change in the AFC frequency-correcting voltage.

The oven temperature is sensed by a silicon diode mounted in the oven in contact with the heating element. A current is applied to this diode, and the voltage drop across it is compared with a voltage derived from a potentiometer. The output of this comparison amplifier is applied to a power amplifier which drives the oven heating element (resistor). The entire heating-control system operates on regulated DC and so is noise-free.

#### 6.1.8 Power Supply

The block diagram of the power supply for the PCL-505 is shown in Figure 14.

Primary AC power is applied to the power transformer via an appropriate fuse. A bridge rectifier on the secondary provides unregulated DC which is applied to a series regulator. This regulator has current limiting and adjustable output voltage.

PCL-505, PCL-505/C -27-(Rev. 2/76)



(MONAURAL OR COMPOSITE)

FIGURE 13

PCL-505, PCL-505/C

-28-



# PCL-505 POWER SUPPLY REGULATOR (MONAURAL OR COMPOSITE )

FIGURE 14

PCL-505, PCL-505/C

1. G

.

.

.

• •

.

-29-

**World Radio History** 

## 6.1.9 LOSS OF LOCK SHUT OFF

The function of this circuit (see below) is to remove the 13.5 volt Direct Current (DC) from the Multiplier Driver which in turn turns off the RF output power. The circuit is comprised of four (4) subcircuits. The first is an active 250 Hz low-pass filter which passes the beat note from the AFC when the Frequency Modulated Oscillator (FMO) becomes unlocked from the AFC Reference Oscillator. The second is a comparator which amplifies the beat note when it is greater in amplitude than the pre-set comparator level. The third is a detector which changes the beat note to DC. The fourth is a clamp switch which removes the DC drive from the base of a series gate transistor on the DC regulator printedcircuit board which in turn removes the 13.5 volts DC from the Multiplier Driver.



Also included on this module are miscellaneous control and metering components.

## 6.2 Receiver (890-960 MHz)

The block diagram of the PCL-505 receiver is shown in Figure 16. Individual module block diagrams are shown in Figures 17 through 25.

#### 6.2.1 Input Bandpass Filter

The input to the PCL-505 receiver is applied to a bandpass filter (see Figure 17). This filter is down 3 dB at about 30 MHz from the carrier frequency and is down 50 dB at about 120MHz from the carrier frequency. The input and output impedances of this filter are  $50\Omega$ .

## 6.2.2 RF Preamplifier

The output of the filter is applied to a preamplifier (see Figure 18). This amplifier has a 4 dB noise figure and provides about 12 dB of gain at the operating frequency. Input and output impedances are  $50\Omega$ .

#### 6.2.3 Balanced Mixer

The output of the preamplifier is applied to the third module, a balanced mixer (see Figure 19). This mixer provides conversion to the 74 MHz first I.F. Immediately following the mixer proper is a single tuned circuit at 74 MHz and then a low-noise I.F. amplifier. Two more tuned circuits at 74 MHz, a high-gain I.F. amplifier, and then another pair of tuned circuits complete this module. The output is at  $50\Omega$ .

PCL-505, PCL-505/C (Rev. 3/78)

-31-



#### PCL-505 RECEIVER BLOCK DIAGRAM (890-960 MHZ MONAURAL OR COMPOSITE)

FIGURE 16

PCL-505, PCL-505/C (Rev. 10/75)

- 32-



( 390-960 MHZ )

FIGURE IT

PCL-505, PCL-505/C (Rev. 10/75)

1

-33-



## PCL-505 RECEIVER ON-FREQUENCY R F PREAMPLIFIER (MONAURAL OR COMPOSITE)

FIGURE 18

PCL-505, PCL-505/C (Rev. 2/76)

World Radio History

.



.

## PCL- 505 BALANCED MIXER (FIRST) (890-960 MHZ MONAURAL OR COMPOSITE)

FIGURE 19

PCL-505, PCL-505/C

-

4

i

World Radio History

Excitation to the first mixer and local-oscillator input port is provided by the Local Oscillator module (see Figure 20). This module uses a crystal in the 50 to 55 MHz region. A quadrupler provides drive to the 217 MHz bandpass filter. The output of this filter is applied to a doubler, a 435 MHz bandpass filter, another doubler, and finally an 870 MHz bandpass filter. The output of this final filter is in the 3 to 5 milliwatt range at an impedance of  $50\Omega$ .

## 6.2.5 74 - 10.7 MHz Converter

The 74 MHz output from the balanced mixer is applied to an integrated-circuit second mixer (see Figure 21) for conversion to 10.7 MHz. Oscillator injection is provided by a crystal oscillator contained within the same module. This oscillator operates at a fixed frequency of 63.3 MHz. The desired 10.7 MHz output from the mixer is extracted by an L-C bandpass filter which is responsible for the basic selectivity of the receiver. The output impedance of this converter module is  $50\Omega$ .

## 6.2.6 FM Demodulator and Meter Amplifier

Due to the requirements of this circuit, it is <u>not</u> recommended that the detector be adjusted in the field. Field adjustments without the aid of a low-distortion FM signal generator and distortion analyzer is difficult. Transformer Tl should only be adjusted using a nonmetallic tuning tool. Adjust Tl pink and blue slugs for maximum AF output from terminal 3 (AF) when observed on an oscilloscope. Adjust Tl blue slug for minimum distortion from terminal 3 (AF) when observed on a distortion analyzer.

## 6.2.7 Metering and Muting

The baseband output from the FM demodulator is applied to a metering and muting module (see Figure 23). This assembly contains a peak-reading voltmeter which can be selected to read multiplex and program levels. It also contains a DC amplifier to operate an all-electronic muting system as well as a relay. The contacts from the relay are brought out to the rear of the receiver for alarm or fail-safe purposes. The output of the muting system is applied to the program and multiplex amplifier.

-36-

PCL-505, PCL-505/C (Rev. 1/76)



PCL-505 IN LOCAL OSCILLATOR (890-960 MHZ MONAURAL OR COMPOSITE)

FIGURE 20

PCL-505, PCL-505/C

.

-37 -

World Radio History



PCL-505 74-10.7 MHZ CONVERTER (MONAURAL OR COMPOSITE)

FIGURE 21

PCL-505, PCL-505/C Rev. 25 February 1980

-38-

World Radio History



## PCL-505 DEMODULATOR AND METER AMPLIFIER

( NONAURAL OR COMPOSITE )

FIGURE 22

PCL-505, PCL-505/C (Rev. 2/76)

ų

-39-





FIGURE 23

PCL-505, PCL-505/C (Rev. 5/18/78)

World Radio History

#### 6.2.8 Program Amplifier

The monaural version of the program amplifier (shown in Figure 24) as used in the PCL-505 contains a 16 kHz low-pass filter whose purpose is to reject subcarriers above 22 kHz. Following this low-pass filter is an output amplifier with adjustable de-emphasis and automatic noise reduction. The de-emphasis adjustments allow the system frequency response to be tailored for extreme flatness. The automatic noise reduction circuit enhances the apparent signal-to-noise ratio, especially over extremely long paths or paths subject to moderate fading. It has no audible effect on the programming and is switch-defeatable. Note that all specifications for the PCL-505 are with this circuit defeated (disabled).

The program and multiplex amplifier assembly also contains a 22 kHz high-pass filter to reject program material below 16 kHz while passing subcarriers above 22 kHz. Following this high-pass filter is an adjustable-gain amplifier, an 85 kHz low-pass filter (to eliminate extremely high-frequency noise from the multiplex output), and an output buffer amplifier. The output of this buffer is applied to the output Type BNC connectors.

The composite version of the program amplifier (see Figure 25) as used in the PCL-505 contains special circuitry for processing the complete stereophonic signal. The first stage is a preamplifier with adjustable high-frequency boost. This stage compensates for the slight baseband response rolloff caused by the selectivity of the I.F. system. This set of equalizers compensates for system envelope delay distortion.

Following the delay equalizer system is the subcarrier-removing low-pass filter. After the filter is an amplifier with adjustable low-frequency phase correction. This stage allows compensation of low-end system phase errors. It drives the output amplifier whose output is applied to the output Type BNC connector.

The program and multiplex amplifier also contains a 100 kHz high-pass filter which removes stereophonic and 67 kHz SCA material from the multiplex output. Following the high-pass filter is an adjustable gain amplifier, a 240 kHz low-pass filter, and a multiplex output buffer amplifier. The output of this buffer is applied to the output Type BNC connectors.

PCL-505, PCL-505/C -41-(Rev. 10/75)





FIGURE 24

PCL-505, PCL-505/C

- 42\_

.



PCL-505/C PROGRAM AND MULTIPLEX AMPLIFIER ( COMPOSITE )

FIGURE 25

ł

ſ

#### OPERATIONAL SUGGESTIONS 7.

## 7.1 Recommended Standards and Data

Mon	auralPCL-505:	
	Program Level	+10 dBm (sinusoid) 0 VU (complex) Note: these are low-frequency values, to be reduced as the audio frequency is raised.
	Program Impedance	$600\Omega$ , resistive, floating, balanced
	Control Subcarrier Frequency	26 kHz
	Control Subcarrier Level	Approximately 1.5 V P-P
	Program Subcarrier Frequency	67 kHz
	Program Subcarrier Level	Approximately 1.5 V P-P
Dua	l Monaural (for stereo): Program	See Monaural, above
	Control Subcarrier Frequency	26 kHz
	Control Subcarrier Level	Approximately 1.5 V P-P
	Control Subcarrier Link #	1
	Program Subcarrier Frequency	67 kHz
	Program Subcarrier Level	Approximately 1.5 V P-P
	Program Subcarrier Link #	2
CompositePCL-505/C (single-link st Program Level		ereo): 3.5 V P-P Note: this signal, which is the composite stereophonic wave- form, should be measured only with a wide-band oscilloscope
	Program Impedance	Approx. $10K\Omega$ (transmitter) Approx. $1K\Omega$ (receiver)
	Control Subcarrier Frequency	110 kHz
	Control Subcarrier Level	Approximately 1.5 V P-P
	Program Subcarrier Frequency	185 kHz
	Program Subcarrier Level	Approximately 1.5 V P-P

PCL-505, PCL-505/C (Rev. 10/75)

-44-

## 7.2 Program Levels

The normal level required for full modulation of a PCL-505 monaural transmitter (or for each transmitter in a dual system) is +10 dBm. This is the level normally required for full modulation using a sine wave at low audio frequencies. Complex waves, such as speech and music, will indicate much lower on an ordinary effective or RMS meter such as the VU-type. Furthermore, the level required for full modulation decreases as the audio frequency rises. This is due to the pre-emphasis circuitry in the transmitter.

NOTE: THE ACTUAL MODULATION OF THE STL TRANSMITTER IS INDICATED BY THE FRONT PANEL METER. THIS METER IS FULL-WAVE PEAK-SENSITIVE, AND IS LOCATED AFTER THE PRE-EMPHASIS CIRCUITRY.

The modulation of the STL transmitter should be controlled by a limiter, preferably one of the frequency-conscious types. This limiter may be preceeded by an audio AGC system at the discretion of the individual station. The recommended method of adjustment of this chain of equipment is as follows:

 Adjust the AGC input level controls. This is best done by using some form of actual program material. Adjust the control until the AGC unit is operating in the middle of its intended range.

In stereo systems, adjust both of the AGC input level controls until the AGC units are operating in the middle of their intended range without any stereo interconnection. It would be best to use actual program material, and preferably a source which is balanced level-wise or else a monaural source with identical material in each channel. After the input level controls have been adjusted, reapply the stereo interconnection.

b) The remaining adjustments will all be made using a steady 400 Hz sine wave for the test material. It is assumed that the output of the AGC unit is connected to the limiter, and the limiter output is connected to the PCL-505 program input. In stereo systems, two identical audio chains will be involved.

PCL-505, PCL-505/C (Rev. 10/75) -45-

 c) Adjust the output of the AGC unit to some standard level. Using the test tone, set the AGC output level to +10 dBm.

For stereo systems, adjust both AGC unit outputs to the same level.

d) Adjust the limiter input control. Using the test tone, adjust the limiter input control for a satisfactory degree of limiting as read on the limiter panel meter.

In stereo systems, adjust both of the limiter input level controls until the limiters are operating with the desired degree of limiting without the stereo interconnection. The limiting activity may be read on the limiter panel meters. After the limiter input level controls have been adjusted, reapply the stereo interconnection.

e) Adjust the limiter output level control. Adjust the control for an indication of "0" on the PCL-505 panel meter in the program position.

In stereo systems, adjust each limiter output level for a reading of "0" on the corresponding STL program level meter.

f) In composite systems where the stereo generator (such as the Moseley Associates SCG-9) is located at the studio, the limiter outputs are set in a slightly different manner. Disable the audio input to one of the limiters. Then set the output level of the remaining limiter for an indication of "0" on the stereo generator output meter. This sets the level for one channel. Now follow the same process for the other channel.

#### 7.3 Subcarrier Levels

In STL subcarrier systems, control or program signals are generated at the control point or studio. The subcarriers, in turn, are applied to the STL transmitter. The modulation of the

PCL-505, PCL-505/C -46-(Rev. 10/75) subcarrier by the control tone or program is commonly called "deviation" of the subcarrier. The modulation of the STL by the subcarrier is commonly termed "injection" of the subcarrier onto the link. The PCL-505 is designed for subcarrier injections of 10% to It will be found that the control systems need less than the program systems; the program systems are more demanding, particularly in the area of signal-to-noise ratio. The panel meter on the PCL-505 is factory calibrated to read percent injection on the lower scale when the MPX push button on the front panel is depressed. This should be coincident with a subcarrier input to the multiplex connector of about 1.5 volts peak-to-peak. At the same time, the PCL-505 receiver should deliver about the same output.

Filters in the PCL-505 are used to separate the program and subcarrier signals. For this reason, be sure to use a subcarrier in an appropriate frequency region when testing is performed. Monaural links will pass subcarriers in the 22 kHz to 85 kHz region, while the composite links are designed to pass 100 kHz to 240 kHz. Subcarrier frequencies above 185 kHz are not recommended for systems operating under FCC regulations.

## 7.4 Proof of Performance--PCL-505

There are three primary areas to be considered in measuring the performance of the STL:

- 1. Frequency response
- 2. Distortion
- 3. Signal-to-Noise ratio

Other items to be considered are cross talk into subcarriers (if used), transmitter power output, and receiver sensitivity. In addition, the composite PCL-505/C must be tested in a manner to insure passage of the stereophonic waveform. The requirements for the PCL-505/C are noticeably different from those of the monaural PCL-505, and will be covered separately.

Presented here are suggestions for proving the performance of the PCL-505. In this discussion, only the link will be considered; preceeding and subsequent apparatus (excepting test equipment) will be left out of these suggested procedures.

PCL-505, PCL-505/C -47-(Rev. 4/76) In measuring the frequency response of the STL, it should be remembered that pre- and de-emphasis are incorporated into the link to enhance the signal-to-noise ratio. Because of this, the STL cannot be truly considered as a "piece of wire." The transmitter modulator and the receiver demodulator have limited signal handling capability. Taken as a system, the link will display the following characteristics:

- a) At low modulating levels (as for example 20 dB below normal) the frequency response of the system and the receiver output capability will both be flat.
- b) At higher modulating levels the audio distortion will increase somewhat, due primarily to the selectivity in the receiver.
- c) At still higher modulating levels the audio may actually be clipped, due primarily to overload in the transmitter or receiver audio processing circuitry. In addition, the receiver output level will fall off. The receiver program output level capability is similar to the deemphasis curve used in the receiver.

These points should be kept in mind when running a proof of performance. If it is observed at any time that either the STL transmitter or the STL receiver program-level metering is indicating beyond the "0" mark, then the modulation level must be reduced by reducing the input to the STL transmitter.

It is undesirable to constantly change the level of an audio generator when running tests. To keep the modulation constant, a de-emphasis network may be connected between the audio generator and the STL transmitter. Such a network will automatically reduce the audio level as the frequency is raised. However, the receiver output level will fall off as the audio frequency is increased, and a comparison with a de-emphasis chart will be required to enable a system response measurement.

A simpler and more commonly used method of testing the frequency response of the STL is to merely reduce the audio level by several dB (deliberately undermodulate) and then make

PCL-505, PCL-505/C (Rev. 10/75) -48-

the assumption that the link is a "piece of wire." This assumption cannot be made at full modulation levels. It is suggested that the audio level be reduced 20 dB for frequency response measurements. It will be found that this expedites frequency response measurement.

Distortion measurements should always be made at full modulation. Regardless of the audio frequency being used, deviate the transmitter fully (to the "0" mark on the transmitter modulation meter). Notice that when this is done that output level from the receiver will be lower at the higher modulating frequencies. For this reason, the distortion meter must have its input level control reset at each audio test frequency.

The signal-to-noise ratio is very simple to measure, but a few pitfalls may obscure the true reading. Establish a reference level in the system by modulating the transmitter with a test tone in the low-audio-frequency region, such as 400 Hz. Observe that the level required will be about +10 dBm, and more importantly, that the STL receiver output level will be at +10 dBm. When this test tone is removed, the remaining signal observed at the receiver output will be noise.

It has been observed that the residual noise output from the STL is sometimes masked by locally-generated interference (such as an AM broadcast transmitter in the vicinity of the test equipment) or by a ground loop, typically in the STL receiver/test equipment combination. It is sometimes helpful to ground the "low" side of the test equipment at one or both sites if the noise is observed to be power line related hum.

AM broadcast interference is best reduced by filtering particularly by using bypass capacitors across the program lines or between these lines and chassis ground.

If the noise is observed to be high-frequency hiss, it is probable that the receiver is receiving an inadequate signal. A review of the antenna installation and/or path engineering would probably be in order.

PCL-505, PCL-505/C - 49-

11

## 7.5 Proof of Performance--PCL-505/C

The basic requirements for the PCL-505/C composite STL are similar in nature to those for the PCL-505 monaural STL, but certain additional tests must be made. These extra tests are due to the more severe requirements placed on the STL in order that it may handle the composite stereo waveform with minimal degradation.

Although frequency response and distortion tests can be made on the STL as such, they will be found to be relatively immaterial with regard to its intended purpose of passing stereo. Typically, the figures obtained by such simple tests will be good enough to tax the test equipment; only an actual stereo signal of proven integrity will truly prove the capability of the link.

The equivalent of the monaural STL frequency-response test is the stereo STL separation test. In order for the stereo signal to easily pass the stereo technical standards, the frequency response of the composite STL must be flat within about 0.2 dB from 30 Hz to 53 kHz; at the same time the time delay must be constant within about one-half microsecond. This is no simple task and is the reason for the equalizing circuitry in the PCL-505/C receiver.

Separation in the PCL-505/C is best measured by using an actual stereo generator of known good performance for a test signal. Apply an audio test tone to one channel of the generator at the normal level and observe the composite (wide-band) output from the PCL-505/C receiver, using a stereo monitor. For modulating frequencies from 30 Hz to 15 kHz the separation should easily comply with accepted stereophonic standards, providing the stereo generator used meets these standards. The separation right to left and left to right should be similar.

Note that this test requires a monitor which can read a baseband signal. If no such monitor is available, then the transmitter's exciter will have to be added onto the chain of equipment under test.

Distortion in the PCL-505/C is best tested by monitoring the cross talk generated in a stereo signal. Cross talk, as the term

PCL-505, PCL-505/C -50-(Rev. 10/75) is used in stereophonic broadcasting, measures unwanted frequencies in the L-R subcarrier channel (23 kHz to 53 kHz) which result from desired signals in the L+R channel (50 Hz to 15 kHz) as well as those frequencies appearing in the L+R channel caused by modulation in the L-R channel. While not generally recognized, cross talk is caused by both linear (vector) and nonlinear distortions in the system. As linear (vector) distortion is introduced only by phasing errors in the L and R audio channels prior to matrixing, the PCL-505/C cannot modify or alter these products. Nonlinear distortion, i.e. harmonic distortion, in the PCL-505/C system can degrade the cross-talk performance. To measure cross talk, both audio channels in the stereo generator are fed with the same test tone, in parallel (in phase) such that the subchannel component is suppressed. Then the stereo monitor is switched to read the level of the subchannel component. For modulating frequencies from 50 Hz to 15 kHz the subchannel component must be suppressed more than 40 dB.

Note that when the monitor is switched to read the stereo subchannel, it is reading harmonics of the main channel, appearing in the 23 kHz to 53 kHz region. In addition, it is responding to any subchannel feedthrough from the stereo generator, a form of vector cross talk. The 38 kHz switching-signal may also be present.

For the second part of the cross talk test, the stereo generator audio channels are fed in opposition (out of phase) with the same test tone such that the main channel component is suppressed. Then the stereo monitor is switched to read the level of the main channel component. For modulating frequencies from 50 Hz to 15 kHz, the main channel component must be suppressed more than 40 dB. Note that when the monitor is switched to read the main channel that it is responding to intermodulation components originating within the L-R channel. In addition, it is responding to any main channel feedthrough from the stereo generator, a form of vector (or linear) cross talk.

PCL-505, PCL-505/C

ľ

- 51-

The signal-to-noise ratio of the PCL-505/C may be measured using any of three different systems:

- a) Measuring the signal-to-noise ratio of the wide-band output;
- b) Measuring the signal-to-noise ratio of the wide-band output; with de-emphasis added to the measuring voltmeter;
- c) Connecting the wide-band output to a stereo demodulator (monitor) and measuring the signal-to-noise ratio of a demodulated audio channel.

Of these three, it appears that the second method is the simplest to implement and yields consistent, meaningful results.

#### 7.6 Cross Talk into Subcarriers

Cross talk into subcarriers may be tested by using a test tone on the program or main channel while measuring the signal-to-noise ratio of the demodulated subcarrier. For this test, be sure the subcarrier injection is correct. Then modulate the subcarrier generator with a low audio frequency such as 400 Hz. Measure the audio output from the subcarrier demodulator. This level will be the standard level. In the case of the Moseley Associates Model SCD-8, this should be +10 dBm. When the test tone is removed from the subcarrier generator, the residual signal from the subcarrier demodulator will be noise. When the main channel of the STL is modulated, it will generally be observed that this noise level will increase; the signal-to-noise ratio will decrease. Cross talk levels measured with steady state tones are usually higher than when measured with normal program content. Main channel to subcarrier cross talk measured during normal main channel programming is typically 50 dB below the standard level.

To measure subcarrier to main channel cross talk, apply the normally modulated subcarrier to the STL with no main channel program. Subcarrier signals appearing in the main channel output must be at least 60 dB below normal main channel program audio output.

#### 7.7 Composite Receiver to Exciter Interface

The composite STL receiver output must be carefully connected to the wide-band input on the FM exciter. The interconnection must be made with shielded wire (small coaxial cable). Attenuation of the composite signal, if needed, should be done right at the exciter, preferably inside any shielded enclosure. A third point of which the installer should be aware is the possibility of a ground loop.

PCL-505, PCL-505/C (Rev. 10/75)

World Radio History

- 52-

This will manifest itself as apparently unavoidable power line related hum. Either the receiver or the transmitter exciter may be operated from an isolation transformer should this occur. If this offers no relief, then an isolation transformer must be wide-band such as a  $600\Omega$  to  $600\Omega$  high-fidelity or a suitable telephone line repeat coil. Such a transformer should not be needed in exciters using a differential amplifier input stage.

## 7.8 Remote Control of the STL Transmitter

The PCL-505 transmitter has been designed to be operated by remote control. Radiate/standby control capability, as well as metering outputs for power and AFC, are built in.

Figure 26 shows the interconnections required for remote control of the transmitter. All connections to the transmitter are made via J1 on the rear of the unit. The interconnecting cable should have not more than a few thousand ohms resistance per conductor The panel itself is shown schematically; this entire assembly is available from Moseley Associates as the Model ECP-5 Extension Control Panel.

The Radiate/Standby switch will place the transmitter in a radiating condition when closed. It is electrically interconnected with the control switch on the transmitter itself. The AFC and Output meters give relative indications of these two parameters. The two calibration potentiometers are set to give a suitable meter deflection, as for example, half-scale.

## 7.9 Adjustment Guides

Various adjustments have been provided in the PCL-505 to allow realistic manufacturing tolerances and to provide operational flexibility. The settings of these adjustments should not be altered unless it has been determined that an apparent problem will be resolved by resetting a specific control. The locations of adjustments, as well as related test points and plug-in components, are shown in the following series of adjustment guide drawings.

PCL-505, PCL-505/C (Rev. 10/75)

Ĩ

-53-



## EXTENSION CONTROL PANEL CONNECTIONS

FIGURE 26

PCL-505, PCL-505/C (Rev. 10/75) - 54-

Should measuring equipment indicate that there is a problem in the system, and further should this trouble be positively traced to the PCL-505, then readjustment may be in order. In any case, controls should not be reset unless it is quite certain that a specific problem will be solved by a specific readjustment. A description of the various controls is given here to assist the operator with the proper test equipment to correct misadjustment. These controls are internal to the receiver and routine readjustment ("tweaking") is discouraged.

The following descriptions of the PCL-505 transmitter internal controls are with reference to drawing 21A2503 (for the composite, monaural, and dual versions).

+13.5V REGULATOR ADJUST - While monitoring the 13.5 volt line with an external voltmeter, set the +13.5V regulator adjustment control for a reading of +13.5 volts DC.

+VDC METER CALIB. - Depress the "+VDC" switch and adjust the +VDC METER CALIB. control for a front-panel meter reading of 13.5 on the lower scale.

OVEN TEMPERATURE ADJUST - This control allows adjustment of the proportional oven temperature. It is set for a reading of 3, 7  $rac{1}{2}$  4.3 volts DC at the oven heater pin 3, located inside the AFC subassembly, and chassis (chassis ground).

This reading should be taken after the temperature (and therefore the voltage has reached a stable value at room ambient. This will take about four or five minutes. If readjustment is necessary, it should be accomplished only in small increments with time given to allow restabilization.

AFC ADJUST (COURSE) - This control sets the free-running frequency of the modulated oscillator. The front-panel AFC ADJUST control should first be set to the middle of its range. While monitoring the AFC switch position, set the internal AFC ADJUST control slowly until "lock" is achieved as indicated by the meter going to the midscale position. Confirm AFC lock by operating the front-panel AFC ADJUST potentiometer and observing that the meter follows the rotation of the potentiometer. Return the potentiometer to the center of its range.

PCL-505, PCL-505/C (Rev. 3/78)

-55-

The following controls are all located in the Multiplier Module:

DOUBLER TUNING; TRIPLER TUNING; BUFFER TUNING; OUTPUT TUNING -These controls are all tuned by monitoring the IPA DRIVE meter position and tuning for maximum meter deflection.

The following controls are located within the Power Amplifier module:

Power Amplifer, the two DOUBLER INPUT TUNE controls are adjusted for approximately 0.5 VDC across R701. Adjust DOUBLER OUTPUT TUNE and DRIVER INPUT TUNE for 0.1 to 0.5 VDC across R702. This voltage is somewhat a function of the tuning of the following stage. Adjust FINAL OUTPUT TUNE and the two FINAL OUTPUT TUNE adjustments for maximum output as indicated on the front panel meter.

FORWARD POWER METER CALIB. - This control is set to read "0" dB when monitoring the FWD PWR meter position.

PROGRAM MODULATION SET - This control sets the deviation of the transmitter. For a composite system an input of 3.5 volts peak-to-peak is standard, and the deviation is 60 kHz peak. For a monaural or dual system the input is +10 dBm at a frequency of 1000 Hz, and the deviation is 40 kHz peak.

MUX 1 LEVEL SET - This control sets the deviation of the transmitter due to a subcarrier applied to the MUX INPUT #1. With a subcarrier of 1.5 volts peak-to-peak amplitude, the control is set for 15% injection (modulation). This will be 6 kHz for a monaural or dual system, or 9 kHz for a composite system.

MUX 2 LEVEL SET - As with the Mux 1 control, this adjustment sets the transmitter modulation for the MUX INPUT #2.

METER DC ZERO ADJUST - This control is used to electrically zero the panel meter deflection in either the PROGRAM or MPX positions. The meter's mechanical zero should be checked prior to adjustment of this control, which is set in the PROGRAM position without any program input.

PCL-505, PCL-505/C

MUX METER CALIB. - This control is set to produce a -3 dB deflection when program material sufficient to produce 100% modulation is applied to the transmitter.

METER ACCELERATION ADJUST - This control enables the panel meter to have the best possible response time in the PROGRAM and MPX positions. It is adjusted while rapidly varying the level of a test tone applied to the transmitter. Adjust for a 0.5 dB overshoot on the panel meter while monitoring a step going from no modulation to full modulation.

The following descriptions of the PCL-505 receiver internal controls are with reference to drawings 21A2501 (composite receiver) and 21A2502 (monaural or dual receiver).

+13.5V REGULATOR - While monitoring the 13.5 volt line with an external voltmeter, set the +13.5V REGULATOR control for a reading of +13.5 volts DC.

+VDC METER CALIB. - Depress the "+VDC" meter switch and adjust the +VDC METER CALIB. control for a front-panel meter reading of 13.5 on the lower scale.

lst L.O. FREQUENCY - This capacitor is adjusted to produce a second I.F. of 10.7 MHz when a carrier of the correct frequency is being received.

OSCILLATOR OUTPUT; QUADRUPLER OUTPUT; DOUBLER OUTPUT OUTPUT TO MIXER - None of these controls should be adjusted unless a spectrum analyzer is available. If an analyzer is available, these controls may be adjusted for maximum output consistent with freedom from noise or spurious outputs. The minimum acceptable output level from this module is 4 milliwatts into a 50 ohm termination.

CONVERTER OSCILLATOR OUTPUT - This control is adjusted for maximum indication of received signal strength. On strong signals, the panel meter may show little change as this control is adjusted.

PCL-505, PCL-505/C (Rev. 4/76)

1

Ľ

-57-
CONVERTER OSCILLATOR FREQUENCY - This control is adjusted for an oscillation frequency of 63.3 MHz. This control is only a vernier on the oscillator and may be left at midrange.

10.7 MHZ I.F. - These four controls affect the shape of the receiver selectivity curve. They are first adjusted by using a sweep generator and are slightly adjusted if required to minimize crosstalk into program subcarriers, if used. Should this readjustment be made, then the filter must be rechecked using the sweep generator.

74 MHZ FIRST I.F. - These controls should be adjusted only when a carrier-frequency sweep generator is available. They are adjusted for maximum output from the module when the local oscillator and carrier-frequency sweep generator are applied to the inputs. Should the controls be more than slightly off-resonance, or should the band-width be insufficient, then the possibility of oscillation exists. This will be revealed by examination with a spectrum analyzer. The correct bandwidth of this module is 4 MHz total width at the 2 dB point.

INPUT PRESELECTOR - These controls are adjusted for maximum indication of received signal strength. On strong signals the panel meter may show little change as these controls are adjusted.

DEMODULATOR - These controls Tl primary and Tl secondary are adjusted for maximum AF output from the demodulator output terminal (right-hand side, rear-most terminal). Then adjust the BLUE slug, Tl secondary, for minimum audio distortion.

MUTING THRESHOLD - This control is set to mute the receiver output when the carrier level is below 50 microvolts (-73 dBm).

OUTPUT AMPLITUDE - This control is used to adjust the receiver output signal amplitude. It is adjusted for an output amplitude of +10 dBm at low audio frequencies for monaural systems or 3.5 volts peak to peak for composite systems, at full modulation of the transmitter. See paragraph 6.2.6 before making adjustments.

PCL-505, PCL-505/C (Rev. 3/78) - 58-

PROGRAM METER ZERO - This control is used to electrically zero the panel-meter deflection in either the MUX or PROGRAM positions. The meter's mechanical zero should be checked prior to adjustment of this control, which is set in the program position when a quiet, unmodulated carrier is being received.

MUX METER CALIB. - This control is used to adjust the panel meter calibration when the MUX button is depressed. It is set to read -3 dB when a subcarrier modulates the system 15%. Prior to adjustment of this control, the transmitter modulation must be set and the receiver output amplitude control must be set.

PROGRAM METER CALIB. - This control is used to adjust the panel meter calibration when the PROGRAM button is depressed. It is set to read "0" dB when a test tone modulates the system fully. Prior to adjustment of this control, the MUX meter calibration must be correctly set.

PROGRAM METER ACCELERATION - This control enables the panel meter to have the best possible response time in the MUX and PROGRAM positions. It is adjusted while rapidly varying the level of a test tone applied to the transmitter. Adjust for 0.5 dB of overshoot on the panel meter while monitoring a step going from no modulation to full modulation.

MUX OUTPUT LEVEL - This control sets the level of the subcarrier(s) appearing at the multiplex output connectors. It is adjusted for a level of 1.5 volts peak to peak of subcarrier when that subcarrier modulates the transmitter 15%. The OUTPUT AMPLITUDE control must be set prior to setting the multiplex output level control.

The following three controls are peculiar to the composite version of the PCL-505:

H.F. GAIN - This control adjusts the frequency response of the composite STL, primarily in the 30 kHz to 50 kHz region. It is set to maximize stereo separation when a test tone of 1 kHz is applied to the stereo generator.

PCL-505, PCL-505/C (Rev. 4/76)

-59-

H.F. TILT - These controls affect the time response of the composite STL, primarily in the 50 kHz region. They are set to maximize stereo separation when a test tone of 15 kHz is applied to the stereo generator.

L.F. TILT - This control affects the frequency response of the composite STL, primarily in the 50 Hz region. It is set to maximize stereo separation when a test tone of 50 Hz is applied to the stereo generator.

The following three controls are peculiar to the monaural (or dual, for stereo) version of the PCL-505:

5 KHZ ADJUST - This control allows the frequency response of the system to be optimized in the 5 kHz region. It is adjusted for the same level from the program output of the receiver, when the test tone applied to the transmitter is switched back and forth between 500 Hz and 5,000 Hz. Be sure the modulation level never exceeds the "0" mark on the PROGRAM position of either the transmitter or receiver panel meters; it is advisable to run this test several dB below normal level.

15 KHZ ADJUST - This control allows the frequency response of the system to be optimized in the 15 kHz region. It is adjusted for the same level from the program output of the receiver when a test tone applied to the transmitter is switched back and forth between 1,500 Hz and 15,000 Hz. As with the 5 kHz adjustment, be sure the modulation level never exceeds the "0" mark.

NOISE REDUCTION - This control enables an enhancement of the measured signal-to-noise ratio of the signal as delivered from the program output of the PCL-505 receiver. It can be disabled by operating the noise-reduction switch to the Off position. The preferred method of adjustment is to adjust the received signal strength until the signal-to-noise ratio is 60 dB, and then adjust the noise reduction control for an enhancement of 8 dB to 10 dB.

PCL-505, PCL-505/C

-60-

FIELD CHANGES - For program inputs (ref. 91C6887) of less than 3.5V P-P into J4, change R201 according to the following;

Signal (V P-P)	R201 (K ohms)
0.5	0 (jumper wire)
1.0	1.6
1.5	3.3
2.0	4.7
2.5	6.8
3.0	8.2
3.5	10

Note: 1.  $Zin = Rin + 1.7 K \Omega$ 

1

.

1

2

2. R201 =  $\frac{\text{Ein} - 0.5}{0.3}$  KΩ

The signal out of AUDIO OUTPUT J3 on the Receiver will still be 3.5V P-P for the correct input selected.

PCL-505, PCL-505/C

-61-



PARENT FIEM ND 9050337

1

- (

SPARE PTS PCL-5056C 890-960HHZSP-38B F

DATE 4/22/81

PAGE 1

HOSELEY ASSOCIATES INC 111 CASTILIAN DRIVE GOLETA CA 93117 805 968-9621

	COMPONENT ITEM NO.	STOCK LOCA	MANUFACTURER PART NUMBER	COMPONENT DESCRIPTION	QUANTITY PER	UM	UNIT SALES PRICE	SALES PRICE
-	3390150	2122	HV-5254	LED GREEN	1	EA	1.37	1.37
	3600145	2721	LN4154	DID 1N4154 25V 4NS SI DO35	2	EA	.16	- 32
	3600160	2144	1N4731A	DIO 21N4731A 4.3V IN 5% ALAY	l.	EA	1.26	1 - 26
	3600178	2744	1 N4 7 3 3 A	DID 21847334 5.18 18 5% ALAY	1	EA	1.09	1.09
	3600186	2144	1N4734A	DIQ ZIN4734A 5.6V IN 58 ALAY	L	EA	1.26	1.26
	3600236	2144	1N4745A	DID 21847458 169 18 5% ALAY	1	EA	•42	- 42
	3610003	2121	1002	010 1002 200V 1A SI 0039	4	EA	. 39	1.56
	3610045	2144	5082-2835	D10 5082-2835 FAST	1	EA	2.24	2.24
	3610094	2721	MDA-980-2	DIU MDA-980-2 LOOV BRIDGE LZA	Ł	EA	7.11	7.11
	3610136	2144	MV-840	DIO VNV-840 030V 90-100PF D07	L	EA	3.33	3 - 33
	3630027	2721	2N2924LFS	XT NS2N2924LF5.2W160H025V.1A7P	2	EA	• 54	1.08
	3630035	2721	2N3053	XT NP2N3053 05W100M080V.7A	1	EA	1.47	1-47
	3630043	2121	2N3U54	XT NP2N 3054 25W030K090V02A	1	EA	2.80	2.80
	3630076	2144	2N3563	XT NS2N3563 .2W600H030V50H2P	1	EA	.49	• 4 9
	3630092	2744	2N3640	XT PS2N3640 .2W500H012V80H3.5P	I.	EA	2.28	2 - 28
	3630159	2144	2N3819	XT NF2N3819 .4W U25V20M	l	EA	.14	- 74
	3630167	2144	2N3820	XT PF2N3820 .4W 020V15M	1	EA	1.51	1.51
	3630191	2144	2N4Q37	XT PP2N4037 01W060M060V01A	1	EA	1.54	1-54
	3630209	2144	2N4058	XT PS2N4058 .4W D30V30M	ł	EA	•46	• 46
	3630241	2744	284428	X [ NP2N4428 3.5W750M055V.42A	· •	EA	4 • 94	4.94
	3630308	2721	2N5179	XT NS2N5179 .2W900H020V50MLP	1	EA	2 - 38	2 • 38
	3630316	2144	215293	、 X T NP 2N 529935556800K 080V 04 A	1	EA	1.73	1-73

1.24																			
------	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--

PARENT ITEM NU 9050337

-t

(

t

.

4

SPARE PTS PCL-5056C 890-960HHZSP-388 F DATE 4/22/81

MOSELEY ASSOCIATES INC 111 CASTILIAN DRIVE GOLETA CA 93117 805 968-9621

COMPONENT ITEM NO.	STUCK LOCA	MANUFACTURER PART NUMBER	COMPONENT DESCRIPTION	QUANTITY PER	UM	UNIT Sales price	TUTAL SALES PRICE
3630399	2743	3N140	XT NF3N140 .4W 020V50M	1	EA	4-17	4-17
3640018	2744	A-400	XT NSA400 .200056015V25H	I	EA	6.62	6.62
3640109	2144	D1-128	XT NPD1-128 5.8W866M036V.25A	L.	EA	21-18	21.18
3640133	2144	DH5-12B	XT NPDM5-128 298 036V02A	L	EA	49.70	49.10
3640141	2744	DH10-128	XT NPDM10-128 50W 036V04A	1	EA	71.05	71.05
3640102	2713	MJ-2955	XT PPHJ2955 115W2.5H060V15A	L	EA	2.52	2.52
3650116	2143	MC1723CL	RGLTR TYPE 1723 VARV .15A 632	2	EA	2.66	5+32
3660008	2812	SN72741P	IC UA741P DPAMP GEN COMP	ł.	EA	.83	•83
3660024	2743	SN72748P	IC UA758P DPAMP UNCONP	1	EA	1.19	1-19
3660297	2143	5N7486N	IC SN7486N QU 21 EXCL OR	1	EA	1.02	1.02
3680170	2713	SCŁ 4020AE	IC SCL4020AE 14 STAGE BIN CT	1	EA	3.50	3.50
3730173	2143	LH-318N	IC LH318N OPAMP HISPEED	1	EA	6 - 48	6.48
3730199	2743	LH- 324N	IC LM324N OPAMP SNGL SUPL	1	EA	1.68	1.68
3730322	2143	MC1350P	IC MC1350P OPANP	L.	EA	2.63	2.63
3730348	2143	HC1355P	IC HC1355P AMP FH/IF	1	EA	3.85	3-85
3730389	2143	MC1590G	IC MC1590G AMP VIDED	1	EA	16.20	16-28

TOTAL PRICE 239-40 \*

PAGE 2

.

.



·

1

(

OPT S/P KIT PCL-505 890-960 SP-388 F DATE 4/22/81

781 PAGE 1

MOSELEY ASSOCIATES INC 111 CASTILIAN DRIVE GOLETA CA 93117 805 968-9621

COMPONENT ITEM NO.	STOCK LOCA	MANUFACTURER PART NUMBER	COMPONENT DESCRIPTION	QUANTITY PER	UH	UNIT Sales price	TUTAL Sales price
3270113	2121	AZ-530-09-2	RELAY HIN PC 2000HH 12V NOH	ł	EA	6.41	6.41
3370228	2735	MDL 1/4	FUSE	5	EA	1.53	1.65
3370269	2735	MUL 1 1/2	FUSE	5	EA	1.79	8.95
4090007	2024	A-20	XFHR	L	EA	53.74	53-14
4090015	2024	SAT-109	XFMR	ł	EA	43.34	43.34
4090254	2022	3-10798	XFNR 8-P-698	1	EA	51.31	51+31
4090312	2024	3-1086	XFMR	L	EA	18-48	18-46
4260204	2731	TVA-1163	CAP HI-TEMP 1000/16V	Ł	EA	2.79	2.19
4210039	2723	360272G025AA2A	CAP PWR LYTEC 2700/25V	t	EA	5.01	5.81
4270088	2723	360X902G025AB2A	CAP PWR LYTIC 9000/25V	L	EA	7.98	7.98
9100033	2214	21A2493 B	XTAL OVEN HOD XHTR PCL-505	L	EA	35.00	35.00

TOTAL PRICE 241-46 \*

R 

]]

PARENT ITEM NO 9051426

1

.

.

(

(

26 OPT S/P KIT PCL-505C 090-960 SP-388 F DATE 4/22/81 PAGE L

MUSELEY ASSOCIATES INC 111 CASTILIAN DRIVE GOLETA CA 93117 805 968-9621

COMPUNENT ITEM NO.	S TOCK L DC A	MANUFACTURER PART NUMBER	COMPONENT DESCRIPTION	QUANTITY PER	UM	UNIT Sales price	TUTAL Sales price
3270113	2721	AZ-530-09-2	RELAY MIN PC 2000HM 12V NOH	1	EA	6.41	6-41
3370228	2135	MDL 1/4	FUSE	5	EA	1.53	7.05
3370269	2735	MDL 1 1/2	FUSE	5	EA	1.79	8 - 95
4090254	2022	3-10798	XFMR 8-P-698	Ł	EA	51.31	51+31
4070312	2024	3-1086	XFMR .	L	EA	18.48	18.48
4260204	2731	TVA-1163	CAP H1-TEMP 1000/16V	1	EA	2.19	2.19
4270039	2723	36027260254424	CAP PWR LYTIC 2700/25V	1	EA	5+81	5.81
4270088	2723	36DX902G025AB2A	CAP PWR LYTIC 9000/25V	L	EA	7.98	7.98
9100033	2214	21A2493 B	XTAL OVEN HOD XHTR PCL-505	L	EA	35.00	35.00

TOTAL PRICE 144.38 \*



PAKENI ITEN NU 70714	<u> </u>	
----------------------	----------	--

.

4

**(** . .

4

COMPONENT	STOCK	MANUFACTURER	COMPONENT	QUANTETY		UNIT	TUTA	L
111 CASTILIA GOLETA CA 805 968-962	N DRIVE 93117	x						
MOSSIEV ASSO	9051228	OPT CR	S PCL-5056C @890-960MHZS	P-388 F	DATE	4/22/81	PAGE	1

COMPONENT TTEM NO.	S TOCK L OC A	HANUFACTURI PART NUMBER	ER R	COMPONENT DESCRIPTION	QUANTITY PER	UM	UNIT Sales price	TOTAL Sales price
3340106	2134	3040042	₿	XTAL 63+3 MHZ PCL-505/PCL-101	L	EA	37.50	37.50
3340478	9600	3UA 00 34	8	XTAL RX 890-960 MHZ PCL505/101	1	EA	37.50	37.50
3340486	9600	3040035		XTAL TX 947-952 MHZ PCL-505	1	EA	37.50	37.50

TOTAL PRICE 112-50 9

.





A FWD ADURT	ATIFICATION	1.1	R, REV. MULT	DATE		-	<u>М</u>	SELEY	A88	OCIATE	B, INC.				
20 MAR	TA IDE	DO GLON	F AMP			PCL - 505 TRANSMITTER ADJUSTMENT LOCATIONS									
- 2	2 -	250	No.	ž	2	TOL	FRACT. ±	1/32XX	± .430,	.XXX = .010,	∠ ± 1/2*				
N.C	0.	OZA	1	20	2	DWN	KF	3 APR 75	SCALE:	NONE					
-	-	3-9	ae	Z	3	CHK	FXY	3APR75	214	2507					
0	U	8	A	*	2	ENG	HHam	4 AF1 75	ZIA	2505	D				



World Radio History



1!

l

1

i

h

L

I. NORMAL = NO NOISE REDUCTION NOISE REDUCTION = NOISE REDUCTION CIRCUITRY ACTIVATED

IN DIATION	SC. L.I.	LLECTOR	NOD LOCATION CPM	DAT		(		SELEY	ABBOCIATES, INC ANTA BARBARA RESEARCH PAG GOLETA, CALIFORNIA 520						
TON 101	CAL O	PUT PRES	TMENT			PCL-505 MONAURAL RECEIVER ADJUSTMENT LOCATIONS									
22		1	92.	2	1	TOL:	FRACT. ±	1/32,	± .430, .XXX ± .010, < ± 1						
	N.	20	10.	19	-	OWN	KF	26 MAR 75	SCALE: NONE						
-	0.3	R.		Z	3	CHR	FXY		214 2502						
0	0	E	A		2	ENG	H. Han	28 Mm 75	LIAZOUZ D						



World Radio History







- I UNLESS OTHERWISE SPECIFIED RESISTOR VALUES ARE IN OHMS 1/4W, 10% CAPACITOR VALUES ARE IN MICROFARADS
- 2 \* DENOTES SELECTED VALUE
- RF VOLTAGES MEASURED USING TEKTRONICS 301 SCOPE, WITH TYPE 02 PLUG-IN, AND NON COMPENSATED PROBE.
- 4 DC VOLTAGES LESTED WITH IO MEG INPUT DVM.
- 5. VOLTAGES SHOULD BE WITHIN 20 % OF THAT SHOWN ON THE SCHEMATIC.
- 6. COMPONENT LAYOUT 20 A 2490
- 7. PC BOARD SIA 5598

SERS CH	10.505 28.82 JUE	1.1 0060	ILTER CHANNEL	ABT FLW.	BATE		) <b>MO</b>	BELEY	A97	ADARA RESEAR	, <b>SPACE.</b> EH PARE MA SEP17		
PIN5 OF HET. BCO	50442 723 2521,5	0 C 5 3 0	Marthol Ch	14.0		SCHEMATIC XMTR AFC							
284	080	30.0			Į	TOL: I	JUMA	7-40 76	SCALE:		<u>+</u>		
1942	~ 310	X -		ē.	łĒ	CHK	FXY	BJAN76	01	C 6994	E		
L m		U	0	1.2	Ē	6146	CALPE	I Joslo	31	0004			





E

I. UNLESS OTHERWISE SPECIFIED ALL RESISTOR VALUES ARE IN OHMS 1/4W,10 % AND CAPACITOR VALUES ARE IN MICROFARADS

2. P. C. BOARD 51A 5594.

3. SCHEMATIC 9186886

	PRR C.H.	536 JAM	A-CHANGED A-ECO 456	N WAS IED D		5-26 L.I.	NE 4415.	ETMINAL 5 OUTSIDE 9 JAN 75	BATE		<b>(</b> ^	MOSELEY ASSOCIATES, INC. SANTA BARBARA RESEARCH PARK GOLETA, CALIFORMIA 13017
	AL SAU	SULYTZ	AL SOL TO	S RUGT RU		0 58225 127. L75	14 - U	420 & T 8415 TO 2		ų	хмт	COMPONENT LAYOUT TR H.F. BUFFER / DIVIDER.
	100	음이	3	E	35	10	1	ŭ₩		Ę.	TOL:	FRACT, ± 1/22, .XX ± .830, .XXX + .010, ~ ± 1/2*
1	ā 9.	ž	10	128	ş i		3	A A A	1	E	OWN	L.I. BOCT 74 SCALE: FULL
ł	<u>v 6</u>	Ť		100	<u> </u>		F-		Ξ	3	СНК	FXY 300EC74 2042489 CI
	5	J	<b>L</b>	<b>W</b>		U	ß	$\triangleleft$		Ξ	ENG	H Hon 30 916 74 20 A 2 400 CH





- I UNLESS OTHERWISE SPECIFIED RESISTOR VALUES ARE IN OHMS 1/4W, 10 % CAPACITOR VALUES ARE IN MICROFARADS
- 2 M DENOTES SELECTED VALUE
- 5 RF VOLTAGES MEASURED USING TEKTRONICS 501 SCOPE, WITH TYPE 82 PLUG-IN, AND NON COMPENSATED PROBE.
- 4 DC VOLTAGES TESTED WITH IOMEG INPUT DVM
- 5 VOLTAGES SHOULD BE WITHIN 20 % OF THAT SHOWN ON THE SCHEMATIC.
- 6 COMPONENT LAYOUT 20A 2489

7. PC BOARD STA 5594



.

1.1

.



ø

- I. UNLESS OTHERWISE SPECIFIED RESISTOR VALUES ARE IN OHMS 1/4 W 10 % CAPACITOR VALUES ARE IN MICROFARADS. 2. P. C. BOARD 51 A 56 32
- 3, SCHEMATIC 91C6888

HOT TO T	ADDED OCTOOL	ALL LAN	STAWN CEL	DATE	SANTA DARBARA RESEARCH PARK GOLETA, CALIFORNIA DRIT
1000 1000 1000 1000 1000 1000 1000 100	FOR Cas	2 WAL 74	WAS LADO.C. WWD R229 R 204 WUD	Ŧ	COMPONENT LAYOUT XMTR AUDIO/MTRG AMP(MONAURAL).
	1 1 1	TC TO	8100 B100	TT. AP	DWN L.I. IBDEC74 SCALE: FULL
PHI	ULU	AUD	A		ENG H. Hang 1 14975 20A2487 J











- I. UNLESS OTHERWISE SPECIFIED RESISTOR VALUES ARE IN OHMS 1/4W,10% CAPACITOR VALUES ARE IN MICROFARADS
- 2. \* DENOTES SELECTED VALUE.
- RF VOLTAGES MEASURED USING TEXTRONICS 501 SCOPE, WITH TYPE 02 PLUG-IN, AND NON COMPENSATED PROBE.
- 4. DC VOLTAGES TESTED WITH ID MEG INPUT DVM.

- VOLTAGES SHOULD BE WITHIN 20 % OF THAT SHOWN ON THE SCHEMATIC.
- 6. COMPONENT LAYOUT 20 A 2487
- 7. PC BOARD 51 A 5632

5.



World Radio Histor

.







- I UNLESS OTHERWISE SPECIFIED RESISTOR VALUES ARE IN OHMS 1/4W,10% CAPACITOR VALUES ARE IN MICROFARADS
- 2 # DENUTES SELECTED VALUE
- 3 RF VOLTAGES MEASURED USING TEKTRONICS 381 SCOPE, WITH TYPE 82 PLUG-IN, AND NON COMPENSATED PROBE
- 4 DC VOLTAGES TESTED WITH IOMEG INPUT DVM
- 5 VOLTAGES SHOULD BE WITHIN 20 % OF THAT SHOWN ON THE SCHEMATIC.
- 6 COMPONENT LAYOUT 2002763 C/
- 7. PC BOARD 5185894 82

		CR 306*	CR 307*	C 314 *	C311,
ITEM I	15D/240/330MHz	88105 A	BB 105 A	.001	20Pf JFD
ITEN 2	450/960 MHz	MV 840	M V 8 4 0	IOPF JFD	IOPF JFD

CC 301	NT CYCS.	00. 330 MML IN ITEM 2. YB0 0.T.W.	9476		IC.
VED J VASD J VASS V VASS V	PONG 18/2	MAS 60 10125		SCHEMATIC XMTR FMD 150/240/330Mt2 8 450/960	D MNI 4
	ן ציי	a de la	ŝ	TOL PRACT 2 1/20, 28 2 620 122 610, 4 2	1/1*
8400919	ΰű	133	ş II	CMR 20 1. 1. 1. 1. 1. 1. CALL	
9 0	8	AG	2	91B7195	D



- I. UNLESS OTHERWISE SPECIFIED, RESISTOR VALUES ARE IN OHMS, 1/4W, 10 % CAPACITOR VALUES ARE IN MICROFARADS.
- SOLDER ALL RESISTORS & CAPACITORS THAT GO TO GROUND ON BOTH SIDES OF P.C.BOARD.
  P.C. BOARD SIAS659 REV. 07.
- 4. SCHEMATIC 9186877. REY GO.
- 5 \* TRIM 0601-3 LEADS TO 1/8" +1/32"
- WHEN USING GREEN 4 PIN SOCKETS.
- 6. SOLDER ALL .OI UF CHIP CAPACITORS WITH SILVER-BEARING SOLDER. FED. SPEC 00-5-571 OR EQUIV.

POL CH	CAH	MAJOR RE-	CAPACITERS PERSON	ADD CLI7 &		100 PKN	11266 11266	TT THE		
S LIST	10. 50	REV-07	100 M	AN 24517	ALMON A	0 CHIT. C	10 1016	A NOD CL		COMPONENT LAYOUT XMTR BUF. MULT.
445	2 4	NON NO	000	3	ORK	1.0	10.70			TOL: FRACT. ± 1/3238 ± .830388816. <= 1/
494	195	Es	1000	36	E	111	01.00			DWN G M 12-19-75 SCALE: FULL
La Um	082	< >	OF08	02	3	-	d BUGN	1 2	5	CHR FXY 21 JAN 76 200 2561
Ŧ	HQ	ອ	L.,	ш	0	U	8	4	2	ENG (8588(21) Jan 16 ZUAZJOI H




- I. UNLESS OTHERWISE SPECIFIED, RESISTOR VALUES ARE IN OHMS, 1/4W, 10%, CAPACITOR VALUES ARE IN MICROFARADS.
- 2. P.C. BOARD 51A5659
- 3. COMPONENT LAYOUT 20A2561.







				-
		an strate		63
_			4	62
				61
				60
				59
				60
-				30
				57
				56
				55
-				50
				34
-				23
_				32
				51
4	WASHERS +4 SPLIT RING		1050632	50
4	NUTS. #4-40		1050582	40
	CORENE - Hallo - Alla BH		1050202	110
-	SCREWS FR-90 X STID DH		1030202	
8	SCREWS F4-40 Y 2/16 BH		1050145	41
2	SCREWS + 4-40 × 3/6 BH		1050103	46
				45
2	NUTS +8-32		1130277	44
2	OT KMASHER EXT TOTAL CA		1130301	43
-	LAND THE REAL POINT FO		1130301	
_	CON 37-490 7 0070	1 91/	1010334	42
-	LUIL 31-VED 3-2039 Ap	116	ADID 732	41
2	8505-E X0TJUUNI	L713,714,715	4010625	40
1	SOCKET INDELC LST2202-2		3250214	39
1	CABLE 4-4001-8-EVO	P701	2200277	38
1	CONNECTOR US SA AAI	1701	3030178	37
			011010	
				36
_				35
1	. CTC DMIO-12B	Q 704	3640141	34
1	" CTC DMS-128	9703	3640133	33
2	TRAUSISTOR CTC DI-125	0701 0702	3640109	32
-		GIOLANDE	30-10103	36
-				31
1	DIDDES INAISA GENELEC.	CR702	3600145	30
1	DIODES HP 3082 - 2835	CR 701	3610045	29
				28
4	· FEED THRU SPECTR 54-794-002-47IM	6701,6702,	4350047	27
-		C7214(_722.		24
-		1 202 20 20		26
2	Z.2#F CRL DTZ-2RZ NPO	217,726	4300018	25
2	* 4-10# EFJ 278-0410-005	C706 C709	4370300	24
1	2-5-5 EE \ 275-005-005	C 7/8	4370204	23
1				
	22-5 CRL 012-22	LC 701	4300133	22
-	22 - CRL DT2-22	LC701	4300133	22
3	22 of CIL DIZ-22	LC 701	4300133	22
37	22 + CRL DT2-22 10AF/20V SPASUE 	LC 701 C 704, 713, 727 C 703, 703, 710, 714, C 703, 703, 710, 714, C 710, 715, 710, 714,	4300133 4280079 4350161	21
37	22 - F CRL DT2-22 - DAF/20V	LC 701 C 704, 713, 727 C 705, 705, 710, 714 C 716, 719, 720 C 723	4300133 4280079 4350161 4350146	22 21 20 19
371	22 - F CRL DIZ-22 DAF/20V - DIZ-22 DIAF/20V - DIZ-22 D	LC 701 C 704, 713, 727 C 723, 725, 710, 714 C 723 C 723 C 728	4300133 4280079 4350161 4350146 4350276	22 21 20 19 18
3 7 1 1	22 of CRL DTZ-22 00AF/20V 100000000000000000000000000000000000	LC 701 C 704 ,73 ,727 C 705 ,705 ,703 ,777 C 72 3 C 72 3 C 72 8	4300133 4280079 4350161 4350146 4370276	22 21 20 19 18 17
3711	22 of CRL DT2-22 DAF/20V SPACUE .01AF WIODEFIDSMP CAPALITOR 33 - ATC 17AH330J500PS CAP, 1-3-5.4 of .EFE 187-0103-005	LC 701 C 704, 713, 727 275, 705, 705, 705, 710, 727 C 723 C 728 C 728	4300133 4280079 4350161 4350146 4370276	22 21 20 19 18 17
371	22 - F CRL DT2-22 - DAF/20V - HODORODZEKAT - DIAF WIODEFIDDMP CAPALITOR 33 ATC 17AH330J500P5 CAP, TDK MCV50A1H030 - 1-3pF CAP, 1-3-5.4 pF, 2+3 187-0103-005 PESISTOR 185 A VAN WE CAPPAL	LC 701 C 704, 713, 727 278, 703, 703, 700 C 723 C 728 C712, 724, 725 R 704	4300133 4280079 4350161 4350146 4370276 4370003	22 21 20 19 18 17 16
- - - - - - - - - - - - - - - - - - -	22 - F CRL DT2-22 DAF/20V - SPACE DAF/20V - SPACE DAF/20V - SPACE CAPALITOR 33 ATC 17AH330J500P5 CAP, 1-3-5.4 pF, EF# 187-0103-006 RESISTOR 1.2K A. 1/4W 107 CARDON	LC 701 C704, 73, 727 C704, 73, 727 C73, 75, 70, 77 C 723 C 728 C728 C712, 724, 725 R 704	4300133 4280079 4350161 4350146 4370276 4370003 4410270	22 21 20 19 18 17 16 15
371	22 of CRL DTZ-22 DAF/20V 10005/002KAT 	LC 701 C704,73,727 C714,73,705,74 C714,74,720 C 723 C 728 C 728 C 728 C 728 C 728 C 728 C 728 C 728 C 728 C 704 R 709 C 709	4300133 4250079 4350161 4350146 4370276 4370003 4410270 4410379	22 21 20 19 18 17 14 15 14
- - - - - - - - - - - - - - - - - - -	22 - F CRL DT2-22 ·	LC 701 C 704, 73,127 C 701, 753,105,105,714 C 72,3 C 72,3 C 72,8 C 72,8 C 72,8 C 72,8 C 72,8 C 72,72,8 C 72,72,8 C 72,72,8 C 72,72,8 C 70,74 R 70,74 R 70,77 R 70,6	4300133 4280079 4350161 4350146 4370276 4370003 4410270 4410270 4410247	22 21 20 19 18 17 14 13
- - - - - - - - - - - - - - - - - - -	22 of CRL DIZ-22 32 of CRL DIZ-22 30AF/20V 194006×0028×A1 01AF WIODSFID OMP CAPALITOR 330 - ATC 17AH330J500PS CAP, 1.3-5.4 pF, 248 187-0103-005 RESISTOR 1.28 A. 1/4 W 10% CARBOU 10K A. 1/4 W 14 56 A. 1/4 W 14	LC 701 C 704, 73, 727 C 73, 53, 100, 744 C 72, 3 C 72, 3 C 72, 8 C 72, 8 C 72, 7 C 7, 7 C 7 C 7 C 7 C 7 C 7 C 7 C 7 C	4300133 4280079 4350161 4350146 4370276 4370003 4410270 4410270 4410247 4410298	22 21 20 19 18 17 14 13 12
	22 of CRL DT2-22 DAF/20V SPACE DAF/20V SPACE DAF/20V SPACE DAF WIODEFID SAF CAPALITOR 33 - ATC 17AH330J500P5 CAP, 1.3-5.4 pF, 244 187-0103-006 RESISTOR 1.28 A. 1/4 W 07 CARON 10K A. 1/4 W 4 1K A. 1/4 W 4 3 - 5.4 μF, 244 197 4 1 - 5.4 μF, 244 197 4 2 - 7.4 μV 107 4 - 7.4 μ	LC 701 C 704, 773, 727 C 704, 773, 727 C 72, 75, 7027 C 723 C 728 C 704 C 706 C 707 C 705 C	4300133 4280079 4350161 4350146 4370276 4370003 4410270 4410270 4410279 4410298 4410098	22 21 20 19 18 17 16 15 14 13 12 11
371111122	$\begin{array}{c} 22 \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \$	LC 701 C 704, 73, 727 C 776, 119, 720 C 723 C 723 C 728 C 777 C 777 C 777 C 777 C 777 C 777 C 777 C 777 C 7	4300133 4250079 4350161 4350146 4370276 437003 4410270 4410270 4410247 4410247 4410295 4410015	22 21 20 19 18 17 16 15 14 13 12 11 10
371111111111111111111111111111111111111	2 3 F CRL DT2-22 3 22 F CRL DT2-22 3 DAF/20V 10000000025KAI 01AF WIODEFIDDMP CAPALITOR 33F-ATC 17AH330J500F5 CAP, 1.3-5.4 pF, 2F3 187-0103-005 RESISTOR 1.28 A.1/4 W 107 CARBON 4 1/4 A.1/4 W 11 5 G A.1/4 W 11 4 7.7 A.1/4 W 107 1 4 7.7 A.2 W 57 177 CAPSON 100 AN 57 CAPBOLI TO	LC 701 C 704, 73,127 C 724, 73,127 C 723 C 723 C 728 C 728 C 728 C 724 R 704 R 707 R 706 R 701, 708 R 701, 8 702 R 703 R 705 R 70	4300133 4280079 4350161 4350146 4370276 4370076 4370076 4410270 4410270 4410277 4410247 4410247 4410298 4410298 4410015 4590048	22 21 20 99 80 17 19 15 14 13 12 11 10 0
	22 of CRL DT2-22 DAF/20V PODOGXODEKAL ODAF WIODEFICODAP CAPALITOR 330 - ATC 17AH330J500P5 CAP, 1.3-5.4 pF, EF8 187-0103-005 RESISTOR 1.28 A. 1/4 W 10% CARBOU 10K A. 1/4 W 11 10K A. 1/4	LC 701 C 704, 73, 727 C 73, 53, 703, 727 C 72, 3 C 72, 3 C 72, 8 C 72, 7 C 72, 8 C 72, 7 C 7 C 7 C 7 C 7 C 7 C 7 C 7 C	4300133 4280079 4350161 4350146 4370276 437003 4410270 4410270 4410247 4410247 4410298 4410215 4590022	22 21 20 9 19 10 17 14 13 12 11 10 9
- <u>7</u> - <u>-</u> <u>1</u> <u>1</u> <u>7</u> <u>-</u>	22 of CRL DT2-22 DAF/20V DEDOKODZKAI ODAF WIODEKODZKAI ODAF WIODEKODZKAI ODAF WIODEKODZKAI CAPALITOR 33d -ATC I7AH330J500P5 CAP, TDK MCV50A1H030 I-3pF CAP, I-3-5,4 pF, EFF 187-0103-005 RESISTOR 1.28 LA 1/4 W 07 I.28 LA 1/4 W 07 I.28 LA 1/4 W 0 I.28 LA 1/4 LA 1/4 LA 1/4 W 0 I.28 LA 1/4 LA 1/4 LA 1/4 W 0 I.28 LA 1/4	LC 701 C 704, 73, 727 C 73, 55, 100, 743 C 723 C 723 C 728 C 709 R 706 R 703 R 705 C 705	4300133 4280079 4350161 4350146 4370276 4370003 4410270 4410270 4410247 4410247 4410098 4410098 4410015 4590048	22 20 9 8 77 3 15 4 13 12 11 0 9 8
- <u>7</u> 7	22 of CRL DIZ-22 DAF/20V DEPODEKODZEKAT DAF/20V DEPODEKODZEKAT DIAF WIODEFID SPACE CAPALITOR 33 of -ATC ITAN3SOJ500PS CAP, 1.3-5.4 pF, EF\$ 187-0103-005 RESISTOR 1.8K A. 1/4W 10% CARBON 10K A. 1/4W 10% 1K A. 1/4W 10% 1K A. 1/4W 10% 1K A. 1/4W 10% 1.356 A. 1/4W 10% 1.47 A. 1/4W 5% 1.47 A. 1/4W 10% 1.47 A. 1/4W 10% 1.47 A. 1/4W 5% 1.47 A. 1/4W 5% 1.47 A. 1/4W 10% 1.47 A. 1/4W 5% 1.47 A. 1/	LC 701 C704,73,727 C72,3 C 72,3 C 72,3 C 72,3 C 72,3 C 72,3 C 72,8 C 72,4 R 70,4 R 70,7 R 70,4 R 70,7 R 70,5 R 70,5	4300133 4250079 4350161 4350146 4370276 437003 4410270 4410270 4410247 4410247 4410295 4410015 4590048 4590022	22 20 19 18 17 19 19 19 19 19 19 19 19 19 19 19 19 19
· · · · · · · · · · · · · · · · · · ·	2 - 5 CRL DT2 - 22 - 3 10AF/20V - 40006X0025KAI 01AF WIODEFIDDMP CAPALITOR 35, - ATC 17AH350J500F5 CAP, TDK MCV50A1H030 - 1-3pF CAP, 1-3-5.4 pF, 8#3 187-0103-005 RESISTOR 1.8K A. 1/4 W 10% CARBON - 1/2 K A. 1/4 W 10% CARBON - 1/2 K A. 1/4 W 10% - - 1/4 K - 1/4 W 10% - 47 A. 2W 5% - JTAC RESISTOR 0.16a, 2W, 5% CARBON, IRC FERROX CUBE VK-200-20/45	LC 701 C 704, 73,127 C 724, 73,127 C 723 C 723 C 728 C 728 C 728 C 728 C 728 C 724 R 707 R 704 R 707 R 706 R 701, 708 R 701, 8 702 R 703 R 705 C	4300133 4280079 4350161 4350146 4370276 4370276 4370003 4410270 4410270 4410247 4410247 4410247 4410298 4410215 4590048 4590022	22 2 2 19 18 17 14 15 14 15 12 11 10 9 8 7 4
	22 of CRL DT2-22 DAF/20V PADOLAXODEKAL ODAF WIODEFIDDAR CAPALITOR 330 - ATC I7AH330J500PS CAP, 1.3-5.4 pF, EF8 187-0103-005 RESISTOR 1.28 A. 1/4 W 10% CARBOU 10K A. 1/4 W 1 10K A. 1/4 K 1 10K A. 1/	LC 701 C 704, 73, 127 C 724, 713, 727 C 723 C 728 C 709 R 706 R 701, 708 R 705 C	4300133 4280079 4350161 4350146 4370276 4370073 4370073 4410270 4410270 4410247 4410247 4410298 4410247 4410098 4410215 4590022 4590022	22 2 2 9 9 18 17 19 15 14 13 12 11 10 9 8 7 6 5
	22 of CRL DTZ-22 DAF/20V PLODEKODZKAT DIAF WIODEFID OMP APALITOR 33 - ATC 17AH330J500P5 CAP, 1.3-5.4 pF, EFF 18T-0103-005 RESISTOR 1.8K A. 1/4 W 07 IK A. 1/4 W 07 ATA 2W 57 TRESISTOR 0.160. 2W, 57 CARBON, IRC FERROX CUBE VK-200-20/45 R.F. CMOKE MA.I. 3-2022	LC 701 C 704, 73, 727 C 714, 73, 727 C 72, 3 C 72, 4 R 70, 4 R 70, 70, 70, 70, 70, 70, 70, 70, 70, 70,	4300133 4280079 4350161 4350146 4370276 4370003 4410270 4410270 4410247 4410247 4410247 4410247 441025 4590022 4590022 4590022 4010567	22 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2
3711 <b>B</b> 112211 4 <b>B</b> 1	2	LC 701 C704, 73,127 C 724, 73,127 C 723 C 723 C 723 C 728 C 702 C 728 C 702 R 700 R 705 C 705	4300133 4250079 4350161 4350146 4370276 4370276 4370003 4410270 4410270 4410270 4410247 4410247 4410256 4590048 4000567 540048 4000567 540048 4000567 540048 4000567 540048 4000567 540048 4000567 540048 4000567 540048 540048 540048 540048 540048 540048 540048 540048 540048 540048 540048 540048 540048 540048 55004	22220991719151413121109879543
· · · · · · · · · · · · · · · · · · ·	22 = F CRL DTZ - 22 32 = F CRL DTZ - 22 3 = DAF/20V   MODEFIDDMP CAPALITOR 33= - ATC 17AH330J500F5 CAP, TDK MCV50A1H030 1-3=F CAP, 1-3-5.4 = F, 2#3 187-0103-005 RESISTOR 1.28 A. 1/4 W 107 (ARBON) 107 A. 1/4 (ARBON) 107	LC 701 C 704, 73, 127 C 724, 73, 127 C 723 C 723 C 728 C 704 R 705 R 706 R 705 R 705 C 7	4300133 4280079 4350161 4350146 4370276 4370276 4370003 4410270 4410270 4410270 4410247 4410247 4410298 4410098 4410098 4410298 4410298 4410098 4410298 4410298 4410298 4410298 4410298 4410298 4410298 4410298 4410298 4410298 44100098 4410098 4410098 4410098 4410098 4410098 4410098 4410098 4410098 4410098 4410098 4410098 4410098 4410098 4410098 4410098 4410098 4410098 4410098 4410098 440008 44008 44	
B     7     1     1       1     1     1     2     2     1     1       4     1     1     2     2     1     1     1	22 of CRL DIZ-22 	LC 701 C 704, 73, 727 C 72, 35, 710, 714 C 72, 3 C 72, 3 C 72, 8 C 70, 70 R 70, 70 C 70, 70 R 70, 70 C 70, 70 R 70, 70 C 70, 70	4300133 4280079 4350161 4350161 4350146 4370276 437007 437007 437007 437007 4410270 4410270 4410270 4410247 4410298 4410247 4410298 4410298 4410258 4410258 4410258 4410258 44102567 3470267 2110138	
	22 of CRL DTZ-22	LC 701 C 704, 73, 727 C 714, 73, 727 C 72, 3 C 72, 5 C 70, 70, 70, 5 C 70, 70, 70, 70, 5 C 70, 70, 70, 5 C 70, 70, 70, 70, 70, 70, 70, 70, 70, 70,	4300133 4250079 4350161 4350146 4370276 4370276 4370003 4410270 4410270 4410247 4410247 4410247 4410015 4590022 4590022 4590022 4010567 3470267 3470267 2110138 2090181	2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2
	22 - F CRL DT2-22 	LC 701 C704,73,727 C 723 C 723 C 723 C 723 C 728 C 704 R 709 R 706 R 705 C 705	4300133 4250079 4350161 4350146 4370276 4370276 4370003 4410270 4410270 4410247 4410247 4410247 4410095 4410055 45900484590048 4590048 45900484500048 459000	2 2 2 9 9 9 7 9 9 7 9 5 4 3 2 - TEM
	22 - F CRL DIZ-22 	LC 701 C 704, 73, 127 C 724, 73, 127 C 723 C 723 C 728 C 728 C 728 C 728 C 728 C 728 C 724 R 707 R 706 R 707 R 706 R 707 R 705 C 703 R 705 C 7	4300133 4280079 4350161 4350161 4350146 4370276 437003 4410270 4410270 4410247 4410247 4410298 4410247 4410298 4410258 441058 44100058 44100000000000000000000000000000000000	22 2 2 9 8 17 4 15 14 13 12 11 10 9 8 7 4 5 4 3 2 - ITEM
	22. # CRL DIZ-22	LC 701 C 704, 73, 727 C 714, 73, 727 C 723 C 723 C 723 C 723 C 723 C 723 C 723 C 724 R 704 R 709 R 704 R 709 R 704 R 709 R 704 R 709 R 705 C 705	4300133 4250079 4350161 4350146 4370276 4370276 4370003 4410270 4410247 4410098 4410247 4410098 4410015 4590048 4590048 4590022 4010567 3470267 3470267 2110138 2090181 5TOCK NOC	2 2 2 9 9 8 17 14 13 12 11 10 9 8 7 4 5 4 3 2 - TEM
	22 of CRL DIZ-22	LC 701 C 704, 73, 727 C 714, 73, 727 C 72, 3 C 72, 5 C 70, 5 C 70, 70, 5 C 70, 70, 70, 70, 70, 70, 70, 70, 70, 70,	4300133 4250079 4350161 4350146 4370276 4370276 4370003 4410270 4410270 4410247 4410298 4410247 4410098 4410015 4590022 4010567 3470267 3470267 3470267 2110138 2090181 5T0CK NO.	
	22 - F CRL DIZ-22 - DIAF/20V	LC 701 C704, 73, 727 C 723 C 723 C 723 C 723 C 728 C 704 R 709 R 706 R 705 C 705	4300133 4250079 4350161 4350146 4370276 4370276 4370003 4410270 4410270 4410270 4410270 4410247 4410295 4410247 4410295 4410095 45900484400048 459000	22 2 2 9 12 17 14 13 12 11 0 9 8 7 6 5 4 3 2 - - - - - - - - - - - - - - - - - -
	22. μ     CRL DIZ-22	LC 701 C 704, 73, 127 C 723 C 723 C 723 C 728 C 705 C	4300133 4280079 4350161 4350146 4370276 4370276 437003 4410270 4410270 4410270 4410247 4410247 4410298 4410247 4410298 440001567 33470267 8 4000567 3470267 8 4000567 3470267 8 4000567 3470267 8 4000567 3470267 8 4000567 3470267 8 4000567 3470267 8 4000567 3470267 8 4000567 3470267 8 4000567 4000000000000000000000000000000000000	22 2 0 9 8 17 3 15 4 13 12 11 10 9 8 7 6 5 4 3 8 TEM RECENT
	22 - F CRL DIZ-22 JOAF/20V - SPACE JOA	LC 701 C 704, 73, 727 C 723, 727, 729, 720, 720, 720, 720, 720, 720, 720, 720	4300133 4250079 4350161 4350146 4370276 4370003 4410270 4410270 4410247 4410247 4410295 4410295 4410295 4410295 4410295 4410295 4410295 4410295 4410295 4410295 4410247 4410295 4410295 4410247 4410295 4410247 4410295 4410247 4410295 4410247 4410295 4410247 4410295 4410247 4410247 4410247 4410295 4410247 4410447 441047	22 21 21 21 21 21 21 21 21 21 21 21 21 2

il

İ

E

l

Ľ

Z





.

NOTES:

I. UNLESS OTHERWISE SPECIFIED RESISTOR VALUES ARE IN OHMS, 1/4W, 10%. CAPACITOR VALUES ARE IN MICROFARADS.

2. P.C. BOARD SIASTSO. REV. - 06

3 COMPONENT LAYOUT 200265

4. LC701 HAS 22pf 5/16" LEAD.



.....





1

.

.

Ì

2

11

- UNLESS OTHERWISE SPECIFIED RESISTOR VALUES ARE IN OHMS,1/4W,10% CAPACITOR VALUES ARE IN MICROFARADS.
- 2 P.C. BOARD SIA5764
- 3 SCHEMATIC 9186955

SCOR. ULYT9 DIW ULYT9 DIW	SNA HOLE IN	DATE		¢	А.	SELEY S	ASSOCIATEL	B, INC. ICH PARK INIA 93017
WAS 01 2445	Arris 1			хм	COMI	PONEN	T LAYOUT	OR
NO E		ŝ	5	TOL	FRACT. ±	1/32XX	± .430, .XXX = .010,	∠ ± 1/2*
1000	dě.	8	-	DWN	FXY	25JAN 77	SCALE:	
a		2	3	CHK		0	2012609	CO
BC	A		2	ENG	esu	25 200 7	2042000	LP





I UNLESS OTHERWISE SPECIFIED RESISTOR VALUES ARE IN OHMS, 1/4 W, 10% CAPACITOR VALUES ARE IN MICROFANADS

#### 2 P.C. BOARD 5145764.

3 COMPONENT LAYOUT 20A2608.







1

L

ł

- 1

4

;\$

- I. UNLESS OTHERWISE SPECIFIED RESISTOR VALUES ARE IN OHMS 1/4 W 10 % CAPACITOR VALUES ARE IN MICROFARADS
- 2. P.C. BOARD 51A5808
- 3, SCHEMATIC 9187048

1	77 EXY	DATE		<b>(</b>	MOSELEY ASSOCIATES, INC. SANTA BARBARA RESEARCH PABK GOLETA, CALIFORNIA 33017
	100 05		ŕ		COMPONENT LAYOUT XMTR LOSS OF LOCK SHUT OFF.
		뵈	Ξł	TOL:	FRACT. ± 1/32, .XX ± .430, .XXX 1:.610,
		믦	Ē	DWN	L.I. IASEPTTT SCALE: FULL
+*	H	Ξl	3	СНК	FXY 27 SEP 77 20A 2C EO A
1	Į		8	ENG	20A 2050 A





I. UNLESS OTHERWISE SPECIFIED RESISTOR VALUES ARE IN OHMS 1/4 W 10% CAPACITOR VALUES ARE IN MICROFARADS 2. P. C BOARD SIA 5808 3. COMPONENT LAYOUT 20A26 50









									-
					the second se				



- I. UNLESS OTHERWISE SPECIFIED RESISTOR RESISTOR VALUES ARE IN OHMS 1/4 W 10 % CAPACITOR VALUES ARE IN MICROFARDS.
- 2. P. C. BOARD SIA 5817-02
- 3. SCHEMATIC STATIOS
- 4. USE JIG TO ASSEM 542562 PLATES TO P.C. BOARD 51A5817 BEFORE ANY COMPONENTS ARE MOUNTED.
- 5. SOLDER GND. END OF C301, CR 301 & R302 TO TOP SIDE GROUND PLANE.

UIT SIDE.	C. Botswith	TOT TO THE	ALMA ADDED A	010E NIN6	AS INDICATES		10 949. 78 FXX	DATE	1	<u>л ме</u>	SELEY S	A	OCIATES, I Reara Research Leta, California	NC. PARK 93017
ON CIAC	APR 12-9	BON SIDE	ALL CLOSE	PLATE SU	CALLOUTS	TA CON	ZEJUL			COM 950 N	IPONE	NT L	AYOUT RECEIVER.	
303	15	ALL ALL		0100	ADDED	10.1	SILE	TAP	DWN	L, I.	1/32, .XX 26MAY78	± .J.M.	FULL	* 1/2*
L	E2	EI	ā	100	U	8	4	NON	CHK ENG	FXY	26JUL 78	20	A2698	F

-



11

.

.

5

61

ł

- I. UNLESS OTHERWISE SPECIFIED RESISTOR VALUES ARE IN OHMS 1/4 W 10% CAPACITOR VALUES ARE IN MICROFARADS
- 2. P. C. BOARD 51A5817
- 3. COMPONENT LAYOUT 20A2698

Tor ProD	MOBELEY ABSOCIATES, INC. SANTA BARBARA RESEARCH PARK GOLETA, CALIFORNIA 2017
13.5.50	SCHEMATIC 950MHz PRE-AMP RECEIVER.
1 1 1 L	TOL: PRACT. ± 1/22, .XX ± .438, .XXX ± .416, < ± 1/2*
10 8 1	DWN L.I. IIMAY 78 SCALE NONE
24 2 3	CHK FXY 26JUL 78 OLA 7100 AM
4	ENG (cn STATIOS A4





- I. UNLESS OTHERWISE SPECIFIED RESISTOR VALUES ARE OHMS 1/4W 10 % CAPACITOR VALUES ARE IN MICROFARADS
- 2. P.C. BOARD 5145692-05
- 3. SCHEMATIC 9186873
- 4. Y501= FC-74,000 MHz (3040034) 16
- 5. INSTALL 1506 1507 1501 & 1509 1/16 OFF BRD.

D.T.V.	LENGTH NOT	AHER IOFF MFG 24JUN	16.5 Afrumer	AND CLETCO	S FEE F F F	MAJOR	DATE		<b>(</b>	<u>М</u>	)SELEY S	ASSOCIATES, I ANTA BARBARA RESEARCH GOLETA, CALIFORNIA	PARK 93817
465 TOP	N N ECO	LOCK WA C 5 03 244,119	NUTON I	Canal, Succession	CHANGE	N 10 10		i.		<u> </u>	MPON	ENT LAYOUT	
1957	12 3	000	5	<b>5</b> 7ª	5-		١Z	Ĩ	TOL:	FRACT. ±	1/22,	± .030, .XXX + .010, 🖌	= 1/2*
1000	28	AAD	2 á	1233	₹3	EG	E.	١ <u>ڊ</u>	DWN	L.I.	4AUG 78	SCALE: FULL	
X	¥	7	н	H	S	Ľ,	N.	MGM	CHK	É HATT	14 . 15	· 20A2560	KI

# 

.



I. UNLESS OTHERWISE SPECIFIED

RESISTOR VALUES ARE IN OHMS 1/4W 10 %

CAPACITOR VALUES ARE IN NICROFARADS,

2.P.C. BOARD 5145692-06

3. COMPONENT LAYOUT 20A2560



|--|--|--|--|--|--|--|--|--|--|--|--|--|--|--|--|--|--|--|



- I. UNLESS OTHERWISE SPECIFIED RESISTOR VALUES ARE IN OHMS, 1/4W, 10% CAPACITOR VALUES ARE IN MICROFARADS.
- 2. P.C. BOARD STAST21 REV. -
- 3. SCHEMATIC 9186914 REV. EØ
- A PUT #26 BUS WIRE THROUGH HOLES IN INDUCTOR AND SOLDER. HEAT FROM TOP OF BOARD ONLY. BE SURE SOLDER FLOWS THROUGH.
- 6. SEE ASSEMBLY INSTRUCTIONS FOR 20A2578.
- 7. FILL ALL UNUSED HOLES WITH SOLDER.
- 8. LENGTH OF PINS IN IC401 SHOULD BE 5/32"

4/6-4/9, 18.48	43-91-90 43-27 47-26 47-26	70 3	VED. 679 D.T.W.	5. PC01287 D.T.W.	ENGTH NOTE.	The Free	NAME ALAND	CHEI WAS	42/14652277 647 L.I.	DATE		¢	A.we	SELEY	ABBOCIATES, IN NITA BARBARA RESEARCH I GOLETA, CALIFORNIA	<b>JC.</b> PARK 19917
5-409, 2,424 M	-01-9561	4- 40 X 3/16	E 5 REMO	E & A00EE	NTA ECO IZ	VENNES!	0763 4 67. C	WA3 4785	240618M		T.	RC	COM		NT LAYOUT	. F.
830		S I	TO DE	101	생품		133 9 5	1410	404	1012	Ē	DWN	GM	5-17-76	SCALE:	
N2	6	FZ	F	Ξ	ш	٥	U	θ	4	<b>REV</b>		CHK ENG	PON	10 JUN 76	20A2578	H2







- I. UNLESS OTHERWISE SPECIFIED RESISTOR VALUES ARE IN OHMS, 1/4 W, 10% CAPACITOR VALUES ARE IN MICROFARADS
- 2. COMPONENT LAYOUT 20A2578 REV. K
- 3. PC BOARD 51A5721 REV. 11, -21













# NOTES: I. UNLESS OTHERWISE SPECIFIED ALL RESISTOR VALUES ARE IN OHMS, 1/4 W, 10 % AND CAPACITOR VALUES ARE IN MICROFARADS. 2. COMPONENT LAYOUT 2082762 3. P.C. BOARD SIBSB93

ł

17 C22 ES FROM 3672 S. J.	. 2 K	LIWAS 22	180 pc	35 26 4 35 2 4 4 9 2 4 9 2 4 0	5 + 12. BBD D.W.	BATE		MOBELEY ASSOCIATES, INC. SANTA SASSAGA SESEARCH PARE COLETA, CALIFORNIA SENT
ZI VALU	WAS 2 3520,5	AS 33PF.	21 LIJ 22 CE 21	VAS	S 78 SFE			SCHEMATIC PCL-505 74-10.7 CONVERTER
REM CHAS	RIV	FCOM	CHE.	121	EC015	EVISIONS	GMT. AP	TOL: FRACT. 2 1722. IN 2 430. JEE 2 410. 2 2 1/2 DUTN (A. J. D. O. (17) SCALE: CHE 201 (1/27.79) QI P.7194 F
L	W	D	S	89	A		3	EH4 3026 11-13-119



Ŀ

- 1. UNLESS OTHERWISE SPECIFIED RESISTOR VALUES ARE IN OHMS, 1/4W, 10%. CAPACITOR VALUES ARE IN MICROFARADS.
- 2. P.C. BOARD SIAS681
- 3. SCHEMATIC 918 6879
- 4. △ SOLDER RESISTOR LEADS ON BOTH SIDES OF P.C. BOARD.
- 5. \* FREQUENCY DEPENDENT PARTS

	C813	
ITEM I	4.7/35	MONAURAL

ITEM 2 47/20 COMPOSITE

APD CAN	IND VIEW	W. ECO	001 & 2 WERE	CRARE WIGHT WAY X.Y.	E RACH WITH JUMPER	IN REV OF MOVE BAIR	BEV OF MONT PARTS TO	TVERET NOTES	VICTOR AND CARLEN AND AND CARD	WAS 27 K ECO 765	074 6 10665	DATE		MOSELEY ASSOCIATES, INC. SANTA BARBARA REBEARCH PARK GOLETA, CALIFORNIA SINT						
WAS 47H	TAB NOTE	11 TH	AS IOWH.CR								WAS 18			RCVI	COM	PONEN	IT L Bam	AYOUT		
926	9	SI	02 M	101	PLAC	DAL DO	100	TA			208	NO	AP.	TOL	FRACT. ±	1/22, .3.8	± .030,	.XXX = .010,	∠±1	1/20
αŝ	AD.	U g	32	54	2.	14	2P	25	20	1	Ka	N.	TH	CHK	FXY	16 SEP 75	20	125/9	1.	
2	×	7	H	I	9	L	ш	0	C	8	1		i	ENG	Ussal.	9 16 75	20	ACJ40	L	2


--



- I. UNLESS OTHERWISE SPECIFIED RESISTOR VALUES ARE IN OHMS, 1/4W, 10%. CAPACITOR VALUES ARE IN MICROFARADS.
- 2. P.C. BOARD SIASGEI
- 3 COMPONENT LAYOUT 20A 2548
- 4 PC BOARD SHOWN IN DASHED LINES.
- 5. NS DENOTES NO SIGNAL (DC VOLTAGE) S MAX. SIGNAL (DC VOLTAGE)
- 6. \* FREQUENCY DEPENDENT PARTS
  - \_\_\_\_\_C813\_\_\_\_
- ITEM I 4.7/35 PCL-505 (MONAURAL)
- ITEM 2 47/20 ... ... (COMPOSITE)

 
 1
 R836 wrs 334
 ADD C032

 1
 CC2072
 3444483
 CA44

 1
 CC37
 3444483
 CA44

 1
 C073
 WS1
 344463
 CA44

 1
 C073
 WS1
 344463
 CA44

 1
 C073
 WS1
 3444
 CA44

 1
 C073
 WS1
 3444
 CA44

 1
 C074
 F080182
 WE16
 F044
 <td MOSELEY ASSOCIATES, INC. SANTA BARBARA RESEARCH PM - GOLETA, CALIFORNIA 10077 SCHEMATIC FM DEMOD & MTR AMP APPR TOL: FRACT. ± 1/32. .XX ± 430. .XXX - .610. REVISIONS < ± 1/₽ DWN JHM ZA DEC 75 SCALE: NONE MGMT. CHR 9186879 DØ V B V ENG CP2998 12 Jan 76



- UNLESS OTHERWISE SPECIFIED, ALL RESISTOR VALUES ARE IN OHMS, 1/4, 10%. CAPACITOR VALUES ARE IN MICROFARADS. i.
- P.C. BOARD 51A 5625 2.
- SCHEMATIC 918 6734 3.

141 ADDED	TO MATCH	SISTORS	02. 140 To ToP 176 3AM	REMOVE R939.	CR POS.	CHA RU.	7 P4 L.I.	15 22 X 500		- 748 To 910	PIENTATION	DATE		1	λ <u>m</u>	D <b>BELEY</b> Si	A880	DCIATES	INC.
A 8 9163	MOVED	E TRAV	ARD REV PHOL AND T	00 . R940		HAC 12 10 19	0 25 0L	ALL COL		ME 1C 90	ECTED KF			RCV	CO R MU	MPONE	ENT L	AYOUT	IFIER
	0.0	39			1	1		0	ER		N N	2	5	TOL	FRACT. ±	: 1/32, .XX	2.838, .3	XXX ± .816, .	< ± 1/1*
F 8	211	E of	100	89	. 2	23		X	80		60	18	2	DWN	KF	II/NOV/74	SCALE: F	ULL	
0.	-	<0	. a .	200	2 -	20		0			- 0	5		CHK	L.I.	19NOV 74	204	2500	NI.
X	5	7	H	I	C	L	ω	0	U	B	A	ġ.	ž	EHR	SLM	1914174	204	2500	NI.



ITEM 1-ADD C915 (MONAURAL)





- I UNLESS OTHERWISE SPECIFIED RESISTOR VALUE ARE IN OHMS, 1/4 W, 10 % CAPACITOR VALUES ARE IN MICROFARADS.
- 2 P.C. BOARD SIAS625
- 3 COMPONENT LAYOUT 20A 2500
- 4 SENSITIVITY PRESET IOO OHMS NOMINAL
- 6 NS DENOTES NO SIGNAL ( DC VOLTAGE )
- 6 S DENOTES MAX SIGNAL ( DC VOLTAGE )

C 915
4.7/35
JUMPER







ų

ţi

6

- 61

ľ

4.

j

) | ) | )

1

i

j

2

- I. UNLESS OTHERWISE SPECIFIED ALL RESISTOR VALUES ARE
- IN OHMS 1/4, 10% CAPACITOR VALUES ARE IN MICROFARADS.
- 2. AC. SOARD SIC 3904-10
- 3. SCHEMATIC 9187211
- A. SILK SCREEN WITH WHITE INK COMPONENT SIDE SIC 5904-40
- S. SELDER MASK CIRCUIT SIDE SIC5904-30.

41	TRANSISTOR 2113520	G908	3012.17	1
40	PEEISTOR 4.7 M	1928 R940	1035	2
39	DIODE SUV. IW	17 904	3600186	l.
36	DIDDE IN4154	CR403	3600145	1
37	300 3001C	CR902	3610003	1
36	RESISTOR 10	R908	4410023	1
35	RESISTOR 20 K	2007 1277	4410536	2
34	RESISTOR 100	R 928,909	4410122	2
33	REALSTOR 41K		4410452	3
32	RESISTOR 1500	R412 R414	4410262	z.
31	TESISTOR 22X	82PR 20PR	4410411	2
30	RESISTOR IOK	939 406 921	4410379	6
62	RESISTOR IK	911 917 927 929 929 919	4410247	
28	RESISTOR 400	R 132	4410353	I.
87	RESISTOR 4700	716 923 34	4410338	4
20	RESISTOR 200	RAID	4410/65	1
25	RESISTOR 110	R937	4410577	1
24	RESISTOR 10K	Siek.	4410494	ſ
23	RESISTOR 470	R924	4430062	1
22				

21	CAPALITOR 4	C 903	4250178-	1
20	CAPACITOR 12	C 909	08-5/8	1
19	CAPACITOR 4%	C.46	1280053	1
1	CAPACITOR 20	905 101 905	4290079	7
17	CAPACITOR #35	C915	4280053	l
16	CAPACITOR 23	C412, C413	4280046	2
15	COPICITOR 2PF	6906	4210027	T
14				
13	16 318	U 901	3730173	1
12	16 741	U402.0403	3660008	г
11	TRANS. 284056	0 -02	3630209	1
10	TRANS. 203619	0 701	3630159	1
9	TRANS. 2N3053	6 403	3630035	1
8	TRANS. 212924	0 704, 905, 906, 907	3630027	4
٦	RELAY AZ-SHO-	K901	3270113	1
6	POT LOOK	R936 R933	4630513	2
5	POT IOK	R913 8901	4630281	3
4	I.C. SOCKET	EU-IU	3250016	з
з	SOCKET TRANSIETOR	9901,0402	3250230	7
2	TERMINALS	USECO 2520 84/5	REF	-
1	P.C. BOARD	5165904-10	3472255	1
TEM	DESCRIPTION	REF. DES.	STOCK NO.	עדם
	An-10 An	Д		
	101 101 RC	COMPONENT	ETERING A	MP.
	RE RE	1- 3. 1 5 EC STAL	2.2 .	= 1/4*
		20	C2777	AL





I UNLESS OTHERWISE SPECIFIED RESISTOR VALUE ARE IN OHMIS, 1/4 W, 10% CAPACITOR VALUES ARE IN MICROFARADS.

- 2 P.C. BOARD 51C5904
- 3 COMPONENT LAYOUT 20C 2777
- 4 SENSITIVITY PRESET 100 OHMS NOMINAL
- 5. N.S. DENOTES NO SIGNAL ( DC VOLTAGE )
- 6. S DENOTES MAX SIGNAL ( DC VOLTAGE )

	• · · · · · · · · · · · · · · · · · · ·	
TEN	IMONO	4.7/35
ITEN.	2 COMPOS	ITE JUMPER

PROULTION. 210 + 0	BATE		<b>(</b>	<u>М</u>	DBELEY	ABBO	CIATES,	INC. PARE A part				
0 1 4 0 4 0 4 0 4 0 4 0 4 0 4 0 4 0 4 0		gi l	RCVI	SCHEMATIC								
1253	E		TOL:	FRACT. 2	1/38, .88	2 030, .22	E					
1268	8	2	<b>B</b> WN			SCALE:	1					
152	ΞÌ	a	CHR	172	1-20-2-1	(ILD -	12	1.1				
N N	Ē	2	-	GAS	2-11 40	1111	1211	1 H M				





### NOTES :

Į

E

8

- I UNLESS OTHEWISE SPECIFIED RESISTOR VALUES ARE IN OHMS, 1/4W, 10% CAPACITOR VALUES ARE IN MICROFARADS.
- 2 P.C. BOARD SIAS627, REV.
- 3 SCHEMATIC 91 8 6787
- 4 +- DENOTES 2C1400-( ) INDUCTOR

NRTT I'M	WE CITAT F.X.Y.	10 745 F.X.Y.	75 FXV	ADD R1142	DATE		K	<b>М</b>	J <b>BRLEY</b> 54	ABSOCIATES, I ANTA BARGARA RESEARCH GOLETA, CALIFORNIA	NC. PARK 13917					
NOE EC	S BIVE WE	4 NOV 1	AL 540	11 SAM			COMPONENT LAYOUT									
	0		-	NY	ž	15	TOL	FRACT. 1	: 1/32, .XX	± .030, .XXX ± .010, ∠	± 1/2*					
	E		9		8	2	DWN	KF	9 DEC 74	SCALE: FULL						
-	40	-			Ē	N	CHK	FXY	9 DEC 74	2042496						
L m	A	S	8	A	=	2	ENG	SLM	10 DEC 74	2042490	1 - 1					



x.



I UNLESS OTHEWISE SPECIFIED ALL RESISTOR VALUES ARE IN OHMS, 1/4 W, 10 % AND CAPACITOR VALUES ARE IN MICROFARADS

- 2 PC BOARD 51A5627
- 3 COMPONENT LAYOUT 20A2496

4 T- DENOTES 2C1400 - ( ) INDUCTOR

110 673	H	CAMOBELEY ABBOCIATES, ING SANTA BARBARA RESEASCH PAN SOLTA, CALIFORNIA DEST									
45 IDK		SCHEMATIC PGM & MUX OUTPUT AMPS (MONO RCVR)									
23		TOL: PRACT. 2 1/20, 318 2 420, 318 2 410, 2 2 1	F								
91		BWH JHM IT JAN 76 BEALE:	-								
+==	E	CHE 9196797									
<		and Millianth SIDOIDI	•								



a



1

1

-1

.

j,

Į

L UNLESS OTHERWISE SPECIFIED RESISTOR VALUES ARE IN OHMS, 1/4 W, 10 %. CAPACITOR VALUES ARE IN MICROFARADS.

Z. PC BOARDS SIA 5885

. SCHEMATIC SIA 6721

CI09	1 77 FXY	CRIOA	MUL HE WAL	TS GM	1 26 0CT 75	18-75 GM	IG RICH. RIOT	DATE		¢	λ <u>wc</u>	SELEY	ASSOCIATE	ARCH PARK
R1094	AS 100K.	C CRIOS 8		RK REV. 0	AHL BOIR	WAS 120	RIOG C			RCV	( R.PWR	COMPO	NENT LAYOU	T BOARD
문습	*	2 m		N.C.	8		5	ž	5	TOL	FRACT. ±	1/32, .XX	± .630, .XXX ± .610,	∠ ± 1/2°
0.2	0	N S	3	R.O	1 4	20	35	8	E.	OWN	JHW	5-6-74	SCALE:	
	-	a a		-				Ξ	B	CHK	FXY	7 NOV 74	004045	
I	6		Ψ	0	0	8	A	æ	2	ENG	5411	\$1. , "4	1 20A 245I	





HOTES:

ł

1

1

1

•

.,

Į

- UNLESS OTHERWISE SPECIFIES RESISTOR VALUES ARE IN OHMS, 1/4 W, 10 %. CAMMITOR VALUES ARE IN BICROFARADS. ١.

- COMPONENT LAYOUT 20A2481
- MATS OUTSIDE P.C. SOARD ARE SHOWN FOR Reference only. See Receiver Schematic.

	2105 1947 L.I.	EC0 904	È CRIO4	DATE		SANTA BARGARA RESEARCH	PARE
E 2700 /	71 500	AS 100K	E CR103	SCHEMATIC RCVR. PWR. SUPPLY REGULATOR BOARI	)		
	E D	5	NO C	2	4	TOL: FRACT. ± 1/32, .XX ± .030, .XXX ± .010, <	± 1/2*
5 Y	00.	110	10	3	12	OWN JHM IZ W 16 SCALE:	
-		-	2 -	E	M	СНК 9146721	D
0	C	8	A	a.	2	ENG STAOTZI	14







I. P.C. BOARDS ARE SHOWN IN DASH LINES 91 PREFIX IS SCHEMATIC DWG. NUMBER. 20 PREFIX IS MODULE NUMBER. SE PREFIX IS P.C. BOARD NUMBER

	SCHE	MATIC	
PCL-505	960 MHz R	ECEIVER	MONAURAL

the second s																			E
--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	---

PARENT ITEM NO 9050337

----

1

(

SPARE PTS PCL-5056C 890-960MHZSP-30B F DATE 4/22/81 PAGE 1

MOSELEY ASSOCIATES INC III CASTILIAN DRIVE GOLETA CA 93117 805 968-9621

....

	COMPONENT ITEM NO.	STOCK LOCA	MANUFACTURER Part Number	COMPONENT DESCRIPTION	QUANTITY PER	UM	UNIT Sales price	TOTAL Sales price
-	3390150	2122	MV-5254	LED GREEN	1	EA	1.37	1.37
	3600145	2721	L N4 L 54	DID 1N4154 25V 4NS SI D035	2	EA	•16	- 32
	3600160	2744	1847314	DIO ZIN4731A 4.3V IN 5% ATAY	1	EĄ	1.26	1 - 26
	3600178	2744	1N4733A	DIO 21847334 5.18 18 5% ALAY	1	EA	1.09	1.09
	3600186	2744	1 N4 7 34 A	DIO 21847348 5.68 IN 58 ALAY	l	EA	1.26	1.26
	3600236	2744	1N4745A	DIO 21N4745A 16V 1W 5% ALAY	1	EA	•42	• 4 2
	3610003	2721	1002	DIO 1002 200V 14 SI 0039	4	EA	• 39	1.56
	3610045	2744	5082-2835	DIO 5982-2835 FAST	1	EA	2.24	2.24
	3610094	2721	MDA-980-2	DIO MDA-980-2 100V BRIDGE 12A	1	EA	7-11	7.11
	3610136	2744	HV-840	DIO VHV-840 030V 90-100PF D07	1	EA	3.33	3.33
	3630027	2721	2N2924LFS	XT NS2N2924EFS.2W160H025V.LA7P	2	EA	• 54	1.08
	3630035	2721	2N3053	XT NP2N3053 05W100N080V.7A	1	EA	1.47	1.47
	3630043	2721	2N3054	XT NP2N3054 25W030K090V02A	1	EA	2.80	2.80
	3630076	2744	2N3563	XT NS2N3563 .2W600M030V50M2P	1	EA	•49	• 4 9
	3630092	2744	2N3640	XT PS2N3640 .2W500M012V80M3.5P	1	EA	2.28	2.28
	3630159	2744	2N3819	XT NF2N3819 .4W 025V20M	1	EA	. 74	.74
	3630167	2744	2N3820	XT PF2N3820 .4W 020V15M	1	EA	1.51	1.51
	3630191	2744	2N4Q37	XT PP2N4037 01W060M060V01A	1	EA	1.54	1.54
	3630209	2744	2N4058	XT PS2N4058 .4W D30V30M	1	EA	•46	• 46
	3630241	2744	2N4428	X[ NP2N4428 3.5W750M055V.42A	· 1	EA	4.94	4.94
	3630308	2721	2N5179	XT NS2N5179 .2W900M020V50M1P	1	EA	2 • 38	2 • 38
	3630316	2144	2N5293	XT NP2N5293 36W800K080V04A	ı	EA	1.73	1.73



PARENT ITEM NO 9050337

t

(

(

( .

1

 SPARE PTS PCL-5056C 890-960HHZSP-38B
 F
 DATE 4/22/81
 PAGE 2

### MOSELEY ASSOCIATES INC 111 CASTILIAN DRIVE GOLETA CA 93117 805 968-9621

----

~

COMPONENT ITEM NO.	STOCK LOCA	MANUFACTURER Part Number	COMPONENT DESCRIPTION	QUANTITY PER	UM	UNIT Sales price	TOTAL SALES PRICE
3630399	2743	3N140	XT NF3N140 .4W 020V50M	1	EA	4.17	4-17
3640018	2744	A-400	XT NSA400 .2W0056015V25M	1	EA	6.62	6.62
3640109	2744	D1-12B	XT NPD1-128 5.84866M036V.25A	1	EA	21.18	21.18
3640133	2744	DM5-12B	XT NPDH5-128 298 036V02A	1	EA	49.70	49.10
3640141	2744	DM10-12B	XT NPDM10-128 50W 036V04A	1	EA	71.05	71.05
3640182	2713	MJ-2955	XT PPHJ2955 115W2.5M060V15A	L	EA	2.52	2.52
3650116	2743	MC1723CL	RGLTR TYPE 1723 VARV .15A 632	2	EA	2.66	5.32
3660008	2812	SN72741P	IC UA741P OPAMP GEN COMP	1	EA	.83	.83
3660024	2743	SN72748P	IC UA748P OPAMP UNCOMP	1	EA	1.19	1.19
3660297	2743	SN7486N	IC SN7486N QU 21 EXCL OR	1	EA	1.02	1.02
3680170	2713	SCL 4020AE	IC SCL4020AE 14 STAGE BIN CT	, <b>L</b>	EA	3.50	3.50
3730173	2743	LM-318N	IC LM318N OPAMP HISPEED	L	EA	6 - 48	6 - 48
3730199	2743	LM-324N	IC LM324N OPAMP SNGL SUPL	1	EA	1.68	1.68
3730322	2743	MC1350P	IC MC1350P OPAMP	1	EA	2.63	2.63
3730348	2743	MC1355P	IC MC1355P AMP FH/1F	L	EA	3.85	3.85
3730389	2743	MC1590G	IC MC1590G AMP VIDED	1	EA	16.28	16.28

TOTAL PRICE 239.40 \$

.

.

PARENT LTEM NU 9050949

-----

(

(

		0C1-505 00	0_040 50	. 189 1	c	DATE	( /22/8)	DACE	
	UFI 3/F AII	FLL-303 07	0-200 36-	- 300 1	r	UATE	4/22/01	PAGE	
ELEY ASSOCIATES INC									
CASTILIAN DRIVE									

MOSELEY ASSOCIATES IN 111 CASTILIAN DRIVE GOLETA CA 93117 805 968-9621

-

-	COMPONENT ITEM NO.	S TOCK L OC A	MANUFACTURER PART NUMBER	COMPONENT DESCRIPTION	QUANTITY PER	UM	UNIT Sales price	TOTAL Sales price
	3270113	2721	AZ-530-09-2	RELAY HIN PC 2000HH 12V NOH	i	EA	6.41	6.41
	3370228	2735	MDL 1/4	FUSE	5	EA	1.53	7.65
	3370269	2735	MUL 1 1/2	FUSE	5	EA	1.79	8.95
	4090007	2024	A-20	XFMR	1	EA	53.74	53.74
	4090015	2024	SAT-109	XFMR	L	EA	43.34	43.34
	4090254	2022	3-10798	XFMR 8-P-698	1	EA	51.31	51.31
	4090312	2024	3-1086	XFMR	ı	EA	18.48	18.48
	4260204	2731	TVA-1163	CAP HI-TEMP 1000/16V	1	EA	2.79	2.79
	4210039	2723	36D272G025AA2A	CAP PWR LYTIC 2700/25V	1	EA	5.81	5.81
	4270088	2723	36DX902G025AB2A	CAP PWR LYTIC 9000/25V	1	EA	7.98	7.98
	9100033	2214	21A2493 B	XTAL OVEN MOD XMTR PCL-505	1	EA	35.00	35.00

TOTAL PRICE 241.46 \*

# []. k **[**]} ], Įį

H

PARENT ITEM NO 9051426

1

1

ŧ

(

(

.

OPT S/P KIT PCL-505C 890-960 SP-38B F \_ DATE 4/22/81 PAGE L

MOSELEY ASSOCIATES INC 111 CASTILIAN DRIVE GOLETA CA 93117 805 968-9621

COMPUNENT ITEM NO.	S TOCK L ÚC A	MANUFACTURER PART NUMBER	COMPONENT DESCRIPTION	QUANTITY PER	UM	UNIT Sales price	TUTAL Sales price
3270113	2721	AZ-530-09-2	RELAY MIN PC 2000HM 12V NOM	1	EA	6.41	6+41
3370228	2735	MDL 1/4 .	FUSE	5	EA	1.53	7.65
3370269	2735	MDL 1 1/2	FUSE	5	EA	1.79	8 - 95
4090254	2022	3-1079B	XFMR 8-P-698	L	EA	51.31	51.31
4070312	2024	3-1086	XFMR .	L	EA	18.48	18.48
4260204	2731	TVA-1163	CAP HI-TEMP 1000/16V	1	EA	2.79	2.19
4270039	2723	360272G025AA2A	CAP PWR LYTIC 2700/25V	i	EA	5.81	5.81
4270088	2723	36DX902G025AB2A	CAP PWR LYTIC 9000/25V	1	EA	7.98	7.98
9100033	2214	21A2493 B	XTAL OVEN MOD XMTR PCL-505	1	EA	35.00	35.00

.

TOTAL PRICE 144.38 \*

1

11

H

Ì

PARENT	ITEM NO	9051228	OPT	CRYS	9 PCL-5056C @890-960MHZSP-38B	F	DATE	4/22/81	PAGE	1
MOSE( 111 ( Gole 805	LEY ASSOC CASTILIAN TA CA 968-9621	IATES INC DRIVE 93117	,							
COMPI I TEM	ONENT NO.	S TOCK L OC A	MANUFACTURER Part Number		COMPONENT DESCRIPTION	QUANTITY PER	UM	UNIT Sales price	TUTAL Sales	PRICE
3340	106	2734	30A0042	B	XTAL 63.3 MHZ PCL-505/PCL-101	1	EA	37.50	3	7.50
3340	478	9600	30A0034	8	XTAL RX 890-960 MHZ PCL505/101	1	EA	37.50	3	7.50

XTAL TX 947-952 MHZ PCL-505

.

(

( .

3340486

9600

30A0035

TOTAL PRICE 112.50 \*

37.50

.

EA

1

37.50

# li 11

## MOSELEY ASSOCIATES, INC.

# FINAL TEST DATA

MODEL PCL-505

Date <u>17 Aug. 19</u> Order <u># 3392</u> Technician <u>Conrad</u>	983	Customer Tx Serial Rx Serial Frequency	KHYX # 39225 # 40699 950,125 MHz
Transmitter Meter	Readings	Receiver Me	ter Readings
Program MPX Chan. 1 @ 26 kHz Chan. 2 @ 67 kHz AFC FRD PWR <u>6.0</u> Watts RFL PWR +VDC Reference Oscillator	0 dB top 10 bottom 15 bottom 15 bottom 0 top 0 bottom 12.5 bottom 14.5 bottom	+VDC Signal (no input) Program @ 100% mod MPX 26 kHz 67 kHz Level for 45 dB SNR:	$\frac{12.5}{\text{bottom}}$ $\frac{12.5}{\text{bottom}}$ $\frac{1.9}{\text{dB}}$ $\frac{10}{10}$ $\frac{10}{\text{bottom}}$ $\frac{15.9}{\text{dBm}}$
H.F. Divider I.P.A. Drive Final Current 2 amp max Power Supply to be set usin	<u>13.0</u> bottom <u>20.5</u> bottom <u>11.5</u> bottom g a DVM	FMO MULT-DRIV FINAL AMP	<u>25</u> 15 MW min <u>180</u> 120 MW min <u>6.0</u> 5 W min
Transmitter Receiver	<u>12.5</u> VDC <u>12.5</u> VDC	Receiver Signal Met	ter Calibration
PCL-505 System Perf	ormance	Microvolts	
Freq. (Hz) Response	Distortion (%)	5	1.8
30 <u>4</u> dB	21	10	1.9
50 <u>4</u> dB	.13	20	2.5
400 <u>0</u> dB	.07	50	7.0
1,000 <u>0</u> ref	.05	100	9.1
5,000dB	.07	200	11.0
10,000 <u>+ .3</u> dB	.06	500	13.0
15,000 <u>0</u> dB	.08	1,000	14.0
System Noise Ultimate SNR:74	dB	1,500	14.0

SNR: <u><u>82</u> dB with noise reduction circuit active Level for 60 dB SNR: <u>-82</u> dBm Squelch set between 15-20 20  $\mu V$ </u>

These readings were noted during final electrical test of the equipment and are intended for reference purposes. Readings may vary with component replacement or aging, adjustment, RF terminations, equipment installation, or path conditions.

Rev. 12 May 1983 ph

