Suppose you wanted an Automatic RLC Bridge that could measure up to 2 M\(\Omega\) resistance, 2000 H inductance, and 0.2 F capacitance, plus equivalent series resistance and leakage current. A bridge with 5-digit resolution for reactance and resistive readouts, automatic decimal point and units of measurement; a 20-measurements-per-second capability; 120-Hz and 1-kHz test frequencies; 5-terminal connections to preserve a basic 0.1% accuracy; a built-in 0 to 3-V bias or external bias to 600 V; optional remote programmability and data output.

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General Radio

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You can load up on X-Y recorders.

The world's first plug-in X-Y recorders let you buy modules, not mainframes, when you want to change applications. So you can order a single workhorse instrument to handle any analog and almost any digital job you have in mind. With the best dynamic performance on the market.

There are eight plug-ins for Hewlett-Packard's 7034A and 7004B X-Y's. You can use two plug-ins per axis. Besides taking care of all the routine tasks, they'll let you plot X-T, Y-T, single channel and discrete analog data. Extract signals superimposed on steady-state DC. Record AC from 5 Hz to 100 kHz. Plot two channels independently. And handle high-speed point plotting, too. You can specify the capability you want now and add to it later.

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The 8½" x 11" 7034A costs just $1295 and the 11" x 17" 7004B is only $100 more. Modules start at $25. To lighten your load in X-Y recording, call your local HP field engineer. Or write to Hewlett-Packard, Palo Alto, California 94304; Europe: 1217 Meyrin-Geneva, Switzerland.

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How to improve your test equipment without blowing your budget.

Simply use Hewlett-Packard's new family of high-performance, wideband general-purpose power amplifiers and preamps. These low cost RF amplifiers improve the sensitivity of your scopes, spectrum analyzers, counters, network analyzers — anywhere you need low-noise, high-gain amplification. These amplifiers are the result of HP's hybrid thin-film microcircuit technology.

The table below gives frequency ranges, prices and performance of the six basic configurations. Dual channel versions of the preamps can also be supplied to improve the performance of 2-channel instrumentation.

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A call to your HP field engineer will bring you the details of how these amplifiers can help enhance the test equipment you're using now. Or write to Hewlett-Packard, Palo Alto, California 94304; Europe: 1217 Meyrin-Geneva, Switzerland.

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HEWLETT-PACKARD 8447 SERIES LAB AMPLIFIERS

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<tr>
<th></th>
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<tbody>
<tr>
<td>Frequency Range</td>
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<td>0.4-1.3 GHz</td>
<td>0.1-300 MHz</td>
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<td>±1 dB</td>
<td>±1 dB</td>
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<td>±1.5 dB</td>
<td>±3 dB</td>
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<td>Noise Figure</td>
<td>&lt;5 dB</td>
<td>&lt;5 dB</td>
<td>&lt;10 dB</td>
<td>&lt;8 dB</td>
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<tr>
<td>Output Power @ 1 dB Gain Compression</td>
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<td>$550</td>
<td>$600</td>
<td>$450</td>
<td>$700</td>
<td>$800</td>
<td>$1225</td>
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</table>

04021
Features

Probing the News
85 Government: Dean Burch: FCC's pragmatic boss
89 Consumer electronics: Stakes are high in home video game
91 Military electronics: Aerospace Corp.'s road widens
93 Instrumentation: Skyjackings spur detection work

Electronics Review
37 Manufacturing: Fairchild line churns out transistors
37 Industrial electronics: NCR's point-of-sale gear
38 Communications: OTP cuts into FCC's preserve
38 Consumer electronics: CBS promises better color prints
39 Memories: Navy looking at Ampex thin film
39 Radar: CAS offered for ships
40 Companies: RCA seeks place in IC market
41 Computers: The Fed moves to eliminate the float
41 Government: Law and order budget to double
42 Government: Can fingerprints be sent by satellite?
42 Military electronics: Crossbow tests miss-distance indicator
44 Electro-optics: Image intensifier with gyroscope

Electronics International
127 Japan: Ceramic filters that operate at 30-200 MHz
128 Great Britain: Implanting ions at production-line rates
129 Japan: Glass fiber laser; pulses in the avalanche mode
130 Switzerland: Magnetic properties

New Products
97 In the spotlight
97 NCR shifts to cores in Century 300
100 Minicomputer controls tester
105 Instruments review
105 IR probe for hard-to-reach spots
106 Industrial recorder from Norway
109 Data handling review
109 Plated wire memory goes to market
113 Packaging and production review
113 IC connectors move in from edge
114 Ellipsometer gives quick reading
117 Semiconductor review
117 Darlingtones have low V(sat)
118 IC amp fits any sound system

Articles

Amorphous semiconductors
56 Nonvolatile and reprogramable, the read-mostly memory is here
part 1 Integrated arrays combine amorphous and crystalline technologies; new memories could help realize the promise of microprograming
R.G. Neale and D.L. Nelson
Energy Conversion Devices Inc.
Gordon E. Moore, Intel Corp.

Amorphous semiconductors
61 Theory gives shape to amorphous materials
part 2 Before conventional semiconductors can be challenged, problem of how amorphous devices function must be solved; new CFO model helps explain behavior
David Adler
Massachusetts Institute of Technology

Amorphous semiconductors
74 Industry's verdict still a long way from unanimous
part 3 Arrayed against those few companies and research labs that think the new technology will pay off are many more that don't; still others are fence-sitting

Circuit design
78 Designer's casebook
- With some discrete aid
IC op amp swings 100 V
- Feedback limits amplifier better than zeners can
- Monitor teams spare gates and solid state lamps
- Op amps find values of buried resistors

82 Audio noise: why settle for more?
Optimizing transistor's collector current yields noise figure far below specified minimum, and it's easily calculated

Departments
4 About this issue
6 Readers Comments
14 Who's Who in electronics
20 Meetings
33 Electronics Newsletter
125 International Newsletter

PB-1 Personal Business
51 Washington Newsletter
120 New Books
121 Technical Abstracts
122 New Literature
Amorphous semiconductor research, like the materials themselves, looks somewhat shapeless and unstructured, what with the variety of research projects going on. One thing is crystalizing, though: amorphous semiconductor materials will surely have a place in future electronic products. Electronics' editors decided to sort out the status and potential of the work; hence the comprehensive three-part presentation starting with Ovonics devices on page 56.

Since their splash in the press about two years ago, the thin-film Ovonics devices promoted by Energy Conversion Devices Inc. have been searching for the mantle of acceptability. Now, through a connection with Intel Corp., Ovonics devices have earned a considerable degree of respectability. Contributing to the article on the first Ovonics product, a 256-bit read-mostly memory, is Gordon E. Moore, Intel vice president and director. His coauthors are Ronald G. Neale, operations vice president at ECD and formerly with England's Mulbard Radio Valve Co. and the Electronic Machine Co., and David L. Nelson, ECD's engineering director and formerly with the Bendix Corp.'s Research Laboratories.

To round out the picture and give an overview of other work, David Adler, associate professor in the Department of Electrical Engineering at MIT, analyzes the somewhat anomalous behavior of amorphous semiconductors (page 61). Then for on-the-spot reporting (page 74), the magazine's field editors in the U. S., Europe and Japan, plus McGraw-Hill World News men, were sent out digging.

Washington staffer Lois Vermillion found during her research on the role of Dean Burch as chairman of the FCC (page 85) that the former manager of Barry Goldwater's Presidential campaign has quite a temper. While interviewing communications industry managers and legal specialists, as well as sources in Congress, the Pentagon, and, of course, the FCC itself, Mrs. Vermillion heard about rumors that Burch might have a conflict of interest as a result of a reported tie with a communications company. To pin down the rumors, she put the question to Burch during an interview and got a cracking denial. Later, the chairman wrote a follow-up letter saying, "The more I thought about that question... frankly, the madder I got." And, he went on, whoever wanted the question posed should "come in and see me about it because I would sure as hell like to straighten him out about it." But, of course, the question was posed by Mrs. Vermillion on the sound premises that public officials are subject to public accounting and the reporter has a duty to take hard questions directly to the source. In a subsequent letter, Burch, calmer, ended the dialogue by saying "since there apparently is such a rumor, I certainly intend to advert to it at some stage in a public utterance."

About this issue

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FROM PIRGO

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available from stock now!

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<th>Type</th>
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<th>$h_{FE}$ @ 1A</th>
<th>$V_{CE}$ (sat)</th>
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*isolated collector

Get them off-the-shelf
by calling your Sprague semiconductor distributor.
Or call Gil Levy at Pirgo. (516) 694-9880
High noise immunity runs in our family of IC's . . . . *

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The digital integrated circuits of the DTLZ II-family are designed for high noise immunity applications, for instance in industrial electronics.

These are DTLZ (diode transistor logic with zener diodes) silicon monolithic integrated circuits. DTLZ means a modified DTL (diode transistor logic) with zener diodes instead of the normally used voltage dropping diodes. So, a high noise margin is reached and also a small temperature drift.

Features:

- Worst case noise margin 4.0 V at 12 V supply voltage over the full temperature range.
- Fan-out of all gates and flip flops 10, power gates 8.
- Propagation delay times of gates: $t_{pd} < 250 \text{ ns, } t_{pd} < 150 \text{ ns.}$
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Readers comment

Regulators

To the Editor:

In William L. Brown's article on regulated power supplies [July 20, p. 94], the circuits and descriptions are good, but hardly what one is by the first paragraph to expect.

His "series shunt-regulator" is a simple, ordinary series regulator, and the only conclusion he actually arrives at is that upping the gain of the feedback loop—in this case by running an op amp open loop—will increase regulation. His "dynamic shunt resistance" analogy is incorrectly applied here: a large current swing cannot occur in the output of the op amp because it would switch the series regulator on or off; and a few tenths of a milliampere change in the output will create a negligible shunt regulating effect for a zero to 5 ampere supply.

Geoffrey W. Torrence
University of Texas
Austin

The author replies: these regulators employ multiple loop feedback, which includes the dynamic shunt path, for which the op amp is closed loop—not open loop. The relative series-shunt action is a function of the frequency response of these paths. Whether the series element in the linear regulator is switched (and it must not be) by the shunt current variations is dependent on frequency, gain, and magnitude. In the switching regulator, the series element is always switching; and the shunt action is isolated from the series action at higher frequencies by the series inductor and 5 microfarad capacitor, making the shunt regulating action much more self evident and simpler to study experimentally.

As part of the original study, the dynamic range of the shunt element was significantly increased by addition of a broad-band power booster stage, and the shunt conductance was kept high, up to many megahertz, with a μA-715 op amp. However, even shunt current variations in the low milliampere range, as provided by the low cost op amp.
Which 5-digit multimeter is your best buy?

The new S-D 7005 offers five-digit resolution and accuracy. Designed for top performance in actual laboratory use.

Price: $1,295.

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Prove it to yourself. Ask these questions in evaluating any competitive DVM:

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- Is the input impedance greater than 10,000 megohms or 1 V and 10 V ranges? Is feedback noise at the input terminals less than 1 mV? Is full scale response time less than 400 milliseconds?
- Does the unit use dual slope integration for max noise immunity at line frequencies? Are plug-in cards used for easy maintainability of all measurement circuitry?
- Does it have an annunciator to remind you what measurement you're making? Are its digital outputs compatible with both IC logic and discrete component logic?

The S-D 7005 offers all these features and more for just $1,295. Request technical data or a demonstration from your local Scientific Devices office or contact: Concord Instruments Division, 888 Galindo St., Concord, California 94520. Phone: (415) 682-6161. TWX: 910-481-9478.

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Readers comment

used to illustrate the article, reduce switching ripple by roughly an order of magnitude. Connecting the op amp as a straight amplifier to increase the loop gain (without shunt action) does not produce such improvement.

On progress

To the Editor:

Bravo Mr. Frank [June 8, p. 4], gradually people seem to be awakening to the tragic dilemma so called progress has driven us into. I would go even one step further than Mr. Frank and not only limit population (how could this be done in those countries that contribute most to the population explosion?) and try to control waste production, but stop progress altogether.

What has progress really brought us? A lot of benefits, no doubt. A longer life span through medical achievements and better hygiene. Now if we could only enjoy all those extra years.

Has anybody ever thought of the effect of a doubling of mankind on the supply of breathing oxygen or about the effect of the added quantities of CO₂ on the climate? All these are side effects of progress. And yet, have not most of us average citizens achieved a standard of living that was unheard of a few hundred years ago? What more could we want? Why not expend some (or most) of our energy to the betterment of our general living conditions and those of our less fortunate neighbors. When are our major political forces going to wake up and, instead of using their influence and their surprising ability to sway people just to elect one or the other candidate, use these talents to motivate producer and consumer to produce less and consume less not so much for economic stability but just to retain the status quo?

If the status quo could be frozen today we could start working on the problems instead of creating new ones with every bit of progress. Hopefully, there are more Frank's: why not join forces?

Claus J. Segebarth
Troisdorf, West Germany
"If you're calling Radiation and Harris answers...

Don't hang up!"

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HARRIS SEMICONDUCTOR, the new name for Radiation Microelectronics, is just in time for the opening of one of the most up-to-date IC manufacturing facilities in the country. Smooth production flow and exacting quality control have been the keynote of its design. To assure environmental integrity there are four completely isolated modules right in the heart of the building: two for wafer manufacturing and inspection...one for engineering development and assembly...and the fourth for photomasking and process development. Each has its own vertical laminar-flow ventilation system which blankets the entire module with super-clean air kept at $72^\circ \pm 1^\circ F$ and $45\% \pm 1\%$ relative humidity. And as added environmental protection, the air is filtered again by the laminar-flow hoods at each individual production station. That assures us of maximum yield for our high-quality chips. And it assures Harris Semiconductor customers that they'll get IC's with the best price/performance ratio on the market.

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All material moving through the manufacturing process is continually monitored to assure conformance to the rigid standards of high quality Harris Semiconductor has been noted for.
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- DTL/TTL compatibility
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- 0° to +75°C, $25.60*
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*100 to 999 unit price

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- Input impedance of 500Megohms
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*100 to 999 unit price

And in addition to our recently introduced PROM and 8-bit D/A Converter, watch for these new products soon to be announced:

<table>
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<tr>
<th>MEMORIES</th>
<th>DIGITAL</th>
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<td>MONOLITHIC LADDER</td>
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<td>9312 SINGLE 8 INPUT MPX</td>
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<td>9316 HEX COUNTER</td>
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Electronics | September 28, 1970
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EASY TO USE:
Simplified wiring rules. Standard double-sided PC boards. Unterminated lines. Logic levels constant over temperature range.

HIGH SPEED:
2nS gate delay (even faster system speeds with MSI elements).

LOW CCST:
Low unit-cost for gates and MSI. Low design and manufacturing costs with conventional board assembly and wiring techniques.
New 9500 ECL Family.

The temperature compensation in our 2nS ECL makes it so nifty to use.

Until 9500 ECL, emitter coupled logic was the prima donna of the industry: a dazzling performer, but so temperamental to work with. As one of the major ECL manufacturers pointed out, "ECL requires a higher degree of sophistication—in printed-circuit board design, thermal/mechanical design, and in designing to minimize noise. But," he concludes on a melancholy note, "the main problem is in getting people to accept this." The fact is, with the advent of the 9500, people don't have to accept these trade-offs any longer.

It's the temperature compensation in our new 9500 ECL that makes it the first low-cost non-saturating high-speed digital IC family that's easy to use.

Elaborate system cooling designs are unnecessary because a unique on-chip temperature compensation network maintains logic levels constant over the temperature range. This, in turn, means that noise margins remain constant with temperature. Constant logic levels also reduce the danger, common in other ECLs, that additional time delays may be introduced by driving input transistors into saturation. And finally MSI functions, such as the 9581, become easy to use, thus reducing system complexity and cost.

Layouts are simplified and PC board manufacturing costs reduced because 9500 can drive up to 8 inches of unterminated line using on-chip input and output pull-down resistors. Standard double-sided PC boards can be used.

In addition, the circuits are stable because these internal resistors maintain the real part of the input impedance positive across the operational frequency range.

If desired, 9500 can drive 50 ohm lines up to 10 feet long.

These 9500 ECL devices—16-pin, hermetically-sealed, ceramic DIP, 0° to 75°C—are now available from your friendly Fairchild distributor (others to follow shortly):

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<td>9581 One of Eight Multiplexer</td>
<td>24.50</td>
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Send for data sheets and applications information on the new 9500 Easy ECL Family.
Who's Who in electronics

The recent reorganization at the Signetics Corp., which actually started just before the summer, was aimed at separating the company's product lines and integrating the functions within each line. Now that it's completed, "the company should be in a better position to serve both itself and its customers," explains Jack E. Halter, newly named vice president of products and marketing at the Sunnyvale, Calif., company.

When a company is small, and has a small product line, many functions can be handled by one person. There can be one design engineer, one process engineer, and one marketing man. But as the company grows, responsibility has to be split. This leads to a major difficulty; sometimes the people who design a product know nothing about marketing it, and the marketers know nothing about production. And as each new product type is added to the line, a new group of people is assembled to handle it.

Halter's answer was to set up a vertical product organization with a minimum of interfacing between groups. Each product group—digital, linear, and metal oxide semiconductor—has its own production control people, design engineers, product engineers, process engineers, and a marketing manager. As Halter puts it, "If linears don't sell one month, I only have to talk to one person—the linear marketing manager."

Timing, says Halter, is one of the biggest problems in reorganizing a company. How large does it have to be before the groups are formed? "We may have done it $10 million too late," he says, which might call for some backtracking. Halter is now working on a scheme whereby each group would operate as a separate company.

Halter, a BSEE graduate of UCLA, has been with Signetics for almost seven years. Starting as a regional manager, he became successively sales manager, product marketing manager, and manager of marketing. He was named vice president of marketing in April, and after Charles C. Harwood was named president of Signetics [Electronics, Sept. 14, p. 52], Halter became vice president of products and marketing.

His new post loads Halter with responsibility for most of the company's operations except R&D and sales. "We've taken sales out," he says. "The OEM sales, distributor sales, and the system-planning people report directly to the president; I've given away sales, but I've picked up manufacturing."

Besides splitting the company into groups, Halter is also faced with tying things together "so that we present a Signetics image." He has formed a product marketing group that does more than its name implies. Things that are common to all groups, like mask preparation and silicon encapsulation, for example, have to be controlled by one single group to allow for proper scheduling and to eliminate unnecessary duplication of equipment. "This way," he says, "every product line is on an equal basis—each has to hold up on its own and if it doesn't, we know where to look."

Few would disagree with the premise that the semiconductor industry is still a long way from maturity, particularly in applying sophisticated management techniques to production problems. It's also no secret that for some time the General Instrument Corp.'s Microelectronics division has been plagued with its share of IC yield troubles. That's where James H. McGarry comes in.

For the last nine months McGarry CI corporate vice president and general troubleshooter for the parent organization, has been on temporary assignment to the division. His specific task is to maximize IC production, and it will be no mean feat if he pulls it off. For openers, the 57-year-old McGarry has managed, since his arrival at the Hicksville, N.Y., facility, to reduce the turnaround time from raw silicon to packaged IC from the usual six or seven weeks to barely...
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**Who's Who in electronics**

seven days, and with a corresponding improvement in yields to boot.

In many ways, McGarry traces his success at Microelectronics to his past experience in the IC business—virtually none. "I really don't know anything about the semiconductor business, per se," he asserts. "What I do know is management and production. As such I was able to come in here and start fresh." Before joining GI corporate nearly 18 months ago, McGarry was an ITT vice president in charge of that company's transmission department in Raleigh, N.C., and before that director of operations at ITT's Federal labs.

McGarry has made changes that might seem obvious to management men in other industries, but are unusual in the semiconductor business. For example, he moved executives closer together so that personal contact eliminated a long chain of paperwork. Then he initiated motivation boosting output charts for the girls who do the IC lead bonding. The more competitive environment increased yields by 50%. He did the same thing with manufacturing managers; individual charts show actual production beside projected output.

**Wafer handling** also came in for scrutiny. Discovering that a wafer is handled by the operators—transferred from one container or one piece of equipment to another—some 70 times, McGarry got together with the factory engineers to develop ways to cut the figure. A typical solution involved a carrier used to hold the wafers when they are inserted in an etching solution. Why couldn't the same carrier be used throughout the processing, McGarry asked. He was told that they were expensive—about $40 apiece—because they had to be made of Teflon to resist the etching solution. McGarry's solution: carriers of identical design, but of a cheaper plastic for about $2. Now, after the wafers are etched, the cheaper carrier is inverted over the more expensive one. When the combination is turned upside down, the untouched waters slide gently into the $2 carrier and continue through the fabrication process.

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Circle 19 on reader service card
**Meetings**

**Optics at stage center**

By sheer weight of numbers, if nothing else, the papers on optical gear will demand attention at the International Electron Devices Meeting to be held Oct. 28-30 at the Sheraton-Park Hotel in Washington. Optical topics will range from methods of achieving greater frequency stability in helium-neon gas lasers to the marriage of semiconductor and electron-beam techniques in camera tubes and new combinations of materials for light-emitting diode displays.

No less significant are the papers on integrated circuits. Field effect integrated circuits receive particular attention, with a new symbology appearing—MIS for metal insulator semiconductor, instead of the usual MOS—thus revealing the variety of forms the basic FET IC is assuming. Some evidence of the diversity of research and development going on can be gleaned from the titles: “Silicon Gate Complementary MOS Integrated Circuits,” by R.R. Burgess and R.G. Daniels of Motorola; “Dielectric Isolation for MOS ICs,” by D.O. Patterson and W.H. White of Radiation Inc., and “The Effective Mobility of MOS Transistors on Insulating Substrates,” by E.J. Boleky of RCA.

**Bipolar ICs** won’t be ignored, of course. One of the remarkable trends that will be in evidence at the conference this year will be the adaptation of MOS (or MIS) fabrication techniques to bipolar devices. Witness “Policrystalline Silicon Technology for Bipolar Integrated Circuits,” by J.A. Schoeff of Motorola and “The MNOS Bipolar Transistor,” by B.A. McDonald of Fairchild Semiconductor.

One of the most critical parts of an IC—the metal used to interconnect circuit elements on the chip—will have a session devoted to it exclusively: “Interconnection Metallurgy,” reflecting the concern of manufacturers with existing metallization systems and the search for new ones. Efforts to improve the workhorse aluminum metallization technique will be examined by H.J. Bhatt of the Cogar Corp. in “Superior Aluminum for Interconnection of Monolithic Integrated Circuits,” and by W.R. McMahon of Texas Instruments in “Anodic Processing of Interconnect Systems for Integrated Circuits.” An overview will be provided in “The Corrosion Resistance of Several Integrated Circuit Metalization Systems,” by J.A. Cunningham, C.R. Fuller, and C.T. Haywood of TI.

Other device types to be covered during the three-day conference include memories, transistors (especially transistor models), crossed-field amplifiers, microwave generators, acoustoelectric devices, traveling wave tubes, and thyristors and diodes.

For further information contact Gerald Sevick, Bell Laboratories, Murray Hill, N.J.

**Price conscious**

When connector makers and their customers get together Oct. 21 and 22 for their third annual symposium, their sessions will reflect the usual mix of technical topics. But the talk between sessions probably will center on the subject uppermost in the minds of all cost-conscious electronics executives: how to bring costs down while improving performance.

Anticipated attendance at the meeting, to be held at the Cherry Hill Inn in Cherry Hill, N.J., is 500 connector specialists and 40 manufacturers. On the technical side, there will be more than 30 papers in eight major categories. They are military and commercial computer packaging; flexible flat cable; connector test methods and evaluation; rf connector improvements; materials and finishes; standardization; new termination connector techniques; and connector potpourri. Each category will contain three or four papers from manufacturers such as Fairchild, Hiller, Texas Instruments, Bendix, AMP, Phelps Dodge, Burndy, IBM, ELCO, Microdot, and Burroughs.

For further information contact James Fletcher, AMP Inc., Harrisburg, Pa.

(Continued on p. 22)
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Meetings

(Continued from p. 20)

Calendar

Mervin J. Kelly Communications Conference, University of Missouri, IEEE; University of Missouri Library, Rolla, Oct. 5-7.

Industry & General Application Group Annual Meeting, IEEE; La Salle Hotel, Chicago, Oct. 5-6.


Symposium on Microwave Energy, International Microwave Power Institute, Scheveningen, Holland, Oct. 7-10.


Western Space Congress, Vandenberg Scientific and Technical Societies Council; Vandenberg Inn, Santa Maria, Calif., Oct. 27-29.

Annual Symposium on Switching and Automata Theory, Switching and Automata Theory Committee of the IEEE; Santa Monica, Calif., Oct. 28-30.


(Continued on p. 24)
It's done with oscillographs. We have a bunch, but let's talk about these two. They can handle on-line or off-line test and measurement of any physical happening you might have in mind. From pulse flutters during surgery to whether or not you're refining that crude the same way today as last year.

The one on the bottom is the 5-134. It does everything but talk. It writes to 25,000 Hz. (But with all that speed, it has a data accuracy to ±1/2%). And can flip into any one of 10 different servo-controlled speeds.

It's modular, of course, with special refinements. Like the timer, servo control board and galvo all plug in. Individual input connectors as standard. An extremely quiet operation. That type of thing.

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The smaller box is the 5-135. It weighs in at 35 pounds (a real portable) as compared to the other's 50 pounds. Both boxes share pretty much the same components. It's just that the 5-135 has broader application by more industries across the board because it's not quite so fancy (9 channels versus the 5-134's 18, for instance). Even though it's smaller, it doesn't skimp on performance. It has the largest range of input power options of anybody going.

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And one more thing. Just in case you're building a system, we've got a range of other new goodies to complement these graphs: 1-172 amplifier, 8-114 bridge excitation/signal conditioner and the 23-111 paper processor.

If anything here piques your curiosity, you can get the full package of specs by writing Bell & Howell, Instruments Division, 360 Sierra Madre Villa, Pasadena, California 91109.
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Meetings
(Continued from p. 22)


Environmental Engineering, George Washington University; Washington, D.C., Nov. 2-4.

Northeast Electronics Research and Engineering Meeting (NEREM), IEEE; Sheraton Boston Hotel and War Memorial Auditorium, Nov 4-6.

Nuclear Science Symposium, IEEE; Statler Hilton Hotel, New York, Nov. 4-6.

Fall Joint Computer Conference, IEEE; Astro Hall, Houston, Nov. 17-19.

Short courses

Computer Graphics in Electronics Design, University of California at Los Angeles; Boelter Hall, Room 2444, Oct. 29-31; $245 fee.

Computer-Aided Circuit Optimization, University of California at Los Angeles; Boelter Hall, Room 4442, Nov. 2-6; $310 fee.

Holm Seminar on Electric Contact Phenomena, Illinois Institute of Technology and IIT Research Institute; Sherman House, Chicago, Nov. 9-12; $125 fee.

Call for papers

Nov. 30 is deadline for submission of papers to academician V.M. Glushkov, chairman, IFIP Congress 71 Program Committee, Institute of Cybernetics, Ukrainian Academy of Sciences, Kiev-28, U.S.R.; or professor C.C. Gotlieb, Vice-Chairman, IFIP Congress 71 Program Committee, Institute of Computer Science, University of Toronto, Toronto, Ontario, Canada; or Professor H. Zemanek, vice-chairman, IFIP Congress 71 Program Committee, IBM Laboratory, Vienna, Parkring 10, A-1010 Wien, 1, Austria.

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Electronics Newsletter

September 28, 1970

Aerojet-General to sell thin film active devices

Aerojet-General, in a radical change in policy, is going to market thin-film ICs. The first device, a three-input NOR gate, is being offered now for evaluation. The firm says it could sell for 25 cents a gate in quantity. The decision may have been triggered by the belief of engineers at the firm’s Azusa, Calif., Electronics division that they’ve gotten around the common problem of thin-film active devices: gate dielectrics that become relatively unstable.

They’ve developed circuits using cadmium selenide or tellurium semiconductor materials. The circuits use insulated-gate field effect transistors with an aluminum oxide dielectric. Previous efforts at thin film ICs have used the same dielectric, but Aerojet engineers feel they’re unique in using electron-beam evaporation of the material in one pump-down of a vacuum system rather than depositing aluminum metal, anodizing it with a plasma, and then etching away unwanted dielectric. The latter method doesn’t lend itself to one-step batch fabrication.

Fairchild making pocket calculator

Fairchild may have killed its minicomputer development program, but it’s going full steam ahead with a calculator. The Microwave and Opto-electronics division has developed a 1.5-pound pocket calculator slated for introduction in January—shortly after the group moves into its new building. The device, with a nine-digit solid state readout, will be more like a computer than a calculator—it will perform trig functions and have a memory.

As for the decision to drop the mini, a highly placed spokesman for the company says this does not mean Fairchild’s policy of “forward integration” has been shelved. Implementation of the policy, aimed at getting Fairchild into the manufacture of subsystems that use its semiconductors, has been shelved “until things pick up,” he says.

Second sources look to cut die sizes

A new trend is surfacing in the integrated circuit business: redesign of a prime source’s product line by second source manufacturers to cut die sizes. And prime sources—the original circuit designers—are taking a hard look at their existing lines to see how to cut real-estate consumption.

The Integrated Circuit Engineering Corp. in Phoenix, Ariz., reports that it’s getting a lot of the overflow redesign efforts the IC manufacturer would rather not do himself. In three cases in the last six months, such redesigns were ordered. For example, a medium-sized semiconductor house asked for redesign of a series 7400 TTL line of medium-scale ICs it supplies as a second source. And another customer wanted to become a second source for Sylvania’s SUHL TTL line and asked Integrated Circuit Engineering to redesign for layout efficiency.

Penney seeks credit-check system

Giant retailer J.C. Penney, after terminating its costly Tradar contract with General Electric, is going to request bids from more than 40 companies for a relatively austere credit-authorization system. Tradar was a complete on-line, real-time, point-of-sale transaction system.

The bids, due Sept. 30, are for a hypothetical four-store, 80-cash-register layout. Penney wants to check credit on every charge transaction.
from a negative account file at an in-store or central location. Later, the system could be expanded to use a positive credit-checking file, which would set limits on the amounts of credit.

The GE Tradar system, first tested in one of Penney's stores in 1967, apparently was canceled not for technical reasons, but because Penney changed its mind after a backer of the system shifted to another job. Comments one competitor: "Tradar would have worked, but it was expensive and was based on a pair of obsolete GE 415 computers."

A digital interface that lets users substitute programable calculators like the Wang 700 or Hewlett-Packard 9100 for computers may offer the least costly route yet to digital process control, data acquisition, and similar applications. Month-old IDEA Inc. of Riverdale, Md., offers a single-channel interface at $2,000; it's expandable to seven channels at $900 per channel.

The interface includes microprogram boards to instruct the calculators in manipulating incoming data, making possible not just calculation and data logging, but closed-loop process control, too—a province formerly reserved for costly full-blown computers. System price, including a calculator and printer, would be $8,000 to $9,000 for one channel.

What may be the largest bipolar read-only memory available, 1,536 bits, is the first product of a new firm, Kenics Electronics of Largo, Fla. Size of the transistor-transistor-logic device formerly was available only in MOS. Designated the B-100, the 64-word-by-24-bit memory is expected to be used as a high-accuracy lookup table, microprogram store, or tape reader replacement. Access time is about 200 nanoseconds; each of the ROM's 24 outputs can sink 5 milliamperes at 0.5 volt. Low-level address characteristics also match TTL at 0.3 mA.

Kenics, formerly a process-control house, moved into the IC business by acquiring the equipment, and about half the staff, of a Honeywell facility that was moved to Minneapolis.

After spinning off its computer manufacturing operation, GE has now turned its attention to cutting costs at its Information Service division by closing down that division's 17 autonomous time-sharing centers and consolidating them into three major units at Cleveland, Los Angeles, and Teaneck, N.J. GE is hoping to cash in on the savings that come from using large-scale GE-600 machines instead of the smaller GE-200s used in the regional centers, even though a nationwide grid of lines will have to be leased to link regional customers with the consolidated centers.

Sylvania officials still are evaluating the market potential of their Soniscan magnetoacoustic bulk memory system [Electronics, Aug. 31, p. 33]. Most recently, the company has been sounding out possible joint venture partners... . . . IVC Inc. of New York has working models of a credit-verification system that stores information on a hologram built into the card. The card is blank with the hologram—a tiny circle of transparent material containing a signature specimen and other personal information for identification—in one corner. The card is read by laser projection.
DUAL FETS for DIFF AMPS from Siliconix

Does your diff amp design call for low noise, low distortion, high input Z and wide dynamic range? (1.) Try this long tailed pair arrangement, (2.) use a Siliconix dual and (3.) get the performance below:

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<th>VOLTAGE GAIN (dB)</th>
<th>BANDWIDTH 3 dB (MHz)</th>
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</table>

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Electronics | September 28, 1970
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Modular assembly turns out hordes of transistors

Fairchild’s concept utilizes originally designed die bonders, wire bonders, and molding machines

When Reed Neddermeyer, marketing manager for discrete devices at Fairchild Semiconductor, said, “Fairchild has enough capacity now to supply the world with transistors,” [Electronics, Aug. 17 p. 75] he was talking about the company’s world-wide capability. But now that Fairchild’s automated plastic TO-92 transistor line is up and running, it could probably supply the world’s needs from one room at the division’s Mountain View, Calif., facility.

The line is built around a modular concept where the modules consist of the bonders, wire bonders, and molding machines. Presently, there are five die bonders (each of which feeds five wire bonders), four molding machines, and one computer-controlled tester. All the equipment was either designed by Fairchild and manufactured by an outside vendor, or designed and built by Fairchild divisions—there is no off-the-shelf equipment. In fact, to keep things completely under control, Fairchild bought the Inland Machine Corp., the maker of the molding machine.

Industry watchers have been waiting to see what C. Lester Hogan, Fairchild’s president, would do in terms of designing a “better automatic line than Motorola’s” since Hogan was the force behind the Motorola line. And from the looks of it, Fairchild has made improvements in at least two areas. For one, operator decisions have been reduced from about six to three—the operator has to align a die under a spot of light for die bonding, and align the base and emitter pads under spots for wire bonding.

The package was also improved. Instead of mounting the die directly on the collector lead (as Texas Instruments does) or on a pad that is an extension of the collector lead (as Motorola does), Fairchild bends the collector pad perpendicular to the leads and bonds the chip on the top of the pad. A Fairchild spokesman says that having the collector pad perpendicular to the plane of the base and emitter leads and positioned below the top of the base and emitter posts permits “up-bonding” and lengths the moisture path.

Upbonding, says Fairchild, eliminates any tendency for the lead wire to short to either the die surface or the collector pad. Secondly, the chance of moisture reaching the chip is reduced. The normal moisture penetration path in a plastic transistor is along the leads. But with the collector pad perpendicular to the plane of the leads, the combined distance (moisture path) from the outside of the package to the die is longer than with either of the other two construction techniques. And moisture doesn’t like to go around corners.

Since there are four machines and the frames hold 100 devices, 24,000 devices are churned out each hour. (Plans are to increase the frame size to 200 units each in the near future.) The tester can turn out about 20,000 devices per hour.

Industrial electronics

NCR apt to sharpen point-of-sale battle

National Cash Register’s introduction of a point-of-sale information system could spur usually conservative department stores—wary af-
NCR's computer-based system automatically flags sales and merchandising data needed to keep track of inventory. The system reads price tags, credit cards, and identification documents via a hand-held wand, verifying credit, calculating the entire sale, and computing any applicable taxes or discounts. Transaction details are stored on magnetic tape for subsequent computer processing. It's also possible to operate the NCR 280 terminal on a stand-alone basis if the central data collector goes down.

At present NCR is the only manufacturer to use an optically encoded system to read data from price tags; others use magnetically encoded sales tickets. Montgomery Ward has been working with NCR in field tests in its Lima, Ohio, store.

Basic price of the system is about $25,000, for which the user receives a 280 terminal, the wand, the 723 magnetic tape data-collection unit, and the 747 tag printer. Delivery time is expected to be around nine months.

Communications

OTP's charter erodes

FCC's primacy

The Office of Telecommunications Policy has been reorganized. President Nixon has put it at least on an equal footing with the Federal Communications Commission, and has made sure the White House will have a hand in future communications decisions.

He has given OTP new responsibility in areas of communications traditionally within the realm of the FCC—the private frequency spectrum and computer-communications interfaces. OTP has been directed to conduct studies and analyses on "the impact of the convergence of computer and communications technologies," that will lead to recommendations for action, indications that the long-awaited FCC decision on its computer communications inquiry could be held up until the commission gets OTP input. Nixon has ordered OTP to make sure that Executive branch views get to the FCC.

OTP also has been told to develop "a comprehensive, long-range plan for improved management of all electromagnetic spectrum resources." Though the White House order notes that the plan will be developed "in cooperation with" the FCC, OTP will be stepping into an area of private spectrum management which has been specifically the province of the Federal Communications Commission.

Consumer electronics

Color TV recorder takes forward step back to film

Color movie reprints for television distribution often lack the intensity of the original, presenting the viewer with faded heroes riding across faded scenery into faded purple sunsets. Now CBS Laboratories has developed a simple means of copying TV signals on film with a laser beam color film recorder to be introduced at next week's Society of Motion Picture and Television Engineers Conference.

Codeveloper Leo Beiser, staff physicist, declares that the new system has "quality so spectacular that there will be no question as to its acceptance by the television networks." the recorder features an unspectacular arrangement of gas lasers and optics. However, the color combiner and beam deflector are the keys to quality color reproduction.

Prototypes due in about six months will accept any NTSC signal from video tape or directly from a camera and make either 16-millimeter or 35-mm film. The recorder's beam deflector scans a single laser light beam for red, green, and blue after the color combiner reconstructs the three lines coming from red, green, and blue intensity mod-
ulators. Three gas lasers provide the light source.

The result, says Beiser, is perfect registration from one beam rather than the superimposition of three separate color beams via three independent cathode ray tubes.

The system presently in use, the Trinescope process, requires three CRT’s, each assigned a color. A beam from the TV signal is split by mirrors to the CRTs which then combine the image optically on film. It's this last stage that causes the registration problem common on color TV movies.

The new CBS approach can also be applied to laser display on large TV screens, but power limitations on the gas types make this possibility uneconomical now. The target for the recorder, on the other hand, will be TV networks here and abroad. The unit records at the standard rate of 24 frames per second, and sound can be tacked on by any means the user chooses, such as magnetic recording track.

“High-quality color was the challenge,” Beiser points out. “There has been no good direct color film system available, although the possibility of using lasers has been clear since we started work on the recorder over four years ago.”

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**Memories**

**Navy airborne processor turns to Ampex thin film**

Military airborne computer users are a demanding lot. Not only do they want memories that are incredibly dense, fast, and rugged, but they also want them low in power consumption. Of all the military users, none is more demanding than the Navy’s Advanced Airborne Digital Computer program team, which is also asking for prices less than a penny a bit for the mainframe store [Electronics, Aug. 3, p. 89].

Industry, however, is standing up to the AADC challenge. The Navy team says a thin-film memory under development by Ampex Corp. seems to meet both the mainframe and task memory needs of the modular computer, designed to fly on all Navy planes in the 1975 to 1985 time frame.

Called the closed flux memory, the nondestructive readout device is produced by electroplating a layer of copper between two layers of permalloy on an aluminum and epoxy substrate. A photoresist then is applied, and five-mil-wide bit storage lines are formed by etching away parts of the structure, creating a sense-digit line sandwiched between permalloy layers. Another plating step deposits word lines that run perpendicular to the bit storage lines along their easy axes. Finally, a metallic film is plated over the structure; this prevents creep by closing the magnetic flux of the lines.

The memory, akin to plated-wire types in operation, has a read-only time of 100 nanoseconds. Read-write cycle time is 150 ns. While only experimental arrays have been produced, Ampex projects that it can build the memory for half a cent a bit, with 75% to 80% of that figure to go for the unit’s electronics, Ampex says.

Because the magnetic flux is contained, the only limitation on density is the resolution of optical masking techniques, which means that the bit lines could be formed on one-mil centers. However, until smaller integrated circuit and better interconnect techniques are developed, Ampex plans to hold back on the densities. Thus, the 8,192-bit stacks Ampex hopes to build for the Navy will contain 44,000 bits per cubic inch, denser by far than any other memory under consideration for the AADC, but far short of the technology’s potential.

An important feature of the arrays is that their 350-milliampere and 20 mA write and drive currents make them directly compatible with integrated circuits. Thus it's possible to build small-task memories that will fit inside a package, says AADC program manager Ronald Entner. Power requirements for the 8k-by-16-bit memory the Navy is looking at are 0.5 watt for the memory array and 19 W for the array plus its TTL electronics.

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**Radar**

**Computerized display aimed at ending ship collisions**

If collision avoidance systems for ships become a new electronics market, as the Lotron Corp. hopes, then the Bedford, Mass., firm has just gotten a head start with its
new computer and display system. Such gear is long overdue for ships, says James S. Coolbaugh, Iotron's director of marketing. Statistics compiled since the 1940s by the U.S. Merchant Marine Council and other groups show that about one in three deck officers misinterprets a radar's plan position indicator display (PPI) unless they can confirm their judgment visually—and of course, radar is supposed to help most in fog or darkness.

During maneuvers, radar may even be a hazard, for more than two out of three deck officers draw wrong conclusions from the PPI while accelerating, slowing, or turning. Instead of cutting the number of collisions, therefore, radar is generally admitted to have added a new variety—the "radar-aided collision."

Iotron's Digiplot console contains two computers and a Monsanto light pen aided display system, the MAN-1. One computer takes digitized data from the onboard radar system's video section, and stores them in dual 100-bit Texas Instruments MOS shift registers. It's capable of retaining range and bearing data on up to 100 targets at a time, along with the amplitude of the radar return. A proprietary circuit discriminates against sea clutter on the basis of target area, its size relative to its range, and whether it appears repeatedly, sweep after radar sweep.

Once this processor has the target data down pat, it hands the computing job to a slightly modified Lockheed MAC-16 computer, which calculates the speed and course of each target. The MAC-16 also runs the display system, showing land masses as dotted outlines and ships as dots with lines stretching away from them to indicate their intended speed and/or course.

"The beauty of the Digiplot is the understandability of the display," says Coolbaugh. Flipping a switch to the "relative plot" position shows any ships that may be approaching in the form of vector lines pointing to the center of the screen—the operator's ship. If the user wants to work with a so-called true plot, the 16-inch CRT shows a course and speed vector for his own ship, radiating from the center of the screen, as well as course and speed vectors for other ships. (The normal PPI scope display area just shows dots, does no course and speed computing, and thus requires extra effort to interpret.)

To figure his way out of a possible collision situation, the skipper can punch the "future position" switch, setting it for, say, five or six minutes in the future. This allows the conning officer to spot collisions many minutes ahead of time just by watching for meeting vector lines on the screen. If a collision looks imminent, the conning officer can try out course and speed changes on the MAC-16, see the result on the CRT, and make sure his decision is the right one before giving his orders to the helmsman.

Iotron plans to offer the system for $30,000, and hopes to find many takers. "With a jumbo tanker expected to run at $80 million to $80 million in the near future, $30,000 is cheap insurance," says Coolbaugh. And considering the oil spill any collision involving a jumbo tanker makes, and the losses, penalties, and ecological damage that could result, Coolbaugh expects to see the Digiplot a widely used ounce of prevention.

Companies

RCA looks for place in the IC sun

When William C. Hittinger moved from the General Instrument Corp. to head RCA's newly formed Solid State division [Electronics, April 13, p. 46], industry insiders wondered if his charter was to make the new division a name to be reckoned with in the integrated circuit business. Now, with six months under his belt, Hittinger has something of an answer: yes and no.

"Sure we have ambitions for a major place in the sun along with the Fairchilds, Texas Instruments, and Motorolas, but not across the board; we'll be sticking with metal-oxide semiconductor, emitter-coupled logic, linear and hybrid ICs, but we'll leave the saturated bipolar market to the others," Hittinger (pronounced with a hard "g") says.

The point is, explains the new vice president and division general manager, that for RCA to get into transistor-transistor logic and the rest, it would have to buy its way in with loss leaders, "something we're not about to do."

Naturally, RCA will continue to emphasize its traditionally strong lines in the semiconductor business—the discrete, especially power, devices. But that's not all. Along with its standard line of MOS products, the division practically pioneered what was once considered a fascinating but limited offshoot of the MOS process—complementary MOS. "Something Hittinger, C/MOS is a potentially large business that is starting to take off. Furthermore, the division has a group in residence at RCA's labs in Princeton, N.J., working on applying silicon-on-sapphire fabrication techniques to the MOS and C/MOS process.

This concept of a "group in residence" is an innovation with Hittinger, one he feels is particularly vital to the division's future growth. "If we've fallen down in the past, it's because we weren't translating technological knowhow into marketable products. Part of my assignment here is to get a kind of interdivisional interaction going with all parts of RCA."

Currently, a group of circuit engineers from Indianapolis (RCA's television manufacturing arm) is in residence at Somerville, N.J., working on linear ICs for TV sets, while yet another group from Somerville is at the company's Defense Electronics Products division in Moorestown, N.J., developing products for the defense group. The idea, as Hittinger sees it, is for one group of engineers to virtually live at another division with the ultimate
intention of “coming back with a bag full of usable technology.”

Another tack being taken by the Hititinger regime toward getting more IC products into the commercial marketplace is to develop, say, a line of linears for RCA TV sets, and then expand the line commercially for customers outside the RCA family.

The solid state division hopes to apply this method to other areas as well. A line of ECL circuits now being developed by solid state for the company’s computer operation

will initially be sold to the Cherry Hill, N.J., facility. Eventually, however, solid state will broaden the line for full-blown commercial exploitation.

Finally, there’s the division’s line of hybrid power modules, or integrated power systems, as Hititinger calls them. These units will have applications ranging from power outputs for computers, at one end of the spectrum, to handheld power drills for home and industry at the other.

Federal Reserve net speeds check clearing

Like millions of other consumers, the engineer who pays his rent with a check on the 25th of the month using funds he will not deposit until he gets paid on the first is in for a rude awakening. The Federal Reserve System, whose 12 regional banks serve as a clearinghouse for transfer of funds between member banks, is dramatically upgrading its communications network to eliminate the time lag between checkwriting and final payment.

That time lag, which bankers view as equivalent to a short-term “loan” to their customers, generates a daily volume of millions of dollars in uncollected funds which bankers term “the float.” Bankers, naturally, want to eliminate the float. Now they believe they are closer to that goal with the Fed’s new network as the start of an all-electronic system for real-time transfer of funds.

The first step taken in upgrading the network is the installation of a Control Data Corp. M-1000 quad store and forward switch at Culpeper, Va., which has already taken over the switching of the Fed’s teletypewriter network. By eliminating the 1-to-2-hour queues of the old Western Electric 81-D torn-tape switch, the four M-1000s have already made it possible to guarantee that wire transfers, the primary use of the Fed’s teletypewriter network, are received the same day they are sent.

Because of the limited bandwidth of the Fed’s telegraph circuits, only large checks are presently cleared by wire; the rest are transferred by ground transportation. But wire transfer traffic is expected to increase by an order of magnitude after the Fed links all of its banks with 2,400-band circuits that will be switched on the Culpeper system by the end of the year.

Bruce Smyth, the chairman of the Fed subcommittee that runs the network, says the leased circuits will be used to carry high-speed interbank traffic transmitted and received via IBM magnetic tape terminals. As the data rates go up, he notes, the cost of transferring checks by wire goes down. Therefore, he predicts, the Fed will be using faster data speeds to transfer more of the billions of checks written each year by wire.

Meanwhile, he adds, more Federal Reserve banks are drawing up plans for what they call computer-to-computer communications. The term, which Smyth says is a misnomer, means that each Fed bank will eventually install its own computer-controlled switch to distribute traffic to and from street banks—the large member banks that serve the public.

Once street banks, which are presently linked to the Fed network by torn-tape switches, are on-line with the network, it is only a short step to the direct electronic transfer of funds from one bank’s computer to another’s.

Nixon seen doubling LEAA budget to $1 billion...

Mounting evidence indicates that the Justice Department’s Law Enforcement Assistance Administration fiscal 1972 budget could approach $1 billion—more than double this year’s $480 million—as part of a Nixon Administration shifting of resources from the Defense Department into domestic programs. Between 40% and 60% of LEAA’s budget is expected to end up in the electronics industry, through Federal grants to states for such programs as upgrading police communications, computerizing court systems, and in general applying advanced technology in the criminal justice system.

Congress is on a law and order tear that will last until appropriation time next summer, and it can be expected to bless such an ap-
proportion request from the White House. Many Congressmen are already pushing for a $1 billion LEAA budget.

LEAA is embarking on a program of workshops across the country aimed at developing the abilities of the states to spend more money wisely. The agency has brought together systems analysts and criminologists from different state agencies, who, with LEAA help, will learn to develop five-year plans for law enforcement in their states and decide the division of resources between nontechnological systems such as community relations and technological systems and evaluate how different approaches can help combat crime.

LEAA sources say initial findings indicate the states will place the most emphasis on communications and equipment upgrading, personnel training, and rehabilitation.

...as LEAA, NASA talk of ATS fingerprint fax

The Law Enforcement Assistance Administration and NASA are negotiating for the use of Application Technology Satellite 3 to transmit fingerprints from California to the East Coast. Paul K. Wormeli, coordinator of LEAA's Project Search (system for electronic analysis and retrieval of criminal histories) says the two agencies are at the barebones talking stage, but that the cooperative effort looks promising and could be the first step toward a dedicated police satellite. [Electronics, April 27, p. 115].

For the initial experiment, LEAA would use one wideband ATS channel to transmit facsimile fingerprints from Mohave, Calif., to the FBI in Washington or the New York State Identification and Intelligence Service. LEAA and NASA are working out the technical details. A 200-line-per-inch resolution—the same as over telephone lines—would be required. But satellite use, says Wormeli, would cut the transmission time to 2 or 3 seconds from the 14 minutes it now takes over telephone lines for coast-to-coast facsimile.

Wormeli says he has been trying for months to win LEAA over to the idea of using a satellite for Search communications. The project now covers 15 states and, having just completed a successful two-month demonstration, will be expanded to states with high crime rates, beginning in 1971. Wormeli believes the Justice Department agency will be receptive to the idea if the fingerprint satellite experiment "proves to be a cost-effective solution" to several problems encountered with telephone lines. Among these are the build-up of delays caused by using low-speed (100 bands) telephone lines, low quality of reproduction, and the expense of using national land lines.

A decision on the NASA-LEAA venture will be made by the first of the year, LEAA sources say. If the program goes, and proves successful, the agency will consider putting up a satellite for fingerprints, Search, and other police communications.

Military electronics

Crossbow checks out miss-distance indicator

Firearms made the crossbow obsolete. But now engineers at Motorola's Government Electronics Division in Scottsdale, Ariz., have resurrected it as a means of testing a training system that indicates missile velocity and miss distance. The system will obviate the need to fire a live missile in training and destroy a target by telling pilots how close their dummy missiles came to a towed target and what the missile's path was, so that the "kill" probability can be determined by a ground computer.

Though the Air Force will be able to use 105-mm howitzer projectiles to simulate missiles, engineers needed something quieter and less expensive. So to test how well they were measuring the angle between the missile and the radar's boresight, or straight-ahead "view," they fire aluminum arrows from a tripod-mounted crossbow through the radar beam.

The Motorola division is now working on the second increment of its contract with Elgin Air Force Base for a feasibility demonstration model. Thomas Koehler, project leader, believes that the phase-comparison monopulse radar system will be the first radar scoring apparatus that gives both miss distance and vector data. Acoustic systems are being developed, but Koehler says they can be hampered by noise at supersonic speeds.

Specifically, Motorola's system will make airborne measurements of the elevation and azimuth angle between the target and the missile. It will also note the time the missile passes certain preset fixed ranges from the target. From this information, which is to be telemetered to the ground, a computer will calculate the miss-distance vector at fouling time of the missile (telemetered separately from the missile), the miss-distance vector at closest range, and the missile-velocity vector. This last is needed to determine how close the missile would have come to the target.

Included in the equipment will be two antennas (one that looks forward from the target and another that looks aft), the transmitter and receiver, and the ground computer complex. The dual-antenna system is expected to be completed by December. To date, the division has built a single-antenna system to prove that angle measurements can be made with it, and that's where the crossbow came in.

The trajectory of the aluminum arrows is consistent enough for the radar to make accurate angle measurements, Koehler says. "If we can measure the electrical phase shift between two points on an antenna in the receive mode," he explains, "we can measure the angle off boresight of the missile."

This is different from the usual goal with this kind of radar, which
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For additional information contact your local Tektronix Field Engineer or see the 1970 Tektronix catalog supplement.

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Electronics review

is to get the target into the bore-sight area of the radar.

To determine the angle, they use a receiver that's range-gated for 125 feet and 225 feet from the target. As the missile passes through two imaginary circles around the target at those distances, its elevation and azimuth angles are measured, and the time it passes four points—two preset distances on both sides of the target—is marked. All this information, along with the time the missile was fuzzed, is telemetered to the ground computer, which then calculates whether the missile, if it had been a live war-head, would have come close enough to destroy the target.

Electro-optics

Image intensifier holds its ground

Though the Starlight image-intensifying night vision scope provided infantrymen in the Vietnam jungle with a picture of the enemy at night, a tank or helicopter mounting was a different story. In these applications, the platform's vibrations and maneuvering made the scope virtually useless.

Now Mark Systems of Cupertino, Calif., has incorporated a gyroscope and a second-generation image intensifier, a single stage device, into its 2310A Image Intensifier and Stabilized Monocular. Not only is jitter removed, but the 2310A is ready to move into production now, says the Army; other advanced systems are still in the development stage. What's more, the intensifier stage itself is about 3 inches long and 3 inches in diameter instead of 8 to 12 inches long and 9 inches in diameter. The entire system, intended for observation and/or recording, weighs 7 pounds and measures 12 by 4.75 by 7 inches.

The micro channel plate device intensifier is classified. All that can be learned is that it is a small solid state component requiring very low power.

The 2310A is basically a lower power (8X) telescope with a 3-inch-long image intensifier inserted in the primary optics. One of the optical elements is mounted on a gyroscope to provide stabilization, which takes out up to 90% of the case motion and is specified as ±5° in pitch and yaw. A special magnetic system between the gyro frame and the inner gimbal allows the unit to be panned at 5° per second. Power for the gyro is supplied by six small batteries, for the intensifier itself by two separate batteries.

The conventional approach to stabilizing an optical image, whether it be in a camera or a viewing device, has been to employ servo-driven stabilized mounts for the entire system. For example, World War II cameras had a servo-driven mount and controls that weighed more than 100 pounds and drew more than 100 watts. In the Mark Systems technique, the instrument is permitted to vibrate or oscillate, and a small, internal optical element stabilizes the image. The stabilizing portions of the system weigh only ounces and the power requirements are a fraction of a watt.

To picture how the system works, think of it as a three-part optical system—a primary objective, which can be thought of as a low-power telescope of magnification $m_1$, a stabilizing prism with magnification $m_2$, and some secondary optics with magnification $m_3$. The prism is affixed to a gyro, so that it maintains an inertia reference. Then, if the system is tipped, a stabilized output requires the input angle of a ray to the third stage to equal the initial tilt angle.

This works out to an equation that states that $m_3$ must equal $1/m_2$ for stabilization. And this is easily achieved. The magnification of the third stage can then be tailored for the desired overall magnification of the system.

For the record

Striplining up. Motorola's Government Electronics division in Scottsdale, Ariz., has followed ITT's Defense Communications Laboratories into the stripline antenna field [Electronics, Aug. 31, p. 34]. Motorola has recently announced the development of stripline antennas at L, S, C, X, and Ka band that use a multilayer board sandwich with a proprietary feed-through technique to eliminate the cyclers sometimes used to connect the dipole radiators and the corporate feeds of such antennas. Early stripline antennas, with unbalanced corporate feeds, needed a balun to match impedance to the...
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...And you get more amps per dollar from the world's highest-current lead-mounted rectifier — the new MR751 "Button" Series.

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But the real big savings is in the initial cost. Compare these MR751 series prices with those of typical stud-packaged 6-amp counterparts:

<table>
<thead>
<tr>
<th>RECTIFIER TYPE</th>
<th>100-UP PRICES</th>
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<tr>
<td>$ TYPICAL, 6-A STUD ASP'S</td>
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<tr>
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</table>

Compared to stud rectifiers of similar current rating, the silicon MR751 series devices install faster, easier and take less space. And their new, button-shaped, voidless plastic case provides insulation against accidental shorting or grounding.

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inherently-balanced dipoles. The feedthroughs that penetrated the topmost stripline board to connect the dipoles to the corporate-feed board, a quarter wavelength below it, often had to double as a baluns as well. Motorola now claims to have dispensed with this arrangement and to have come up with a feedthrough technique that yields a perfectly flush topmost surface.

ITT has a design that also eliminates feedthrough. A 30-by-30 inch monopulse array with a dual-circular polarization capability and made of four modular subarrays has been successfully tested. ITT's design mates dipole elements and balanced corporate feeds on the same sides of stripline boards, so that neither feedthroughs nor baluns are needed. The company has contracts from both the Army and the Navy for perproduction prototypes of X-band arrays.

Raytheon sues Intersil. The Raytheon Co. has filed a plea for $2 million damages and a permanent injunction against Intersil Inc., its subsidiary the Intersil Memory Corp., and 14 Intersil employees. Among other things, Raytheon charges the men and companies with raiding Raytheon Semiconductor division personnel, specifically men in key integrated circuit marketing, memory design, and processing slots. Raytheon's initial complaint is about 30 pages long and corporate rage shows even through its legal jargon. It accuses the companies, Intersil board chairman Jean Hoerni, and 13 former Raytheon employees of causing "inestimable damage" to Raytheon's stance in the semiconductor memory field, of trading in proprietary information—not just of Raytheon but that of Raytheon's customers—and in some cases of taking what were in effect secret plans for at least two new products, a 64-bit and a 256-bit bipolar memory.

Said Intersil president James Riley, who just moved over from Signetics: "Legal counsel has advised me that, after their initial pass through the papers, Intersil has committed no wrong."

Comprehension. The Institute of Printed Circuits, looking to the day when punched cards or magnetic tape will replace drawings and printed specifications, is taking steps to make sure that everyone speaks the same language. A committee on computer-aided technology has issued a proposed standard covering formats for descriptive data. It's to be offered for acceptance at next fall's institute meeting after expected trimming at the April conference.

Me too. "What the 'ins' can do, we can do better." That was the RCA theme, referring to IBM, when RCA announced its new line of computers. RCA claimed that the line, the RCA 2, 3, 6, and 7, would make it No. 2 in computers by the late 1970s despite the Honeywell acquisition of General Electric's computer market.

The new line's principal feature is its compatibility with existing RCA Spectra 70s and IBM System 360s. In addition, the machines can emulate both RCA and IBM second-generation machines. And the 3 and 7 have extensive virtual memories, an important feature for time-shared operation [Electronics, Sept. 14, p. 34]. This isn't a new idea, having been used previously on certain models of RCA's Spectra 70 and on the GE 645, the IBM 360/67, and several Burroughs machines. GE and IBM have been soft-pedaling their virtual memories because of serious difficulties with software; Burroughs' success with software for its disk and drum systems, including virtual memory, has been legendary. The memories are assembled from standard 131,000-byte modules in capacities that reach over two million bytes in some models. Cycle time is 1,440 nanoseconds for the two little ones and 765 ns for the two big ones.

RCA is particularly proud of its new policy of guaranteed conversion to any of the new machines from an IBM 360/30, 40, or 50.

Electronics review

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How did Hughes get a reputation for innovation?

That's how.

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September 28, 1970

One-man rule sought for FCC and other regulatory agencies

A proposed reorganization of the Federal Communications Commission and other independent U.S. regulatory bodies would give FCC chairman Dean Burch [see p. 85] and his counterparts vastly increased authority by effectively substituting one-man rule for the majority voting of a multimember commission. The proposal is contained in an Executive branch reorganization study by a Nixon task force—one of a score named before his election—and may show up in next year's legislative program. Former FCC chairman Newton Minnow confirms the plan, noting that his views were solicited by the Administration. "I think it's a good thing," he says, adding that he proposed comparable changes to President Kennedy. Members of Congress aware of the program are skeptical of its chances but are keeping silent until they see what is proposed.

NASA delays station to save shuttle

The 12-man space station program will slip at least two years as NASA tries to free more of its declining budget for its No. 1 manned space project—the $7 billion space shuttle. Meanwhile, the space agency is asking North American Rockwell and McDonnell Douglas to rethink their preliminary design studies, which call for about $500 million in electronics developments for the $3 billion station.

Many of these restudy areas—the impact of metric tooling in building the craft or the use of on-board nuclear power plants, for example—look like make-work projects designed to hold design teams together long enough for NASA to try rallying Congressional support for the station. To date, NASA's efforts have been aimed at stirring the scientific community's interest in the station's potential. If this fails, chances are NASA will promote the military surveillance and missile defense uses.

Whitehead cuts back staff at Office of Telecommunications

While still awaiting Senate confirmation, Clay T. Whitehead moved in as director of the Office of Telecommunications Policy and started heads rolling. Facing a Congressional cut of about $1 million in a $3.3 million fiscal 1971 budget request, Whitehead is eliminating an estimated 16 to 30 jobs to make space for systems analysts and econometricians.

General Dynamics to compete in race for AADC memory

The Navy is proving it's serious about ferroacoustic memories by looking for a second source. A memory developed by General Dynamics for antisubmarine warfare applications will compete with Sylvania's thin film Soniscan [see page 34] for the mass memory of the Naval Air System Command's Advanced Airborne Digital Computer program. Navy sources disclose that a $20,000 contract will be awarded to General Dynamics to deliver a memory with 32 data lines, each packing 256 bits, for study.

The firm's approach differs from Sylvania's in that General Dynamics' memory uses strands of plated wire with PZT transducers fixed at the end of each wire instead of using thin film sandwiched between glass plates. Access to a block rivals Sylvania's—between 1.5 and 2.5 microseconds—but total block unload time, 150 nanoseconds, is only half as fast. General Dynamics claims ease of fabrication and an order of magnitude higher output level are offsetting advantages. Storage density is expected to run to $10^{10}$ bits per cubic foot and the cost, although General Dynamics won't discuss it, to around 0.1 cent a bit.
NASA labs open for down-to-earth research projects

As cutbacks in the space program force layoffs and shutdowns on NASA, the agency is turning from the problems of outer space to those of inner space—such as environmental monitoring and law enforcement [see p. 42]. NASA's agreement with the Interior Department to provide space and equipment at its Mississippi Test Facility to the Bureau of Commercial Fisheries may be followed by others.

The bureau will move in for studies on the location and abundance of ocean fishery resources, and for environmental monitoring. The bureau plans to experiment with aircraft-mounted lasers, low light-level image intensifiers, and other sensing equipment to detect schools of fish and to monitor pollution of the Gulf of Mexico. BCF research will help pin down the remote sensing techniques that will be included on NASA's Earth Resources Technology Satellite and the manned Skylab.

House computers gain with dropping of joint data unit

The House of Representatives' computerization program [Electronics, Aug. 31, p. 41] got a boost when the House overturned an amendment to the Legislative Reform Act that would have established a joint committee on data processing. Capitol Hill sources say a joint House-Senate effort would have slowed down the work of the House, delayed issuance of requests for proposals, stalled its two-year specification development program, and given the Senate the opportunity to dominate the design of House computers. The reform bill, which must still clear a House-Senate conference, also provides for electronic voting on the House floor. A Senate computer services subcommittee, headed by Sen. B. Everett Jordan (D., N.C.), has just begun planning a system and, with the outcome of the reform bill vote, the Senate will have to go ahead on its own.

FAA move dooms uhf satellite of NASA/ESRO

The joint NASA/European Space Research Organization plan to fly two uhf satellites for relaying North Atlantic airline communications looks dead. Federal Aviation Administrator John Shaffer, dooming the plan, instead will support a hybrid system that would relay operational vhf and experimental uhf traffic. The first hybrid would be launched by Communications Satellite Corp. over the Pacific in 1973. Uhf vs. vhf is still a controversial issue in Europe, one reason why the Pacific route will be first.

Airlines and FAA will split the $6.7 million annual cost of the uhf channels, while the $7.8 million uhf costs will be written off to R&D. Since most satellite costs are in R&D, an identical satellite for relaying North Atlantic communications could be launched by Comsat for $4.5 million a year, instead of $30 million for the NASA/ESRO plan.

Addenda

Donn L. Williams, president of North American Rockwell's Autonetics division will lead an Electronic Industries Association study of what the industry will look like in 1985. The study will include social, political, and technological factors that could affect the industry, and predictions about technological developments. . . . The Department of Labor is supporting an American Institute of Aeronautics and Astronautics program to transfer unemployed defense and aerospace engineers and scientists to other industries. The department will fund a pilot project starting in Philadelphia and extending to Boston, Huntsville, Dallas-Fort Worth, and Seattle. Success could push the program into 25 other cities.
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Read-mostly memory is nonvolatile and reprogramable

A new type of semiconductor memory that features the nonvolatility of the read-only memory combined with the flexibility of the random access type has been developed and made available in evaluation quantities. This read-mostly memory exploits the property of amorphous semiconductors that change from an ordered to unordered state with a pulse of energy. The array can be programed by converting the individual cells to the proper state; the data can be read by measuring the cell's resistance. And since conversion of cells is reversible, reprogramming is achieved by changing state.

Theory gives shape to amorphous materials

Several amorphous semiconductor devices have been built and are working in laboratories. But before they'll present a real challenge to conventional semiconductor technologies, the problem of learning how amorphous devices function must be solved. Progress in this direction is being advanced by a new model that explains the puzzling behavior of threshold and memory switches.

Companies split on future of amorphous materials

Arrayed against the few semiconductor companies, research labs, and Government groups that see a real future for amorphous devices are many firms doubtful that the devices will ever hit the market. In the middle is a substantial group that has adopted a wait-and-see attitude. The picture abroad is somewhat more optimistic, and work is proceeding apace in several countries.

Audio noise: Why settle for more?

Manufacturers of bipolar transistors often don't specify optimum noise figures, and designers who accept these figures as the best available put up with much more noise than they need to. Now there's a simple way to calculate the optimum collector current; it's used in a complementary two-stage amplifier with a noise figure below 1.5 dB and gain greater than 60 dB at audio frequencies.

Coming

Semiconductor memories are just beginning to become attractive from a cost standpoint. But the real cost of a semiconductor memory often remains elusive. The typical cents-per-bit quote for memories can be quite deceptive unless the oft-hidden packaging costs are taken into account.
Amorphous semiconductors Part I

Nonvolatile and reprogramable, the read-mostly memory is here

Integrated arrays combine amorphous and crystalline technologies; new memories could help realize promise of microprogramming

Gordon E. Moore, Intel Corp., Mountain View, Calif.

Both the read-only and random access varieties of semiconductor memories leave something to be desired in many applications. RAMs’ volatility allows data stored to disappear if power fails. On the other hand, ROMs’ inflexibility commits them to data that cannot be changed.

A new kind of integrated circuit, the “read-mostly memory,” avoids these problems. An integrated array of amorphous and crystalline semiconductor devices available in sample quantities, the RMM can be programmed, read, and reprogrammed repeatedly. And once programmed, the RMM retains data unless it’s intentionally altered. The RMM, therefore, doesn’t need the data storage on card, tape, or disk required to back up a RAM if power fails. And the same RMM can be used even if the program must be changed; there’s no need for the time-consuming and expensive process of making new masks and fabricating a new IC, nor is it necessary to substitute an electrically programable but fixed type circuit.

Microprogramming—a computer technique in which a programmer can at will change an operational code or sequence—probably will emerge as the most important application for these devices. The great promise of the microprogramming concept has yet to be fully realized—even though the idea originated almost 20 years ago—because of the difficulty and cost of changing the contents of microprogram stores with available memory devices.

Several fertile areas exist where the read-mostly memory’s special property of electrically alterable, nonvolatile data storage could be usefully applied:

Airborne computers, which often require a different set of data for a particular mission or operational situation.

Industrial control systems, in which “canned cycles”—stored instructions—must occasionally be altered for new tooling, instrumentation, or test procedures.

General purpose computers in which, even though storage of fixed data is required, it is still desirable to make changes in the data during the design phase.

Physically, the new 256-bit RMM’s organization is a 16-by-16 matrix of amorphous semiconductor cells, which must be isolated from each other by integrated silicon p-n junction diodes, as shown on opposite page, to prevent spurious paths in the array. The 122-by-131-mil chip size gives a packing density comparable to that of bipolar or MOS techniques, and should improve with fabricating experience. The chip is enclosed in a 40-lead ceramic dual in-line package.

Each cell in the memory consists of an Ovonic amorphous semiconductor device and an isolating diode in series on a silicon substrate, as shown opposite. The Ovonic structure itself consists of a film of amorphous semiconductor material between two molybdenum electrodes. Many such cells—series combinations of Ovonic devices and silicon diodes—are arrayed over the silicon chip, with each cell addressable by an x-y grid, as shown on opposite page, below. The cell behaves like a nonvolatile bistable resistor with an on-to-off resistance ratio of about $10^4$.

Without the series diodes, a few adjacent Ovonic devices in the on (low resistance) state might make the Ovonic device being interrogated appear to be on when it’s really off. The resistance of the adjacent cells would shunt the off resistance. With the diodes, however, the back resistance of the diodes added to the on resistance of the adjacent cells prevents this ambiguity.

Despite some superficial resemblances, the amorphous semiconductor memory is quite a different animal from the electrically alterable, fusible type of memory recently introduced by such companies as Radiation Inc., Motorola Semiconductor, and the Solid State Scientific Corp. Although both types can be reprogrammed in the same way, the fusible type’s program can’t be changed, whereas the amorphous semiconductor RMM can be reprogrammed repeatedly.

Changing the memory cell from a high-resistance (disordered) to a low-resistance (ordered) state and vice versa—that is, programe—does by applying a pulse of a certain voltage, current, and duration. The cell can then be interrogated, or read, without changing its state by applying a constant current and measuring the voltage to determine whether the Ovonic device is in its high or low resistance state.
What defines a set or a reset pulse is not so much its energy as its energy-time profile. Thus, the SET, RESET, and READ operations for the 256-bit memory array require significantly different drive conditions, with voltages varying from a few volts to 25 V, currents from a few milliamperes to over 100 mA, and pulse widths from nanoseconds to milliseconds.

Typical configurations for SET, RESET, and READ driving are shown on the next page. Preparatory to a SET operation, the critical voltage of the Ovonic memory must first be exceeded and a current of several milliamperes must flow for several milliseconds to ensure stable conversion to the low resistance state. This action is accomplished by bringing a selected y line (connected to the cathode of the memory cell to be set) to ground through a saturated transistor. Simultaneously holding all other y lines at 25 V (the inhibit voltage) reverse biases the other diodes and thus isolates the memory cells. At the same time, a selected x line (connected to the anode of the memory cell to be programed) is driven by a 5 mA constant current source at 25 V to insure that the amorphous semiconductor is in the ordered state. The drive voltage, however, must not be allowed to increase above 25 V, since a breakdown of the isolation diodes might result.

In the RESET operation, the procedure is similar except that the current source is increased to 200 mA and the pulse width is reduced to 5 μs. As with the SET operation, the voltage is limited to 25 V.

To read a cell it's merely necessary to apply a fixed current to the cell and measure the voltage drop. A low voltage indicates an on (or SET) cell, and a high voltage indicates an off (RESET) cell.

The circuitry for the READ operation must identify the SET and RESET states of a cell quickly. A typical READ condition is a 2.5 mA constant current applied to the selected x line and a grounded y line. The READ output voltage will then be less than 3 V for a SET memory cell, 5 V for a RESET cell.

The memory cell array's line capacitance and the storage time of the isolation diode determine reading speed. For fast reading, the large capacitance of the y line should be driven by a low-impedance source. The small capacitance of the x line should be driven by a constant current source to forestall excess current through the amorphous memory switch. Such conditions could produce an array access time of about 65 ns. However, the access time for the system would be somewhat longer because of the propagation time of the decoder and driver circuitry.

Of course, the drive circuitry will vary with the particular system. Some applications may not require SET or RESET circuits in the system itself; programming would then be done by external equipment. Also, dropping the 25 V requirement for setting makes the READ circuit relatively easy to implement.

The simplicity and cost of the READ circuit depends

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Array. The isolating diodes and Ovonic switches are connected in series across on x- and y-address line. The RMM consists of 256 such combinations.

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Electronics | September 28, 1970
How to read. The operations of SET, RESET, and READ require significantly different values of voltage, current, and pulse duration. Typical conditions are shown in this diagram. The inhibit voltages are for the x and y lines, respectively; n is the number of diodes; V<sub>n</sub> is the critical voltage of the amorphous semiconductor; I<sub>p</sub> is the pulse duration, and τ<sub>x</sub> and τ<sub>y</sub> are the time constants affecting READ speed.

on speed requirements: at lower speeds, simple pull-up resistors will suffice, but higher speeds necessitate active pull-up drivers.

One of the knottiest aspects in the RMM development was how to integrate the amorphous-semiconductor memory cells and the silicon substrate containing the isolation diodes. The processing conditions and sequence had to be adjusted to insure compatibility with both types of device.

The problem centered on the aluminum that connects the isolation diodes to the Ovonic devices. This aluminum must be alloyed to the silicon substrate to assure a good contact. However, alloying requires a temperature of about 550°C, which would be too high, since it would also destroy the amorphous semiconductor materials.

This impasse was resolved by depositing and etching the molybdenum film that serves as the lower electrode for the amorphous semiconductor and then alloying or sintering the aluminum to both the silicon diode and the molybdenum. A clear molybdenum surface contact is thus left for the amorphous semiconductor. The aluminum applied over the amorphous semiconductor cell in a subsequent step need not be alloyed, since it doesn’t contact the silicon.

As in other p-n junction isolated silicon ICs, the breakdown voltage is an important consideration. The diodes consist of p-type regions of material in n-type channels, as shown on the preceding page. Each of the 16 n-type channels contains 16 diodes. The maximum voltage that appears across the n-type channels must not exceed the breakdown voltage between the n-type channel and p-type isolation channel.

The resistivity of the n-type channel is about 20 Ω per square for the process used (the same process used to make transistor-transistor logic ICs). For a square memory cell (Ovonic device and isolating diode combined), the line resistance for 16 diodes would be 320 Ω. A RESET current of 100 mA passing through the channel develops a voltage drop of 32 V. In the TTL process, however, breakdown voltages usually range from 25 to 30 V, and the channel isolation would fail.

Diffusing a shallow n<sup>+</sup> region along the channel reduces the channel’s resistivity from 20 Ω per square to 3 Ω per square and, hence, the voltage drop across it. Moreover, no extra process step is required as the n<sup>+</sup> diffusion is needed anyway to establish an ohmic contact between the aluminum and the n-type channel.

In addition, the diode channels have both ends shorted by an aluminum conducting strip around the outside of the array, reducing the effective interconnection resistance to the cells. This places the channel in parallel with the addressing line, reducing their effective resistance. Thus, the channel resistance for a channel of n cells is nR/4 when both ends of the channel are shorted, whereas it would be nR without the short.

Repeatability of SET and RESET cycles was the prime concern at the outset of the RMM development. Large quantities of devices were therefore tested to
Benign disorder

The useful bistable characteristic of amorphous semiconductor material depends on a reversible phase transition between two states of greater and less disorder—high resistance and low resistance. To understand the effect, consider the diagram of a thin film of amorphous semiconductor in a memory system, shown below. (The physics of amorphous-semiconductor switching are explained in detail in the article immediately following.)

To switch a cell to the low resistance state, a voltage exceeding a certain threshold value \( V_t \) is applied across the amorphous film. This is an electronic process, not a thermal one, which switches the film and establishes an initial conducting condition that enables electrical energy to be deposited in a confined film channel. A 50-microjoule pulse applied in about 10 milliseconds heats the amorphous material sufficiently to change phase from a disordered or glassy state to a more ordered structure.

The crystallization, or ordering process, is related to the participation of carriers from the valence band—those that form the bonds in the material—in the conduction process. The removal of these carriers from the bonds allows atomic restructuring to take place more easily. Accompanying this change is a drop in resistance of some 3 or 4 orders of magnitude. The sequence of events is called the SET process.

The duration of the SET pulse is important. For example, a pulse of only 0.1 ms won't create a permanent change in the amorphous film even though the pulse's amplitude is greater than the critical value \( V_t \). The memory cell will switch to the low resistance state, but when the pulse terminates, the cell reverts to the high resistance state. But with a 10-ms pulse, the cell will remain in its low resistance (or on) state after the pulse is removed. The cell then can be continuously interrogated nondestructively. This is called the READ operation.

To return the cell to its high resistance (RESET) state, the material must be changed back to the disordered state. This is achieved by applying a high current for a short period (approximately 5 microseconds). With about 5 \( \mu J \) of energy deposited in the cell, the heat generated returns it to the amorphous state, as shown below. The cell cools rapidly, and the disordered, high-resistance state is maintained at room temperature. A typical cell with critical voltage of 15 \( V \) is about 1.5 micrometers thick and 5 \( \mu m \) in diameter; such a cell cools to room temperature in about 1 \( \mu s \).
Time and energy. To ensure that the memory is stably SET or RESET without any damage, the energy-time product should fall within the shaded regions. Also an effective change of state requires sufficient RESET energy to reconvert the entire volume of material that has been SET. Thus, a cell that has been SET in region B must be RESET in region A evaluate the effects of repeated cycling on various switching materials and geometries, and under differing processing and operating conditions. Thousands of test cycles were made.

It was found that the composition of an amorphous semiconductor can dramatically affect the lifetime of the device. The curves shown at left illustrate the effect: the nearly vertical short-lifetime curve (marked E) represents a three-element glass amorphous semiconductor.

The addition of only 2 atomic percent of an additional element shifts the cycle lifetime behavior to the nearly horizontal long-lifetime curve (marked A). The three upper curves represent material compositions that Energy Conversion Devices is studying and optimizing for use in the RMM.

The energy-time combination used for SET and RESET also profoundly affects life expectancy. Certain regions on an energy versus time plot result in effective setting and resetting, as shown above. An energy-time combination that falls below the regions defined by the roughly triangular regions will not produce a stable change in the state of the memory cell, not even after repeated cycling.

Another consideration is the energy-time balance; a cell set with a certain energy within the SET safe area must be reset at a compatible energy in the RESET safe area. Thus, if a cell is set within the ellipse marked B on the energy diagram, then the proper RESET condition should lie within the circle marked A.

The reason for this correspondence requirement is that the area of the converted region in the amorphous semiconductor film depends on the SET energy-time. Thus, as more material is converted to the ordered state, a larger energy is required to achieve a stable reconversion.
Amorphous semiconductors Part II

Theory gives shape to amorphous materials

Before conventional semiconductors can be challenged, the problem of how amorphous devices function must be solved; the new CFO model explains hitherto puzzling behavior of threshold and memory switches

By David Adler, Massachusetts Institute of Technology, Cambridge

The promise of amorphous semiconductor materials won't be realized until their characteristics can be precisely defined and their operation controlled. The problem is that they behave quite differently from crystalline semiconductor materials. And two batches of apparently the same amorphous material could behave quite differently from each other.

The possibilities inherent in amorphous semiconductor technology are enormous. One promising application is memory switching, leading to densities on the order of a million bits per square centimeter. For optically controlled memory switching, amorphous alloys could be developed that would be transparent to red light when switched on and opaque when off. This might be the heart of an easily alterable memory with a capacity as high as \(10^{12}\) bits.

And that's not all. Amorphous technology lends itself to electroluminescent displays; its hysteresis characteristic can be used as a threshold switch to turn a lamp on and off continuously over a large voltage range. Another possibility is ac thermostat switching, because threshold switching for conduction decreases sharply with increasing temperature.

But trumpeting these laboratory devices and projections would be premature if the theory behind their operation were still as uncertain as the models for the behavior of those recent casualties, the tunnel diode and the LSA diode. Nevertheless, new models provide the amorphous materials with a firmer theoretical basis that has hitherto been available.

One, called the CFO model after Cohen, Fritzche, and Ovshinsky, its originators, characterizes the energy band structure and electrical properties that are observed in many amorphous (chalcogenide) glasses, besides accounting for other observations, such as their optical absorption and radiation hardness. Still more recently, two additional models (HF0 and F0) use the CFO model to build a coherent theory of amorphous switching. And although there remain some observations that are as yet recalcitrant, these models, taken as a theoretical foundation, go a long way towards elucidating the elusive properties of the amorphous state of solids.

These properties are best understood in terms of the differences between amorphous and crystalline solids. A crystalline solid is a periodic array of atoms.

The environment of any atom in it is exactly the same as that of any other equivalent atom. But though the environment of an atom in an amorphous solid may be identical with its nearest neighbor's, its second-nearest neighbors may be twisted around. And most likely its tenth-nearest neighbors in the amorphous material will have essentially random orientations. In short, amorphous solids have a high degree of short-range order, but no long-range order.

Another point is that the amorphous state of a given composition generally has higher energy than the corresponding crystalline state; said another way, amorphous solids are metastable. This is not the same as unstable, which refers to materials that readily crystallize. But amorphous solids will in fact crystallize if heated above a critical temperature and slowly cooled (annealed), though they will not readily do so at the temperatures ordinarily associated with crystalline solids. So to avoid crystallization, they must be made by rapid cooling (quenching) from the higher-temperature disordered state.

Early investigators fabricated amorphous material by quenching from the liquid state, and the resulting compounds were generally called glasses. However, an important class of crystalline semiconductors, in-

Subtle. MIT's amorphous chalcogenide film, sandwiched between 200 micron-diameter molybdenum contacts, was sputtered in argon atmosphere.
cluding germanium and silicon, enter an amorphous state only when deposited on a cold substrate directly from the gaseous state. Quenching their liquid form merely turns them from molten to solid metal, which is not a semiconductor. Another kind of amorphous solid can be produced by chemical reactions at temperatures below the diffusion point, where crystallization does not occur. This is the method used to manufacture electrolytic capacitors.

Altogether, no fewer than a dozen methods have been used to produce amorphous materials with widely different properties. Moreover, it is possible that several different types of metastable disordered structures can be formed from a single chemical composition, depending on the exact preparation technique, or perhaps even on pure chance. And, it can seriously be questioned whether only one type of elemental material, say, Ge, yields only one type of amorphous compound. But though such an abundance may be a boon to device designers looking for a particular functional property, it poses a serious problem in characterizing amorphous solids.

In classifying such solids, the criterion is the type of chemical bonding between atoms that is primarily responsible for their cohesive energy. For amorphous semiconductors, two types are important—covalent bonding, in which atoms share electrons, and ionic bonding, in which ions with opposite charges are held together by the Coulomb attraction. The former group can be subdivided into pure elemental materials and mixed systems, making altogether three classes of amorphous material of value to semiconductor technology: the elemental amorphous semiconductors, examples of which are germanium, silicon, tellurium, sulfur, and boron; the covalent amorphous semiconductors, which encompass such compounds as GeTe, As2S3, and the chalcogenide, arsenide, and boride glasses; and the ionic amorphous semiconductors, compounds such as V2O3, Al2O3, and the transition-metal oxide glasses.

Once classified, the structure of the material in each class can be identified. The fact that amorphous semiconductors are spatially disordered systems presents a problem in analysis. But other disorders have been handled in the past, and these provide a feel for the disorder effects in amorphous compounds.

A perfect crystal in its ground energy state has perfect order—compositional, positional, and magnetic (spin). Given the position and type of an atom anywhere in the crystal, and the crystal's orientation, the exact position and type of every other atom can be determined, as well as the orientation of any permanent magnetic moments.

However, crystals are never perfect; and their imperfections can be characterized. An impurity represents an imperfection in compositional order; a defect (i.e., a vacancy or an interstitial atom, one which is located between regular lattice sites in a crystalline solid) is an imperfection in positional order; and a reversed spin is an imperfection in magnetic order. And crystals always have surfaces, which cause further positional disorders.

In addition, there are dynamic imperfections, often called elementary excitations. For example, lattice vibrations (phonons) are dynamic imperfections associated with positional disorder, and spin waves (magnons) are dynamic imperfections associated with magnetic disorder. Since elementary excitations often

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Then there was Ovshinsky

The surge of interest in amorphous semiconductors excited by the Physical Review Letter published by S. R. Ovshinsky in late 1968 was as much a surprise to active researchers in the field as to the rest of the scientific and engineering community. Why all the fuss? Application of amorphous solids is everywhere. Glass is a venerable example. Electrolytic capacitors that are made of amorphous aluminum oxide or tantalum oxide are familiar to everyone working in electronics. And there are the new developments with xerography, based on the photoconductive properties of amorphous selenium or arsenic triselenide.

Nor could the tumult over Ovshinsky's disclosure have been due to the promise of switching. For amorphous switching had been applied by Ovshinsky himself over 10 years before, and was widely reported in the literature [Electronics, Aug. 14, 1959, p. 76]. Since that time variants have been rediscovered by many people. Further, such switching is not unique to amorphous materials; having been found in polycrystalline materials, such as antimony sulfide, nickel oxide, and recently even in single crystals of gallium arsenide, tin sulfide, and magnetic oxides. And switching aside, current-controlled negative resistance was observed in boron as early as 1913.

If this were not enough to dampen enthusiasm for new amorphous phenomena, the final quench should have been the documented proof from many well known investigators that the switching was the uncontrollable result of such side effects as chemical reduction or sputtering of electrode material into the sample. Besides, early devices were reported to be unreliable, failed early, often could not be reproduced, changed characteristics with time, and melted during operation.

So why the massive rush into an archaic field with useless products? The answer is clear. While amorphous semiconductor materials have been known and used for centuries, only in the last few years has a theory been developed to explain their electrical and optical properties. This work has suggested new and fascinating concepts, which studies of crystalline semiconductors have not even hinted at. Although switching and current-controlled negative resistance have been observed in many materials over the past 50 years, switching, for example, in thin films of certain chalcogenide glasses is a different story entirely. In fact, it's because of their properties and our increased understanding of the basic physics that the probability of commercial applications of amorphous phenomena has increased many times over.
require little or no additional energy, they have important effects at low temperatures.

Then there are situations, like high temperatures, in which complete disorder occurs. A ferromagnet above its Curie temperature has no long-range magnetic order. An alloy above its ordering temperature shows long-range compositional disorder. Any liquid has long-range positional disorders. However, in every case, short-range order persists.

Similarly, all amorphous solids exhibit complete long-range positional disorder. For the elemental amorphous semiconductors, this is the only form of disorder: the position of any atom in amorphous Ge will reveal its identity. The covalent amorphous semiconductors, however, also have compositional disorder, and thus some ambiguity could exist in its chemical formula. For example, As$_2$Se$_3$, nominally contains 40% As and 60% Se, but it can be made just as easily with 39% As, 61% Se, or 20% As and 80% Se, or any other proportion. In such compounds, when an atom is finally located, it's not possible to know more than the probabilities of which kind it is.

In any case, the electrical properties of each class can be dealt with once the amorphous crystals have been classified structurally. The basic theory of electrons was applied to solids by Bloch and Wilson some 40 years ago. Bloch showed that, for a perfectly periodic arrangement of atoms, the energy states available to an electron as it moves through a solid are continuously distributed over certain energy ranges, called bands, but are excluded from other energy ranges, called gaps. More important, these states are itinerant, so that every electron is equally likely to be found anywhere in the crystal, rather than being localized around a particular atom, as might have been inferred from atomic physics.

Building on Bloch's conclusions, Wilson showed that a material is an insulator when it has just the right quantity of electrons for all of its energy bands to be exactly full or completely empty, with an energy gap between the highest filled band and the lowest empty band. Other materials, having one band partially filled, are metallic (conductors).

With the Bloch-Wilson theory, only a minimum of computation is needed to predict whether a particular crystal will turn out to be insulating or metallic. More important, the theory cleared up the problem of metal's high conductivity. Previously it had been thought that ion cores, by scattering conducting electrons, should produce a mean free path about as wide as a lattice spacing. Instead, mean free paths of over 100 lattice spacings were commonly observed—the well-known mystery of the unscattered electrons. The Bloch-Wilson theory, however, showed the itinerant nature of the outer electrons to be responsible for the long mean free paths. And this itinerancy follows from the assumption of perfect periodicity.

At first glance it would seem that periodicity is vital in explaining the electrical properties of crystals. Not so. Consider electrical conductivity as a function of temperature, taken through the melting point, which is the temperature at which long-range periodicity disappears. If periodicity were the controlling factor in electrical conductivity, a sharp drop in conductivity at the melting point would be expected. However, except for several materials whose short-range order changes discontinuously at the melting temperature and leads to a semiconductor-metal transition, almost nothing happens to the electrical conductivity of solids when they melt—metals remain metallic, semiconductors remain semiconducting, and insulators remain insulating.

Typical conductivities of several materials over a range of temperatures are shown in Fig. 1. As seen, small discontinuities may occur at the melting point, but in every case these can be attributed to the discontinuity in material density and not to disorder. Furthermore, in some materials that experience no sharp change in density upon melting, it is impossible to pick out the exact melting temperature.

Clearly, long-range periodicity does not appreciably affect electrical properties. So a promising tack to take in determining the electrical properties of an amorphous material, which exhibits long-range disorder, is to assume perfect short-range order and

![Graph of Resistivity vs Temperature](https://example.com/graph.png)

1. Insensitive. Resistivity as a function of temperature for several materials in both the crystalline and liquid states shows insensitivity of material's conductivity to long-range disorder. At the melting point indicated there is almost no change in the observed resistivity, and in a few cases (CdTe and Cu$_2$Se), where there is no observed density change upon melting, no change at all is observed in the resistivity.
then, as with crystalline solids, introduce the effects of long-range disorder. These would include both a perturbation on the band structure and an additional scattering process that could limit the free carriers' mobility (velocity per unit of applied field).

The question, then, is if a material, whether crystalline or amorphous, maintains its short-range order. A liquid is known to possess a great deal of short-range order just above the melting point, where its long-range order is destroyed. X-ray and electron diffraction techniques show that the same is true for amorphous solids. For example, the nearest-neighbor environment of amorphous Ge is the same as in crystalline Ge; each Ge atom is surrounded by the same regular tetrahedron of Ge atoms. Since the band structure of a semiconductor is determined primarily by the nearest-neighbor configuration, the electronic density-of-states for amorphous and crystalline Ge should not be very different.

Now, the density of states \( n(E) \) of crystalline Ge are known and are shown in Fig. 2, left. The perturbation induced by the deviations of second and higher neighbors from the Ge crystal structure will move some of the states up in energy, others down. In the relatively flat portions of the bands, where \( n(E) \) is large and essentially independent of the energy, these perturbations will have very little net effect. But near the valence and conduction band edges, they will have a profound effect, since they will move some states into the energy gap—and this, as a first approximation, is what the density-of-state of amorphous Ge might be expected to look like. The plot for amorphous Ge is shown in Fig. 3, right. Small densities of states exist in what previously was the forbidden gap and are called band tails.

This new band configuration makes it possible to describe the conduction properties (electron mobility) of amorphous Ge. Consider a state in the high-density part of the conduction band (point A in the figure). The density-of-states at \( E_A \) is of the order of \( 10^{22} \) per cubic centimeter per electron-volt. From this value, it can be calculated that the average separation of states within an energy \( kT \) (0.025 eV at room temperature) at \( E_A \) must be about 10 angstroms. This
means that, assuming all states to be localized and separated from each other by a potential barrier, an electron in one of the states of energy $E_A$ near the negative electrode can tunnel to any other state of equal energy that's within a 10 Å radius. Since the tunneling distance is so short, the probability is relatively large. Thus, despite the localized nature of the states, the electron can reach a reasonably high velocity when an external field is applied.

However, for an electron in the conduction-band tail, the average separation of states is much greater. For example, at point B, where the density of states is only on the order $10^{18}/cm^3/eV$, the average separation between states of energy within $kT$ (at room temperature) would be about 250 Å. The tunneling probability for this separation is near zero, as is the mobility of an electron placed in such a state. In fact, the only way to obtain conduction once an electron is localized deep in the band tail would be to excite it to a delocalized state higher in the band.

For an electron in a state in the region of intermediate density ($10^{18}$ to $10^{21}/cm^3/eV$), there are two possibilities: as the density-of-states decreases, its mobility might either decrease gradually to essentially zero or it might make a sharp transition. Although not final, recent theoretical work indicates the transition is sharp. If so, the energies $E_\nu$ and $E_\sigma$, which separate localized from delocalized states in the conduction and valence bands, represent sharp structure only in the mobility. They can be thought of as mobility edges rather than band edges as in crystalline Ge. Similarly, the minimum energy separation between delocalized states in the two bands can be called the mobility gap; it plays the same role as the energy gap plays in crystalline structures.

Such a model can be used to characterize the band structure in elemental amorphous materials. In a material like amorphous Ge, enough electrons are present just to fill the valence band, including the states in the tail, leaving the conduction band empty. Thus, at low temperatures, the Fermi energy is located almost midway between the band tails. However, unlike crystalline Ge the localized states in the band tails are not donor or acceptor states—those below the conduction-band mobility edge are empty and those above the valence-band mobility edge are filled. Furthermore, the material does not degenerate when the Fermi level rises into the conduction-band tail: degeneracy occurs only after the Fermi level crosses the mobility edge, $E_F$.

Most important, this band model can be used to explain some of the observed but seemingly irreconcilable properties of elemental amorphous semiconductors. For example, the electrical conduction of amorphous Ge is primarily p-type (dominated by holes) but, in sharp contrast to crystalline Ge, its electrical properties are rather insensitive to impurities.

Most probably, the p-type nature of the conduction represents the effects of defects (acceptors) in the amorphous structure. As the defects are removed, say, by annealing away the pores that are produced during vapor deposition, the resistivity of amorphous Ge increases sharply. This would be expected to happen if the acceptor (p-type) levels disappeared—but the conductivity is observed to remain p-type.

The explanation derived from the band model argues that the conduction band is more spread out in energy than the valence band. According to band theory, the higher the energy, the wider the band, all other things being equal. For a material such as Ge, all other things may well be equal, since the valence and conduction bands represent bonding and anti-bonding bands of the same electrons. So the conduction-band tail also is more spread out than the valence-band tail. Since the Fermi energy is midway between the band tails, it must be closer to the valence-band mobility edge than to that of the conduction band. The concentration of free holes, proportional to $\exp \left( -\frac{E_F}{kT} \right)$.
The doping riddle

Pure crystalline semiconductors are highly sensitive to small impurity concentrations. For example, a few parts per million of arsenic in zone-refined germanium increase charge transport (conductivity) by a factor of $10^4$. However, charge transport in chalcogenide glasses is virtually independent of impurity concentrations of even a few percent. Conductivity remains small, still given by the standard equation, $\sigma = \sigma_0 \exp (-E_A/kT)$, with the same energy at point A and $\sigma_0$ as in the pure material.

It's difficult to figure out what a donor-type impurity might be in a chalcogenide glass with a composition such as arsenic 30%, selenium 18%, germanium 21%, tellurium 31%. Ge has four outer electrons, As has five, Se and Te have six. It could be naively assumed that introduction of, say, iodine atoms, with seven outer electrons, as substitutes for Te, might provide extra electrons which could be easily excited into the conduction band. But the electrical conductivity of As$_5$Ge$_{18}$Te$_{31}$I$_5$ is indistinguishable from that of As$_5$Se$_{18}$Te$_{31}$I$_5$.

The explanation: chalcogenide glasses have both positional and compositional disorder. Thus it's probable that, during formation (in the liquid state), each atom seeks out a favorable position—one in which all valence requirements are locally fulfilled. This means that every Ge atom can find a position where it is surrounded by four, tetrahedrally arranged, nearest-neighbors, each sharing one electron with it in a covalent bond. According to the 8-N rule of covalent bonding, the lowest energy state of an As atom is one in which it forms covalent oxide with three nearest neighbors; during glass formation, each As atom can find such an environment.

Similarly, each Se or Te atom can find a position in which it has two nearest neighbors, thus fulfilling its local bonding requirements. Thus, the combination of positional and compositional disorder makes possible a network structure with no dangling bonds. But these dangling bonds lead to donor and acceptor levels in ordinary crystalline semiconductors. The extra electron contributed by an As impurity at a tetrahedrally coordinated Ge site can be freed from its atom with much less energy than an electron that forms part of a saturated chemical bond. So donors and acceptors may need not exist in chalcogenide glasses.

As for introducing donor levels by substituting I for Te, each I atom during the glass formation can find a position in which all of its valence requirements (one covalent bond) are locally fulfilled and no dangling bonds are produced. The fixed structure of a crystal enables I atoms to fill Te positions. But there is no way to accomplish this in an amorphous solid and it is virtually impossible to obtain ordinary conduction.

Junctions, too

In crystalline semiconductors, contacts can be either ohmic or rectifying, depending on the work functions of the materials in the junction. But it is peculiar (and useful) feature of chalcogenide glasses that contacts are always ohmic. In this sense, metal-chalcogenide glass junctions resemble metal-metal junctions.

Ordinarily, in the case of a negative electrode such as gold, it would be expected that a Schottky barrier as shown in Fig. 4 left, would be set up at $E_r$ and would impede injection of electrons into the semiconductor. But since a chalcogenide glass has an extremely large density of localized states in the gap, the entire position space charge can be accommodated within an estimated 30 Å from the electrode, as indicated in Fig. 4 right. Thus, electrons can now tunnel easily from the metal into the conduction band of the glass, providing the ohmic contacts that are observed.
if properties of common, traditional questions: university-of-states is overlap, composition, so overlap if band that negatively in temperature. band exist, states exist, which answer in equilibrium? The answer to the first question is that, despite the overlap, a state in the valence band tail can be distinguished from one in the conduction band tail. A localized state in the valence tail is normally filled, in which case the corresponding atom is neutral; if empty, it must represent a positively charged ion. On the other hand, a localized state in the conduction-band tail ordinarily is empty, and the corresponding atom is neutral; an electron input would create a negatively charged ion. Thus there is a sharp distinction between localized states in the two bands.

CFO's answer to the second question is that indeed a repopulation must occur, in which case the electrons fill the lowest possible energy states at zero temperature. Provided no donor or acceptor states exist, there will be enough electrons to fill the valence band exactly and leave the conduction band empty.

However, electrons in the highest valence-band states may lower their energy by falling into the states at the bottom of the conduction-band tail. This process will continue until the energy of the system is minimized and the Fermi energy then can be drawn above the highest filled states, as shown in Fig. 3. The same process, however, also empties some valence-band states, which therefore represent positively charged traps in the gap. Further, it fills some conduction-band states, which represent negatively charged traps. And most important, equal densities of positive and negative traps always exist in this model.

This is the essence of the CFO model. It accounts impressively for many experimental results on chalcogenide glasses, including some unusual properties. For example, it predicts that two types of electronic transport are possible—a bandlike, intrinsic conductivity due to excitation of electrons either above E_0 or below E, in Fig. 3, and a hopping type of conductivity among the localized states in the gap. The former should dominate at high temperatures, the latter at low temperatures. The mechanism underlying low-temperature conduction is not yet clear, but clearly high-temperature transport is the intrinsic process.

On the question of optical absorption, the CFO model accounts nicely for the observation that the absorption edge isn't as sharp as found in most crystalline semiconductors. Instead, the absorption coefficient is exactly what would be expected if large valence and conduction band tails existed, as the CFO model suggests. In this theory the mobility gap can be estimated as on the order of 1 eV.

Thermolectric effects also can be accounted for. The evidence from Seebeck-effect measurements shows that conductivity is bandlike and always p-type, regardless of the nature of the impurities. The discussion of amorphous Ge indicated that its independence of impurities was easily explained on the assumption that the conduction-band tail is more spread out. The CFO model has a still more successful explanation, because now it's assumed that the Fermi energy is pinned in the gap. But conductivity reveals an inconsistency: Hall-effect measurements, not necessarily trustworthy, indicate an n-type conductivity. This Hall-Seebeck puzzle, which sometimes occurs in crystalline semiconductors, has not yet been resolved.

The fact that amorphous solids are more resistant to radiation than the corresponding crystalline solids also can be explained by the CFO model. Radiation induces large densities of defects in crystalline semiconductors, thus increasing conductivity sharply. However, in amorphous semiconductors, where atoms already are disordered, radiation damage should not be very extensive. Exactly the extent of radiation...
Amorphous switching...

Many of the mechanisms that at first glance could be used to explain the switching of amorphous semiconductors in fact do not agree with the observed data. For example, threshold switching very likely is not due to avalanche breakdown, particularly since the mean free path of free carriers is less than 10 Å. With a threshold field of $10^5 \text{ V/cm}$, the carriers can gain only 0.01 eV from the field between collisions, and it would appear impossible for these carriers to bridge a 1-eV gap by impact ionization. Thus, no hot-electron effects should be expected in chalcogenide glasses.

Zener breakdown can also be eliminated, either because fields of $10^9 \text{ V/cm}$ will not yield a tunneling probability sufficiently large to overcome the recombination rate, or because the large negative resistance region is incompatible with the necessity of maintaining the tunneling rate.

Likewise, switching that depends on a field-induced increase in carrier mobility also can be ruled out. This would be a strong possibility if room temperature conduction were achieved primarily by means of a hopping mechanism, since such a field could bring about exponential mobility rises or even a transition to band-like conductivity. However, in the chalcogenide glasses, band-like conduction predominates at room temperature.

A structural transformation under high-field conditions would also appear to be possible. But this mechanism should be more likely in ionic materials than in primarily covalent compounds like the chalcogenide glasses, and the absence of any thermally induced transition is further evidence against such a model.

Thermal runaway, arising from extremely rapid Joule heating, also has been a frequent explanation. Such a model accounts for the negative resistance region, the pre-switching delay time, and filament formation. There seems to be little question that switching is thermally induced for bulk material and for films thicker than about 8 microns. And thermal runaway has been found to occur for particular compositions and geometries even in thin films. However, although a purely thermal model cannot fully explain the observed switching characteristics, the effects of Joule heating, particularly in the on state, cannot be ignored.

But the most promising method for reconciling the observed phenomena at present is an electronic mechanism of some kind. A large nonohmic region exists before switching in which current rises exponentially with voltage, and thermal effects are negligible. This represents a non-equilibrium electronic effect, strong evidence for an electronic mechanism for switching. Furthermore, the fact that the switching characteristics of thin films of chalcogenide glasses appear to be qualitatively different from those in other materials suggests that a mechanism must be sought which employs the unusual electronic properties of amorphous semiconductors.

The Cohen-Fritzsche-Ovshinsky model offers a useful feature for threshold switching—it requires the material to have essentially equal densities of positive and negative traps. This implies that a non-equilibrium state, in which all traps are filled, is neutral, and thus can support a high conductivity.

Recently, two possible models for threshold switching have been proposed which make explicit use of the cr0 model. Henisch, Fagen, and Ovshinsky (198) suggested a double-injection space-charge model, outlined in Fig. 5. Its main feature is the formation of a negative space charge near the cathode, caused by the trapping of electrons emitted from the electrode, and formation of a positive space charge near the anode. These space-charge regions limit the current and act as virtual electrodes, increasing the electric field in the bulk material. Eventually, the two space-charge regions overlap in the center of the film, producing a region in which all positive and negative traps are filled. Since this region is also neutral, it is highly conductive. Consequently, the state initially is unstable, electrons will be accelerated toward the anode, holes toward the cathode, and a highly conductive state formed very rapidly. It is sustained by double injection, provided that a holding voltage greater than the mobility gap is maintained across the film.

Fritzsche and Ovshinsky (190) suggested a competing mechanism, shown in Fig. 6. They point out that field-induced excess carriers can lead to formation of depletion regions near the electrodes, since the anode and cathode are unable to supply the holes and electrons necessary to maintain the non-equilibrium current. The resulting Schottky barriers grow with increasing voltage,

![Diagram of electron injection and space-charge regions](image)

5. Switching model I. The HFO model details threshold switching in chalcogenide glasses in several steps: (a) initial state of low conductivity, immediately after voltage is applied; (b) formation of space charges and space-charge limited current; (c) unstable situation resulting from overlap of the positive and negative space-charge regions; (d) conducting state, maintained by double injection of electrons and holes.
but the large density of traps in chalcogenide glasses enables the entire depletion region to be accommodated within 30 Å of the electrodes, maintaining ohmic contacts. Trap-limited current proceeds until all traps are filled; then switching occurs rapidly. The resulting on state is stable, provided the voltage that remains across the material exceeds the mobility gap.

Both the HFO and FO models can account for threshold switching, and both may even occur in the same materials at different ambient temperatures. The HFO model is essentially a low-temperature mechanism, while the FO model requires intermediate temperatures, in order to set up the Schottky barriers before threshold. The evidence now points to the fact that the transition between the two models occurs below room temperature. The pre-switching conductivity appears to be bulk limited rather than electrode limited, and the delay time has been shown to be largely independent of polarity reversals, in accordance with the FO model. Both models are consistent with the minimum charge injection observed before switching. As of now, the FO model has the virtue of at least surviving experimental elimination. The exact threshold behavior mechanism may not yet have been clearly determined, but switching can no longer be considered the complete mystery it once was.

6. Switching model II. The FO model for threshold switching in chalcogenide glasses: (a) initial state of low conductivity, immediately after voltage is applied; (b) formation of Schottky barriers, due to non-equilibrium carrier concentration in the bulk; (c) double-injection state with trap-limited current; (d) conducting state, obtained after all traps are filled.

immunity in chalcogenic glasses was discovered in the course of testing switches. Even when the radiation was intense enough to produce nuclear transmutations and destroy the cables attached to the devices, the switches remained within 10% of their original characteristics.

This phenomenally high radiation resistance is remarkable since it must be true that nuclear transmutations create defects that act as donors; in the band structure of Fig. 3 there are states originating in the valence band (i.e., neutral when occupied) near the conduction band edge.

These donor states, however, fail to produce extrinsic conduction. The explanation, according to the CFO model, is that there are unoccupied states in both the valence and the conduction band tails that are below the donor states in energy. The excess electrons fall into these states at equilibrium, leaving ionized donors behind them. And all that happens is that the Fermi energy shifts slightly to \( E_F + \Delta E_F \), which hardly affects the conductivity at all.

In addition to their radiation hardness, the photoconductivity of chalcogenide glasses could make them a versatile semiconductor material. With crystalline semiconductors, when light of an energy larger than the band gap impinges on their surface, intrinsic electron hole pairs are formed, which contribute to conductivity while the light is on. When the light is turned off, a rapid \( (10^{-8} \text{ s}) \) recombination reduces the conductivity to its equilibrium value.

According to the CFO model, the same would be expected of an amorphous semiconductor, provided that the photon energy exceeded the mobility gap. (This principle is the one behind xerography.) This just is what observed in chalcogenide glasses at room temperature. However, at the temperature of liquid nitrogen (77°C), Fagen and Fritzsche observed an excess dark conductivity that persisted for hours after the incident light was removed.

Again, this last result is explicable by the CFO model if the mechanisms for recombinating photo-excited electrons and holes are taken into consideration. At the moment that a light is turned off there are about \( 10^{12} \) per cm³ free electrons with energy above \( E_F \). These may recombine directly with free holes, which are mobile and are attracted to the free electrons by Coulomb interaction. A second possibility is that the electrons may be trapped in the localized states in the valence and tail that are unoccupied because of photoexcitation. These states are positively charged, and thus will attract free electrons. However, this process should be dominated by direct recombinations and its effect minimized, since the free holes are mobile and the concentrations of photo-excited free and bound holes are the same order of magnitude.

But most significant of all, free electrons may be held by the localized, positively charged traps that exist above the Fermi energy in Fig. 3. Although these traps are immobile, they are present in great numbers—roughly \( 10^{19} \) per cm³. This is about \( 10^7 \) times the concentration of holes below \( E_F \) and should result in the predominance of this process over direct recombination, even though its density-of-states cross-
section is lower than that for direct recombination. (Electrons also could be trapped at the neutral sites in the conduction band tail, but they lack Coulomb attraction, and the cross section is negligible.)

In all likelihood, therefore, most free electrons are trapped above the Fermi energy. There they become localized states, and cannot return to their equilibrium positions below \( E_F \) without the assistance of atomic vibrations (phonons). At room temperature, the atoms vibrate with sufficient vigor to restore equilibrium in less than 1 millisecond. However, at 77°K, their vibrations are very sluggish and electrons can stay above \( E_F \) for hours. Since the electrons are then nearer the conduction band mobility edge, they induce excessive dark conductivity, as observed.

Clearly, any nonequilibrium creation of excess carriers should lead to a similarly long relaxation time at low temperatures. A completely analogous result, in which excess conductivity remained long after the application of voltage pulses, also was observed by Fagen and Fritzsch at 77°K.

If the trapping of electrons above the Fermi energy level is the dominant recombination process, it should be observable in the fluorescent radiation emitted after photoexcitation. Kolomiets investigated the recombination radiation from a chalcogenide glass having a 1.1 eV mobility gap, and found two peaks. One, near 1.1 eV, results from direct combination, but a peak 50 times more intense was observed near 0.67 eV.

The interpretation of this result is far from clear. The CFO model predicts that dominant fluorescence should occur at less than half the gap, rather than at 60% of the gap, as observed by Kolomiets. In chalcogenide glasses, however, \( E_F \) would be expected to be nearer \( E_v \) than \( E_c \), to explain the p-type conductivity. If so, a third peak should exist near 0.42 eV, representing the trapping of photoexcited free holes below \( E_F \). Since Kolomiets’ equipment cut off below 0.60 eV, this peak has not yet been investigated.

Nevertheless the CFO model accounts for many experimental results. Assuredly, an adequate theory and an adequate method of classifying the space of amorphous materials are essential before their unique and revolutionary properties can be fully realized.

One such property, first considered by Ovshinsky, is threshold switching. Exhibited by a large class of chalcogenide glasses, threshold switching is a consequence of a material’s I-V characteristics, an example of which is shown in Fig. 7. In the material’s off state, the resistance is 1 to 10 megohms at low fields. Above about 10 V/cm, current starts to rise exponentially with increasing voltage. The threshold field is of the order of 10 V/cm, and is largely independent of thickness. This makes the threshold voltage proportional to thickness and it can be made to vary from 2 V to 300 V. On the other hand, the holding voltage is independent of thickness, and can be varied from 0.5 V to 1.5 V, depending on the glass.

Conduction in the on state occurs along filamentary paths, and its characteristics are independent of polarity. Switching time is of the order of 100 nanoseconds. Between the application of the threshold field and the occurrence of switching, there is a delay that decreases exponentially with voltages above threshold. It can be reduced from about 1 \( \mu s \) to less than 1 ns.

Switching parameters depend on the composition of the material, but some form of switching—threshold or memory—is almost universal in chalcogenide glasses. One property found during differential thermal analysis of good threshold-type material is the absence of any significant thermodynamic phase transition up to 400°C. This may turn out to be the major material requirement. As the temperature increases, the glass appears neither to crystallize nor melt—it just becomes less and less viscous.

Amorphous material also can be made to perform memory switching. Ovshinsky found that if chalo-
9. Lighting up. A simple circuit can be built that provides bistable control of an electro-luminescent lamp by means of a threshold switch. Perhaps the most promising application of threshold switching, it can provide bistable operation for \( 100 \, V < V_B < 200 \, V \).

gemide glasses contain small percentages of cross-linking elements, such as As and Sb, which require threelfold coordination to fulfill their local valence requirements, their current-voltage characteristics have the sort of memory cycle shown in Fig. 8. Off-hand, these characteristics greatly resemble those of the threshold-type material of Fig. 7, except that no hold voltage is necessary, and the switching is bistable. The device remains on until a rapid current pulse of either polarity is applied.

Material compositions of memory switches are much nearer the edges of the glass-forming region than those of threshold switches. Because of this, differential thermal analyses of bulk memory-type materials reveal very different behavior from that of threshold-type materials. Generally speaking, threshold-type glasses show no thermal transitions, whereas memory-type glasses have at least two. Upon heating, an exothermic (heat-releasing) transition occurs near 300°C, which may be associated with a phase separation and crystallization in the material. Near 400°C, there is an endothermic (heat-absorbing) transition, correlated with melting. A low-resistance state always is obtained when the material is cooled after reaching a point intermediate between the two transitions. Above the second transition, however, fast cooling produces a high-resistance glass, while slow cooling yields a polycrystalline state of low resistance.

These results, taken together with X-ray, electron diffraction, nuclear magnetic resonance, and transport data, suggest a simple mechanism for memory switching. Initial breakdown occurs as in threshold switching, producing a conductivity state that requires a holding voltage for stability. In the course of the breakdown, a rapid phase separation of the positive and negative ions in the conducting filament occurs, and results in a channel of low-resistivity polycrystalline material. After breakdown, ionic motions are much more likely, since many of the covalent bonds responsible for the local atomic positions are broken. A short current pulse returns the material to the off state by first melting it, thanks to a large \( I^2R \) Joule heating, and then by quenching it. Since the environment of the filament remains at low temperature, after a short pulse a rapid cooling of the melted material insures glass formation and low conductivity.

In addition, bulk studies of a particular memory material, \( \text{Te}_{81} \text{Ge}_{18} \text{As}_4 \), have shown that the switching is reversible and does not require significant compositional changes. They also establish that its on state is due to the semimetallic properties of doped, polycrystalline Te. High conductivity therefore is not an artifact of fabrication but a bulk material property.

Clearly, both threshold and memory switching could have a profound effect on semiconductor technology. The most promising application for threshold switching is in electroluminescent displays. A simple circuit, which demonstrates the compatibility of threshold switches with electroluminescent devices, is shown in Fig. 9. Using a switch with a threshold voltage of 200 V, the EL lamp (which acts as a capacitor in the circuit) will be permanently off for source voltages, \( V_s \), below 100 V, while it will be continually on for \( V_s \) above 200 V. Therefore, bistable operation is possible for \( 100 \, V < V_s < 200 \, V \).

If, for example, \( V_s = 100 \, V \), and the capacitor is initially uncharged, \( V_t \) is never exceeded, and the lamp remains off. When switch \( S_1 \) is closed, a charge of \( +100 \, V \) is placed on the upper capacitor plate. Nothing happens until \( -100 \, V \) appears at point A. This results in \( -200 \, V \) across the threshold switch, which fires and transfers the \( -100 \, V \) to the upper capacitor plate, causing the lamp to glow. As soon as the capacitor is fully charged, the holding current cannot be maintained, and the threshold switch turns off. Again, nothing happens until the voltage at A reaches \( +100 \, V \), 8 milliseconds later, at which point 200 V appears across the switch and it fires. The lamp
11. Highly unstable for use as a potential high-frequency oscillator, the current-voltage characteristics of a threshold switch in series with a very large load resistance shows lack of any stable state over a range of applied voltages.

then emits light and locks +100 V on the upper plate, restoring the initial conditions. The lamp glows every 8 ms and is continually on. Closing switch \( S_2 \) removes the charge from the capacitor, and turns the lamp off.

One of the simplest applications of threshold switching, is as an ac thermostat. Some chalcogenide glass compositions can be prepared in which the threshold voltage for conduction decreases sharply with increasing temperature (a 3% decrease per degree C). Thus if \( V_n \) is the threshold voltage at temperature \( T_n \), application of a bias voltage equal to \( V_n \) will cause the switch to be on for all temperatures less than \( T_n \) but off at all higher temperatures.

Another possible use is in high-frequency oscillation. The principle is illustrated in Fig. 11. When the load resistance exceeds 100 kohms, and the applied voltage is sufficiently high, it is possible that no stable switch state exists. This is because the threshold voltage of the non-conducting state can be exceeded with a bias too small to maintain even the holding voltage of the conducting state. Since the switching time is less than 1 ns, gigahertz oscillations are possible, in principle. However, a device has not yet been successfully operated in this frequency range.

Nor should the use of the threshold switch in logic circuits be overlooked. Although basically only a two-terminal device, many three-terminal circuit applications already have been devised.

As an example, consider the quasi-three-terminal device shown in Fig. 10. With 20 V across two identical switches, 10 V is dissipated across each. Since the threshold voltage is 15 V, neither fires. However, if a positive 5 V pulse is applied at B, 15 V appears across BC, and the lower switch fires. This causes the voltage at B to drop to the holding voltage of 1 V, and leaves 10 V across AB, causing the upper switch also to fire. The output voltage across AC is now about 2 V. Turn off can be accomplished by applying a negative voltage pulse at A large enough to reduce the current below the hold value. If \( CR_1 \) is large enough, both switches turn off, and output voltage returns to 20 V. An interesting point is that the turn-on pulse could equally well be \(-5 \) V, in which case the upper threshold switch would fire first, adding the bonus of switching independent of polarity.

In memory switching, the high packing density and few exterior connections necessary for operation make amorphous semiconductors an ideal medium for bulk computer applications. In addition, their inherent radiation hardness is an important advantage, especially for bulk data storage.

A recent development that uses amorphous memory switching is a 256-bit electronically alterable read-mostly memory, built by Energy Conversion Devices on substrates from Intel Corp. [For a detailed description, see the article immediately preceding.] What's more, there appear to be no obstacles to an expansion of its capabilities to the order of 10^6 bits.

Recent work on optical, as opposed to electrical, switching, has broadened the range of applications of memory switching. Feinleib of ECD has shown the possibility of switching memory-type material both on and off with the same laser. What happens is analogous to field-induced switching: the on state is produced by phase separation under nonequilibrium conditions, in which there are large densities of photo-induced carriers, and the off state by local heating and subsequent quenching. More exciting, however, Feinleib has been able to develop an amorphous alloy that is transparent to red light when off, but opaque when on. This could be the kernel of an easily alterable optical memory and, since a laser beam can be focused within 1 micron diameter, a 10^12-bit memory becomes an imminent possibility.

The fact that memory-type material can be switched with a laser beam opens up the possibility of electrostatic printing. The plate produced by laser-induced switching of a memory-type material, as opposed to a xerographic plate, is permanent, and can be used to prepare as many copies as desired. In addition, the master plate can be altered very easily.

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Amorphous semiconductors  Part III

Industry's verdict still a long way from unanimous

Arrayed against those few companies and research laboratories that think the new technology will pay off are many that don't, while between are some fence-sitters; overseas firms express considerably more optimism.

By Laurence Altman,  Advanced Technology Editor and George F. Watson,  Solid State Editor

☐ Will amorphous semiconductor technology make it out of the laboratory and into the highly competitive semiconductor market? Those who think so are Energy Conversion Devices in Detroit, Intel Corp. of Mountain View, Calif., and a handful of university and government laboratories. Almost everyone else must be counted as a nay-sayer and there are also a few fence-sitters.

The following interview typifies the last group's attitudes. The subject, who requests that he and his company remain anonymous, is a widely respected man in a high position in a well-known research laboratory.

Q: What do you think of the prospects of amorphous semiconductors?
A: Frankly, I wouldn't give you a nickel for them.
Q: Why not?
A: Because the material is unrepeatable, the devices unreliable, and the theory shot full of holes. Besides, no two observations are alike.
Q: Is your laboratory working on them?
A: You bet we are.
Q: Can we print that?
A: No.

But, however much established companies may dislike encouraging a technology whose success could annihilate conventional semiconductors, can they afford to ignore it? The attractions are undeniable:

- Unlike crystalline semiconductor materials, raw amorphous glass is dirt cheap. Almost any amorphous glass has switching properties, and none requires the stringent purity of bulk silicon, gallium arsenide and germanium. Films can be made simply by quenching the bulk glass; no expensive p-n junctions, no multiple diffusion layers, no epi-growth, are necessary, since the amorphous material does the switching by itself.
- Driving power requirements for amorphous devices are low and independent of polarity, lending great flexibility in addressing schemes. Operation is immune to the effects of radiation, which the political climate makes essential.
- Amorphous material, being electro-optic, can be switched between transparent and opaque states, opening up the prospect of 10^12-bit storage memories if used with narrow laser beam scanners. Perhaps most importantly, all the IC techniques built up over the years are directly and naturally applicable to amorphous devices.

Consequently, the companies excited over the prospects for the future of amorphous semiconductors have something to be excited about. And few as they are, their voices are loud. By far the most vocal is Detroit-based Energy Conversion Devices, headed by Stanford Ovshinsky, who is considered the founder of amorphous semiconductor technology, and enthusiasm is heard on both coasts: MIT's Materials Laboratory.

The broader view abroad

Unlike their U.S. counterparts, many overseas semiconductor manufacturers are letting a distinct tone of optimism break down their traditional reservations about amorphous semiconductors. In fact, work in the United Kingdom, West Germany, the Soviet Union and Japan is proceeding briskly, although often at variance with the application goals pursued in the U.S.

Perhaps the most extensive device-oriented work is going on at Standard Telecommunication Laboratories Ltd. Cyril Drake, who heads up amorphous semiconductor work there, feels that Europe generally is at least as far along as the U.S. in fundamental understanding of glass semiconductor behavior. And despite the fact that there's no real prospect of production devices appearing in the next year or two, the long-term outlook is optimistic, Drake claims.

Drake's work is exclusively with oxide glasses to provide useful inexpensive memories and switches for telephone and computer applications. Drake has made tens of thousands of devices over a five-year span. The biggest problem in his work is random variation in the performance of a single device from cycle to cycle, he explains. However, Drake says that he has reduced the separate problem of long-term drift to negligible levels.

At Marieco Co. Ltd., Dennis Bryant, chief of semiconductor physics, says, "We think that active device matrices covering large areas eventually might be made more easily in glass than in silicon, and be very useful for display control." His company has made experimental switch and memory devices which have shown
in the East and Intel on the West Coast.

At ECD they’re interested in producing working devices. This has not been easy. For several years, the company could only supply a few samples to interested customers. But by now it is in pilot production of a new read-mostly memory circuit [see p. 56]. And the company’s R&D isn’t standing still, either. Work is proceeding on improving the capacity and performance of the RMM. And the company is attempting to exploit the properties of amorphous semiconductors in other applications, including an electro-luminescent display panel, a computer mass memory, and high-speed computer printout.

A big boost for ECD production efforts in amorphous semiconductors came from Intel Corp, Robert Noyce, president of Intel, feels that Ovshinsky “did the industry a service by bringing amorphous semiconductors to its attention.” Intel’s endorsement may prove the strongest boost for amorphous technology.

In the joint Intel-ECA RMM venture, Intel is supplying the silicon substrates on which ECD is placing glass films. The silicon chips contain the p-n junctions necessary to electrically isolate the amorphous memories, pointing up the importance of the compatibility of amorphous films with silicon technology, which offers a practical way to address devices.

Noyce notes that “there is no license agreement either way. We feel that it’s an intriguing way to make memories and we’re working with ECD”.

Noyce is well aware that many in industry attack amorphous materials as unreliable, and the mechanism of operation as undefined. But Noyce thinks the amorphous switching effect no longer can be dismissed as a mere laboratory curiosity. Says Noyce, “Now I think they [Ovshinsky and his company] understand how and why it [the process] works.”

On the other hand, Fairchild Semiconductor, a neighbor and competitor of Intel’s, is not so bullish. In fact, Fairchild hasn’t changed its low opinion of amorphous devices much, even in light of the recent ECD and Intel collaboration. Says C. Lester Hogan, president of Fairchild: “The effect is there, but I am still skeptical about the time and money necessary to make it into a usable product. It took four years, after having a firm theoretical background, to get a good silicon transistor product. And with Ovonic devices, we’re not that far along in understanding them as we were in understanding silicon in 1950. People are being naive,” he adds, “if they think that Ovonic devices will soon change the world; it will take $100 million and at least another five years.”

But Fairchild may give it a try. Harry Sello, second in command at the Semiconductor R&D Lab in Palo Alto, says, “We are getting devices and evaluating them. I have looked at diodes to see about their lifetimes of up to 1 billion cycles. The main development problem is that the temperature in the conducting channel in the glass is very close to its softening temperature.

Amorphous semiconductor work in the USA has reportedly been proceeding for at least 20 years, considerably longer than in the West. Important research centers are at the Physico-Technical Institute in Leningrad, the N.S. Kurnakov Institute of General and Organic Chemistry, in the Moscow Academy of Sciences, and the Physics of Solids Laboratory of the Lenin State Teachers’ Training University in Moscow. A television camera using the photoresistive properties of amorphous semiconductors reportedly has been developed for space applications.

Siemens AG’s Munich laboratory has been working on amorphous devices for the past five years. But so far, commitments of manpower and research funds have been relatively modest. Right now, Siemens is not sure whether amorphous devices will ever become a commercial success. “It depends largely on whether the instability problem can be satisfactorily resolved,” says Max Guntersdorfer, the Siemens researcher responsible for amorphous device development.

But even if this problem is solved, amorphous semiconductor applications will be rather limited, Guntersdorfer says. “They will never replace conventional germanium or silicon devices, but will be restricted to special applications.” For example, they may be used where insensitivity to radiation is a requirement, or in large memory arrays or Xerox-like printing systems, where an array of amorphous elements covering a large area would be charged according to certain patterns and be used for printing.

Siemens’ experimental devices so far include discrete thin and thick film switching elements and thin and thick film memory devices. Next year, the company expects to make radiation-resistant amorphous photoresistors that could be used for space applications.

These devices look attractive to Siemens—provided they can be made at considerably lower cost than the present Ovshinsky elements and can be easily integrated. But availability is difficult to pin down, Guntersdorfer says. “Most likely functional photoresistors will be ready first,” he says.

In Japan, too, on amorphous semiconductors is proceeding at a healthy pace. Several organizations are active: Hitachi Ltd., Mitsubishi Electric Corp., Matsushita Electric Industrial Co. Ltd., and a large group (by amorphous semiconductor standards) of about 15 people doing research at the Electrotechnical Laboratory of the Ministry of International Trade and Industry.

The largest operation appears to be at Hitachi, which is working on amorphous devices for vidicon targets and optical memories. Amorphous semiconductors show promise for image devices because a large area with uniform characteristics can be made easily. In fact, vidicon targets are close to becoming a product.

Hitachi is not attempting to make switching devices; engineers feel that amorphous semiconductors are not sufficiently reliable in this application. Matsushita is working on an organic amorphous semiconductor. Organic material, rather than the glasses, was chosen in the belief that there is no point in repeating the work of others, especially when the others do not have a product.
possibilities.” But like many people, he adds that the materials problem will continue until the mechanism is understood. “The material is not reproducible—it can’t be laid down the same way twice. But if it works, and this is a big if, then it may be good for some memories. But there are other techniques like bubbles and magnetic domains that are contenders as well,” he asserts.

The Bell Telephone System is not keen on amorphous semiconductors either. According to W.S. Boyle, executive director, Bell Labs took an early independent look at amorphous materials and decided they probably wouldn’t be suited for Bell System devices, where high reliability is mandatory.

Boyle feels there is a misconception on the question of cost. Although amorphous materials would be cheaper because the raw material requirements are lax, the major cost of a device is not in materials, but in processing. For example, the cost of a slice of single-crystal silicon is only one-tenth of the device fabrication cost, he feels, adding that the major cost of amorphous semiconductor devices likely would be in fabrication.

Agreeing with Boyle is A. D. Pearson, materials research scientist at Bell Labs. Long a specialist in amorphous-type materials, Pearson feels that any device whose operation relies on a localized phase change in the material will find reliability problems. “Building devices is no trouble—you can build one from scratch in half a morning,” he says. “The problem is making the device work the same way over and over again. And although there are people at Bell Labs working in the general area of amorphous materials—including the semiconductors—presently there is no hard evidence based on our work to change our earlier opinion that long-term reliability will end up the final cruser on amorphous devices.”

The story’s about the same at IBM, which has had a research program in amorphous semiconductors for about two years. “We think it’s scientifically interesting,” says Marshall Nathan, manager of the cooperative phenomena group of the physics department at the Thomas J. Watson Research Center. However, he adds, “we haven’t seen any devices.” As he sees it, the basic problem is one of stability and repeatability. But Nathan points out that IBM is not confining its research in amorphous semiconductors to just switching, but is looking at a much broader range of properties.

Also on the East Coast, RCA Laboratories in Princeton, N.J., reports it has no device work in progress. But according to one RCA scientist Ben Abeles, RCA has an active interest in the transition metal oxide glasses, such as V_2O_5, especially for thin film devices. And since RCA is a major supplier of military electronics, it has a natural stake in exploiting the radiation hardness exhibited by amorphous materials.

Some of the best theoretical and early device work in amorphous materials is coming from physicist-oriented university and government-supported laboratories, where the climate is free from the pressures of product development. (This is the basis for the charges leveled against amorphous semiconductors that they’re just a plaything for the physicists.) One such establishment is the MIT Materials Laboratory (Stanford is another), in Cambridge, Mass., which have been in it from the start.

So far most of the MIT work has been with compounds composed of 81% tellurium, 15% germanium, and 4% arsenic. Using both bulk and thin film samples, the group is attempting to apply their observations to experimental devices. For example, MIT researchers have noticed that a current pulse less than a millisecond wide applied to an experimental thin film memory device will be sufficient to cause breakdown, but the device will switch back when the pulse is removed. However, if the pulse is applied for at least one ms, the film remains on even after the pulse is removed. This implies that the chemical bonds that hold the glassy structure are broken during the pulse time; this could help in determining operation.

The MIT amorphous group also is working with a compound composed of Te_{50}As_{50}S_{10}Ge_{10}. And preliminary tests with these films have been made. The group has spent several months perfecting the film (sputtering) technique. In the process they observed an interesting result: although most researchers thought that the material would be best manufactured at the lowest possible fabricating temperature the MIT group found just the opposite—annealing at high temperatures makes it better.

The Government’s interest in amorphous devices stems primarily from its radiation resistance property. The high-energy neutron field that blankets wide areas after a nuclear burst destroys solid state devices by dislocating their crystalline structure. The Pentagon, fearing that a series of bursts could destroy most military electronic systems, is actively searching for alternate systems. Military devices using amorphous materials, with their relative immunity to radiation (the atoms already are at random) may provide the ideal solution.

Much of the Army’s interest is centered on the Materiel Command’s Harry, Diamond Laboratories in Washington. Preliminary work is primarily directed toward exploring operational mechanisms of in-house prototype devices.

One interesting amorphous device under study at the Diamond Labs is one made by hot-pressing non-crystalline gallium arsenide into an amorphous compound. According to Marvin Cohen, a Diamond Labs physicist, these devices provide the uniformity needed to explore switching phenomena. Equally important, he says, “we know just about everything there is to know about GaAs—something that can’t be said for the glasses Ovshinsky uses in his devices.”

Picatinny Arsenal in Dover, N.J., has maintained a lively interest in amorphous semiconductors, largely because of their radiation resistant properties. Picatinny’s work is directed more toward characterizing devices than understanding basic properties.

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With some discrete aid
IC op amp swings 100 V

By Robert P. Patterson
University of Minnesota, Minneapolis

It costs much less to make a hybrid operational amplifier with low-voltage and high-voltage stages than to buy a complete high-voltage op amp. The first stage can be a monolithic op amp.

The design here is a unipolar inverting amplifier with an output swing in closed-loop operation of +100 volts to −10 V. An output between −100 V and +10 V can be obtained by reversing the second-stage supply polarities by making Q1 and Q3 npn instead of pnp transistors, and by making Q2 a pnp type.

Q1 is a dc level shifter. Q2 and Q3 form a direct-coupled amplifier with a gain of about 10, determined by (R1 + R2)/R1. With the loop open, approximately −13 V is required at the base of Q2 for zero output. Capacitor C2 reduces output noise by rolling off the gain of the discrete transistor stage. Offset and drift are determined by the integrated amplifier.

The IC cannot be an internally compensated op amp. Compensation capacitor C1 must be much larger than normal (30 pF for the LM101A) to prevent oscillation. The additional open-loop gain of 10 could make the total circuit gain roll-off approach or exceed 12 dB per octave, causing instability.

Low in, high out. Low-voltage monolithic amplifier controls high-voltage discrete amplifier, allowing the output to rise to nearly the high-voltage supply. Output swing of this unipolar inverting amplifier is +100 V to −10 V in closed-loop operation.

Feedback limits amplifier better than zeners can

By Paul K. Yee
Zeltex Inc., Concord, Calif.

High gain in a feedback loop of an amplifier limits orders of magnitude more sharply than a conventional zener-diode amplifier limiter, since active limiting is unconstrained by the usual component impedance.

Operational amplifier A1 operates normally when its output, inverted by A2, doesn't exceed the limit voltages, ±E_L.
Diodes for amplifiers \(A_3\) and \(A_4\) forestall amplification until the sum voltage at the inverting input of \(A_1\) and \(A_2\) goes above the threshold. Then they amplify open-loop, making their outputs

\[
e_3 = -G_3(e_L + e_2) = G_3 e_L + V_{out}
\]

for \((E_L - V_{out}) < 0\)

\[
e_4 = -G_4(-E_L + e_2) = -G_4 E_L - V_{out}
\]

for \((-E_L - V_{out}) > 0\)

where \(G\) is open-loop gain.

\(A_1\) also inverts; thus, if the output of \(A_1\) drops below \(-E_L\), it is driven positive by the added negative feedbacks from \(A_3\), and if it rises above \(+E_L\) the positive output of \(A_1\) drives \(A_1\) negatively.

The limiting slope is defined as \(\Delta V_{out}/\Delta e_{in}\), or simply the reciprocal of gain in this case. Low-cost amplifiers can provide a slope of \(2 \times 10^{-6}\).

Loop stability requires that \(A_3\) and \(A_4\) have gain-bandwidth products at least five times lower than those for \(A_1\) and \(A_2\).

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**Monitor teams spare gates and solid state lamps**

By T.F. Prosser

*PD Labs, Cupertino, Calif.*

Usually, extra gates or inverters are available in IC packages on a logic or power-supply card, and red light-emitting diodes are inexpensive now. Together, they can monitor supply voltages cheaply and reliably.

Variable resistor \(R_1\) is set to switch gate \(G_1\)'s output from high (logic one) to low (zero) if the positive supply voltage exceeds a maximum such as 5.5 volts for a 5-volt supply. This switches \(G_2\) from low to high and turns on \(D_1\). Conversely, \(R_2\) is set to switch \(G_4\) from low to high and turn on \(D_3\) if the positive voltage drops below a minimum, for example 4.5 volts.

If the voltage is neither too high nor too low, gate \(G_4\) has two low inputs and a high output that keeps \(D_2\) lit. In this normal voltage condition, both \(D_1\) and \(D_3\) are off.

Resistor \(R_3\), \(R_4\), and \(R_5\) limit the logic one output currents of the gates to a value that is safe for the logic family used. Currents as low as 3 milliamperes will light low-cost diodes such as Monsanto Company's MV50.
Over Voltage

Gates G1 and G2 sense an over-voltage on the power-supply line and drive a light-emitting diode D1 on with a logic “one” output from G2. If the voltage falls too low, the output of G3 goes high, and D3 glows red. Otherwise, only the output of G4 is high, so only D3 is lit, when the voltage falls within the limits set by R1 and R2.

Op amps find values of buried resistors

By William J. Travis

Sprague World Trade Corp., Ronse (Renaix), Belgium

One resistor in a “T” attenuator or ladder network must be replaced, but which one? The network has been encapsulated and only the terminals can be probed conveniently. Solving three simultaneous equations for each “T” section will identify the resistor—if the measurements or calculations are without errors.

Or, the value of each resistor can be measured directly by connecting the network and meter to a unity-gain current amplifier, A1 and A2.

Having the resistors in the “Y” connection guarantees that the current from the ohmmeter entering terminal 1 will be the same as that drawn from terminal 2. Moreover, no current flows in resistor R1, making the voltage across R2 zero and placing node A at ground potential. Therefore, the meter current can represent only the value of R5.

By transposing the terminals with a selector switch, R4 and then R3 can be measured.

The ohmmeter reading is accurate for currents up to about 0.3 milliampere for the resistor values shown. The values can be scaled down or up for higher or lower currents. For example, currents to about 3 mA can be measured with R1 and R3 at 3 kilohms, R2 at 1 kΩ, R4 at 531 ohms, and R5 at 1.593 kΩ. If desired a multiscale circuit can be prepared on a wafer switch to handle various currents.

Error sources include input bias currents and offset voltages of A1 and A2 and common-mode errors of A5. Field effect transistor inputs will minimize measurement errors.
Philips PM 3200 scope puts an engineering test on the production bench for Analog Devices

Production Bench Test — Analog Devices

Production Test Manager John Lang added depth to his production test procedures for op amps and multipliers while testing more of them than ever before — and with non-technical personnel. To the list of Analog Devices' 100% tests (which include chopper waveform, offset voltage, common mode ratios) the check for slewing rate, previously an engineering-only examination, has just been made routine.

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Audio noise: Why settle for more?

Optimizing a transistor’s collector current yields a noise figure far below the specified minimum; it’s easily calculated for amplifier design

By J. A. Roberts* and N. A. Jolly, University College of Swansea, Great Britain

\( \square \) Most designers settle for much noisier audio amplifiers than necessary. They assume that data sheets on small-signal bipolar transistors specify the best obtainable noise figures.

Most manufacturers, however, do not list the minimum noise figure for their devices—they simply list it at a convenient collector current, usually about 100 microamperes. And the optimum value of this current, so far as noise is concerned, is normally well below 100 \( \mu \)A. Moreover, manufacturers often fail to specify the other essential design parameter—current gain—at much below 1 milliampere.

Fortunately, ascertaining actual minimum noise figures is straightforward. After selecting a transistor type and a power supply voltage (\( V_{cc} \)), and after determining the driving source resistance (\( R_s \), the designer need only

\[ I_{C_{\text{opt}}} = \frac{1}{40R_s} \left( \beta_0 \right)^{1/2} \]

where \( \beta_0 = \text{dc current gain} \)

\( \triangleright \) Calculate the minimum noise figure for the optimum collector current from

\[ F_{\text{min}} = 1 + \left( \frac{\beta_0}{R_s} \right) \]

where \( \beta_0 \) = intrinsic base spreading resistance.

\( \triangleright \) Determine the maximum voltage gain from

\[ A_{\text{Vmax}} = 40 V_{cc} \]

If the design requirements cannot be met with the selected device or supply voltage, then either, or both, must be changed.

As these relations are only approximate, adequate safety factors must be allowed. For example, since the dc gain in some transistors drops off sharply as they approach saturation, the collector-to-emitter voltage \( V_{CE(ma)} \) in any transistor should be assumed to be at least 1 volt. This means that the maximum gain realizable is 40(x \( V_{cc} - V_{CE(ma)} \)). The value of the intrinsic base spreading resistance for small signal transistors usually lies between 60 and 400 ohms.

Typically, these calculations yield a far more flattering picture of a device than does its data sheet. A case in point is a 2N3707 transistor, for which the manufacturer claimed a dc gain of more than 100 and a noise figure of less than 5 decibels at a collector current of 100\( \mu \)A. However, a paper by Faulkner and Harding shows average gains of 250 and 150 at 100 \( \mu \)A and 1 \( \mu \)A respectively, and a minimum noise figure of less than 0.5 dB for all six transistors tested.

Once the basic design requirements have been satisfied, remaining amplifier parameters, such as input resistance and capacitance, can be determined from standard design relations. For instance, any of the usual methods of biasing the amplifier can be used, since they do not affect its minimum noise figure.

However, it is unusual to design a single-stage high gain amplifier unless a very narrow bandwidth is required. More commonly a two-stage feedback arrangement is used; choosing a complementary pair permits the use of direct coupling with excellent bias stability, and capacitive coupling is unnecessary.

**Two-stage amplifier design** is handled in the same manner as single-stage design. The optimum collector current and then the noise figure of the first stage are determined from the source resistance, the intrinsic base spreading resistance and the dc gain of the transistor. For the complementary amplifier and equivalent noise circuit shown on top of p. 83 the first-stage collector current is:

\[ I_{C1} = \frac{V_{be2}}{R_3} + I_{R2} \approx \frac{V_{be2}}{R_4} \]

The base current of the second stage, \( I_{B2} \), can be neglected since it is much smaller than \( V_{be2}/R_3 \), where \( V_{be2} \) is the base-to-emitter voltage in stage 2 and \( R_3 \) is the bias resistor to the collector of the first stage of the amplifier. The criterion for choosing the second-stage collector current for negligible noise contribution is given by Faulkner²

\[ \frac{\beta_{eq}}{I_{C2}} > \frac{1}{I_{C1}} \]

The transconductance parameters, \( g_m1 \) and \( g_m2 \), and the base-to-emitter resistance of stage 1, \( r_{be1} \), are dependent on the respective collector currents:

\[ g_m = \frac{qI_C}{KT} \]

where \( q = \) the charge on an electron, \( K = \) Boltzmann’s constant, \( T = \) temperature in degrees K, and

* Mr. Roberts is now with Marconi-Elliott Automation.
Two stage. Complementary design permits the use of direct coupling between stages without affecting the amplifier's stability. Little change in the noise figure will occur if the parallel combination of \( R_p \) and \( R_f \) (feedback resistors) is very small compared to \( R_s \) and the parallel combination of \( R_1 \) and \( R_2 \) (bias resistors) is very large.

\[
r_{b's} = \frac{\beta}{g_m}
\]

where \( \beta = \text{ac gain of the transistor} \).

The maximum possible gain of the complementary two-stage amplifier occurs when the feedback resistance and capacitive reactance from the emitter to ground in stage 1 are both zero. This is the open-loop gain, \( A_{\text{VOL}} \), and is given by:

\[
A_{\text{VOL}} = g_{m1} R_{L1} g_{m2} R_{L2}
= g_{m1} \left( \frac{R_A r_{b's1}}{R_1 + r_{b's1}} \right) g_{m2} R_A
\]

where \( R_A \) is a resistor from the collector of stage 2 to the emitter of stage 1. The closed-loop gain, \( A_{\text{VCL}} \), can be found by including the resistors \( R_p \) and \( R_B \) in the circuit. If \( A_{\text{VOL}} \gg A_{\text{VCL}} \), the closed-loop voltage gain is simply:

\[
A_{\text{VCL}} = \frac{R_A R_F}{R_B + R_F} + R_A
\]

But the introduction of \( R_F \) and \( R_B \) as well as the bias resistors \( R_1 \) and \( R_2 \) must now be considered in the noise figure calculation. Little change in \( F_{\min} \) will occur, however, if:

\[
\frac{R_F}{R_1 + R_2} \gg \frac{R_E R_B}{R_F + R_B}
\]

This minimum noise figure approach was applied to the design of a two-stage low-noise audio amplifier, shown at center left. Its open loop gain is greater than 60 dB \( (R_F = 0) \) and its noise figure is less than 1.5 dB with selected devices. When \( R_F \) is increased to 2 kilohms, the voltage gain is reduced to 20 dB without any significant change in the noise figure.

A general purpose design incorporating special matching features is shown at lower left.

References
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Dean Burch: FCC’s pragmatic boss

Some welcome his “laissez faire” philosophy; others flay his quick decisions on complex issues

By Lois Vermillion, Washington bureau

Now facing the Federal Communications Commission are several very complex decisions that are critical to the future of the $10 billion communications industry. Because of this, many in industry are anxiously watching and worrying about the hand on the FCC tiller—Dean Burch, the first Republican in nearly a decade to chair the commission.

It’s hard to figure out the lean, 42-year-old Arizona lawyer who first made a national name for himself running the 1964 presidential campaign for Sen. Barry Goldwater (R., Ariz.). To his supporters, Burch is a champion of free enterprise, who will let the marketplace regulate communications. To his critics, the chairman is a “reckless intellectual hipshooter,” inclined to render simplistic, rapid-fire judgments on complex issues.

With his background and political stripe, it’s not surprising to hear Burch describe himself as a “laissez faire” capitalist. But he acknowledges that he has “no fixed opinion” on many of the crucial issues now facing the commission. “I don’t have preconceived notions about what systems will look like in five years,” he says. “I don’t have that kind of expertise.”

Loading the FCC agenda are the following blockbuster issues: ownership and number of domestic communications satellites and the services they would offer; distribution of overseas communications and how it should be regulated; and the future of digital communications and of the special service common carriers who plan to offer it in competition with American Telephone & Telegraph.

One thing worrying industry is that Burch is too pragmatic; he wants to clear the FCC agenda as quickly as possible. The chain-smoking, often impulsive chairman has become known for his desire to bring an issue to a vote, no matter how involved or far-reaching it may be, for the sake of getting it off the agenda—a practice which a former FCC chairman calls “a bit rash; he could be pushing too hard.” Burch’s defense here is that “when a technology is ready, it shouldn’t be kept in the wings,” and he cites the domestic satellite decision, deferred through two Administrations, as an example of unnecessary delay.

As a professional politician, he has been successful so far in keeping his agency out of hot water with Capitol Hill and, unlike some top Nixon Administration officials, he’s known to have an oft-used pipeline into the White House. There’s no firm evidence, though, that he’s merely a chorine boy for the Executive branch.

But so far, Burch has presented no clear-cut image of himself in his FCC role since his appointment a year ago, except for a stated unwillingness to expand the FCC’s tiny bureaucratic empire. Sitting before his vintage rolltop desk, Burch is inclined to speak of his FCC role in negative terms, such as “not imposing artificial barriers to technological development.” He says that he favors a competitive communications environment. Ironically, this puts him on the side of the Democrats on the commission, which, shortly before he became chairman, cracked AT&T’s virtual monopoly on common car-

At the helm. The chain smoking chairman “favors competition” but has “no fixed opinion” on many FCC issues.
Probing the news

rier hardware interconnection and service in its landmark Carterfone and Microwave Communications Inc. (MCI) decisions.

Though Burch has only one vote on any item before the commission, he will have substantial influence over FCC decisions when the spot left vacant by commissioner Kenneth Cox, a Democrat, is filled. This will give the Republicans a 4-3 voting edge. The only continuous opposition he is likely to have to face in most major issues is from Nicholas Johnson, a Democrat whose term expires in 1973, and who began a slugfest with Burch over AT&T rate separations almost as soon as Burch took over [Electronics, Jan. 19, p. 34].

Burch's influence over the votes of the independent agency is important to another power—the Office of Telecommunications Policy in the White House. Now headed by 31-year-old Clay T. Whitehead, the recently reorganized and strengthened office has broad powers to shape government policy on computers and communications. Part of OTP's mission is to ensure the effective presentation of the Administration's views to Congress and the FCC (see page 38). In Dean Burch, OTP has a strong voice and a sympathetic ear. There's no evidence he functions merely as OTP's cheer boy, but he was quick to accept the White House's policy on open competition in domestic satellites. And the FCC's recommendations for the U.S. position at the World Administrative Radio Conference [Electronics, Sept. 14, p. 45] strongly reflect those of OTP.

Many in industry and even within the commission are pushing for an in-house engineering research capability [Electronics, July 7, 1969, p. 67] so that the FCC could make more intelligent decisions. Burch rejects this proposal. Reliable engineering expertise is available outside, he says, and "we don't need it here—we'd have an organization like NASA before it was all over." Still, this may not spell the end of the possibility; an independent committee of industry and university experts told the Office of Telecommunications Policy that outside think tanks may produce conflicting points of view. And without an in-house capability, the commission will have to solicit help from industry and probably the National Bureau of Standards, if it's to define the standards for foreign attachments on telephone lines.

Burch does agree, though, with industry that the FCC should have at least one member with an engineering background. The last such commissioner was T. A. M. Craven, who left in 1963; he is credited by former chairman Rosel Hyde with having initiated the development of a satellite communications policy. Burch says that while an engineering commissioner "wouldn't solve all the engineering problems," he could help provide alternate viewpoints. "I'm not going to contradict the chief engineer," says Burch, "but an engineering commissioner could."

Though Burch says he has "no fixed opinions" on the individual items on the FCC agenda, it's possible to predict some likely interactions between his general philosophy and specific issues. Domestic satellite communications is one area where he may not see his ideal of free competition realized. For though he opposes a "carrier's carrier," sources predict that with estimates of costs at $150 million or more each, simple economics will rule out the dozen or so systems being planned. The idea most acceptable to industry and government remains a system put up by the Communications Satellite Corp., in which users would work through Comsat to set up a multipurpose network.

The rules for owning an earth station in a domestic system will probably be flexible. Burch foresees "great advantages in ownership by users in certain cases," perhaps in receive-only systems, such as commercial tv networks would use, but adds he hasn't yet made up his mind about the ownership of international earth stations.

The FCC also has to decide on a mix between cables and satellites in international communications, and its chairman lays claim to "no nifty formula." In view of Burch's observation that cable is a known technology while satellite technology is comparatively untried, he probably will emphasize cables, yet allow satellites room to grow.

As for the computer-communications inquiry now drawing to a close after four years of vigorous lobbying by all parties, Burch comments, "We must create a climate for computer operations, with as much competition as possible." Ruling on this subject will probably resemble the FCC's tentative decision to let common carriers into the data transmission business under close FCC supervision, but not to regulate as strictly such new, special-service carriers as Data Transmission Co. and MCI.

Despite the laissez-faire Republicanism now apparent at the FCC, there is an aspect of competition on which it hasn't touched—the relationship between AT&T and its captive supplier, Western Electric. Phase II of the general AT&T investigation began in 1965, which would delve into the so-called "incestuous" relationship between AT&T and Western Electric, may be shelved. FCC sources say there is a growing feeling that the Carterfone and MCI decisions have mitigated the problem.

Estimates of how Dean Burch will serve the public interest vary widely. Some in the communications industry feel he will support AT&T's dominant position. Similarly, some land mobile operators believe they'll make little headway in acquiring more spectrum space because, they say, Burch supports broadcasters in their entrenched position. Yet broadcasters worry because of Burch's record so far in accelerating the growth of CATV.

Burch's views on competitive development of domestic satellites blend neatly with those of the White House, but Comsat sees his support of a balance between satellites and undersea cables as limiting competitive expansion of overseas satellite links.

Commenting on these differing views, one industry observer summarizes, "It's like viewing a piece of abstract sculpture. From one side it looks like one thing; from another, the image is altogether different."
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Probing the news

Consumer electronics

Stakes are high in home video game

Companies promise much in rush to market video playback and recording units, but they're gambling that state of the art will deliver for them in time

Behind the stampede of companies rushing to announce video playback and recording systems lies a big technological gamble, perhaps as big as the one taken with color television. Companies are betting that the technology will move ahead fast enough to help them make good on today's promises of inexpensive home units in two years or less.

Intensifying the rush to market is the basic incompatibility of the systems under development. In this go-for-broke situation, only one or two approaches are likely to make it big. The 16-or-so companies already in the race feel the risks are worth it because the stakes are so high—some predict a billion-dollar video playback and recording market within a few years.

The technological gambles being taken by companies range from a laser-hologram system—perhaps the riskiest—to units using motion picture film or disks. Falling in between are those who are sticking with magnetic tape, hoping for improvements that will achieve higher resolution at slower speeds. Others are working on data reduction circuits to minimize the information needed for an acceptable picture.

RCA's SelectaVision color playback unit, which works by shooting a laser beam through embossed holograms into a vidicon, is perhaps the most ambitious effort. The company plans to sell the unit for $400, starting sometime in 1972. Since its announcement of the playback unit last fall [Electronics, Oct. 13, 1969, p. 43], the firm has ruled out one of the four sound encoding techniques being considered, come up with a new one, and narrowed down a dozen color encoding methods to only two.

At the same time, it claims to have eliminated all those patterns and interferences that play havoc with images and that competitors claimed were inherent in holograms. Now RCA says it can produce acceptable black-and-white images, thanks to expensive recording optics, semi-clean rooms to reduce fringes caused by dust, a higher signal level achieved by changing the laser frequency, and a new holographic format that will make the images even smoother and linear, and don't crack when wound onto a reel, RCA says.

CBS, on the other hand, is taking a more conventional, route in its EVR playback unit [Electronics, April 27, p. 94]. EVR tape essentially is a silver halide photographic emulsion flanked on either edge by an ordinary sound stripe. However, color encoding requires some fairly complex circuitry, though not beyond the current state of the integrated circuit art. EVR systems are being sold now for $140 and $280; disks can be retailed for between $2.70 and $5.40, Teldec believes.

Both Telefunken and Teldec were attracted to this medium because of the theoretical advantage that ordinary video disks offer a considerably higher frequency response than...
that required for audio. RCA scientists had taken the same tack but lost interest when they concluded that only a few minutes of video could be stored on a platter.

However, the Germans chucked the traditional stylus-cartridge combination in favor of a fixed pressure-sensing stylus shaped like a skid with an abrupt edge on one side and a rounded edge on the other. The team reduced groove widths from mils to microns. And amplitude modulation of lateral deflections gave way to frequency modulation of vertical deflections, allowing adjacent grooves to touch.

Right now the 12-inch disk only plays 12 minutes of black-and-white video, but Teldel expects to have color capability soon and eventually extend the time up to 15 minutes. "We could reproduce color right now," says Redlich, "but the method is too costly and would make the price too high. But we have no doubt that we'll be ready with a color system that's as cheap as black and white when we go on the market in 1972." One important development here may be a foil that would improve both video and audio reproduction.

Back in the states, Avco is trying its own method of video data reduction in its Cartavision record-playback system. Using only every third field of the video image and playing it back three times should eventually permit an acceptable image to be reproduced on half-inch tape running at 3.8 inches per second, Avco believes.

Sony tried a similar technique in its magnetic video cassette system, which records every other field and plays it back twice [Electronics, May 12, 1969, p. 239], but encountered problems. It produced a respectable image with ¾ inch tape running at 2.6 inches per second, but adjusting heads for proper angular and axial offset was extremely difficult, servo specifications were critical, and high-precision recording heads of extremely narrow width were needed. Sony says the problems are compounded when two fields are skipped.

Taking a more routine approach, Sony is planning to sell a color playback unit sometime next spring in Japan and by the end of 1971 in the U.S. [Electronics, Nov. 10, 1969, p. 236]. The unit would probably cost about $400 initially and would play for an hour; thinner tape yielding 90 minute playing time would follow. Sony thinks it will probably reach an agreement with Matsushita and Victor of Japan on a system by this year's end.

Meanwhile, it's trying to develop higher performance tape with a thickness under one mil. The tape would probably be coated with chrome or cobalt. And it's trying to develop a simple and inexpensive mechanism and a cassette suitable for mass production that will prove trouble free in home usage.

Matsushita has developed a magnetic transfer copy [Electronics, March 31, 1969, p. 181] with a 15:1 playback to copy ratio; it hopes eventually to raise the ratio to 30:1. Adding an additional step to make multiple submasters (via routine electronic copying) would allow a number of these units to be run in parallel.

While many of the video units planned initially will be for playback only, the Netherlands' Philips expects to be selling a color recording-playback system near the end of 1971. "That's one of the reasons we chose magnetic tape," says Kees Jongelie, product manager for home video recording in the electroacoustics division. The company is trying to trim copying time down while maintaining quality, and is studying various transfer systems, including those of Memorex and Ampex. Chromium dioxide would be the tape used.

One cornerstone of the company's sales strategy is that "a recording-playback unit gives us some independence from preprogrammed video material. He says that a complete unit will sell for around $500. Jongelie doubts whether some of the other systems will really be ready for market by 1972, but when they are, he adds, there'll be tough competition among them until one or two leaders emerge.

Another company well along the record-playback route is Ampex, which recently announced its Insta-
Military electronics

Aerospace Corp.’s road widens

Beginning its second decade, nonprofit firm’s major work is still for the Air Force, but transportation and nondefense work is increasing

By Lawrence Curran, Los Angeles bureau manager

The times have caught up with the Aerospace Corp., but the nonprofit firm hasn’t been caught napping. During its first 10 years the company stayed close to its original charter as the system engineer, technical program director, and over-all technology monitor for what is now the Air Force Space and Missile Systems Organization (Samso). But with defense cutbacks steadily eroding contract funds, the El Segundo firm is applying its technical knowhow, developed with Department of Defense funds, to pollution control, transportation, medical research and other general problem areas.

Of course, Air Force contract work continues to represent most of the firm’s business as it starts its second decade. But new emphasis is being placed on computers and software.

The green light to seek non DOD business flashed last year when Defense Secretary Melvin Laird ruled that the firm could spend up to 20% of its time on outside contracts without DOD permission. While probably less than 1% of the firm’s fiscal 1970 funds came from outside sources, the figure is expected to jump to 8% this fiscal year.

Additional work is expected to come from other Federal agencies, such as NASA, the Department of Transportation, the Department of the Interior, and from state and local governments. The corporation is working in several nondefense areas using fee funds. These are earned on every contract and amount to 3% of the total.

One concept the corporation has been trying to sell to the Department of Transportation involves a personalized public transportation system that would use small vehicles propelled by pulsed linear dc electric motors. The vehicles would travel along a computer-controlled system of guideways.

Aerospace Corp. planners believe such a system could be the answer to mass-transit problems in traffic-clogged cities. They have already briefed interested officials in DOT, and in Los Angeles and Tucson, Ariz. The next step is to develop a scale model, using fee funds, though they’re trying to get outside contracts either from DOT or local governments.

In another fee-funded project, the firm is working on a laser technique to locate and measure nitrogen dioxide pollutants in the air. A Q-switched ruby laser transmitter has been built that will use a frequency-doubled beam. Pumping the beam through a tunable dye permits tuning to the absorption band of nitrogen dioxide, which in turn will fluoresce after absorbing certain wavelengths of the laser’s radiation. The task now is to determine at what wavelength fluorescence takes place. Arnold H. Silver, Director of the Electronic Research Laboratory, expects to be able to locate the pollutants with a resolution of 10 to 20 ft.

Other fee-funded work in air-pollution control includes development of a computer model of the chemical reactions that produce smog, and efforts to reduce pollutants emitted by automotive engines. Aerospace Corp. researchers are fitting an internal combustion engine with a device that produces a flame front rather than a spark to obtain more complete combustion on a leaner fuel mixture. They’re working on a way to increase efficiency by introducing the fuel mixture into the chamber as a gas rather than as an aerosol or liquid.

The nonprofit firm often literally gives its technology away—for example, to Loma Linda Calif. University Medical Center. Heart specialists at the center have used X-ray motion picture techniques to show the heart’s function for research and diagnosis. In the past, several weeks were required to thoroughly study a patient’s X-ray film one frame at a time. But a technique developed by Aerospace Corp. for missile test flight data reduction has speeded up the process considerably.

The X-ray film is projected onto a cathode ray tube linked to a computer, and an eight-point ellipse is superimposed over the picture of the heart. Using the ellipse, the computer calculates heart dimension and blood volume in a few milliseconds. Only three to five minutes are required for a printout of the heart’s contractions, with the computer driving a California Computer Products plotter. Although this joint effort was initially undertaken by Aerospace Corp. with its own funds, it has been bolstered by a grant from the National Heart Institute.

The changing nature of defense contracting is reflected in the firm’s Air Force work on guidance and navigation systems and communications hardware. “Ten years ago, there were three or four boosters in the inventory, and they used radio guidance mainly,” relates Max T. Weiss, general manager of...
the corporation's electronics division. "But now, with the Titan 3C, which has the most modern guidance we're working with, we're attempting to upgrade it with a new inertial measurement unit and computer," Weiss is convinced that some of the responses to a Samso request for proposals for the upgraded system will be for strapdown units which "have a very good future because they can be small and simple."

The company has come a long way since the early days of communication satellites and the Echo reflector balloons. "If we got a few signals through a voice channel," Weiss recalls, "we thought it was great. Now we've got 26 satellites still in orbit in the Initial Defense Communications Satellite Program (IDCSP) and all but two of them are 100% functional." Aerospace Corp. did the concept formulation work that led to randomly spaced intermediate-altitude satellites. The firm handled the systems engineering and commander direction tasks.

Weiss points out that the program's second phase will use four synchronous-orbiting equatorial satellites in a system that will provide higher power and narrower beams than the initial phase.

The firm's new emphasis on computers and software is being directed by the year-old Information Processing division. Its general manager, Stewart I. Schlesinger, believes one of the division's prime strengths is in real-time system software. This expertise could be employed in a complex system, such as the Army's Safeguard program, although Aerospace Corp. isn't directly involved in that effort. "Safeguard represents a complicated discrimination job," Schlesinger notes. "And the Air Force has the same kind of problem in software development."

Schlesinger doesn't say so, but his division's talents could already be at work in real-time discrimination tasks, such as those encompassed in the Minuteman ICBM's multiple independent re-entry vehicle (MIRV) system.

One important area for future Aerospace Corp. work is millimeter wave communications for the military, Weiss feels. "The advent of Gunn and Impatt oscillators and good low-noise mixers makes it obvious that applications will come," he asserts. "Our laboratory people have been doing mixer diode work from 40 to 110 gigahertz. These components," Weiss continues, "could be used for military satellite communications where you need broad bandwidth and some degree of security."

Though he sees no immediate application of lasers to military communications, Weiss believes lasers may win out in the long run for space communications. They offer greater bandwidth than millimeter waves as well as smaller antennas, although atmospheric attenuation still hampers lasers more than it does millimeter wave sources.

Arnold H. Silver opts for lasers over the long haul, too. "But the question is when, and it's mainly a money question," he says. The earlier millimeter wave work didn't get too far because there wasn't enough understanding of atmospheric effects, he maintains. "This is an area in which we're greatly interested."

Proliferate or perish

Shrinking contract funds were an important spur to Secretary of Defense Melvin Laird's approval of more non-DOD business for the Aerospace Corp. The firm's annual contract with Samso, which had reached a maximum of $79.1 million in 1964 dropped to $73.0 million in fiscal 1970, and 1971 projections fall to $70.3 million.

Cancellation of the Air Force's Manned Orbiting Laboratory program last year hit the corporation hard—funding for that program alone was running at about $10 million a year and occupied between 200 and 250 technical staffers. Some 200 technical employees were laid off at the beginning of fiscal 1970.

At the same time, most of the major systems engineering work on the Titan 3 family of launch vehicles was winding down, and there were no major new programs to take up the slack.
Instrumentation

Skyjackings spur detection work

Ferrous detector use on rise, and work to develop other sensors increases; fallout could provide new anti-pilfering devices

By Herman Lowenhar, Military/aerospace editor

The boarding gate at the San Juan airport earlier this month was jammed as 350 people inch ed aboard a giant Boeing 747 for the three-hour flight from Puerto Rico to New York. The jam resulted from precautions taken by worried Pan Am officials who, having had one of the $24 million aircraft destroyed by Arab skyjackers the week before, were passing the passengers through one at a time and checking carefully for weapons. Travelers faced not only two searches, but were scanned one at a time by ferrous metal detectors. One detector did its job and spotted a five-inch knife in a passenger's pocket.

The scene at San Juan is being duplicated at airports around the world as airlines frantically try everything to prevent the skyjackings that have accelerated recently. The airlines are buying detectors in large quantities, but they won't be the entire solution to the problem.

What happens if a passenger is carrying a nonferrous weapon—an aluminum handgun or a stick of dynamite, for example? Then there is the problem of where to set the sensitivity of the metal detector. Women wearing metal-reinforced bras, for example, had been delaying flights out of London. "The metal detecting device went practically mad," said one official, adding that the number of women wearing the special metal bras was "extraordinary." Worried English authorities had set the sensitivity on their detectors too high.

The push now is to come up with more effective "hands off" equipment to detect concealed objects of all kinds. These range from chemical sniffers using gas chromatography to those using so-called "nonlinear radar" techniques.

Particularly significant to the electronics industry is that the fall-out from this new push could supply the design for entire families of detectors for business and industry. One potential market is the prevention of retail store and factory thefts. The stakes are enormous: store pilfering losses alone run to $3 billion a year.

Meanwhile, the airlines are continuing to buy the metal detectors. At present, the Friskem division of Infinetics Inc., Wilmington, Del., and Schonstedt Instruments Co., Silver Springs, Md., share the ferrous detector market. Friskem, by far the largest producer, offers about 40 models, all using flux gate magnetometer sensors.

Twenty foreign and domestic airlines have bought 800 walk-through station metal detectors, most of them in the past year, after a joint test program by Eastern Airlines and the Federal Aviation Administration.

Walk-through stations cost $800 apiece, but none of the airlines has more than a few at each airport—there are just too many gates to check and police.

Oddly enough, a bill now before Congress to finance installation of detectors actually may be putting a temporary crimp on the market. Many airlines and airports may be delaying purchases pending passage of the bill, which would make them eligible for a 50% reimbursement and would provide U.S. marshals to assist in anti-hijacking operations.

Meanwhile, the airlines are increasingly worried about concealed explosives. One area attracting renewed interest is chemical sniffers that use gas chromatography to sample the air.

Three years ago, the IIl Research Institute developed a gas chromatography dynamite detector for the FAA that could sense one part per billion of the ethylene-glycol-dinitrate, which invariably contaminates dynamite. This detector uses a hydrogen flame as an accurate spectral source and the sampled air as a differential spectral absorber.

The same principle could be applied to detection of almost any material. And if the devices only performed spectral sampling, leaving the determination of the compounds sensed to a remotely located computer linked to it via a data-entry unit, hundreds of inexpensive sensors could be serviced.

One still unresolved problem is that to achieve maximum sensitivity, it takes 1.5 minutes to slowly scan the spectrum. A possible solution is to sample baggage in batches. Another is to use an interferometer spectrometer, a device developed by Block Engineering Inc., Cambridge, Mass. While an ordinary spectrometer excludes all energy outside the scanning filter's passband, the interferometer spectrometer accepts all the received energy, but produces a brief, cyclic null at each frequency as a small mirror is rapidly swept through all the positions needed to produce 180° interference paths for each spectral term in sequence. The output is an interferogram, equivalent to the Fourier transform of the spectrum. A Block-developed com-
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Probing the news

puter then digitally samples the interferogram and processes it to rapidly derive its inverse Fourier transform, which is the spectrum itself. Sensitivity is extremely high and the computer can be used to service several Interferometer spectrometers on a time-shared basis. Once the absorption spectra are extracted, they, in turn, could be analyzed by a second computer.

An even faster and simpler approach is to have the computer cross-correlate directly the sample interferogram and those of the compounds being searched for.

To detect nonferrous metallic weapons, such as brass-pipe bombs, "nonlinear radar" techniques are being applied. These have evolved from Microlab/EXR's early work on its Dynaid system, which uses diodes as nonlinear detectors and reradiators of microwave energy.

Weapons-detection is based on the fact that any metallic junction, even one with similar metals, has diode-like attributes. These "corrosion" diodes absorb incident microwave energy and reradiate at harmonics of the illuminating frequency. A gun, a knife, even a brass pipe with its ends brazed shut, contain many such "diode junctions." At microwave frequencies, since even the first harmonic which they radiate is well separated from the source frequency, a simple front-end filter can screen a suitable receiver from ordinary backscatter.

IIT Research Institute has produced an effective system for the Army's Mobility Equipment Research and Development Center at Ft. Belvoir, Va. The goal is to use it in Vietnam to rapidly search large groups for concealed weapons. Microlab/EXR has developed a similar device through an in-house funded program. This gear operates at 915 megahertz (allocated by the FCC for experimental work) and uses an fm-cw technique to effect the equivalent of range gating, thereby limiting the accepted signals to a range of a few feet and, therefore, eliminating spurious returns.
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What's a 500 kHz plug-in doing in a 100 MHz mainframe?

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When you need 100 µV sensitivity, rather than 100 MHz bandwidth, HP's new dual-channel 1806A plug-in lets your 180 Scope System do the job. With the 1806A, the versatile 180 Mini-Giant becomes a powerful tool for measuring and analyzing low-level signals up to 500 kHz.

A 100 µV/div deflection factor lets you see beyond the surface of a signal — to detect ripples, discontinuities and harmonics invisible at less sensitive levels. To get the most out of the 100 µV sensitivity, drift is typically <50 µV/hr, CMRR is 100 dB, noise is <20 µV tangential at full BW — and a pushbutton lets you limit bandwidth to 50 kHz, when you need to. Yet the price of the 1806A is only $675.

For applications where you need high-bandwidth capabilities, there are a host of other plug-ins for the versatile 180 System. There's a 50 MHz, 5 mV/div plug-in for only $695. 100 MHz with 10 mV/div and 50Ω input, $1200 (10 MΩ active probe, $95 extra).

Other available plug-ins include: a differential/dc-offset amplifier, a 4-channel amplifier, and a 35 ps time-domain reflectometer that doubles as a 12.4 GHz sampler.

Today, oscilloscope technology is at a crossroads. The HP direction points to getting the best, now, at a low price — with assurance of increased measurement capabilities down the road, using existing mainframes. The new 1806A plug-in is the latest example of this approach at work.

To see the 180 System, call your local HP field engineer. Or, write to Hewlett-Packard, Palo Alto, California 94304. In Europe: 1217 Meyrin-Geneva, Switzerland.

HEWLETT PACKARD

OSCILLOSCOPE SYSTEMS

Circle 96 on reader service card
New products

September 28, 1970

NCR shifts to cores for memory of Century giant

Company finds thin film rods used in earlier machines too bulky for 300 design; supply of MOS insufficient

Memory systems manufacturers held their breath until the IBM 370 computer series announcement, thinking that there would be more extensive use of semiconductors in the mainframe. Those who were betting on continued use of cores breathed a sigh of relief to find that the concept of a semiconductor cache linked to a large mainframe core memory had been carried over from the 360 series. Then when the National Cash Register Co. unveiled its Century 300 computer last week, the sigh of relief was heard again—the top of the line will use an all-core mainframe memory that can vary in size from 128 to 2,048 kilobytes.

It had been known that NCR wanted a metal oxide semiconductor mainframe memory in the Century 300, but had difficulty finding enough suppliers to deliver 300,000 devices a month [Electronics, June 22, p. 34]. Cores have supplanted the thin film rod memory that had been used in the previous entries in the Century series—the 100 and 200 [Electronics, March 18, 1968, p. 49]—because the rods would take up too much room for the memory range offered in the 300.

NCR wanted at least three qualified suppliers of MOS memories before committing system design to semiconductors. General Instrument Corp. and Signetics Corp. both had contracts to supply 256-bit random access memories, but scale-up problems from pilot line to production line prevented volume deliveries in time for the 300. NCR has not abandoned semiconductor memories, however. North American Rockwell Microelectronics Co. is pursuing NCR business, as are other companies, and an MOS facility is being built by NCR in Dayton to assure close control over at least one supplier.

Spokesmen at NCR's Electronics division in Hawthorne, Calif. say the Century 300 is intended to compete in a class that includes the RCA Spectra 70/60, the Control Data Corp. CDC 3500 and IBM's 370/155; the rental price will vary from $15,000 to $50,000 a month, depending on the memory size and peripheral equipment chosen. The computer would carry out such large-scale, multiprogramming tasks as real-time industrial accounting operations.

The memory cycle time is 650 nanoseconds per word, with a word size of four bytes. That speed is 2 to 4 times faster than that of the Century 200, depending on whether an instruction or an operand is being fetched; the 200 had a two-byte-wide instruction fetch vs. the 300's standard four bytes. The new machine also offers up to 4 times the memory size of the older version. Although compatible with the Century 100 and 200, the latest model has some new features.

One of these is the ability to access—simultaneously and independently—any of four memory modules driving a single memory cycle. The arithmetic/logic processor consists of two units—a command setup unit and a command execution unit. Each unit has its own trunk, and can be fetching an instruction or an operand simultaneously with, but independently of, the other unit.

At the same time, the input/output control has independent memory access, offering users true simultaneity in that three operations can proceed at once.

None of these—the separate command setup and command execution units, nor the independent I/O control access to memory—was offered with earlier Century series computers. Nor did earlier models have four-way memory interleaving, a technique that greatly reduces effective access time by placing successive words in adjoining

Control station. A crt unit at the operator's console of the Century 300 can display setup routines, job queues, priorities and other system data.
Silectron selection.

What you want, when you want it—Arnold's rugged family of cut, toroid, and DG Silectron cores.

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New products

memory modules," says an NCR spokesman. The independent memory access designed into the I/O control gives the system a transfer rate of 4.3 million bytes with minimum interference to internal processing.

As another new feature, the 300 includes as standard equipment an interactive cathode ray tube console to help the operator communicate with the system. The display unit is said to aid in multi-programing by providing such information as setup instructions for the operator, notification of programs terminated, peripheral status, and a review of job queues and program priorities.

The I/O structure is greater than that of the Century 200, allowing all the 200's commands plus eight additional ones, including decimal divide and word binary commands that enhance scientific performance.

All peripheral equipment used with the Century 100 and 200 systems are compatible with the 300, and a new peripheral has been added. It's a drum memory that can store 4.2 million bytes, and has an average access time of 8.3 milliseconds, with a 1-million-bit transfer rate. There's also a new controller that will increase the capacity of a recently introduced disk unit the company offers. The controller allows operation of the disk to store either 30 million or 48 million bytes. It has an average access time of 60 ms in either mode and will have a transfer rate of 315 or 500 kilobytes per second, depending on the recording mode being used.

Software for the 300 will include a complete set of high-level Cobol elements and Fortran, plus NCR's own NEAT/3 programming language. A multiprograming system under development will include job accounting, automatic job scheduling, command systems disk and program library, file and unit sharing, and system-aided recovery.

First deliveries are scheduled for February, 1972.

Marketing Department, NCR, Dayton, Ohio 45409 [338]
New products

Instruments

Minicomputer calls signals for testing circuits, cards, LSI

By Owen Doyle, Instrumentation editor

Versatile system can run 6,000 tests per second; no software skills needed to prepare sequences

All minicomputer makers face the temptation of getting into the automated systems business. Computer Automation Inc., for one, has given in and turned a home-grown setup for checking digital-circuit boards into a commercial tester called Capable. Built around the company's 208 minicomputer, the unit runs functional tests at rates of up to 6,000 per second.

Primary target for the company is high-volume builders and buyers of logic cards, such as big-computer houses and communications companies. However, Capable may attract other types of customers since it can check out any digital network from an off-the-shelf integrated circuit to a 128-pin circuit board or LSI network. In addition, it can run continuity and short-circuit checks on cables.

All of the tester's hardware, except for its teletypewriter, is packed inside a 20-by-27½-by-30-inch cabinet. Besides the computer with its 8,192-word memory, the cabinet holds interface circuitry, a patchboard, connectors for the device under test, a paper-tape reader, a control panel, and the power supplies.

In layout and operation, Capable resembles most automated test systems. The operator puts the test program into memory via the reader, plugs in the device, and pushes a button.

The computer sends test signals through the interface and patchboard to the device, and compares the actual responses against the desired ones. After running the device through a complete program, the computer turns on a GO or a NO-GO light.

Capable's 64 input and 64 output channels give it the capability to check devices with up to 128 pins. An option that permits the programmed selection of pins during a test sequence extends this range to 256 pins.

Testing a device with eight inputs and eight outputs, Capable runs at the rate of 6,000 tests per second, and handles programs with up to 1,536 tests. At the other extreme, a 128-lead network can undergo 192 tests at a rate of 600 tests a second.

Computer Automation has a good idea whom it will be selling against. "Capable is head-on competition for General Radio," says director of applications Richard Drew, referring to GR's 1790 logic-circuit analyzer [Electronics, Aug. 18, 1969, p. 149]. Capable sells for $27,500, versus $35,900 for the 1790. Drew claims that, being a computer maker, his company will have an edge in the test market. "We have a great deal more flexibility when it comes to adding peripherals to the equipment in order to meet specific customer requirements," he says. "People might want data logging, or they might want a disk source for program storage or a (magnetic) tape source. Instrument makers can add some features, but not all the ones we're able to put in."

Another selling point is that Capable is easy to program. "The whole system is unique in that you don't have to learn a language," says Drew. "Many people who have a requirement for testing aren't familiar with computers, so they're faced with the task of first learning how to operate the computer and then how to work the programming language. We've completely eliminated these steps."

Programming starts with an engineer writing the test sequence on a form supplied by Computer Automation. Each step in the sequence contains the desired inputs, the expected outputs, and the identity of the device terminals to be masked.

The form goes to the operator.

Fast look. Making 1,536 tests a second, the Capable system checks a 128-pin circuit board.
Now true precision in Allen-Bradley thin film networks.

Resistance networks for A/D and D/A conversion, digital voltmeters and numerical control systems demand extreme precision. Allen-Bradley can deliver. Precision that starts with a patented chromium-cobalt resistive material vacuum deposited on a substrate made to Allen-Bradley specifications. Precision based on exclusive computer drawn grids. Precision backed by extensive design and testing facilities. Precision on a continuing basis assured by Allen-Bradley’s 14 solid years of experience.

Add the reliability of a single substrate, uniform temperature characteristics, much lower attachment costs and you see why Allen-Bradley thin film networks are the logical replacement for discrete precision resistors.

SELECTED SPECIFICATIONS

<table>
<thead>
<tr>
<th>RESISTANCE RANGE</th>
<th>1K ohms to 2 megs, standard 25 ohms to 50 megs, special (Single substrate range - 0.000 to 1)</th>
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<tbody>
<tr>
<td>TCR LEVELS</td>
<td>-55°C to +125°C</td>
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<tr>
<td></td>
<td>± 25 ppm/°C</td>
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<td>± 10 ppm/°C</td>
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<td></td>
<td>± 5 ppm/°C</td>
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<tr>
<td>TCR TRACKING</td>
<td>± 5 ppm/°C standard to ± 1 ppm/°C special</td>
</tr>
<tr>
<td>TOLERANCES</td>
<td>Absolute to ± 0.1% @ +25°C</td>
</tr>
<tr>
<td></td>
<td>Matching to ± 0.05% @ +25°C</td>
</tr>
<tr>
<td>RESOLUTION</td>
<td>Line width and spacing to 0.0001 inch</td>
</tr>
<tr>
<td>ENDURANCE</td>
<td>Exceeds MIL-R-10509F Characteristic E Procedure: MIL-STD-202D</td>
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Circle 101 on reader service card
who puts it onto paper tape with the teletypewriter. To help him, an executive program already in memory tells the operator what information is to be entered next.

Once the operator loads the finished tape, the tester is ready to go. Each type of device has its own patchboard, so one program can often test several similar types of devices. Changing from testing one device type to another involves changing patchboards and possibly the program.

Programming can be further simplified with SMART, another program supplied with the tester. The engineer preparing the test forms puts down only the desired inputs. A device known to be good is plugged into Capable, and the inputs-only program is run. SMART tells the computer to add the good responses to the program.

Besides SMART, the tester includes all the software for writing and running programs, and for diagnosing.

Switches on the front panel permit the operator to pick various modes for Capable. The purpose here is to allow the operator to find the cause of device failure.

As Drew explains: "The major thing you get with a computer in a tester is the ability to go through your tests in a wide variety of fashions. Let’s say you have a hundred tests. You go through 1 to 100, and your printout tells you there’s a problem at test 33. You say: ‘Let’s start at test 30 and end at test 35.’ Now you have the capability of going in a tight little loop.”

This is just one example of how Capable handles a program. With the front-panel switches the operator can stop and start the program anywhere, order continuous testing, repeat a single test or block of tests or ask for syncs at various steps. He can also stop a test after the input signals are generated but before they’re applied, and switch to the DEBUG program.

The customer specifies Capable’s connectors, which can be edge connectors, cable terminals, or even IC sockets. The normal way to handle ICs, though, is to plug the circuit into an adapter, and the adapter into a Capable connector.

Inputs and outputs don’t necessarily have to go to and come from a device’s leads. Probes can run to points on a circuit board. For example, a “connector” may include a probe that clamps onto an individual IC.

Delivery time is 90 days.

Computer Automation Inc., 895 W. 16th St., Newport Beach, Calif. [339]

Wire versus write. The computer sends signals to the device under test through a patchboard. Software can do the patchboard’s job, but in many cases it’s cheaper to wire the interconnections rather than program them.
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And if you'd rather use an external op amp in the output, we'll leave ours out. Or if all you want is our TaN ladder and IC switches, you can get them separately.

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Circle 104 on reader service card
New products

Instruments

Infrared probe reaches the untouched

Data from optical fiber converted into dc voltage, displayed on remote meter

It's difficult to measure temperature in out-of-the-way places, in high-voltage or high-radiation work areas, or where noise is an impediment, moving objects are involved, or direct-contact sensors are considered to be undesirable.

Job areas such as these suit the model 1017 thermal probe developed by Vanzetti Infrared and Computer Systems. The infrared thermometer is a modification of the company's thermal bond monitor introduced last year [Electronics, Dec. 22, 1969, p. 133].

A single optical fiber picks up IR radiation, which is a function of temperature, and transmits it through a detection head to a console. There the IR is converted into dc voltage and displayed on a 3½-digit voltmeter. The fiber's 14-in. length, thinness (0.14-in. outside diameter), and flexibility allow it to measure IR in places where ordinary instruments can't go.

Detection of IR in high-voltage or high-radiation areas is possible with the 1017, since the optical fiber is inert and available in different lengths. The console and detector head can be placed a safe...
New products

distance from the radiation area while the fiber monitors semiconductors and other electronic devices dissipating electrical power, or elements in nuclear reactors, in real time.

Since the fiber assembly is hermetic, it also can measure the IR of devices in a vacuum bell jar without vacuum loss. This hermeticity also allows the fiber to monitor IR during the deposition process of semiconductors when the devices are in an environment containing special gases.

The optics assembly is made of a single optical fiber, enclosed in a stainless steel tubing. The optical fiber is made of infrared-transmitting glass clad with a thin layer of a different glass. The refractive indexes of these two glasses are chosen in such a way to contain the field of view of the fiber within a 50° cone around the fiber’s axis.

The optical fiber fits into a detector head where it illuminates an IR sensor, whose output is chopped and sent to the electronics cabinet. There the signal is processed and displayed by the cabinet’s digital panel meter.

The figures shown on the meter’s face are directly correlated with the temperature of the targets.

The thermal probe, the company says, can be an important tool for measuring the temperature inside combustion engines. Its fast response records temperature variation during engine cycles and at different fuel compositions.

Both Alfa Romeo and Fiat, of Italy, are planning to use the probe to measure temperatures of engine parts, such as valves and bearings.

Another application is in monitoring the temperature of semiconductors and other components during soldering processes.

In the thermal probe the optical fiber and the detector head remain the same as in the thermal bond monitor, since the means of gathering information remains unchanged. However, in the thermal bond monitor, the values of the three threshold lights have to be set by the user for each type of chip, whereas the probe simply displays the dc voltage values on a digital panel meter. This voltage is directly correlated to the temperature, and the company supplies correlation charts.

The probe has a temperature range from 200°C to 1200°C, a five-step attenuation switch allows the instrument to cover the whole range. Custom fibers can monitor higher temperatures. The detector has an accuracy of ±1% of the reading, and response time is 1 ms.

Introductory price of the probe is $1,375. Delivery time is 30 days.

Vanzetti Infrared and Computer Systems, 515 Providence Highway, Dedham, Mass. 02026 [369]

Norwegian firm invades industrial recorder field

An old-line Norwegian producer of consumer-type tape recorders has moved into the instrumentation market with a four-channel recorder for engineering and research.

The Series 100 machine, built by Tandbergs Radiofabrikk of Oslo and sold for $3,685 in the U.S. by Intermed Inc., is designed to compete with a Japanese recorder, Teac Corp.’s R331F, which sells for $5,225, and with Hewlett-Packard’s $4,285 model 3960. Intermed says the Norwegian machine is the lowest-priced quarter-inch-tape machine designed to meet IRIG specifications for the intermediate band.

Chief designer of the Tandbergs unit is Hans H. Westerby, who returned to Norway after working at H-P, where he was principal designer of the 3960.

The Norwegian company estimates that the U.S. market for instrumentation-type recorders amounts to about $5 million, adding that H-P now has “a big share of it.”

The principal competitive features of the Series 100 are size and price, says Jerry Ramsey, director of manufacturing for Intermed. The Tandbergs machine weighs 25 pounds, against 50 for H-P’s and 117 for Teac’s. Another feature of the 100 is a built-in cathode ray tube that displays the percentage deviation of the four channels simultaneously. This facilitates selection of input range.

The recorder provides three tape speeds: 1%, 3% and 7½ in./s. Maximum frequency response is 2.5 kilohertz, and signal-to-noise ratios are 42, 46 and 48 decibels, respectively. The ratios are not affected when copying data or transmitting the modulated carrier from one recorder to another. Drift is ±0.01%.

Normally, all four channels are in the frequency-modulation mode. However, channel 2 can be operated to provide electronic flutter compensation, and channel 3 has three modes: data only, voice only, and data interrupted by voice.

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Plated wire bids for mainframe memory

Basic module has 4,000 72-bit words; speed-cost combination challenges cores

Memory systems suppliers have generally shied away from plated wire technology, betting that semiconductors will give the speed advantage often cited for plated wire and that cores will remain the largest storage medium for computer mainframes until semiconductor memories truly come of age. But a few have chosen the plated wire route, primarily because of the combined speed and cost advantage it offers over ferrite core systems now available.

One such advocate of plated wire is Bruce Kaufman, president of Memory Systems Inc. Formed about 18 months ago to develop and produce these memories, the company plans to deliver in December what it claims is the first large mainframe plated wire memory available to original equipment manufacturers.

The nondestructive-readout memory will have a basic module of 4,000 72-bit words and will cost as low as 3½ cents per bit. A diode matrix version will have a 300-nanosecond read or write cycle and 150-ns access time. A transistor matrix version will have a 200-ns...
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Pc board connections move in from edge

Rigid wires soldered at any desired location permit greater flexibility

Many engineers accept without question the traditional placement of plated pads or connector pins at the edge of a printed circuit board. But placing the pads or pins at the edge also entails some traditional problems. They may need long connection runs on the board to get to them. They must be plated to make them stand up to many insertion cycles. The plating must be protected during soldering operations, and the mounting scheme is all but fixed because of the edge connector.

Molex Inc. now offers a way to make connections at any spot on the board with a system of connectors that allows flexibility in pc board and cable design. In the Conectcon system, rigid wires are inserted in plated-through holes in the board and soldered to the circuity to serve as male terminals for the connectors.

The wire that's used is 0.045 inch square, readily available since it's the type used with wire-wrapped terminals. The square cross section is used because it presses equally well in any range of hole sizes, while round wire would require im-

General purpose wire stripper model 2670 automatically measures, cuts and strips hard and soft insulations from wire in lengths from 1 inch to 100 inches without damaging the conductor. It handles single conductor, solid and stranded wires from 26 to 16 AWG. Top output for 2-inch lengths is 11,000 per h. Eubanks Engineering Co., W. Duarte Rd., Monrovia, Calif. [421]

Molding machine JD-11 is designed to encapsulate transformers, DIPs, coils, ICs, and produce a broad variety of cups, cases and lead forms. It uses economical (handy) molds of aluminum to process soft-flow, epoxy molding compounds. Bench-mounted, the press has a base measurement of 1 sq. ft. Morris Enterprises Inc., Schoenborn St., Sepulveda, Calif. [422]

Computer-controlled Wrapmaster 101 is a semiautomatic wire wrapping system designed to provide increased productivity at a minimum capital investment. Up to 16 independent wrap stations can be controlled by the 704 computer on a time sharing basis, using less than 10% of the 704's capacity. Unit can wrap panels up to 24 x 44 in. Raytheon Co., Lexington, Mass. [423]

Numerical control insertion/wire termination system offers flexibility and economy for the user whose operation doesn't justify the cost of two individual machines. It is a double-headed unit, with one head for automatic insertion of components on pc boards; the other for semiautomatic back plane wire termination. Universal Instruments Corp., Binghamton, N.Y. [424]

Ultrasonic generator model 4320 uses integrated circuits, a simple design power generator and a compact package to achieve low cost. Primary application is in ultrasonic wire bonder used in assembly of transistors, diodes and ICs. The generator is available in 5- and 10-watt ratings. Data Systems Corp., 135 Commerce Dr., Fort Washington, Pa. [425]

Heat dissipater series is designed to accommodate ICs packaged in the large TO-8 style cases having diameters up to 0.550 in. The base accepts square or circular 1C lead patterns to 0.400 in. Standard dissipators and the re-tainer clamps are made of commercial black anodized aluminum. International Electronic Research Corp., W. Magnolia Blvd., Burbank, Calif. [426]

Die bonder series 2000 is designed for high speed processing of integrated circuits and devices on both lead frames and individual headers. It operates with rates in excess of 3,500 per hour on discrete packages and over 5,000 per hour with specially prepared and presented dice. On ICs, rates of more than 2,000 per hour can be achieved. Kras Corp., Fairless Hills, Pa. [427]

Coil winding machine model 44A, designed for short production runs, prototypes, and laboratory work, comes complete with standard dual-cone drive center assembly. The coil bobbin is supported between 45° conical centers connected by a 10-24 threaded rod and thumb nut. Machine winds No. 20 to No. 48 AWG. Price is $245. Henry Mann Inc., Box 65, Feasterville, Pa. 19047 [428]
practical tolerances on the pc board holes.

The female terminal of the Conector uses a double cantilever with an opening that prevents pins larger than 0.80 inch diameter from entering, to avoid overstress on the cantilever.

The connectors are injection molded and are available in several materials. Presently, they are made of nylon and center spaced on 0.156-in. centers. The wire is crimped into the housing, which will accept wires 20 gage or smaller.

The wires can be inserted in the board holes with a vibrating device, a set of templates, and a press. With such a setup, a vibrator can insert up to 300 pins a minute, says Kerry M. Kraftshe, Molex project engineer.

In an application, the square wire pins are first cut in a high-speed machine. The pc board is placed in the template, and the template closed and placed on the vibrator. The pins are then sprinkled onto the template surface. A preset vibrator cycle time, amplitude, and frequency guarantee that all holes in the template are filled; unused pins fall into a hopper. The template then is removed, placed in a press, and the completed board is removed from the template.

The board holes are 0.050 in. diameter, with tolerances of 0.002 in. —larger holes don't hold the pin as well, while smaller holes result in extrusion of the substrate material beneath the board. Round wires would require tolerances of 0.001 in., which would be too difficult to maintain in production.

Average contact resistance is 2.7 milliohms and the millivolt drop at 1 A is less than 3 mV.

The board scheme can be used for connecting cables to the board, for interconnecting boards directly with a perpendicular entry, or even for interconnecting boards in parallel, at any points on the surfaces.

Price of the interconnections system is about 1 cent per circuit in volume usage.

Molex Inc., 5224 Katrine Ave., Downers Grove, Ill. 60515 [429]

Ellipsometer gives reading in less than two minutes

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Ellipsometer gives reading in less than two minutes

**SPEED** and ease of handling—features that are difficult to combine in an ellipsometer—have been built into one developed by Applied Materials Technology. The company, which does dielectric film deposition on silicon wafers, was running into quality control problems, where essentially "we didn't know what we had for film composition," says Ted S. Rafaiovich, marketing manager. Available ellipsometers were difficult to use in a production environment, he says, so Applied Materials decided to build its own.

Ellipsometers derive their name from the elliptical vector pattern generated as polarized light passes through a quarter-wave plate. They measure a silicon wafer's refractive index and the thickness of dielectric films, parameters of particular importance in manufacturing silicon devices that require an insulating layer such as silicon dioxide.

Nitride and oxi-nitride passivation layers contain oxygen, and the more they contain, the lower their refractive index. This oxygen content is directly related to the dielectric constant, but is hard to control. However by using the new ellipsometer to take sample measurements of its refractive index, an operator can determine the percentage of oxygen in the nitride film in less than two minutes. Rafaiovich says that the only other method of determining the oxygen content is by X-ray backscatter, a lengthy and complicated procedure.

The unit comprises a polarizer, analyzer, horizontal wafer-mounting table, quarter-wave plate, mercury lamp, and photomultiplier tube, all mounted on a single cast chassis, a feature that contributes to ease of measurement. The operator places the wafer on the table, where it reflects light transmitted from the polarizer to the analyzer via the quarter-wave plate. Then he adjusts the polarizer and analyzer for a minimum signal on a current meter, and plots the readings from the two arms and the scale on a nomogram to come up with the index number.

Film thickness readings are accurate to within ±20 angstroms and refractive indices to within ±0.03A over the range of 20 to 2,700A. The unit measures 20½ in. wide by 11 in. high by 10 in. deep, and weighs 22 pounds. The price of $4,975 includes the direct-reading refractive index and thickness nomogram, as well as computer generated tables.

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Semiconductors

Planar Darlontons stress reliability

Stability, price advantages also claimed over mesa types; two devices have low $V_{\text{SAT}}$

Taking strong exception to the view that an integrated power Darlington amplifier is best made as a mesa device [Electronics, July 20, p. 137], Unitrode Corp. is introducing a series of power Darlontons made by the planar process. Howard Wasserman, Unitrode product marketing specialist, says the planar Darlington offers greater reliability and stability than its mesa counterpart because it is completely passivated. He adds that it can be priced competitively with a mesa device. The company's U2T101, for example, an 80-volt unit in a four-lead TO-5 metal can, sells for $2.75 each in quantities of 100; a TO-66 version, the U2T2201, costs $2.95. "High-quantity pricing is comparable to alternative approaches such as using two discrete plastic transistors," Wasserman claims.

Perhaps the most significant characteristic of the Unitrode device is its low saturation voltage of 1.5 V maximum at 5 amperes. This low value "disproves the adage that Darlington transistors inherently have poor saturation specs," Wasserman says, and makes the

Digital-to-analog converter monolithic chip. It consists of an internal voltage reference, 6 precision current sources, 6 current steering logic switches, a diffused resistor network, and an internally compensated operational amplifier. Precision Monolithics Inc., 1500 Space Park Dr., Santa Clara, Calif. [436]

Random-access LSI memory type 3102 is a 256-bit bipolar unit that operates with a companion decoder to make main-frame memories with 120 ns maximum access time. Both memory unit and decoder exhibit only 10 µA input leakage current. Load current is only 0.25 mA for the decoder and 0.50 mA for the memory unit. Intel Corp., Middlefield Rd., Mtn. View, California [437]

High-voltage npn transistors feature excellent beta linearity from 1 mA to 50 mA. Offered in plastic packages, they are registered as the 2N5964 and -5, which have dissipation capabilities of 700 mW, and as the 2N5830, -1, -2 and -3, which can dissipate 310 mW. Prices are 41 to 84 cents depending upon quantity. Fairchild Semiconductor, 464 Ellis St., Mountain View, Calif. [438]

Rf attenuator diode MA-47083 functions as a silicon p-i-n device designed for use as a current-controlled resistor. It operates within the 1 MHz to 10 GHz frequency range, providing low capacitance of 0.25 pF and low series resistance for fast switching applications. Minimum breakdown voltage is 300 V. Microwave Associates Inc., Burlington, Mass. [439]

Varactor tuning diode chips in the C1NS139-C1NS148 series are for use in hybrid ICs in the uhf/vhf and lower microwave frequency ranges. The diodes' 4V capacitance values vary from 47 pF through 6.8 pF with corresponding Q values as high as 350 measured at 50 MHz. Price (100-999) is $3.55 each. MSI Electronics Inc., 34-52 57th St., Woodside, N.Y. [443]

Read-only memory IC 8224, of medium-scale complexity, is organized to read 32 eight-bit words. It is compatible with TTL and DTL input/output structures. Maximum propagation delay time is 50 ns. Typical power dissipation is 310 mW; maximum is 400 mW. The unit comes in 16-pin dual inline or flatpack configurations. Signetics Corp., 811 E. Arques Ave., Sunnyvale, Calif. [440]

Diode MW20FR90 has 1.5 µs recovery time and can operate at 12,500 V dc reverse voltage. Other specifications include: leakage at rated voltages of less than 5 mA, and maximum average current of 20 mA. The epoxy-encapsulated devices are 0.187 in. square by 0.500 in. long and have 1-in.-long axial silver wire leads. Sarks Tarzian Inc., Bloomington, Ind. [441]

Three AND gates added to the 7400 TTL IC line include US7408A quad 2-input, US7409A quad 2-input with open collector output, and US7411A triple 3-input. They are useful in high-speed arithmetic sections to avoid excess propagation delays experienced when using NAND gates connected to perform the AND function. Sprague Electric Co., North Adams, Mass. [442]
New products

Unitrode device suited for such high-gain switching applications as print-hammer, solenoid, and lamp drivers, and switching regulators. In addition, the Unitrode Darlington can be used in linear circuit applications such as motor controls and amplifiers.

The Darlington offers a valuable alternative to the silicon controlled rectifier. Like the SCR, it is able to control large amounts of power with a minimal input signal; however, unlike the SCR, the Darlington can be turned off easily, simply by reducing the control signal.

Other key specifications for the U2T101 and U2T201 are maximum collector-emitter breakdown voltage of 150 V, and a minimum direct-current gain range from 2,000 to 10,000 at 5 A collector current. Minimum current rating is 10 A, and the manufacturer is planning to introduce a 20-A version.

Both the driver and the output transistor on the monolithic chip are npn overlays, multiple emitter devices. The output device itself has more than 100 distinct emitter sites.

Wasserman expects the major market areas to be industrial and consumer, with special emphasis on computer peripheral equipment.

Unitrode Corp., 580 Pleasant St., Watertown, Mass. 02172 [444]

Darlington power. Passivated planar structure adds reliability.

2-watt IC amplifier fits any sound system

More flexibility and self-sufficiency were the goals of design engineers at Motorola Semiconductor when they set out to develop a competitive 2-watt 1C audio amplifier.

The monolithic amplifier, designated the MC1316P, can be used with all sound systems—that’s its principal advantage, says Clay Tatom, manager of linear IC product planning.

Craig Marshall, a member of the product planning staff, adds that the amplifier requires fewer outboard components than comparable devices to work in a circuit. Just two resistors and two ceramic capacitors will do the job, Marshall points out.

“We think we’ve designed in enough flexibility to make it useful as a general audio amplifier and a television audio amplifier,” he says. For example, the device’s built-in preamplifier and power amplifier use separate pinouts, allowing the user to put a tone and volume control between the preamplifier and the power amplifier. This flexibility is an advantage, say Motorola engineers, because some TV sound systems put out 3 volts and some as little as 100 millivolts. With the MC1316P the lower-level voltages can be preamplified without introducing noise.

The device’s sensitivity can be altered by changing the ratio of the two outboard resistors. Marshall says an input signal as low as 27 mV won’t diminish the MC1316P’s output power. The built-in power transistors are designed to limit the current themselves, contributing to short-term short-circuit proofing. The chip has been short-circuited under both ac and dc with a screwdriver and has survived for 10 seconds or more. “And if you heat sink it, you can get more than a 10-s short without destroying the device,” Marshall says.

An additional lead frame provides heat sinking. Although tabs are bent up from the socket pins, the plastic dual in-line package is a standard configuration that’s pin compatible with present sockets.

Ripple rejection is 23 decibels. Marshall says this allows the unit to operate with completely unregulated supplies—an extreme example. Input sensitivity of the power amplifier at 2 W is typically 0.4 V. Total harmonic distortion at 1 kilohertz with 2-W output is typically 1.5%; typical signal-to-noise ratio at 2 W is 70 dB.

Available from stock, the MC1316P sells for $1.40 in quantities over 1,000.

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Electronics | September 28, 1970

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New Books

Special propagation problems


Those microwave engineers who must deal with plasmas, dissipative media, and other special space and re-entry phenomena have obtained little illumination in the many otherwise comprehensive books on antenna design and application. This book, while far from comprehensive, does cover these special problems and related topics in great detail. It also attempts to bridge the gap between macroscopic phenomena, represented by Maxwell's equations, and microscopic phenomena, represented by quantum theory.

The engineer who designs antennas for submarines, for missiles and re-entry vehicles, and for operation in such difficult environments as those attending nuclear bursts will find it especially useful.

The book has been prepared under the auspices of the Atomic Energy Commission and incorporates the results of much basic and applied research performed for that agency by the Sandia Corp., where Harrison is a staff member. King, a consultant to Sandia, is a professor of applied physics at Harvard University, and is well known as the author of several books on antenna theory, with special emphasis on dipoles. This type of antenna, singly and in clusters, bent and folded, is almost the exclusive focus of the book.

The book starts with a resume of basic electromagnetic theory, and then immediately goes into the properties of ionized media, stressing interaction at atomic levels. Treatment here, as throughout the book, is rather theoretical. In fact, the text often seems a collection of reports on selected topics, with theory as the common thread. However, most topics also are augmented by relevant experimental data so that the design engineer retains some grasp of the physical phenomena involved. Thus, the sections on L, T, dipole, transmission-line, and cylindrical antennas all include detailed results of significant experimental work.

A separate chapter on coupled antennas provides good coverage of both circular and linear arrays. It including considerable material about current distributions, resistances, and reactances of the various elements, and composite radiation patterns, especially for comparison of theoretical and measured results. A brief chapter on receiving scattering arrays also is included, and another chapter covers the problem of antennas near the interface between a semi-infinite dissipative medium and the air. The chapter on cylindrical antennas is particularly good. It comprises 112 pages, and discusses the various equations which have been developed to approximate the current distribution on a cylindrical radiator. The authors then proceed to analyze the performance of a dipole, and an electrically short, moderate, and long antenna in a dissipative medium. Theory and experiment are regularly compared through extensive use of graphs.

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Technical Abstracts

Registering the impact

Radiation hardened transistor magnetic register
D. Hampel and K. Prost
RCA Advanced Communications Laboratory
Somerville, N.J.

There is little point in having reliable, radiation-hardened active circuitry in an electronic system if the system's memory turns amnesiac when subjected to the photon and neutron irradiation of a nuclear burst. The Army's Harry Diamond Laboratories has funded a great deal of research in techniques for hardening memories, and this paper details some very successful results.

RCA's work has combined circumvention—the use of inherently immune components—with rather sophisticated compensation techniques. The results have been magnetic shift-register memories hardened to the same level as the best off-the-shelf hardened ICs, and capacitive memories with a potential hardness that is almost an order of magnitude better, or beyond the theoretical limit to which these ICs can be hardened.

Magnetic cores have a high natural immunity to neutron damage but can suffer a change of state when their driver circuitry saturates as photons from a nuclear burst sweep an intense, if transient, photo-current through them.

One solution developed by RCA uses magnetic cores as an immune temporary storage medium for the shift register's flip-flops, and has a drive configuration that pushes equal and opposite photon-induced photocurrents through the cores during nuclear irradiation. This type of circuit has been shown to work reliably in environments up to $8 \times 10^{10}$ rads/seconds, which is about as intense a test environment as can be generated by any but the giant flash X-ray machines, such as Physics Internationals'.

Another technique developed by RCA relies on capacitive interim storage, which permits the register's flip-flops to saturate as they please and depends on a residual charge imbalance to reset the flip-flops after the photocurrent surge has passed. This type of circuit has proven itself in test environments up to $5 \times 10^{10}$ rads/s and is estimated to be reliable in environments 10 times as severe.

Presented at IEEE Conference on Nuclear and Space Radiation Effects, La Jolla, Calif., July 21-23.

Minimizing errors

A low-level, high-speed, solid state multiplexer with programable gain amplifier
David Hartke
Xerox Data Systems
El Segundo, Calif.

Up to 1,024 analog input channels can be multiplexed onto a single line with the Xerox Data Systems DM40 differential multiplexer, which provides good linearity while rejecting practically all common mode noise and generating little noise of its own. Achieving this level of success required recognition of many sources of error—sources that can be found in the design or that pop up when the multiplexer is improperly used.

There are seven major sources of errors. First is input capacitance, which may cause a current to flow from the line toward the source at the instant a particular channel is connected. This current can cause an erroneous signal to appear on the line. Second is dynamic pump-out current, which occurs when the current flowing in or out of input capacitance is different when it's turned on than when it's turned off.

There's also a steady state pump-out current that doesn't amount to much unless the multiplexer dwells on a particular channel at some length. And there are the usual problems of overloading, crosstalk, zero offset, and noise.

The Xerox design takes all these error sources into account in an eighth-chassis design. In the Xerox scheme, 128 channels are connected to each chassis.

In addition, there is provision for several different kinds of contractors on the input lines, a variety of signal conditioning functions, power-off protection, and a multiplexer switch.

New Literature


Electronic keyboard. Controls Research Corp., 2100 S. Fairview, Santa Ana, Calif. 92704. A standard electronic keyboard providing all ASCII characters is described in a four-page product bulletin. [447]

Laser system. Raytheon Co., 190 Willow St., Waltham, Mass. 02154, has published a product folder on the model SS-218 laser system for trimming resistors and other deposited materials. [448]


Coaxial connectors. ITT Gremar, 10 Micro Dr., Woburn, Mass. 01801, offers an up-to-date 148-page handbook covering rf coaxial connectors. [450]


Plastic transistors. Fairchild Semiconductor, Box 880A, Mountain View, Calif. 94040; offers a cross reference guide listing its complete stock of plastic transistors that are the nearest equivalents to more than 400 standard industry 2N types. [452]

Preset counter. Vorne Industries Inc., 5641 N. Northwest Rd., Chicago, Ill. 60631. A solid state, preset counter capable of 60,000 counts per minute and having decimal display of the actual count is described in a new bulletin. [453]


Metal film resistors. Dale Electronics Inc., Box 609, Columbus, Neb. 68601, has available a report on established reliability metal film resistors. [455]

Echo suppressor. Lenkurt Electric Co., 1105 County Rd., San Carlos, Calif. 94070, has issued a four-page product brochure describing the 931C echo suppressor. [456]


Plasma stripping. International Plasma Corp., 25222 Cypress Ave., Hayward, Calif. 94544, has released an applications bulletin describing how to employ the plasma stripping process when using KMER photoresist. [458]

Color tv camera. Ampex Corp., M/S 7-13, 401 Broadway, Redwood City, Calif. 94063. Four-page color brochure V285 describes the model BC-230 color TV camera for studio and remote origination. [459]

Function generator. Esterline Angus Division of Esterline Corp., P.O. Box 24000, Indianapolis 46224. A function generator that has a 0.01-Hz to 100-kHz frequency range is described in a two-color catalog sheet. [460]

Numerical control. The Singer Co., Fridge division, 339 East Ave., Rochester, N.Y. 14604, offers a 76-page manual on numerical control systems. [461]

Coordinate measuring and inspecting. Ehrenreich Photo-Optical Industries Inc., 623 Stewart Ave., Garden City, N.Y. 11530. Precise optical scanning and coordinate measuring of workpieces ranging from integrated circuits and printed circuits to precision metal stampings are described in brochure ID-16. [462]

Semiconductor devices. International Rectifier, 233 Kansas St., El Segundo, Calif. 90245, has released a catalog presenting all its major semiconductor devices in an easy-reference format. [463]


Adhesives and coatings. Epoxy Products Co., 166 Chapel St., New Haven, Conn. 06513. Conductivity of epoxy-silver adhesives and the effects of cure conditions and heat-aging are included in the technical data offered with information bulletin No. 7. [465]

Computer system. Infotronics Corp., 8500 Cameron Rd., Austin, Texas 78753, has released a 20-page brochure on its recently announced MINI/Max computer system. [466]

Digital data acquisition. Esterline Angus, P.O. Box 24000, Indianapolis 46224. A two-color catalog sheet describes the model D-2020 digital data acquisition system. [467]
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Electronics | September 28, 1970

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SINGER INSTRUMENTATION

Electronics | September 28, 1970
The Campus Scene: Facing the Marijuana Malaise

This month the teenage clan heads back to the high school and college scene. And parents head back to the same concerns that had everybody uptight all during the 1969-70 school year. Heading the list: marijuana.

Today there's still nothing that creates more anxiety in families. And there is nothing more loaded with misconception than "pot" and its use and abuse by young people. On this, you can get outdated fast—even opinions that were current last spring need some shaking out and a fresh look.

Top experts (psychologists, psychiatrists, sociologists) are now stressing two points:

- Pot smoking by a teenager should not be met with indignant alarm.
- Pot smoking should be met by firmness and careful investigation.

The pros make these additional points in addressing parents: Most teenagers who experiment with pot are not "hung up," alienated, or disturbed. (Harvard experts note that at least 66% of the students in some colleges have tried it.) Many, even most, teenagers can control it.

Still, if a parent looks further ahead, the risks begin to appear.

Teenage pot users fall into at least three groups.

The experimenter sees pot as an "experience" of his generation. The protester uses it as a way of speaking out. These types, say the experts, may be in no great danger.

The problem smoker is different. He's the heavy user. His school and social adjustments are often poor. He may be a loner and potentially a drop out. If he uses pot to cope with growing up, a step-up to hard drugs is a danger.

If you learn that your teen-ager has been experimenting with pot, dialogue is what is needed. He will probably want to talk it out, if not with a parent then with a trusted— and informed— third person. What you need to learn about, say the experts, are his motivations. The idea is to discover any underlying emotional attitude or pressure that may point to the problem smoker.

One way or another, your answers may come fast. But drawing this line can be most difficult. If a parent must seek help, a well qualified school or college guidance counselor—or a psychiatrist— should be consulted. A parent should first talk with this person privately.

A specialist comments: "A smart teen-ager is apt to know a good deal about pot. Get someone who's well informed—who really knows the answers."

Added danger: Along with all of this, it's now pointed out by a specialist with the National Institute
Pot may be adulterated with hard drugs or toxics

Memory, for instance, can be distorted. A parent can explain this uncertainty to a teenager. And there's the added chance that pot may be adulterated with hard drugs or toxics. Adulteration is a growing menace. Some say that pushers "hook" kids this way.

As for stepping up to hard drugs, remember that there is a sharp difference between smoking pot in limited amounts and using drugs such as LSD and heroin. Hard drug addiction is generally limited to the depressed and disturbed. In any case, there is no known drug progression per se from pot on up the line.

The blase parent makes a dire mistake

Another reason not to take pot experimentation lightly is that a teen-ager sometimes has admitted pot smoking to get off the hook with "easy" parents and cover up the fact that he's using a hard drug.

The parent who is blase about marijuana may be making a dire mistake, say the pros. At the same time, the parent who coddles his own martinis and takes a self-righteous attitude about pot — will find his preachments falling on deaf ears.

A book that clarifies problems underlying teenage drug use is Graham Blaine, Jr.'s, The Parent's Guide to Adolescence (Little, Brown, $1.95).

MONEY & TAXES

Keeping Close Tabs On Your Stock Portfolio

A man who has taken losses in the market can throw up his hands, fire his broker, go into real estate — or he can bone up on the stock market, and this may be a smarter proposition.

After the 1970 market flop, self-help will be a widespread trend. "For the average businessman," says one professional, "more knowledge can mean realistic investment objectives for a change, and smarter dealings with brokers and others who hand out advice."

It can also help a man avoid brokers who churn accounts.

How should you investigate an industry or company group? Many investors, says a top pro, not only brush aside industry surveys, but fail to evaluate the ones they do read. The surveys — obtained from a broker — should tie the industry into the whole economy (many don't), state the pros and cons (many make a sales pitch), compare leading companies (not lump them together), and draw some straightforward conclusions without too many hedges.

Many, if not most, surveys, especially those that go to the individual investor, are pretty bad, say some pros. "The big institutions get the good inside stuff," says one. "So pick your surveys with care, and compare them."

"Caution: Some brokers have cut down lately on their research stuffs, and this is a point to check if you want any type of survey material.

Since many investment areas no longer fit into neat industry groupings, a second approach is needed. "Too many people ignore the 'sector' philosophy, and it hurts them," says Richard Cantor, a Manhattan consultant.

Here companies are grouped according to how well they meet rising needs of society, or how much they might benefit by new trends. Examples: manufacturers who would ride an ecology boom (makers of testing devices, filtration systems, etc.), or companies that would benefit greatly if interest rates should fall (homebuilders, leasing outfits, finance companies).

But keeping abreast of economic sectors takes time — and study. A carriage-trade suburban broker notes that many nonfinancial executives read annual reports naively. "They judge too much by the president's optimistic statement," he says. Smart reading of an annual report can include the fancy language, but it must bear down on the footnotes — and the dollar figures.

Look at the company's sales growth. If it doesn't roughly parallel earnings growth, find out why.

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product. A doubling in, say, five years, means top performance. But this won’t guarantee a good future, so you have to investigate the company—closely.

Stock charts are another field for study. A good chart shows the 10-year price activity of the company’s stock, the industry’s composite, and a general index such as the Dow-Jones Industrials or Standard & Poor’s 500. A chart may not tell you when a stock will hit a bottom or a high, but understanding it will help you to keep tabs on a broker and judge a tip or recommendation more clearly.

A warning: Market letters can be good, indifferent—and very bad. They should be read for background and mood, but not for choosing stocks.

The Battle for Investment Survival, by Gerald M. Loeb, covers Wall Street’s ins and outs and is good for anyone who takes a serious view of handling his own portfolio (Simon & Schuster, $5.95). For solid, basic information, see Eiteman, Dice, and Eiteman’s The Stock Market (McGraw-Hill, 564 pp., 4th ed., $10.50). The Money Game by “Adam Smith” reveals a lot about the purely psychological side of the market (Random House, $6.95).

1040 Woes: Investors’ Deductions

There’s much confusion over investment-related tax deductions. “Few areas in the law get so muddled on the Form 1040,” says a Midwest tax pro who works with high-income businessmen.

A new federal court case, for instance, points up the frequently forgotten rule that if you spend money to inspect a possible investment—as opposed to one you already own—you cannot deduct the expense.

In this case, the taxpayer tried to deduct his travel expenses. All he could do, it turns out, was to add these items to his cost basis—if and when he actually paid his money to make the investment.

It’s understood that you can deduct the cost of a safe deposit box, and books and periodicals, on investments—but you can go further. Fees for investment advice are deductible, and this is true whether you pay for counseling alone or full portfolio management.

State taxes on a stock sale are deductible—which some people miss.

Not only can you deduct the cost of travel to inspect investment property you own—assuming you have a valid business purpose—there are also some occasions when you can deduct the cost of travel to an annual meeting of a company in which you own a sizeable share. This, though, is a gray area: be able to prove that the trip was necessary—not just an outing.

A pitfall: IRS will clamp down fast if it discovers that you are deducting broker’s commissions or any expenses related to tax-exempt bonds.

For a good straightforward review of most types of deductions, see J. K. Lasser’s Your Income Tax, by Bernard Greisman (Simon & Schuster, $1.95).

INSURANCE

Homeowners get a Break on Insurance

Some new fine print has been tacked to the homeowner’s standard insurance policy. The changes are in effect in about 45 states, and the list is still growing. Now is the time to take stock of your coverage, fill in some gaps where necessary, and maybe even alter the type of basic protection you’re carrying.

One of the major new built-in benefits—for which you pay no extra premium—raises the payoff limit on personal property from 40% of the coverage on the house to 50%. This means that “unscheduled” personal property which is not listed for special floater protection, such as furniture, china, silver, art works, clothing, and sports equipment, gets better protection.

Example: If your house is insured for $50,000, your unscheduled property is covered up to $25,000, instead of $20,000.

Another improvement helps the man who moves from one house to another. Formerly, personal property was covered only in transit, pending a new homeowner’s policy. Now the coverage extends for the first 30 days up to the 50% limit. The in-transit coverage, however, is for just 10% of the usual protection.

The rule on theft from an unlocked car is a bit better too. Formerly, a car had to be locked. Now you’re protected if you place the car keys in the hands of an authorized parking attendant. But you are still without protection if you leave the car unlocked and unattended.

Finally, “mysterious disappearance” is defined more liberally. Say that your wife leaves a ring on a wash stand in a restaurant, returns later, and finds it gone. The policy now presumes a theft, so you’re covered. But note: Protection in case of simple loss—where you have no idea what happened—now requires a special floater policy, under a somewhat tighter rule.

The new limitations add up, too. On standard policies, there’s a maximum recovery of $500 for all jewels and furs lost in any one theft—formerly $1,000.

You might consider broadening your list of such items already insured by a floater policy which covers virtually all risks. Rates vary. Furs run about $1 per year to nearly $1.50 (Los Angeles) for each $100 of value, and jewels about $1.50 to as high as $2.80 in New York city.

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The Road. We drove most of the test over portions of Interstate 71. From Akron to Columbus. And back. And, of course, the usual in-city traffic. Wet, dry, day and night, our teams went an average of 1200 miles a day — around the clock — for 50,000 miles. We kept to the legal speed limit, traffic and weather permitting, driving the way you drive — maybe a little harder.

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many of the unlicensed vehicles that people are buying these days, such as golf carts, snowmobiles, and beach buggies. These are no longer covered for theft and damage.

An easy guide to your basic homeowner protection: A Form 3 policy will give you broad coverage that's usually ample; Form 4 is for a tenant and Form 5 — the broadest protection — may overshoot your needs. The Form 5 covers all risks, but may cost double or more the premium for Form 3.

The point is that with a Form 3 you can add floater coverage tailored to your needs, paying only for what you buy.

Special features: Instead of the usual $50 deductible, you can get deductibles of $100, $250, and $500. This lets you save up to 20% of your basic premium cost. You can also buy inflation protection. For an added outlay of about 5% of your regular premium, you can get a proviso for a periodic increase in your basic property damage coverage of 1% every three months.

Has Inflation Trimmed Your Coverage?

With real estate values sky high, you'd be smart to check on the amount of your homeowner's coverage.

Say that five years ago, you bought a house for $40,000. Today — assuming it's like most property in its class — it might be worth, say, $52,000. This means a 30% jump in value, or 6% a year. "For some areas this is quite conservative," says a Homeria, Inc., executive.

Suppose you have a total loss by fire. If you haven't increased your insurance, you're out of pocket at least $12,000 on the structure alone.

If you have a partial loss, with fire or storm damage of $10,000, say, you'll meet up with the 80% rule. Generally this means that to collect fully on your loss you must be carrying insurance up to 80% of the current market value of the house. If you are just a shade under 80%, the payoff may be somewhat under $10,000; it varies depending on the case.

But if your coverage is substantially under 80% you get a "depreciation recovery" in case of a partial loss. If you have a $10,000 fire, you might recover only about $5,000 or so.

A man in the 50% top tax bracket would still be out of pocket $2,500 despite a casualty loss tax deduction.

Don't underestimate the danger of finding yourself underinsured-despite a current softening of home prices in some areas. In executive neighborhoods, 5% to 6% annual increases in value have been par all over the country for the past five years. In the past three years, values in some posh suburban areas have inflated 8% to 10%.

In Connecticut towns such as New Canaan, Greenwich, and Westport, and in New Jersey towns such as Short Hills, Saddle River, and Ridgewood, the yearly price boost in some neighborhoods has run 12%. Lake Forest and Lake Bluff near Chicago have been up about 8%.

The cost of more insurance runs $2.50 to $5 per $1,000 a year.

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6 Pursuit Ringway
The government’s National Cancer Institute notes that there’s growing promise in the hunt for vaccines that would head off cancer.

As for the present best hope—treatment—results are looking better. Says ACS Vice-President Roald Grant: “A number of types of cancer—leukemia, Hodgkin’s disease, skin, prostate—are yielding more and more to modern therapy.”

Fifteen years ago 25% of cancer cases were “cured”—that is, patients lived five years with no recurrence. Now that rate is 33%.

But results in some areas are far better than that over-all average. Skin cancer is curable 92% of the time, and uterine cancer is thought to be 100% curable—if it’s detected early enough.

That’s a vitally important point—early detection. If a malignant growth is detected while it is still localized, the chances of survival increase greatly. With prostate cancer the survival rate is about 50% in the average case, but up to 71% with early detection. When lung cancer is found early, the survival rate is about 27%, but it’s only 7% if the disease has spread.

At least half of all cancers can be detected during a full yearly physical examination. Doctors know the danger signs, and new testing methods make detection easier. Rectal exam with a sigmoidoscope is one example. With the latest equipment, it can reveal 75% of all rectal-colic cancers if the exam is thorough.

Says Dr. Richard Winter, director of New York’s Executive Health Examiners: “If the sigmoidoscope is avoided because it’s uncomfortable, the patient could be avoiding a relatively simple test that might save his life.”

Among other cancer checks that should be made in an annual physical: a chest X-ray for lung cancer, and a digital examination of the prostate.

If you’re over 50 and haven’t had a gastrointestinal series of X-rays in several years, you should have it. The series will pick up an incipient carcinoma.

Cancerophobia—fear of the disease—keeps people away from early diagnosis. It’s a killer in its own right. Says Winter: “The man who misses a few exam appointments because he’s too busy often has some fear lurking in his mind.”

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Personal Business 7
Take Up Cycling To Spin off pounds

Want a change of pace this fall? Try riding a bicycle. With steady pedaling, you can keep as physically fit as a jogger and do it a lot less monotonously. You can keep in shape with three or four sessions a week, each half an hour to an hour. And note: Cycle makers are now showing lightweight models made to order for the middle-age physique.

How long does it take to get in shape? If you start sensibly (on flat runs) and keep at it regularly, says one specialist, “in less than six weeks, you can be going on 25-mile bike rides.” Added inducement: At 5 mph on a flat surface, you burn up an extra 4.5 calories a minute.

If you haven’t been in a bicycle lately, you may be surprised by the variety: sleekly designed unicycles, tandems, triplets, tricycles, mini-bikes, high-risers (for kids), and derailleurs (multi-speed bikes).

Your best bet, however, is to start off with a three-speed, lightweight (36 lb. to 46 lb.) touring bicycle. It will take you comfortably around town.

With thin (1½ in.) tires, these bikes are easier to handle than the heavier, coaster-brake bikes with balloon tires that you rode as a youngster. By changing the gear ratio, you can also take hills more easily or increase speed without over-exerting yourself.

Be sure to get a bike with hand-brakes—they act on both wheels and work immediately. The coaster brake (operated by pushing back on the pedals) is slower, stops only the back wheel, and may cause skidding.

Top names are Raleigh and Schwinn. Raleigh, an English bike, is slightly lighter and easier to maneuver. The Schwinn is sturdier and can take more abuse. Other makers: Columbia, Huffman, Murray Ohio, Stelber ($75 up).

For the man with long-distance riding in mind, the 5- to 10-speed derailleur bike is a must. Features include dropped handlebars and thinner seats, enabling you to ride in a less tiring, hunched-forward position ($80 to $120).

If you’re determined to get really serious, you may eventually want a custom-built bike. You can get them weighing as little as 22 lb. This lets you cover long distances—50 miles a day—with less effort ($175 up).

If you want a bike you can stash in a car, boat, or small plane, see the folding bikes. Some of the imported models are hinged to fold in half merely by pulling a lever. But their 16-in. or 20-in. wheels make them uncomfortable for riding any distance. Names to check: Graziella (which has larger folding bikes also), Amica, Atala, Falter, Schwinn, Stelber ($70 to $90).

Safety note: Though not all states require it, the Bicycle Institute of America recommends that if you plan to ride at night, get a headlight visible for 500 ft. and a rear reflector that can be seen for 300 ft.

Fun on Wheels — On and Off the Highway

Recreation vehicles—from four-wheel-drive Jeeps used by surf fishermen to rugged giant-size campers for hunters—will be more popular this fall than ever before.

Some will be bought at high prices: from $1,500 or so for a small all-terrain vehicle (ATV) to as much as $15,000 for a big camper outfit that has everything but an indoor duck blind.

More will be rented, at costs ranging from $100 to $350 a week, with bookings already under way.

The top brands in self-contained campers are slicker than ever. For instance, Volkswagen’s Campmobile—which sleeps two adults comfortably—comes with improved suspension to prevent side-sway in wind and give a steadier ride. Complete with pop-up top and tenting accessories, the tab is about $3,700. Anybody who owns a VW bus can get a camping-conversion kit for $600.

Lavish equipment. Ford and Dodge feature small panel trucks that convert easily into campers. The Dodge Tradesman Van, for example, is a little heavier and more rugged than the VW and has a new selling point: factory-installed power steering and power brakes. It costs from $4,000 to $7,000, depending on the options and camping equipment.

Ford’s Econoline camper and Chevy’s Beauville Sport Van—which is convertible into a camper—fall into the same price range.

With a Detroit camper, you can get power in the 200-hp range, as against about 60-hp with a VW Campmobile. The added power will carry you over rough roads and through mountains under full load. And you can use all sorts of heavier camping equipment. A buying tip:
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Shop all makes — and test drive before you buy.

Top of the line among self-contained campers is the Dodge Motor Home, with engine and chassis by Dodge and body by various U.S. makers. Units run 18 ft. to 28 ft. in length, sleep four to eight, and can be fitted with showers, TV, big stoves. You name it. Cost: $7,500 to $15,000 — or if you want to rent before you buy, $250 to $350 a week (plus mileage).

The pickup truck camper, with the unit fitted over truck and cab, is another rent-first item ($150 to $200 a week, plus 10¢ a mile). Pickup trucks by Chevrolet, Dodge, Ford, GMC, American Motors (Jeep), and International Harvester cost around $4,000. The camping unit, put together by regional makers, runs about $2,000 to $5,000.

A truck such as Jeep’s 350 V-8 can handle a camper up to 2,500 lb. A small pickup, such as Datsun’s 1600, will support far less weight.

Jeep-type cars are another idea for rough driving. American Motors’ four-wheel drive Jeepster Commando is small but carries four passengers and is sleeker than a standard Jeep. The Jeep Renegade I is a modern version of the famous World War II model (both $3,000). The super-rugged Land Rover by British Leyland is well worth a look, too — especially by fishermen who want to do stream-bed driving ($3,500).

Among ATVs, that crawl like bugs, the Attex St-300-D is typical. It’s smaller than a Jeep, has six wheels, simple controls (no steering wheel), seats three. Its 20-hp engine takes it 35 mph on land and 4 mph on water — $1,600 range (ATV Mfg. Co., Pittsburgh).

The breed is multiplying. Now in pre-production in the $4,000 range is the XJ-002 Jeep ATV (Jeep engine). It has 160-hp, with possible speed over 75 mph. (Bolide Motor Co., Huntington, N.Y.).

• The value of your property will be increased about 50% of cost.
• Cost of equipment can go sky-high. A pool table, for example, sells for about $300 to as high as $2,500. Figure roughly 40% to 50% more than basic renovation cost if you get everything from the latest indoor sports gear to stereo and color TV.

Finding a good man to do the work may take doing. Some tips: Get recommendations from a local contractor or architect; get three bids (to weed out the man who’s too high or cut-rate), and be sure to obtain a contract that itemizes the work and gives a completion date. If it’s an elaborate job that runs to $5,000 or more, have a lawyer check the paperwork.

Before you re-do a basement, garage loft, or any other space, be sure you have a full 8 ft. ceiling; one of 7 ft. isn’t good enough. Latest trend is the luminous suspended ceiling with lighting built-in, so figure this in your calculation.

**REAL ESTATE BEAT**

**Putting a Roof Over Relaxation**

If you want to move your relaxation indoors for the winter, this is the time to start making plans for renovation of your game room.

Converting basement space is the usual way, of course. And if you decide not to tackle the job yourself, you’ll have to allow at least two weeks for a remodeler to get started. Then, allowing for delays, he’ll take about three to four weeks for the average job.

Here are some starting points:

- Cost of a game room in a $40,000 to $50,000 house can easily run from $2,500 to $5,000 or more. (Do-it-yourself will run at least 50% less.)

Paneling can be high-quality — and high priced. Teak, rosewood, or walnut veneer sells for up to $100 per 4-ft. by 8-ft. panel. But serviceable vinyl plywood is just $7 to $10. In carpeting for basements, ask about hydrofoam, a moisture-absorbent covering.

In laying out your plans, remember that indoor sports gear can take up plenty of space. A professional-
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size pool table is 4½ ft. by 9 ft., and with a heavy slate bed weighs 1,000 lb. You'll need about 14 ft. by 18 ft. for a comfortable game. If you're a bit cramped for space, a family-size pool table is 4 ft. by 8 ft. ($300 to $800). takes less space, because shorter cues can be used.

Table tennis outfits run about $75-up for top quality, with sturdy kids' models in the $50 range. Besides wood, see a metal-framed composition table with easy-to-fold top (Abercrombie & Fitch, $175).

A regulation size table tennis outfit is 5 ft. by 9 ft. But you should have 8 ft. at each end and 4 ft. on each side for a good fast game. Space saver: Add a table tennis top to your pool table: you can have a carpenter rig it; or check your sporting goods store ($40).

The latest in high quality poker tables is octagonal, seats eight, has a padded surface, hidden trays for drinks and chips (Brunswick $150-up).

A movable bar with built-in refrigerator and the usual gadgetry runs $200 to $400. A well-designed, custom-built, 8 ft. bar with sink and refrigeration costs $1,500.

The home gym has become a big game room item, especially for the middle-aged. Here space is no problem. All you need is a cubicile (maybe 8 ft. by 8 ft.) to mount a horizontal bar, and a vertical one for isometrics.

After that come higher-ticket items. Among the possibilities: an indoor jogging machine with adjustable treadmill, meter, and timer ($135 up); rowing machine, manual ($90) or hydraulic ($260); bicycle machine with timer, speed switch ($150 to $300). Check Sears, Montgomery Ward, Abercrombie.

For less strenuous relaxation, you can fill a corner with a base-
ment piano and drum combination. A 63 key cocktail piano costs $400 to $600, and a complete drum outfit $400 to $600. A dealer trade-in basement piano (a full 88), guaranteed tunable, costs $200 to $300.

Leasing instead of buying

Leasing an apartment—or a house, if you can find one—may be a way to sit out the high-priced housing market for a year or two.

It depends on your personal circumstances and where you live, but you could be better off leasing—even if you had to break the lease later on. The penalty is usually one to three months’ rent, maybe a bargain in view of current mortgage rates.

If you have been a houseowner for years, you may be surprised by all the details a lease can cover. For instance, an apartment lease should spell out the question of redecoration—even down to the number of coats of paint. If you’ll be putting in a partition or installing, say a special lighting fixture, it should give you permission and tell who will own any new fixtures when the term is up.

Look for special house-rules: things like use of the garage, late-hour entertaining, and so on. Trouble is that such rules—if reasonable—can become grounds for eviction if breached. If it’s a new building or an old one being renovated, get a safeguard that won’t let you be without utilities (heat, light, water) for longer than is absolutely necessary. It can be an important protection.

Generally, you can sublet unless a clause prohibits it. Try to get a firm statement giving you the right. You would continue to be liable for the rent, but you would have an out if you found a house to buy.

In a suburban town, go directly to the building manager; a real estate agent isn’t apt to list many apartments. And sometimes if a broker does handle a rental, you (as tenant) may have to pay his commission. If leasing a house, try hard to swing an option to buy.
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HEALTHY, WEALTHY AND WISE

Taxes: hitting young, and old

If you set up a custodian account for a child—under a uniform state law covering such accounts for minors—you would be smart to appoint someone else in the family to serve as custodian. Internal Revenue claims that if you serve as custodian, the securities in the account become part of your taxable estate if you die before the child reaches age 21 when the account automatically terminates. Now, the U.S. Tax Court has cleared away some doubt on the point—it backs the IRS.

For most people, nursing homes mean hard decisions and high costs. One confusion: whether the cost is tax deductible. A new Tax Court case sounds a warning: there must be medical care before you can take a deduction. But how much care? The nub of this is that if a member of your family goes into a home because of poor health, and a principal reason for going is the availability of medical care—then the whole cost, including meals and lodging is deductible. But it's a case of your mustering evidence of medical need—and care.

New slant on college admissions

There's more than money to consider in helping a youngster with future college plans. Take a sharp look at new changes in the philosophy behind admissions policies. Instead of just good grades and top-ranking high schools, admissions officers are paying a lot more attention to underprivileged kids from cities and small towns. A check of campus officials this month shows that the trend will continue....There will also be less emphasis on college boards; whereas 500-plus SAT scores have been a must at top colleges (even at second-line colleges), the policy will be to look more closely at students in the 400-plus range. Drive, creative ability in almost any field, and demonstrated interest in public affairs, especially, will be weighed most carefully. Net effect: greater competition for a freshman place. Even earlier planning for admission to a good college is suggested.

Early planning aid: Try Cass and Birnbaum's Comparative Guide to American Colleges (1970); it's outstanding (Harper & Row).  

Manners & modes

Unofficial but highly reliable medical research shows that Gatorade is good for a hangover. The quick-energy drink for football players (cyclamates removed) enters the bloodstream 12 times faster than water and thus quickly replaces fluids lost. Dose: two 10 oz. cans before bed, and one upon awakening. Users say it works wonders.

Potpourri

Dining in Manhattan: With the first wisp of fall chill in the air, try lobster stew as made at the Plaza hotel's new Oyster Bar. Into 8 oz. half milk-half cream place 4 oz. pre-cooked lobster meat and 1 thick pat of butter; simmer 5 min. Remove half of the lobster, and put 1 slice toast in the stew—then put the meat back atop the toast and sprinkle with paprika....The Complete Outdoor Cookbook, by Dan and Inez Morris, is fine for hunters and anglers; camp cooking stuff is first rate (Hawthorne, $7.95).

If you'll be flying to a remote tropical haven this winter, it might be smart to contact the International Assn. for Medical Assistance to Travellers. Get a free medical directory—a usable listing of local physicians in distant places, including tropical cities from Kingston to Honolulu to Nairobi. You're assured attention by qualified English-speaking MDS (IAMAT, 745 5th Ave., New York).
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You can be sure...if it’s Westinghouse
Common Market contract plans come under fire

West Germany's Central Association for the Electrotechnical Industries is up in arms about recent Common Market proposals for concerted action among member countries on dishing out public contracts for large international projects. Under these proposals, the governments would get together to decide how such contracts should be let and to which countries—and companies—they should go. Affected are the communications, aerospace, data processing, and electrotechnical and nuclear power engineering sectors. Such government action, the association says, is a breach of open-market principles because it stifles free competition. It also means that politics would influence contract letting and could hurt Common Market technological performance and capabilities.

Deeply concerned because the German electrical and electronics industries are heavily dependent on public contracts at home, the association is calling for elimination of all obstacles to competitive bidding. The association also is dead set against Common Market plans to foster mergers in various industries by contract letting practices, fearing ultimate political control of corporate decisions.

Electronics gain in Japanese cars...

Japan's Toyota soon may be the world's most electronics-filled automobile. By the end of this year, the cars can be bought with electronic skid control and electronic fuel injection systems as options. The skid control was developed jointly by Toyota, its affiliate Nippon Denso Co., which will make the computer controlled system, and Asin Seiki Co., a transmission maker. The fuel injection system, while developed entirely by Toyota and Denso, will be built under licenses to use Bendix and Bosch patents.

...as Toyota talks with TI on patents

Meanwhile, Toyota is negotiating an agreement with Texas Instruments that will probably include system patents, and also joint development of new systems. Neither Toyota nor TI will reveal details yet. But Toyota is interested in new systems—which might include air-bag crash control, radar or other detectors of nearby cars, and exhaust emission controls. Agreement might also include production of equipment for Toyota, although Toyota would probably prefer to have Denso handle the production. Denso already has concluded license arrangements with TI and Fairchild in semiconductors [Electronics, May 26, 1969, p. 182]. Although Denso is fabricating some small ICs on a trial basis, it is purchasing ICs for its automatic transmission control system [Electronics, Feb. 16, p. 69] and upcoming antiskid and fuel injection systems from regular semiconductor manufacturers, including Toshiba.

Automatic train control nears trial in Britain

British Rail, the state railroad monopoly, looks certain to start operational trials of its automatic train control system within a year [Electronics, April 15, 1968, p. 275]. The rail authority is giving the system a tryout to achieve higher traffic densities by improving on-time performance and cutting distance between trains while maintaining proper safety margins. In the system as presently visualized, the driver is told the maximum permissible instantaneous speed that cannot safely be exceeded. The brakes are applied automatically above that speed. The limit is
International Newsletter

calculated continuously by a train-borne computer, which integrates permanent data, such as train weight and train length, with local data, including upcoming signals, gradient of inclines, hazards, and other changing conditions.

Local data is sent to the trains by twin shielded cables laid 12 inches apart between the rails. Each has a low-frequency carrier on to which the data is frequency modulated. The cables cross over about every 100 yards. The shielding is broken at each crossover and also shorted by an interconnect between crossovers to minimize the effects of weather on impedance and attenuation. British Rail hopes that this system, plus further developments such as speed links and fixed computers at busy junctions, will increase traffic capacity by up to 20%, regardless of weather conditions.

Siemens unveils its biggest computer

Siemens AG, which saw its computer sales surge ahead at better than 35% in the last year to $187 million, is readying a new weapon for its battle with IBM, which still holds 63% of the German market. The company's newest computer, the model 60 of its 4004 series, will have a cycle time of 765 nanoseconds for four bytes and a memory expandable to more than 1 million bytes. More significant, claim company spokesmen, is a price/performance ratio that tops IBM's 360/50 and even the recently introduced 370/155. When it's delivered next year the new machine, aimed at real-time and time-sharing jobs, will start renting at less than $13,500 a month.

Government, coop launch tv cassette company in Sweden

The Swedish government, through a joint agreement with the Consumer Cooperative Federation, is going into the television cassette playback business. "We view this field as being of vital importance to society as a whole, and we want to be in it to prevent it being monopolized by advertising or commercial interests," says Karl-Axel Linderoth, an official of the cooperative.

It's uncertain just what the new company will do—simply handle programming and tape distribution or get into the hardware side, perhaps as a sales agent for foreign or domestic makers. However, it is not expected to start manufacturing. The only other firm in this field, Bonnier Audio-Visuals, owned by a large publishing house, has been criticized for having something of a monopoly. A firm spokesman says it welcomes the state-coop entry. But Bonnier has a big jump on the new competitor. It will have a line of recording and replay gear on the market by spring.

Viatron lines up overseas makers

Troubled Viatron Computer Systems, which only recently changed its top management [Electronics, Aug. 3, p. 93] now appears about to turn the corner. And some of the help is coming from abroad. The Japanese trading company Nichimen Co. has agreed to form Viatron Japan Ltd. Nichimen, which will own 51% of the venture, will exhibit the System 21 at the Osaka Trade Fair this week. What's more, Viatron appears near a similar agreement with a large British firm. In both Japan and England, the local firm would hire Viatron's present dealers and salesmen, making and selling System 21 data processing gear on a royalty basis. Perhaps more significantly Viatron has won the support of two large stateside banks: loans exceeding $1 million and financial counseling.
Ceramic filters operate at 30-200 MHz

Vhf capabilities put filters in good position as video i-f amplifiers and CATV channel filters

A ceramic filter element with a strong physical resemblance to the germanium point-contact diodes that started the solid state revolution for aperiodic circuits may do the same for periodic circuits. The resemblance is especially close to the end of the production process, where the elements are batch processed in a wafer of about the same thickness as those used in semiconductor diodes. Scribing separates the elements, which are mounted in all-glass, hermetically sealed package used for diodes.

The ceramic elements—called Piezonators—were developed at the Wireless Research Laboratory of the Matsushita Electric Industrial Co. They operate in the 30- to 200-megahertz range of the vhf band, which makes them ideal for use in the video i-f amplifiers of TV receivers and also as CATV channel filters. Matsushita expects to tool up for filter production, design a color TV set using them, and put the set on the market within a year.

The new material is a lead titanate with a small amount of dopants; the composition of which the firm considers proprietary. With a dielectric constant of about 200, an order of magnitude lower than that of previous materials, the material operates at higher frequencies by reducing stray capacitance and maintaining a sufficiently high impedance for matching to the circuit. The new material has a very high electromechanical coupling coefficient, which gives the broadband response, because of directional characteristics that can be controlled to give a high coefficient for desired modes and a low one for undesired modes. Its physical structure minimizes spurious response due to mechanical coupling.

Piezonator material starts with a thickness of 500 microns to counteract warping during firing. After firing, wafers are ground and lapped to about 120 microns for use in the 50-MHz region. A final reduction in thickness to within 0.5 micron of the target puts filters within ±100 kilohertz of design value. Final frequency is adjusted by controlling the thickness of electrode material evaporated on the wafer’s faces.

Polarized wafers are scribed and diced to produce filter element chips. Because of the resulting rough edges the chips are asymmetrical on a microscopic scale, which further reduces spurious response by scattering and dissipating unwanted modes. Chips are mounted in glass packages between a nailhead and a pointed spring contact that applies a predetermined pressure on the middle of the chip. This point-contact mounting contributes to low vibration loss. Insertion loss is only about 5 or 6 decibels.

These filter elements have arrived on the scene at the right time, because it is just becoming possible to build monolithic ICs with the required gain at video i-f amplifier frequencies. But the value of an IC amplifier disappears if the TV set still uses conventional filter circuits.

For example, current Matsushita color sets require a total of seven interstage transformers and wavetrap, a trimmer capacitor, three fixed capacitors, and two trimmer resistors in the frequency selective circuits of the video i-f amplifier. Two large shielded enclosures must be used to prevent inductive component coupling from degrading circuit performance. What’s more, the components need 10 adjustments after assembly to assure desired frequency characteristics.

All these components can be replaced by two Jaumann-type ceramic filters [Electronics, Nov. 14,
Electronics international

1966, p. 160], one at the input to the i-f amplifier and the other between the i-f amplifier and video detector. The input filter—the more complex of the two—contains a ferrite-core transformer, a fixed capacitor for tuning, two ceramic filter elements, and a fixed capacitor for balance. The design uses the antiresonant frequency of one ceramic filter element to give about 26 dB attenuation of the sound i-f frequency of the channel being received. The antiresonant frequency of the other ceramic element is used to reject the adjacent channel. This filter replaces both the traps and bandpass filter used in a conventional color TV. Initial versions of the filter require one tuning adjustment, but company engineers say coils requiring no adjustment can be produced.

The output filter—which needs reject only what remains of received channels' sound i-f frequency—can be simplified and has only one ceramic filter element and a balancing capacitor.

The company's 110-MHz CATV channel filter is similar to the TV input filter but uses a trimmer capacitor rather than a fixed capacitor for balance. The trimmer is needed to obtain balance at the higher frequency, but it may be possible to use a fixed capacitor in the future. And for CATV two Jaumann filters in cascade can provide a channel filter with a wide bandpass and good selectivity.

A ceramic filter has also been developed at Matsushita for automatic fine-tuning circuits. Two transformers and three capacitors, which require two adjustments, can be replaced by one ceramic filter that requires no adjustments.

Great Britain

Ion implant machine aims at assembly-line job

A promising technology, ion implantation is still waiting for improvements in production techniques to get the throughput to up commercially practicable levels. In Britain, scientists at the Atomic Energy Research Establishment at Harwell have designed and built an implanter they believe is suitable for industrial batch processing. It's made up of a high-current isotope separator, which generates and shapes the ion beam, and a specially designed target chamber.

The chamber is the key to the implanter's efficiency. It can pack in what the designers say will be enough semiconductor slices to make the installation economically viable. They claim that in an hour it will turn out 30 2-inch or 150 1-inch slices doped to $5 \times 10^{15}$ ions per square centimeter and uniform to $\pm 1\%$.

The ion separator portion is already being made by Lintott Engineering Ltd. of Horsham, Sussex, and sold on its own in the U.S. for around $135,000. Now Lintott plans to make the target chamber and early next year will offer a complete implant installation at somewhere around $180,000 in the U.S. Because the cost is high, potential customers will be able to try out their slices in the Harwell unit.

In France, atomic energy research work also has spawned an ion implantation machine aimed at production lines [Electronics, Feb. 2, p. 63]. It made its debut with a $100,000 price, delivered in Paris.

The ion separator of the British unit consists of an ion source with a maximum energy potential of 40,000 electron volts and a magnet for shaping and focusing the beam. The beam enters the target chamber through an accelerating lens that can add a further 150,000 eV to beam energy. Maximum beam current obtainable is about 1 milliampere with most elements, including all the usual donors and acceptors. Harry Freeman, who designed the machine, says that in sustained operation 150,000 eV total potential is most likely the practical maximum, with a beam current at the slice of 0.5 mA. "This should be more than enough to make production implantation commercially viable" Freeman claims.

Once formed by the separator magnet, the ion-beam remains stationary, and all-scaning motions are obtained by moving the slices. Freeman says he did this to avoid the problems of deflecting intense ion beams, and to reduce ion-beam heating of the silicon. The beam is normally 1 inch high and 0.5 inch wide.

Inside the target chamber there are up to 20 rectangular frames measuring 5.5 by 4.5 inches and holding slices in horizontal rows. One-inch slices fit into a frame in four rows of five, 2-inch slices
in two rows of two wafers each. To start a cycle, the operator loads the frames onto a rack in the top section of the chamber, closes it, and pumps it down to about $10^{-6}$ torr. From then on all motion of the frames is automatic and governed by preprogrammed timing. The frames are lifted off the rack one at a time and fixed in a carrier that moves the frame from side to side and downward in steps, exposing each row of slices. When the carrier reaches its bottom position, the frame is lifted off and stacked in the bottom of the chamber while the carrier rises to pick up another frame.

Implantation depth is determined by voltage, and the total dosage by a combination of beam current and exposure time. To keep operation times down, current should be as high as possible. But high currents degrade ion beam uniformity, causing a nonuniform dosage across the silicon. Freeman compensates for this by having each horizontal row move from side to side a number of times, each time dropping a fraction of an inch.

The uniformity desired determines the vertical step intervals and the number of horizontal passes necessary, and hence the minimum cycle time. Then beam current is adjusted to provide the required doping level. Uniformity of 2% needs a total hatch cycle time, including loading and normal dead time, of 1.75 hours; 1% uniformity needs 2.75 hours; and 0.5% uniformity, if it were ever required, would need 4.5 hours.

Electronics International

announced almost two years ago [Electronics, Dec. 23, 1968, p. 5E].

The companies also have taken the wraps off a continuous-wave glass laser oscillator that uses a similar fiber with about 4% neodymium doping. Output pulses of the glass fiber laser are shorter than those of other lasers, which should greatly increase system information transmission capacity. What's more, an amplifier for repeating the signal at the transmitted frequency is also being developed. It consists basically of the laser structure with mirrors replaced by lenses to inject energy into the amplifier and extract the signal at the output.

The fibers' low-loss characteristics stem from a continuous grading of the refraction index across the fiber radius. That gradation helps guide the light beam by reducing the amount of light lost through the fibers' sides. The new fibers exhibit losses of about 100 to 300 decibels per kilometer at the 0.63-micron output of a helium-neon laser, compared with 600 dB/km for earlier fibers.

Losses are quoted for helium-neon laser light, which often is used as a standard by laser workers. However, the glass fiber transmission line operates with much lower losses at long infrared wavelengths such as those generated by glass fiber and semiconductor lasers. The glass fiber laser operates at 1.06 microns, and semiconductor units operate in the region somewhat shorter than 0.9 micron, with exact wavelength depending on temperature.

At these wavelengths, loss is only in the order of 50 to 150 dB/km. Nominal gain for a repeater would be in vicinity of 30 dB, so the best fibers now can be used with repeaters more than 0.5 km apart.

There is a difficulty in using the new amplifier with semiconductor lasers, in which frequency is a sensitive function of temperature. Without temperature control, these lasers cannot be used as repeaters in a system with straight-through amplification at carrier frequency. Otherwise, semiconductor lasers require repeaters that detect the incoming signal, regenerate it at baseband, and then use it to modulate another laser for further transmission.

The companies have lengthened individual fibers from 1 meter to 20 meters. Development is under way on fabricating longer fibers, with the 250-meter length that is standard for communications cables as the goal. Experimental methods for joining fibers include simple butt joints, immersion of two ends in liquid having a similar index of refraction, coating the two ends with nonreflective coating, and insertion of a lens between the two ends.

The laser's configuration is similar to that used for other glass

Japan

Glass laser fibers help transmit and amplify beams

Laser communication systems designers have another material to work with: glass fibers. Nippon Electric Co. and Nippon Sheet Glass Co. have jointly produced improved low-loss optical fibers for use as transmission lines. Called Selfoc, these fibers have much lower losses than similar materials

Guidance. Low-loss glass fibers, which can be used in laser oscillator and amplifier, have a graded index of refraction to guide light beam.
units. The neodymium-doped fiber and a krypton arc lamp pump source are located at the two foci of a surrounding elliptical mirror. The fiber has a diameter of 1.5 microns or less and a length of less than 40 centimeters—typically about 10 cm. Typical continuous power output is 3.5 watts at 1.06 microns.

Now an avalanche and trap recovery diode

Operating in a new avalanche mode, a gallium arsenide diode developed in Japan promises to find uses in laser ranging systems and as a more versatile replacement for mercury pulse generators. An offshoot of Gunn device work at Matsushita Research Institute, Tokyo, the new diode is similar to the Impatt diode, but can handle higher power. Researchers report output voltages of several tens of volts and currents of several hundred amperes. Typical rise times are on the order of 300 picoseconds.

Even though maximum repetition rate is below 1 megahertz, the usefulness of the device depends more on its capability, when biased just below the negative resistance point, of being accurately synchronized by applied pulses. The diode’s inability to sync with pulses having higher repetition rate can be turned into an advantage, though, because the device can be used as a frequency divider. Although frequency characteristics are inferior to those of tunnel diodes, the new device’s much larger current and voltage outputs may find it applications where the small output of the tunnel diode is unsatisfactory.

Under development for three years, the new device contains a region with higher doping than used in Gunn devices. Iron diffused about 10 microns into the 100-micron-thick n-type GaAs chip compensates somewhat for the impurities inside the chip. Even where compensation is maximal, though, material retains its n-type characteristics and becomes a type often called nu-type. Carrier density, which is about 10^16 carriers per cubic centimeter within the chip, becomes about 10^14 carrier per cm^2 at the surface of the chip. A gold-germanium n⁺ contact is made to the nu layer, and a suitable contact is made to the other side of the wafer, to form a nu-n diode.

Almost all the voltage applied across the terminals of the device appears across the nu layer. If the voltage exceeds a threshold value, the device is biased into a negative resistance region. If the current then exceeds a threshold value, a pulse is generated.

Each pulse is followed by a period of steady moderate current flow through the device, then another period during which the current through the device decreases. Decreasing current increases the voltage until biasing is reestablished and another pulse can be generated. Pulse repetition rate can be adjusted by modulating the current through the diode.

The mechanism of the device operation has not been thoroughly explained, but it is thought to be dependent on iron doping—which acts as a deep-level impurity. Initial switching of the device appears to be by avalanching. This fills the recombination centers with holes, and the device terminal voltage falls to a low level because the semiconductor material behaves as if there are no deep impurity levels. As terminal current continues to flow into the device, the captured holes are released. When the charge flowing into the device about equals the charge captured in the deep impurity levels, the terminal voltage rises and the cycle repeats. Experimentation shows that charge storage is almost identical with the number of iron atoms.

Switzerland

Rare earths attract magnet researchers

At the Baden-based laboratories of Brown, Boveri and Cie, permanent magnet researchers are taking a hard look at compounds made of rare-earth metals and cobalt. Judging from the results they have obtained so far, it looks as if conventional materials—alnico and barium ferrites, for example—soon may be replaced in some specialized permanent magnet applications. Among the materials the Baden scientists have investigated so far, compounds of cobalt with such rare earths as samarium, yttrium and praseodymium look like the most promising.

For users of permanent magnets, the two most important properties of such devices are their maximum energy product and their coercive force. The two largely determine how small and strong a magnet can be made for a given power source and how large a load can be handled by a magnetic circuit—for example, in motors.

The coercive force of a magnet has an upper theoretical limit at the material’s so-called anisotropic field. For rare-earth cobalt compounds this limit is very high—between 130,000 oersteds for yttrium-cobalt and 290,000 Oe for samarium-cobalt. Since about 15% of the theoretical value can be achieved in practice, coercive forces of up to 30,000 are obtainable. So far, however, BBC researchers have reached such values only with SmCo₅, fine-particle magnets. For comparison, alnico has a coercive force of around 2,000, say company experts.

The high coercivity should appeal to designers of small motors, the permanent magnets in which are often destroyed by demagnetization when overloading occurs. For the magnetic material, on the other hand, a higher coercive force will make for a sturdier device.

As for the maximum energy product, values of up to 20 million gauss-oersteds have been obtained for SmCo₅ permanent magnets—double the value that’s possible with high-quality alnico alloys or with very expensive platinum-cobalt magnets. Such high energy product will make the new material suitable for magnets in electronic watches, for example.
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<table>
<thead>
<tr>
<th>PACKAGE E</th>
<th>MODEL</th>
<th>ADJ. VOLT.</th>
<th>MAX. AMPs.</th>
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Package G models are only available without meters. For chassis slides, add suffix "-CS" to the model number and $40 to the price.

NOTES: (1) Current rating is from zero to 1 max. Current rating applies over entire output voltage range. Current rating applies for input voltage 105-132 vac. 57-63 Hz.
(2) Prices are USA list prices only. FOB Melville, N.Y.; North Hollywood, Calif.; Montreal, Canada. All prices and specifications are subject to change without notice.

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