

APRIL 1, 1976

PREVIEW OF THE ELECTRONIC COMPONENTS CONFERENCE/96

Digital electronic TV tuner uses nonvolatile memory/86

What to look for in dc-to-dc converter power supplies/91

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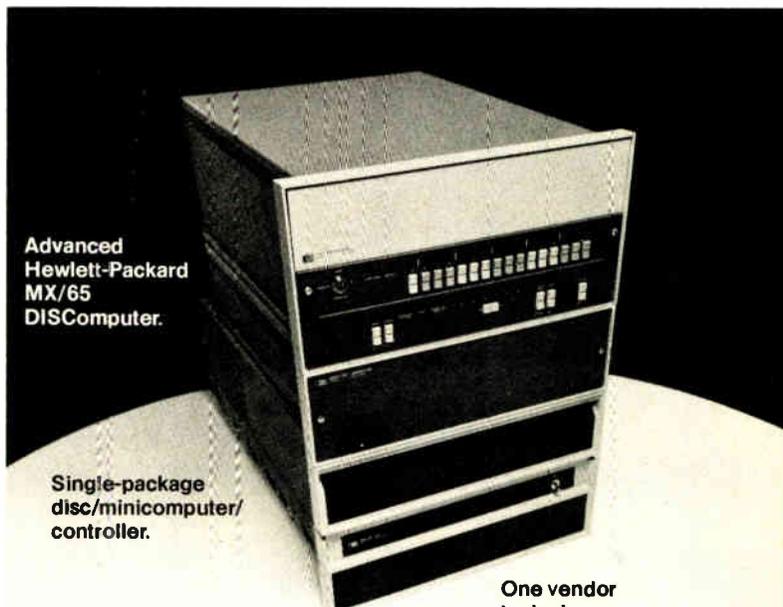


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Cover: MOS repeats its surprise performance, 73

Yet again, metal-oxide-semiconductor technology has done the unexpected—more than doubled in speed and more than quintupled in density, thanks to such process innovations as double silicon layers, V notches, double diffusion, and charge coupling.

Cover is by Art Director Fred Sklenar.

Europe is buying microprocessors, 62

The West European market in microprocessors is expected to hit \$150 million by 1980, up from \$10 million last year. Scrambling to pin down shares in that market are leading semiconductor houses in Germany, France, Holland, Italy, and the UK, as well as U.S. manufacturers operating usually through second sources.

Digital tuner remembers to turn on the TV, 86

A metal-oxide-semiconductor large-scale-integrated tuner, which handles 12 vhf and 70 uhf television channels with equal ease, is being equipped with a nonvolatile memory that can store an entire week's viewing times.

Films rate highest at components conference, 96

Speakers at the 26th Electronic Components Conference, scheduled to open in San Francisco on April 26, will be testifying to the versatility of thick- and thin-film technology. Optical devices, semiconductors, and organic materials will also get special treatment.

And in the next issue . . .

Special issue on microprocessors—who makes them, where to use them, and how to design with them, program, and test them.

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Robins are not the only harbingers of spring. In electronics, the beginning of spring coincides with the beginning of preparations for attending two important components shows.

So in this issue, our first of spring, we have put together a wide-ranging package of stories and features about the trends in components as the shows approach. On page 96, you'll find a five-page preview of what you can expect at the 26th Electronic Components Conference, which opens in San Francisco on April 26. The report, by our components editor, Lucinda Mattera, not only summarizes the highlights of the more than 70 technical papers being presented there, but fills in the background against which the papers must be viewed.

Then, on page 109, we continue the preview by presenting some of the more significant new products that will be shown at the companion Design Engineers' Electronic Components show. The new arrivals range from standardized components for panel interfaces to a plastic connector for fiber-optic bundles.

The opening of the Paris Components Show, which runs from April 5 to 10, is a good time to round up the components trend in Europe. After a rather dismal time there last year, components makers and marketers are feeling a lot more chirpy, according to the reports gathered by *Electronics'* European editors, who were assisted by Michael Johnson and Andrew Heath of McGraw-Hill World News. You'll find their story starting on page 59. Then, too, because microprocessors are looming so large in the designers' tool box,

we have a report on how the battle for Europe's microprocessor market is shaping up (see p. 62).

Les Penner, the author of the article on digital electronic tuning for television (p. 86), is something of a pioneer in metal-oxide-semiconductor technology, having joined General Instrument's MOS operations 10 years ago. Yet he was a stranger to the TV receiver when he headed the microelectronics design group that participated in GI's multi-division development of the digital tuner.

"Working with the Sickles group [GI's tuner division] was a revelation," Penner says. "I tended to think of consumer electronics as 'cheap and dirty,' a lesser quality product than other electronics instruments. In fact, TV-tuner reliability was harder to meet than some military products because of the TV-chassis environment. Picture-tube arcing is unique to TV sets, and making semiconductors survive arcing is a formidable task."

Although he is not certain where the next use of digital technology will be in the TV receiver, Penner is already looking at other applications of this type of tuner to automobile radios and citizens' band instruments.

"Being in MOS for 10 years is about as long as anybody in the field, but I don't feel like a pioneer," Penner observes. "I guess I've been too busy."



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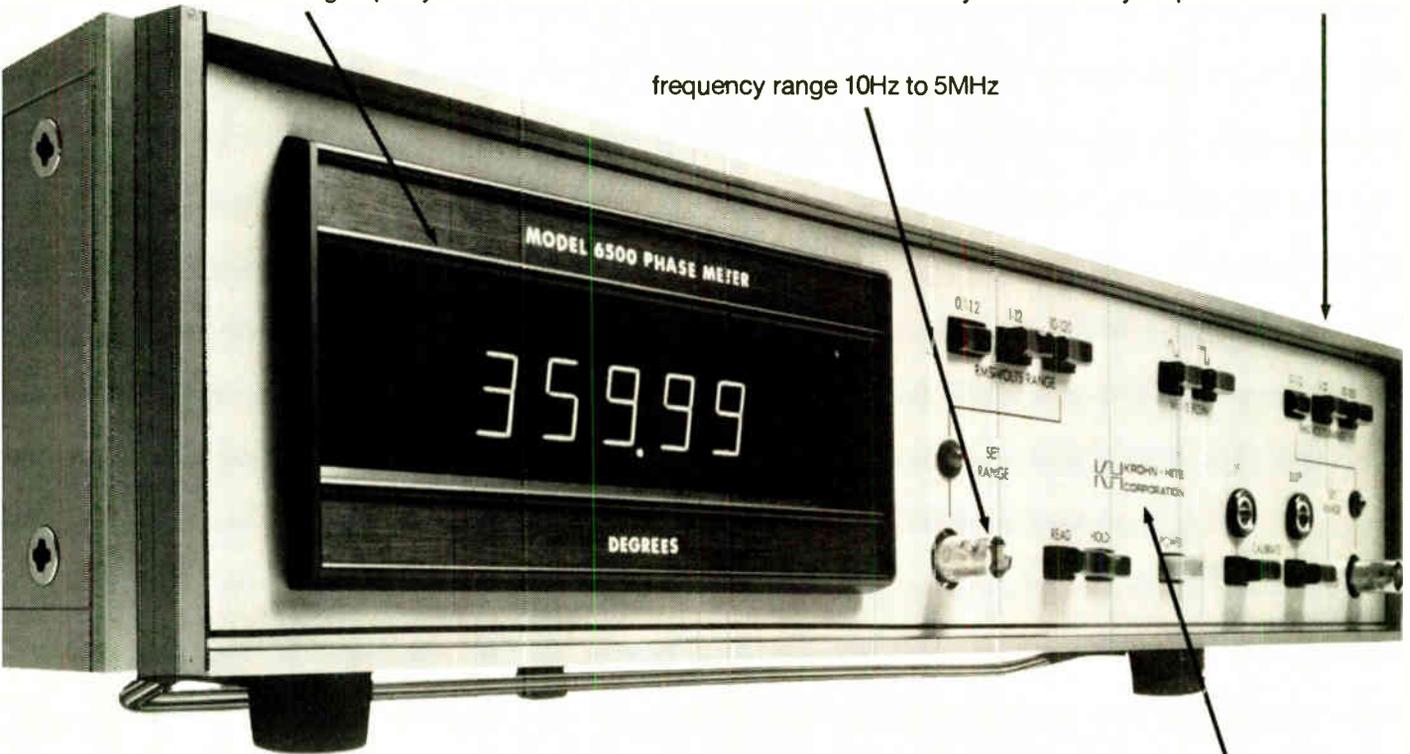
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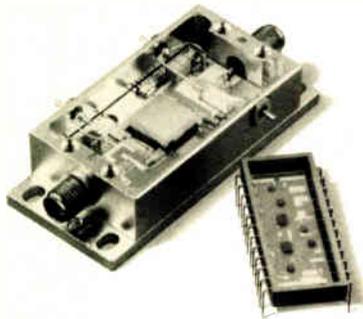
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Readers' comments

Switch that protection

To the Editor: In my article, "Getting the most out of C-MOS devices for analog jobs" [Dec. 25, p. 69], the caption for Fig. 8 and the text describing the figure imply that, in the dielectric-isolated switch shown, the on resistance is purposely modulated with an analog signal level, affording some kind of protection. This is erroneous.

Actually, the purpose of the circuitry is to minimize the leakage current in the off state and to minimize the modulation of the on resistance in the on state when the input signals are within their normal analog signal range.

The dielectric-isolated switch is much less likely to fail catastrophically from brief input-voltage spikes than other complementary metal-oxide-semiconductor devices, since it will not latch up and will not go into avalanche.

But it could fail from prolonged high currents flowing through forward-biased junctions to the power lines under low-impedance over-voltage conditions.

Nor does capacitor N_2 in the circuit in Fig. 9 give protection. Its purpose, as with N_3 in Fig. 8, is to minimize modulation of on resistance. The protection is accomplished by resistor R_1 and transistors Q_5 and Q_6 .

Ernie Thibodeaux
Harris Semiconductor
Melbourne, Fla.

No restrictions on chip sale

To the Editor: In the article "Teletext decoder combines IC types" [International edition, Feb. 19, p. 10E], you gave the impression that Texas Instruments Ltd. would not sell the Schottky read-only memory and the serial-to-parallel converter chips to Teletext competitors.

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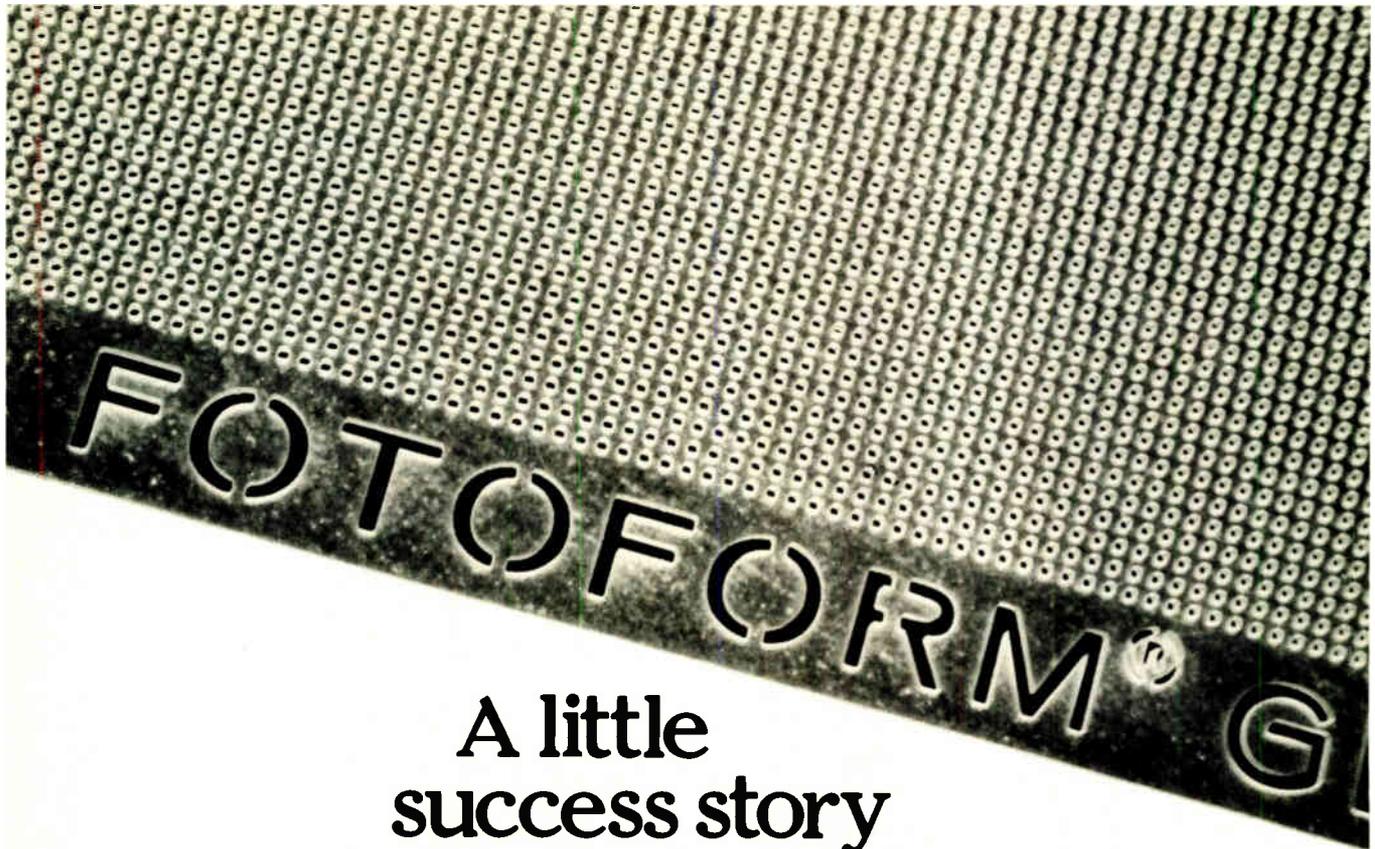
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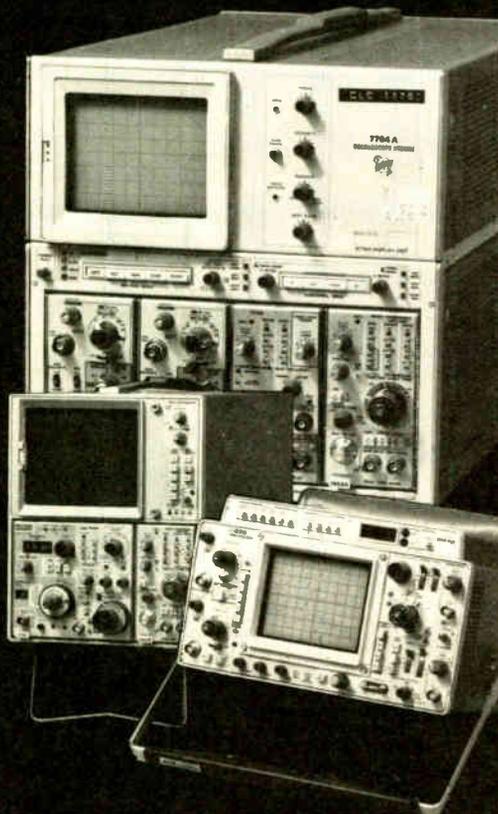
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News update

■ The analog technique used to compress or expand recorded speech in audio and audiovisual systems developed by Cambridge Research and Development Group, Westport, Conn., is catching on in a big way. Cambridge calls its method variable speech control [Aug. 22, 1974, p. 87]. It's a technique to get rid of both the Donald Duck effect when tapes are speeded up and the "growl" when the tape is slowed. Kenneth Sherman, a general partner in the company, emphasizes that Cambridge isn't a production house. "Our bag is licensing," he says, and the list of licensees now includes La Belle Industries, Oconomowac, Wis., Sony Corp., and National of Japan. Sanyo has also indicated it will become a licensee, according to Sherman; General Electric Co. has announced that it will use VSC in a tape recorder this year, he says, although GE isn't actually under license yet. National, says Sherman, has already introduced a system in Japan that sells for \$493, and La Belle Industries has prototypes of a single-cartridge VSC system that incorporates both 8-track sound and 16-millimeter film.

■ A television tuner incorporating a nonvolatile metal-nitride-oxide-semiconductor memory could be in American TV sets by midyear. The Omega tuner [May 15, 1975, p. 40], made by the F.W. Sickles division of General Instrument Corp., Chicopee, Mass., is in pilot production, according to Eli Cohen, vice president and general manager. "The system and technology are performing exactly as had been projected [see p. 86], and we're definitely in people's programs," Cohen says, although he won't say which set makers have designed Omega in.

Cohen doesn't expect production volume to be great this year, but is looking forward to brisk sales in the second and third production years, as set makers become more familiar with the unit. GI is making the MNOS memory, and has completed tooling for the LSI device, Cohen adds.

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Circle 151 on reader service card

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It's a pin-for-pin replacement for the old 22-pin boss — TMS-4060. But young SuperRAM cuts average power dissipation by 33% to 375 mW maximum when operating under worst-case conditions at 200 ns access.

The new EA 4122 delivers the same performance in the same sockets on the same PC boards built for TMS-4060s — but with far lower power drain. And it's available in the same three speeds as the TMS-4060 family:

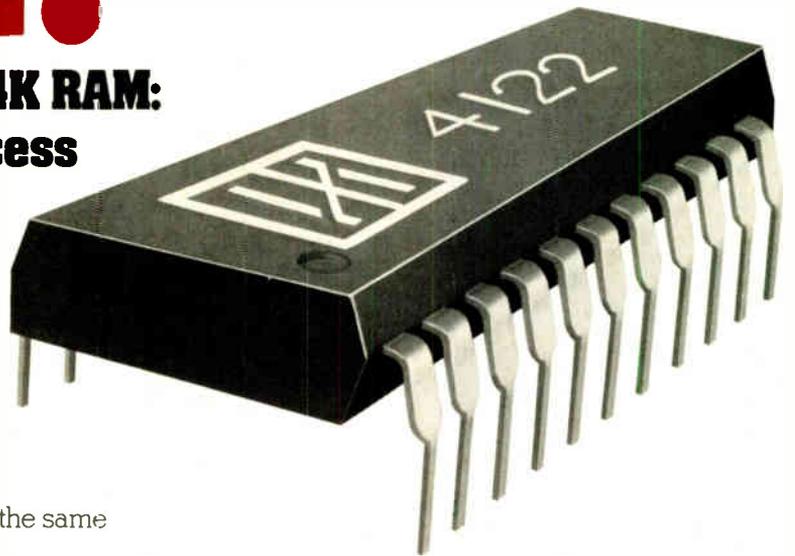
1. EA4122-2 with 200 ns max. access, 400 ns min. read/write cycle, 375 mW max.
2. EA4122-1 with 250 ns max. access, 430 ns min. read/write cycle, 350 mW max.
3. EA4122 with 300 ns max. access, 470 ns min. read/write cycle, 325 mW max.

In case you don't require lower power dissipation, EA also offers devices specified identically to the TMS-4060, 4060-1 and 4060-2 as well as the Intel 2107B, 2107B-2, 2107B-4, and 2107B-6.

All these are available right now. Soon to come are military versions having an extended temperature range and meeting criteria of MIL-STD 883, Level B.

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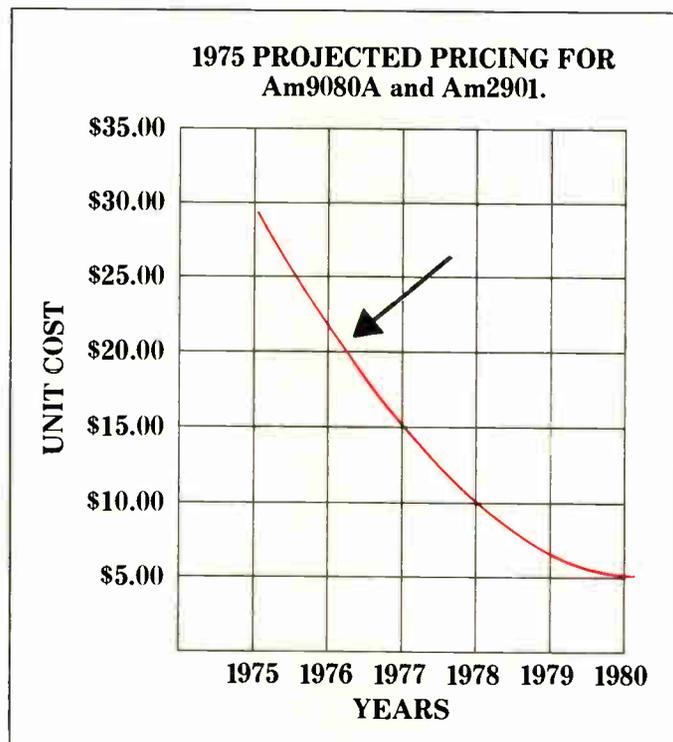


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Advanced Micro Devices announces the 100-
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Just like we said.



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high speed. It can do a 16-bit register-to-register add in just 145 nanoseconds. It has 16 working registers in a cycle-saving, two-address architecture, and is controlled by nine instruction lines that permit it to do almost anything you can think of.

Another thing. Advanced Micro Devices offers a full line of support circuits for either unit—support circuits that optimize your system, not compromise it.

Whether you can best use the MOS Am9080A or the bipolar Am2901, you can now do it for less. **\$21.00** in 100-piece quantities.

Just like the curve said.

Am9080A System Circuits

AMO Part Number	Description	Availability
CPU		
Am9080A/-2/-1/-4	Speeds to 250 nsec 0 to 70°C	In Dist Stock
Am9080A/-2	Speeds to 380 nsec -55 to +125°C	In Dist Stock
Static Read/Write Random Access Memories		
Am9101A/B/C/O	256 x 4 Speeds to 250 nsec	In Dist Stock
Am91L01A/B/C	256 x 4 Speeds to 300 nsec	In Dist Stock
Am9102A/B/C/O	1K x 1 Speeds to 250 nsec	In Dist Stock
Am91L02A/B/C	1K x 1 Speeds to 300 nsec	In Dist Stock
Am9111A/B/C/O	256 x 4 Speeds to 250 nsec	In Dist Stock
Am91L11A/B/C	256 x 4 Speeds to 300 nsec	In Dist Stock
Am9112A/B/C/O	256 x 4 Speeds to 250 nsec	In Dist Stock
Am91L12A/B/C	256 x 4 Speeds to 300 nsec	In Dist Stock
Am9130A/B/C/E	1024 x 4 Speeds to 200 nsec	In Dist Stock
Am9140A/B/C/E	4096 x 1 Speeds to 200 nsec	In Dist Stock
Dynamic Read/Write Random Access Memories		
Am9050C/O/E	4K x 1 Speeds to 200 nsec	In Dist Stock
Am9060C/O/E	4K x 1 Speeds to 200 nsec	In Dist Stock
Mask Programmable Read-Only Memories		
Am9208/B/C/O	1K x 8 Speeds to 250 nsec	Available Now
Am9214	512 x 8 mask programmed Speeds to 500 nsec	Available Now
Am9216B/C	2K x 8 mask programmed Speeds to 300 nsec	Available Now
Erasable Read-Only Memories		
Am9702	256 x 8 Speeds to 10 μsec	In Dist Stock
Am1702A	256 x 8 Speeds to 10 μsec	In Dist Stock
Am2708	1024 x 8 Speeds to 450 μsec	2nd Q 1976

AMO Part Number	Description	Availability
Processor System Support Circuits		
Am8212	8 bit I/O Port	In Dist Stock
Am8224	Clock Generator	In Dist Stock
Am8228	System Controller	2nd Q 1976
Am8216/26	Bus Transceiver	2nd Q 1976
Am25LS138	1 of 8 Decoder	In Dist Stock
Am9555	Programmable Peripheral Interface	2nd Q 1976
Am9551	Serial Communications Interface	2nd Q 1976
CPU 9080A=4E0 nsec -2=380 nsec -1=320 nsec -4=250 nsec Mem A=500 nsec B=400 nsec C=300 nsec D=250 nsec E=200 nsec		

Am2900 System Circuits

AMO Part Number	Description	Availability
Am2901	4-Bit Microprocessor Slice	In Dist Stock
Am2902	Carry Lookahead Chip	In Dist Stock
Am2905	4-Bit Transceiver For Open Collector Bus	In Dist Stock
Am2906	4 Bit Transceiver For Open Collector Bus With Parity Generator/Checker	In Dist Stock
Am2907	4 Bit Transceiver For Open Collector Bus With Single Data Input	In Dist Stock
Am2909	Microprogram Sequencer	In Dist Stock
Am2911	Minimicroprogram Sequencer	2nd Q 1976
Am2914	8-Level Priority Interrupt	3rd Q 1976
Am2915	4-Bit Transceiver For Three-State Bus	2nd Q 1976
Am2916	4 Bit Transceiver For Three-State Bus With Parity Generator/Checker	2nd Q 1976
Am2917	4 Bit Transceiver For Three State Bus With Single Data Input	2nd Q 1976
Am2918	1 By 2 Port Register	In Dist Stock
Am2919	Priority Interrupt Expander	2nd Q 1976
Am2950/51	256 Bit RAM Open Collector Or Three State	In Dist Stock
Am2952	1024-Bit RAM Open Collector	2nd Q 1976
Am2954/55	16-Word By 4-Bit Two Address Register Stack Open Collector or Three-State	2nd Q 1976
Am2970/71	256 By 4 PROM s With Open Collector or Three-State Outputs	In Dist Stock

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Are manpower predictions of any value?

In the years immediately following Russia's launching of the first earth satellite, the future supply of engineers was a truly national concern. The big question then was whether there would be enough engineers to ensure that the United States regained its top technological position.

Helped in part by predictions that there would be a severe shortage of skilled manpower unless more students chose to enter engineering—and in part by the new-found glamour of the field—the enrollments at engineering schools rose and the engineer soon had one of the hottest careers going.

Things are, of course, vastly different now. The glamour has faded and so have many job opportunities. But, oddly enough, the predictions of engineering manpower shortages in the near or far future continue. The result, charge some observers, is an oversupply of engineers and an erosion of the quality of engineering as a career. Indeed, one study charges that the engineer-need predictions made by the Engineering Manpower Commission of the Engineers Joint Council, when compared with actual demand figures, were wrong 77% of the time from 1963 to 1974.

It is not surprising, therefore, that there have been increasingly vocal calls for a moratorium on the council's predictions, or at least a serious study of ways to change its

prediction process to take into account what really happened. Unfortunately, the growing concern among engineers over the effects of the consistently optimistic predictions apparently is not shared by the Engineering Manpower Commission.

Provided an opportunity at a recent meeting to take decisive action to suspend its manpower studies until it can prove out their accuracy, the EMC instead chose to continue business as usual while a committee studies the charges of inaccuracy recently brought to light. This move was taken despite the fact that the directors of the National Society of Professional Engineers, at their January meeting, went on record as doubting the credibility of the EMC projections.

Instead of ducking the issue—ensuring the accuracy of demand predictions that influence potential engineering students—EMC should analyze its projections. First, are the surveys accurate and unbiased? Second, is there a procedure for follow-up to find out if the actual job market matched the predictions? Third, is there any allowance made for refining the predictions based on present realities? Fourth, are overly optimistic projections harming the engineer's career?

If these questions are too hard to answer, then maybe more engineering groups should begin to question the Engineering Manpower Commission's actions.

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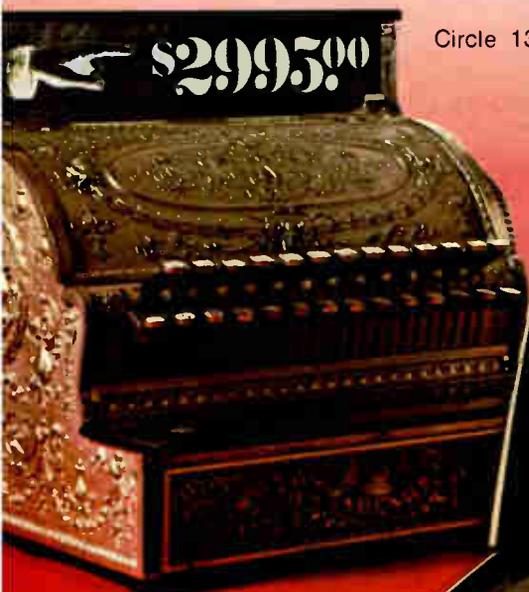
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People

New team comes in at Electronic Associates

With a few tough years and several acquisitions and divestments behind it, Electronic Associates Inc., West Long Branch, N.J., is back on what it hopes is a profitable track and concentrating on building the machines it knows best—hybrid computers, which combine analog and



Hybrid vigor. Incoming Electronic Associates vice presidents Brown and Sanders see growth for hybrid computers.

digital processing techniques.

And it has two new vice presidents to help direct its specialty. Milton Sanders, who until recently was a corporate vice president of Conrac Corp., New York City, is vice president for marketing, and Robert N. Brown, is the new vice president for engineering. Before joining EAI, Brown held the same position at the Singer Co.'s Business Machines division in San Leandro, Calif.

Brown is no stranger to either analog or digital computers. After a Navy tour during World War II, he began his engineering career at Singer's Kearfott division working on an analog computer for the Navy. More recently, he was responsible for the development of digital-computer and peripheral lines at Singer.

One of the first problems that

Brown plans to start attacking is the pricing of his products. EAI's price-tags range from \$25,000 for desktop models to \$1 million for simulators. "Hybrid computers have a lot of versatility, but if their cost is out of line with a less efficient way of doing, for example, simulation, people will frequently take the lesser system and put in the additional manpower to do the job. So we're addressing ourselves to costs to make the product more attractive."

A second goal is to increase the life of the hardware. "There's a lot of our gear out there," says Brown. "But its life is perhaps near an end, not because it is worn out, but because it's not upgradable."

He particularly wants to apply new programable large-scale integrated devices. Besides taking advantage of their increasing power and decreasing cost, he believes they'll add to the common-

ality of parts in his company's products.

Sanders, who began his career in the early 1940s as a research engineer with the U.S. Naval Ordnance Laboratory, sees the market broadening for hybrid computers, particularly in process control and simulation systems. "Over the next five years, commercial simulation modeling will grow, mostly in the energy area where there are a lot of questions that must be answered without messing around with the actual system."

Cars are different vehicle
for Nortron's Pat Lynch

Patrick D. Lynch was one of the executives caught in the corporate wringer when Motorola Semicon-

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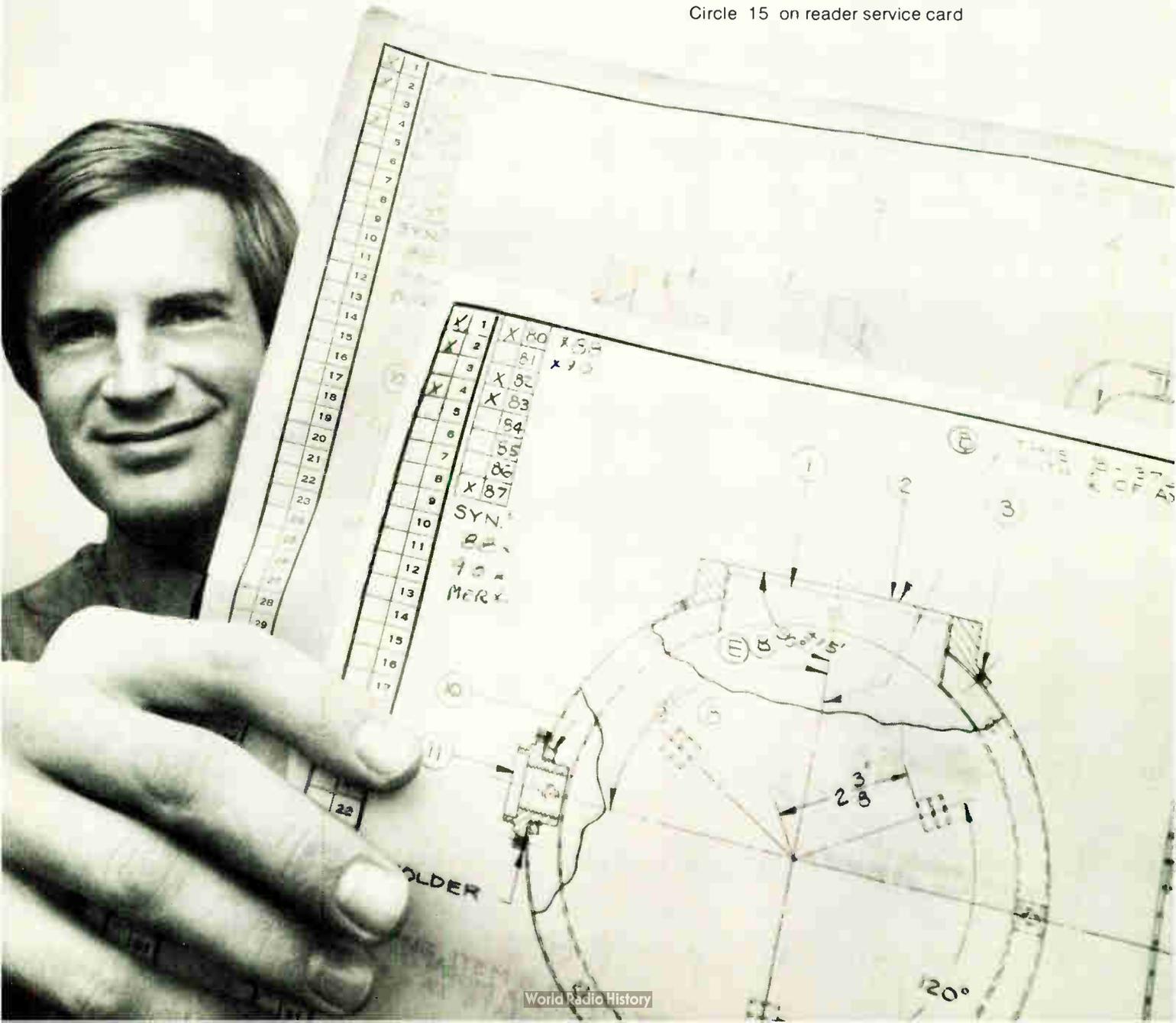
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92 TO-

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2N3905/6	PNP General Purpose Amp/Switch, Cu Lead Frame	.11/.12	2N4402/03	PNP Medium Speed, Medium Current Switch, Cu Lead Frame	.13/.14
2N4123/24	NPN General Purpose Amp, Cu Lead Frame	.10/.11	NPC2369	NPN High Speed, Low Current Switch, Cu Lead Frame	.15
2N4125/26	PNP General Purpose Amp, Cu Lead Frame	.10/.11			
2N5088/89	NPN Low Noise Amp, Cu Lead Frame	.14/.15			
2N5088/87	PNP Low Noise Amp, Cu Lead Frame	.14/.15			
2N5209/10	NPN Low Noise Amp, Cu Lead Frame	.14/.15			
NPCA20/21/22	NPN General Purpose Amp, Cu Lead Frame	.11			
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ductor Products division underwent a massive reorganization in mid-1975. Now he has surfaced again—but not at a semiconductor house.

The 42-year-old former vice president of Motorola's \$400-million-plus semiconductor operations in the U.S. [*Electronics*, May 15, 1975, p. 38], is now president and chief executive officer at Nortron Corp. The main product of this \$2-million-a-year, Sunnyvale, Calif., company is an electronic wheel balancer for automobiles.

While the company's product line may seem prosaic, its potential is anything but that, says Lynch. He sees the company as excellently positioned for expansion into sophisticated auto testers built around microprocessors and other semiconductor technology.

Expansion coming. Under Lynch's direction, the company plans to grow in a number of directions. First, using medium-scale integrated circuits and discrete electronics, the wheel-balancer line will grow from one general-purpose machine to three systems aimed at specific market segments. This will be followed by a second generation of balancers and aligners designed around a low-cost microprocessor.

"To anyone used to the semiconductor business, this would be a mundane marketplace," Lynch says. "But the numbers are far from mundane." With about 180,000 service stations and auto-service centers in the U.S., the potential market for sophisticated electronic wheel balancers and aligners is about \$22 million.

"As auto makers move toward sophisticated electronic control systems," says Lynch, "Nortron with its knowledge of both the auto and electronics industry, as well as auto servicing, is in a key position."

By the end of the company's fiscal year this June, sales will have quadrupled over last year. For next year, Lynch, who spent 15 years at Motorola and three at Texas Instruments, predicts at least another doubling. And he adds with a smile, "Another benefit of this job is that I can get my wheels balanced free." □

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Meetings

Acoustics, Speech, and Signal-Processing International Conference, IEEE, Marriott Hotel, Philadelphia, April 12-14.

Computer Software Engineering: Reliability, Management, and Design, IEEE, Barbizon Plaza Hotel, New York, April 20-22.

Reliability Physics International Symposium, IEEE, Caesars Palace, Las Vegas, April 20-22.

Eighth Annual Southeastern Symposium on System Theory, University of Tennessee and IEEE, Knoxville, Tenn., April 26-27.

Seventh Annual Pittsburgh Conference on Modeling and Simulation, IEEE, University of Pittsburgh School of Engineering, April 26-28.

Third FAA/Georgia Tech Workshop on Grounding of Electronic Systems, Federal Aviation Administration and Georgia Tech, Atlanta, April 26-28.

Electronic Components Conference, IEEE, Jack Tar Hotel, San Francisco, April 26-28.

Optical Computing International Conference, IEEE, Capri, Italy, April 27-29.

Circuits and Systems International Symposium, IEEE, Technical University, Munich, April 27-29.

Offshore Technology Conference, IEEE, Astorhall, Houston, Texas, May 3-6.

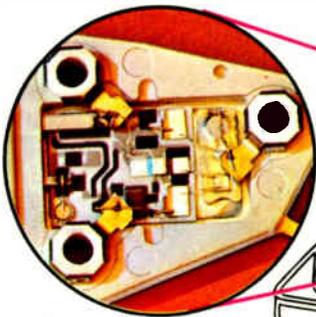
Carnahan Conference on Crime Countermeasures, IEEE, University of Kentucky, Lexington, May 5-7.

Industrial and Commercial Power Systems Conference, IEEE, Hyatt Regency Hotel, Los Angeles, May 10-13.

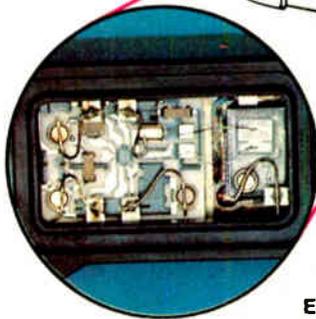
Electro 76—IEEE International Convention, IEEE, Hynes Auditorium and Sheraton-Boston Hotel, Boston, May 11-14.

National Workshop on Low-cost

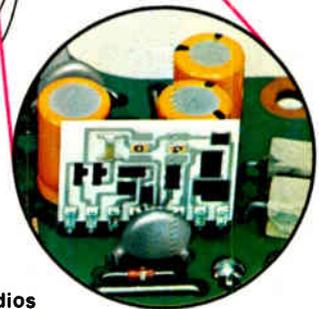
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Meetings

Polycrystalline Silicon Solar Cells, National Science Foundation and Energy Research & Development Administration, Southern Methodist University, Dallas, May 18-19.

Naecon Aerospace and Electronics Conference, IEEE, Dayton Convention Center, Dayton, Ohio, May 18-20.

Eighteenth Israel Annual Conference on Aviation and Astronautics, Technicon-Israel Institute of Technology, Haifa, Israel, May 19-20.

Third International Euromation Exhibition-Instrumentation and Automation in Industry, Belgian Institute of Automatic Control, Brussels International Fair, May 22-26.

Conference on Lasers and Electro-Optical Systems, IEEE, Town and Country Hotel, San Diego, May 25-26.

Semicon West '76, Semiconductor Equipment & Materials Institute (c/o Golden Gate Enterprises, Santa Clara, Calif.), San Mateo County Fairgrounds, Calif. May 25-27.

International Symposium on Multi-valued Logic, IEEE, Utah State University, Logan, Utah, May 25-28.

Trends and Applications in Micro and Mini Systems, IEEE, NBS, Gaithersburg, Md., May 27.

National Computer Conference, IEEE et al., New York Coliseum, June 7-10.

Hybrid Microcircuits Symposium, U.S. Army Electronics Command, Fort Monmouth, N.J., June 8-9.

Power Electronics Specialists Conference, IEEE, NASA Lewis Research Center, Cleveland, Ohio, June 8-10.

Electrical Insulation International Symposium, IEEE, Queen Elizabeth Hotel, Montreal, Ont., June 14-16.

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CMOS FEATURES

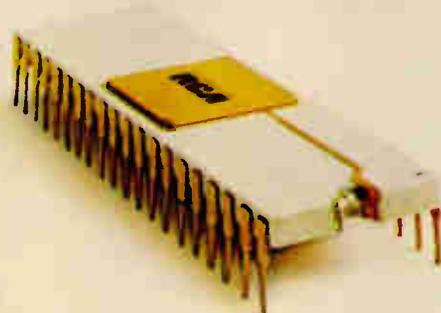
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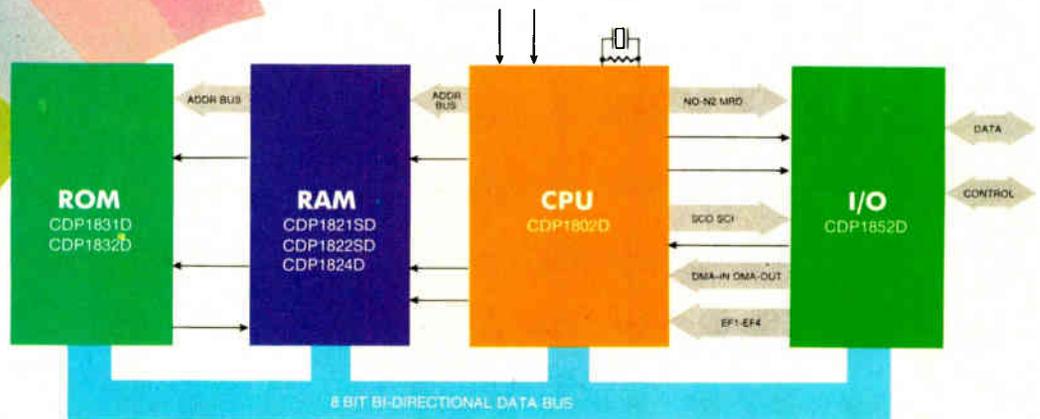
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faster: 2.5-3.75 microseconds for any instruction. And cost is way down: \$23.50 for the CDP1802 in 100 piece quantities.

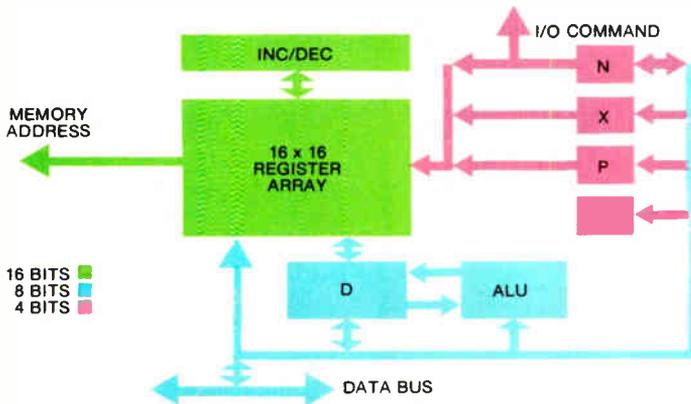
In addition, we've further simplified control signals. And you get the single-chip benefits of simpler assembly and less design time, plus an expanded instruction set.

All this on top of other things you liked about the 1801. Especially its COSMAC architecture, simple yet powerful. Plus its easy learning and programming.

COSMAC architecture lowers memory costs

The big advantage is 1-byte instructions: less to store, less to fetch — which means less memory to buy. Simple subroutine calls can take just one byte.

Instructions need no addresses because the 16 internal general purpose registers act as pointers. You can point these registers at data areas or program areas. They can also be used to store data directly, reducing the need for RAM. One register even acts as a built-in DMA address generator—an RCA first.



RCA offers ROMs and RAMs as standard support. But if you have reason to use memories from other manufacturers, you can use almost any industry standard in x1, x4 or x8 configurations.

I/O costs less, too

You'll find much of what you need in our line of standard I/O devices—some available now, many more in development. Or if you need special interfaces, design your own with our industry-leading CD4000 line of logic devices. If you want, we'll build a custom LSI I/O part for you. In any case, COSMAC architecture can greatly simplify your system design.

Result: system cost effectiveness

For all the reasons given, we challenge any other microprocessor to match the 1800's system cost effectiveness. Figure in, too, the other CMOS economies. One power supply. A single-phase clock, which can be implemented by an external crystal to work with the on-chip oscillator. Also consider the cooling and other equipment you *don't* have to buy because of CMOS low power dissipation and tolerance to noise, power-supply and temperature variations.

What about design time?

CMOS also makes it easy and inexpensive to assemble a prototype. Then, static logic makes your prototype easy to debug. Data exchange is carefully strobed. Everything about our 1800 Series helps make system design less time-consuming.

Use your 1801 programs

Any 1801 programs you may already have will run on the new 1802. So your program investment is safe. In fact, we offer a new emulation board that you can simply plug into COSMAC Development System to adapt it to the new CPU.

From RCA: a total system—all CMOS!

Available now:

- CDP1802D CPU
- CDP1821SD 1024x1 CMOS SOS RAM
- CDP1822SD 256x4 CMOS SOS RAM
- CDP1824D 32x8 CMOS RAM
- CDP1831D 512x8 CMOS ROM COSMAC Interface
- CDP1832D 512x8 CMOS ROM
- CDP1852D Byte I/O

Note: recommended operating range for above parts is 3 to 12 volts. They are also available in "C" versions (e.g. CDP1802CD) with recommended operating range of 4 to 6 volts.

In development:

- UART (Universal Asynchronous Receiver/Transmitter)
- MDU (Multiply-Divide Unit)
- 3- to 8-bit Latch-Decoder
- Bus Buffer Separator
- 128x8 RAM
- 1024x8 ROM
- Programmable Bit I/O part
- A/D Converter

Design support

- Microtutor (CDP18S011): a minimum computer for learning microprocessing.
 - Evaluation Kit (CDP18S020): kit of parts including printed circuit board.
 - COSMAC Development System with 1802 Emulator (CDP18S004) for easy breadboarding and resident software development. Includes chassis, power supply, simple control functions, CPU, RAM, byte I/O, terminal interface, utility routines, plus the capability of editing, assembling and debugging software programs.
 - Floppy Disc for COSMAC Development System (CDP18S801).
 - COSMAC Software Development Package (CDP18S910, CDP18S911): assembler, editor and simulator/debugger programs available on GE Time-Share or in Fortran IV for purchase.
 - Manuals, seminars and application notes.
- Plus applications assistance by RCA engineers.

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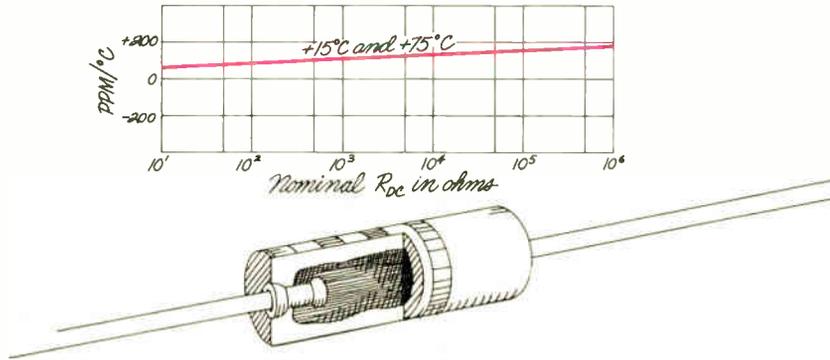
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New cable eases task of designing optical data links . . .

A new fiber-optic bundle cable from DuPont makes it possible for the first time for a designer to use standard cable bundles and associated hardware in a package approach to optical analog or digital data links. **Previously, the bundles were custom products.**

Called PFX, DuPont's seven-fiber, all-plastic cable can be used with standard fittings from several manufacturers, including Amphenol and AMP. Its attenuation is 470 decibels per kilometer at a wave-length of 656 micrometers—visible red—and it is easy to use, even in the field. The fiber is simply pushed into the connector with hardware that's supplied; then, excess fiber is trimmed with a razor blade, and the ends are polished with emery paper. DuPont claims splicing losses of 3 dB without any concern about fiber alignment. The PFX plastic cladding is very thin relative to the bundle, giving the cable a packing factor that passes 95% of the light.

. . . as ITT goes standard with fiber cables

Another significant step in the progress of optical fibers toward standardization has been taken by ITT's Electro-optical Products division. While the Roanoke, Va., division has been doing extensive work in the technology, it has been mostly on a contract basis with virtually no standardization of components. **Now, however, ITT has issued its first data sheets**—detailing a line of general-purpose plastic-clad silica fiber cables.

Included are two six-fiber heavy-duty versions, one with attenuation of 18 decibels per kilometer at 0.85 micrometer and 8 dB/km at 1.06 μm , the other 50 dB/km at 0.79 micron. The third, for light duty, has seven to 19 fibers. It is specified as 50 dB/km at 0.79 μm . Prices are \$6 to \$15 per meter.

AMD builds 1-microsecond version of 8080A

Using its ion-implanted depletion-load process, Advanced Micro Devices Inc. of Sunnyvale, Calif., has developed a 1-microsecond version of Intel's 8080A microprocessor, **probably the fastest instruction cycle time for an 8-bit n-MOS processor** achieved to date. Designated the 9080A-4, the part dissipates a maximum of only 1.1 watts. Intel's fastest part, the 8080A-1, has a 1.3- μs instruction time at 1.3 W maximum.

Production-line board tester coming from HP

Hewlett-Packard's Automatic Measurement division will announce **its first entry soon into the market for production-line printed-circuit board testers**. Called the DTS-70, the \$70,000 digital test system is capable of go/no-go testing and fault isolation of 250 MSI-type circuits in any logic family. Available with three test stations, costing \$10,000 each, the system also has a logic-simulation console that allows users to develop and debug test programs while the DTS-70 is at work on the production line. Control is provided in the form of an HP 2112 minicomputer with 32,000 words of memory.

Wind shear sensor sought

A request for proposals will be issued this summer by the Transportation Systems Center for a laser radar sensor to detect wind shear and wake-turbulence phenomena in the vicinity of airports. Wind shear—the sudden shift in wind direction **that can cause aircraft when landing to drop**

suddenly or overshoot the runway—causes crashes such as the one last year at New York's John F. Kennedy Airport. Wake turbulence is the trail of disturbed air created by an aircraft that can affect following planes. It also can be especially troublesome during landing.

TSC officials in Cambridge, Mass., are confident they can detect both phenomena at slant ranges up to 1,000 feet using a carbon-dioxide laser radar operating at 10.6 micrometers. Their confidence stems from data being gathered in tests sponsored by the Federal Aviation Administration using a Lockheed-built laser at Table Mountain, Colo. The request for proposals will seek both the laser radar and associated display.

Northrop delivers guidance system for Awacs

The first production navigation and guidance system for the Air Force's E-3A airborne warning and control system aircraft has been delivered. Northrop Corp.'s Electronics division is system integrator for prime contractor Boeing Corp., **under a \$4.7 million contract awarded last June to supply systems for six production aircraft.** Northrop has provided five pre-production navigation and guidance systems for the controversial Awacs craft, which has been a subject of dispute and has been in and out of the defense budget over the past decade. Its mission is to serve as an airborne command and control center, with the additional ability of detecting and tracking targets at long ranges.

As integrator for the E-3A equipment, Northrop has responsibility for a system consisting of its own Omega radio navigation set, a Delco Carousel IV inertial platform, and a Ryan doppler velocity sensor. Two of the five systems already delivered were for testing, and the remaining three will be converted to operating units. Current E-3A flight testing is taking place with production-configured avionics. Qualifications testing is expected to be completed this year.

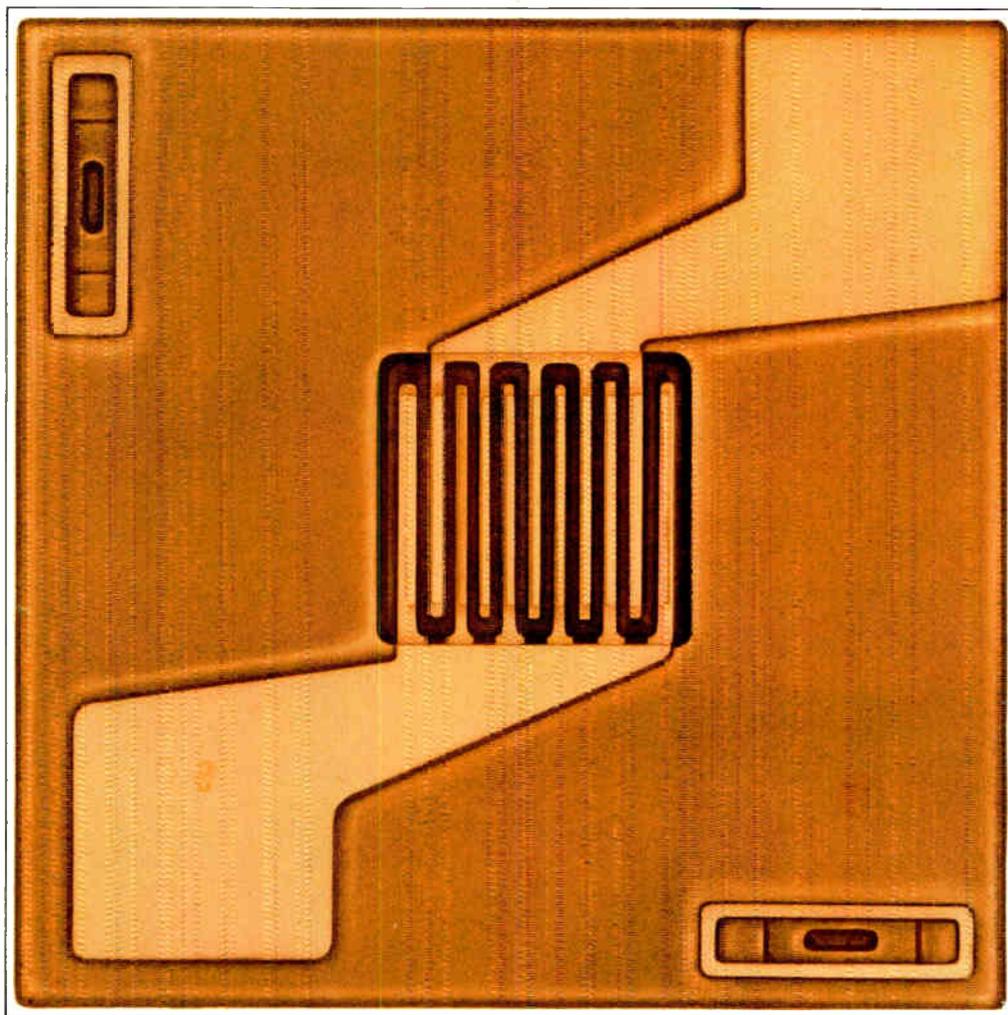
Siemens establishes company to market medical gear in U.S.

West Germany's Siemens AG is expanding its medical-electronics bridgehead in the U.S. Only six months after starting a plant in Connecticut to produce X-ray equipment, Siemens is establishing a company in Union, N.J., to market hearing aids and infrared communications systems for schools to teach the deaf and partially deaf. Siemens expects a considerable increase in sales of these teaching systems, already being used by some German schools. **About 10% of the company's worldwide \$20 million a year in sales of hearing aids is exported to the U.S.**

Addenda

Look for Control Data Corp. to announce its entry into the computer-based education field within the next few weeks, **going commercial with its long-awaited system, Plato**, which is designed to meet many of the training needs of education, business, and government. . . . Japanese exports of **citizens' band transceivers to the U.S. climbed rapidly during 1975**, from 137,046 units in January to 692,498 in December, for a total of 3,759,333 for the year. The 1974 total was 1,317,602. . . . In an effort to improve its balance of payments deficit, Israel has negotiated several military contracts with U.S. firms stipulating that **10% to 15% of the hardware contain Israeli components.**

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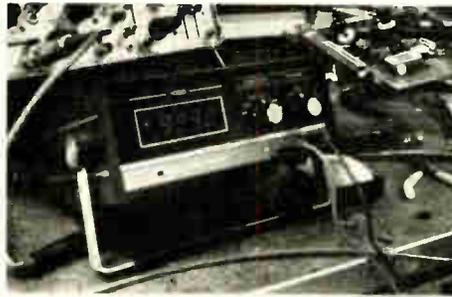
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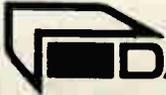
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Reliability study checks moisture levels in ICs

Goal of Martin-Marietta tests is the determination of real need for hermetic packaging to stop leakage

Leakage in a hermetic package? For high-reliability integrated circuits that's been an industry-wide no-no, although some leakage always exists. But leakage may no longer be such a taboo. Now being launched is a series of experiments on the effects of moisture on integrated circuitry. By correlating leakage rates with moisture content, they may show that ICs can tolerate some leakage after all.

"Present industry-wide reliability standards for hermeticity have been established through experience, not scientific measurement," explains Saul Zatz, a staff engineer and the program's leader at the Martin-Marietta Aerospace's Orlando, Fla., division.

Martin-Marietta has developed a

technique for introducing a controlled leakage rate through micro-diameter holes, or microvents, into an otherwise hermetic package. The company includes a dew-point sensor in each IC package to measure the actual moisture.

In the process, for which a patent is pending, the company uses a laser to drill a 0.001-inch-diameter hole in the nickel cap of a standard metal-can package. Then, to smooth out irregularities, the hole is electropolished to remove unwanted metal from the periphery and electroplated with gold to prevent corrosion from closing it up. The plating reduces the hole diameter to between 0.0001 and 0.00001 in., which corresponds to a leakage rate of 10^{-4} to 10^{-8} atmosphere cubic centimeters per second.

Initially, Martin-Marietta will be "microventing" over 200 linear ICs for a 5,000-hour life test at 85°C and 85% relative humidity.

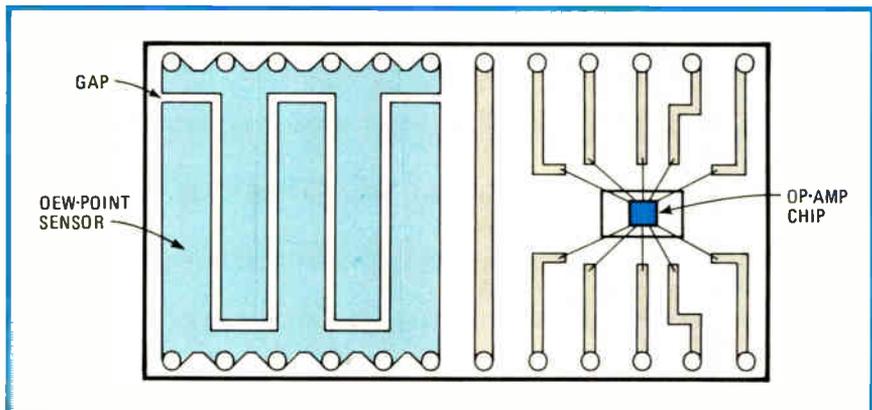
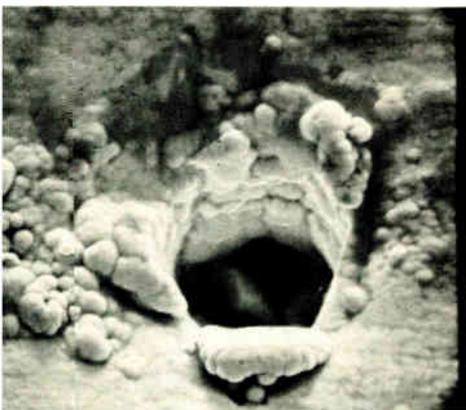
Linear devices. The ICs are 741-type operational amplifiers made sensitive to moisture by not being

passivated with glass. The op amps, in chip form, are attached to ceramic substrates on which special thick-film dew-point sensors made of gold are deposited. Finished substrates with both chip and dew-point sensor are encased in 24-pin metal dual in-line packages that are sealed by welding.

To measure the moisture level, the package is cooled down while a constant potential is maintained across the gap of the sensor. At the dew point, current increases abruptly, indicating moisture has condensed across the gap.

Periodically, Zatz and his group will interrupt the life test to determine how much moisture is in the packages and to check out the parts electrically. Besides trying to establish a scientific base for allowable leakage rates, the experiments will also explore how much moisture can be tolerated before failures occur and what failure modes are caused by moisture.

The tests are expected to be completed within the next 15 months.



Hole in one. For Martin-Marietta tests, a 1-mil diameter hole is drilled with a laser in cap of a standard metal-can package (left) to form openings, after plating, between 10 and 100 microns in diameter. Dew-point sensor in package is used to track moisture content.

Funds for the project are from the Advanced Research Projects Agency, with administration through the Semiconductor Technology Program of the National Bureau of Standards. □

Security

Baggage scanner uses gamma rays

The year-end bombing at New York's LaGuardia Airport that killed 11 persons has triggered a Federal Aviation Administration probe of gamma-ray systems to detect explosives and weapons.

The gamma-ray approach operates on the principle that explosives absorb more energy than surrounding material and can therefore attenuate a beam enough to set off an alarm. Its major advantage over conventional X-ray imaging systems, according to the FAA, is that does not have to be monitored by security personnel and is less subject to human error.

FAA administrator John L. McLucas says the agency is awarding \$194,000 to Westinghouse Electric Research Laboratories, Pittsburgh, for development of prototype systems for fast and effective automatic screening of checked as well as hand-carried baggage. Westinghouse Electric already is testing a prototype gamma-ray screening system for hand-carried luggage at the American Airlines gate at Washington's National Airport.

The Westinghouse system for screening checked baggage will be an advanced version of the demonstration model, says Arthur E. Anderson, manager for electronic systems at the Pittsburgh laboratories. "A system for screening checked baggage has a more difficult task because it must be designed primarily to detect bombs or explosives, rather than weapons," he explains, "and it must operate at faster speeds on larger pieces." Both systems are controlled by Digital Equipment

Corp. LSI-11 minicomputers. However, the checked-baggage system will employ a more sophisticated computer program to accommodate the higher speeds required.

Operation. In the hand-baggage demonstration system, the barium-133 gamma-ray sources are mounted in a special container designed to provide two shaped gamma-ray beams. As baggage is carried along on conveyors, the fan-shaped beams scan detector arrays on the opposite side of the belts each time they move 1 centimeter. The result is a vertical profile showing the reduction in radiation caused by items in the baggage. The minicomputer adjusts the system's electronic circuitry to compensate for differences in the characteristics of various types of baggage and calls for search of a bag when the beam attenuation drops below a predetermined level.

Masking the shape of a weapon or explosive is futile, because the

masking material provides even further attenuation of the beam, enhancing detection. What's more, the system is not easily fooled by how a weapon or explosive is positioned, Anderson says.

To minimize scattered radiation and bag exposure, the fireproof gamma-ray-source holder constricts the fan beam to the active detector area on each side of the conveyor, Westinghouse says. Radiation exposure per bag is less than 10% as much as single-pulse X-ray-imaging systems.

When the equipment is turned off, radiation ports close to shut off the gamma-ray beams. In another protective measure, the minicomputer monitors the detectors and warns of any deterioration in system performance, including the need for recalibration. The detector can be recalibrated automatically in about 20 seconds during slack periods with circuitry built into the equipment. □

Computers

Wang data-processing line competes in business arena

The other shoe has been dropped by Wang Laboratories Inc. in the company's push into the distributed business-data-processing market. The Tewksbury, Mass., firm, known for years primarily as a maker of scientific and engineering calculator products, last week unveiled a broad range of products that will put it squarely into competition with IBM, Tektronix, and Hewlett-Packard Co., among others, in distributed data processing.

Less than a year ago, Wang went to market with its three-computer WCS line, including processors with cassette, floppy-disk and hard-disk storage, the company's first entry into the small-systems market. To date, Wang has shipped or booked orders for some 1,700 of those systems. The latest machines go much further, however, especially to broaden the top of the line by offer-

ing Wang-developed microprocessor-based peripherals and two software packages for multi-user distributed data processing.

There are two new computers in the new line. One is the portable computer system (PCS), a 57-pound machine selling for \$5,400. It will compete head-on with the IBM 5100, says Wang's Carl Masi, director of product marketing. The PCS price includes a new processor in Wang's earlier-introduced 2200 series, 8,000 bytes of semiconductor random-access memory, and 42,500 bytes of read-only memory. The PCS includes a 9-inch CRT display, full alphanumeric keyboard, a tape cassette for program loading and storage, and controllers for a new printer and plotter. The random-access memory is expandable to 32,000 bytes.

The second new processor is in-

cluded in the 2200 series work station that might be laid out as a three-station multiprocessing system selling for \$41,000. It would include the 2200 central processing unit with a five-megabyte disk, 16,000 bytes of memory, a 200-character-per-second printer, a removable floppy disk for program loading, disk multiplexer, and three hard-disk work stations.

The other hardware entries in the Wang line are a 120 character-per-second printer and a drum plotter based on an Intel 8080A microprocessor. Both are priced at \$2,900, and both will eventually be sold to original-equipment manufacturers, says John Cunningham, senior vice president for North American operations. There are also two new communications controllers, and two software packages—a management-planning system, and Wang/CASH accounting package.

Cunningham admits that Wang is late getting into the distributed data-processing market, but he believes the company's substantial end-user support and service, its lower prices and broad use of Basic language, rather than a more complicated operating system, will quickly make Wang competitive in a range of systems prices from \$5,400 to about \$75,000. □

Memories

Mostek 4-k RAM is high-performance unit

Although all the news from memory makers seems to be about 16-kilobit RAMs, a new wave of 4,096-bit random-access memories—new designs aimed at high-performance appli-



In business. Wang Laboratories Inc. is aiming its new computer and peripheral line at business applications.

cations—is about to give a boost to the 4-k market. One such part, Mostek's 4027, also foreshadows the firm's design goals for its as-yet unannounced 16-k.

Mostek's first high-performance device is the 4027-3, which has a worst-case access time of 200 nanoseconds, making it the fastest 16-pin 4-k RAM to date. When in June it introduces the 4027-2, a mechanical-shrink version with 150-ns access times, it will have the smallest—104 by 140 mils. An even faster RAM is waiting in the wings. "We've left one slot open," says Derrell Coker, an applications engineer at the firm's Carrollton, Texas, headquarters. "We'll build the dash-1 part when the market says it needs it and when someone starts to second-source the others."

Unlike Mostek's first 4-k RAM, built with a complex double-level metal-silicon process, the 4027 relies on the industry's straight forward n-channel silicon-gate process, combined with ion-implanted depletion loads—the same process used for its interpretation of the 2102 1-k static RAM. The company will revert to a double-level process, this time two layers of polysilicon, for the 16-k part, the 4116.

Adoption. "The new features of the 4027, however, are being carried over into the 4116," Coker says.

"You might say we needed a 4-k RAM that could serve as a test vehicle for the 16-k." Improvements include more multiplexing, more operating modes, and simpler timing, than competing versions. In addition, the input levels have tripled the noise margin for operation with Schottky transistor-transistor logic, and a more powerful driver has been added at the output. Besides multiplexed addresses, Mostek is now multiplexing input buffers and row and column decoders and greatly reducing the active silicon area on the chip, as well as cutting input capacitance in half.

New operating modes include a page mode, handy for block transfers or direct-memory-access applications. Mostek has also added a row-address strobe-only refresh cycle that cuts the cycle power requirement to $\frac{2}{3}$ the normal operating power, while keeping data from the previous cycle valid.

Making the 16-k. For the 16-k version, Mostek has replaced the chip-select signal in the 4027 with the seventh address input and is using the double-poly silicon process. Company designers, who figure power dissipation can be kept to less than 600 milliwatts, are looking at an 18–20-mw maximum standby-power dissipation. Chip size is in the range of 125 by 230 mils, Coker says, and access time for the 4116-4 chip, to be sampled in June, will be 250 ns.

Another difference between the 16-k and 4-k RAMs is the former's lack of a latch on the three-state outputs. Without it, Coker says, the part is suited for common input/output operation, enabling the memory to be connected directly to most microprocessors' bidirectional data buses. □

Core system has a special wiggle

Two veterans in the core-memory business have developed an unusual electrically alterable read-only memory that achieves nondestructive

tive readout by only partially switching the cores.

Significantly, the memory can function not only as an electrically alterable ROM but also as a random-access memory, or any combination of the two. And it does not require the complex restore circuitry that is part of the usual destructive-readout core systems.

Bruce Kaufman, president, and Orville Moe, executive vice president, founded Controlex Corp. last year in Van Nuys, Calif., to build the memory for the digital industrial control market. Both have long experience in cores. Kaufman worked as an engineer for Litton Industries' Data Systems division and NCR Corp. and founded Memory Systems Inc. Moe was chief engineer for Litton's Memory Systems division, founded Computer Component Corp. and served as a consultant for major computer firms.

Reading a wiggle. The Controlex memory capitalizes on a transient phenomenon that occurs when a core is in one magnetized state and a "read" pulse that is insufficient to drive it to the other state is introduced. Ordinarily a conventional core system detects the difference between a logic 1 and 0 by sensing the presence or absence of an output pulse when a pulse sufficient to switch the core is applied to the read line.

When Controlex's read pulse comes along, the flux in the core will change along the hysteresis loop of the material. But because the pulse is too small, the core will revert to its original state of magnetization. However, the flux state of the core has changed, so that there is a relatively small, almost noise-like signal on the sense line—a "wiggle," as Kaufman calls it. "We read the wiggle," he says. Outputs are generated for all bits of any word line, and sense amplifiers shape it for processing.

Another fundamentally different approach is the configuration of two cores to each bit, compared to the usual one for one. Moe explains that they use two 18-mil cores—although one would do—to provide a signal

with higher electrical and magnetic symmetry, and better noise rejection.

Flexible nature. With the flexibility afforded by its nondestructive nature, the Controlex unit allows data to be selectively read and written at any random address without affecting any other parts of the memory. Therefore, it can operate as a nonvolatile RAM, or as any combination of RAM or ROM. Moreover, complementary-metal-oxide-semiconductor parts are used in the circuitry to keep power low.

Capacity of Controlex' first memory is 4,096 by 20 bits, but Kaufman says the size and organization is variable. The cycle time is about 1 microsecond, for either read or write, while access time is 750 ns. Power requirements average 12 watts, from supplies of ± 5 volts and ± 13 v dc. Drive current is in the 200-milliampere range, Kaufman says, which could be cut in half with further development.

The entire 4-k module is packaged on a single 8½-by-11-inch printed-circuit board. The high density of the core matrix (6,000 cores per in²) allows it to be placed just on part of one side.

Price for the memory will be \$1,000 to \$1,200 each, depending on the extent of the electronics that's required, Kaufman says. This makes it about twice the price of

semiconductor memories, he says, and only slightly more than ordinary core systems. Initial deliv-

Solid state

Motorola adding M6800 hardware

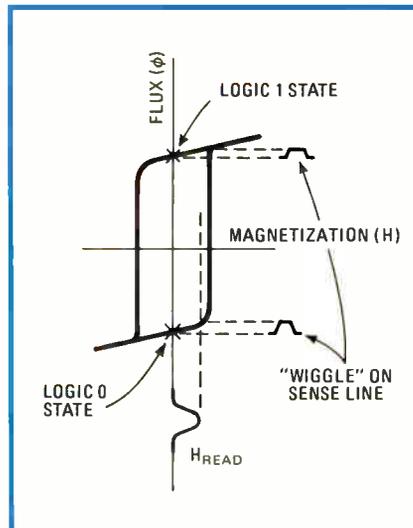
Much of Motorola's effort for the 6800 microprocessor has been tied up in software and firmware. Indeed, users have seen few significant hardware introductions since the 5-chip kit bowed early last year.

"That's about to change," says William Toon, microprocessor marketing manager at Motorola's LSI operations. New metal-oxide-semiconductor parts are now in pilot production at the Austin, Texas, wafer processing facility, and the recent decision to emphasize MOS over integrated injection logic has given the group added impetus [*Electronics*, March 4, p. 25].

Parts rush. Customers have been warned to expect a rush of new MOS and linear support circuits. Speed enhancement will come as soon as the firm changes to a depletion-mode process in late summer.

Price cuts on the MC6800 microprocessor, effective April 5, will chop the price from the earlier \$69 to \$29.95 in quantities of 50 to 99. That will put it into the same ballpark as Texas Instruments' aggressively priced TMS 8080. At the same time, price of the MC6860 low-speed modem will be reduced from \$24 to \$14 in lots of 100.

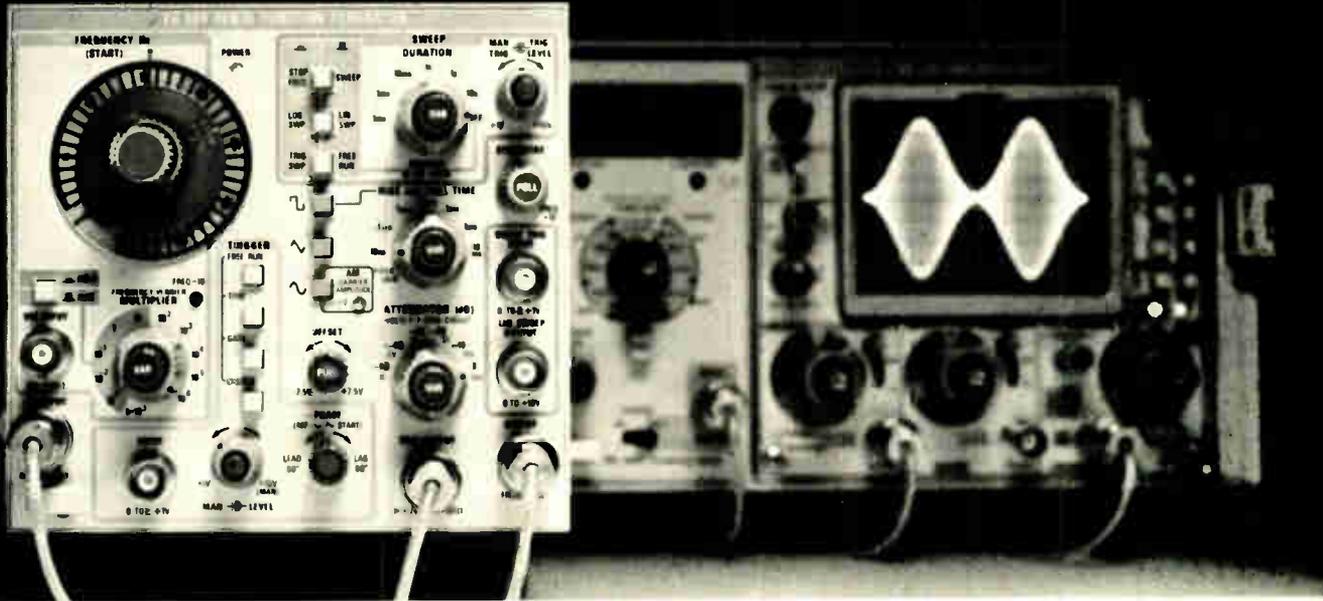
The first depletion-mode part, a 128-by-8-bit static random-access memory will be announced next week. It will also be the first of the family to be packaged in plastic—a move that signals the availability of the entire family in plastic packages by May, Toon says. The plastic parts should lead to further price reductions, but random-access memory, the MCM6810, will be sold at the same price as earlier ceramic versions because the new process has halved its access time. The series are scheduled for this month. □



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am, fm, log or linear sweep, and more.**



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In its phase lock mode, the new FG 504 Function Generator will capture and track a periodic signal, such as a logic pulse or house standard frequency, letting you synchronize the generator's output or convert from one waveform to another. Its am input circuitry senses the presence of an applied modulating signal and reduces the output amplitude by one-half, allowing 100% upward modulation without adjusting the amplitude control. It can be frequency modulated, and it can be swept with its built-in logarithmic or linear sweep. Concentric knobs set the frequency start and stop points.

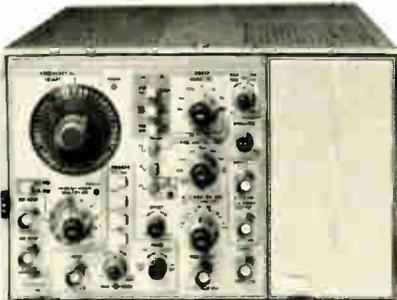
Frequency range: 0.001 Hz to 40 MHz • up to 6 ns rise/fall time • 3 basic waveforms • a wide range of pulse shaping with variable rise and fall and symmetry controls • up to 30 V p-p output • built-in attenuator • post-attenuator offset • external and manual trigger or gate.

In the phase lock, trigger, or gate modes, a phase control shifts the output of the FG 504, so that you can create setups such as a bi-phase clock. A capable instrument for driving logic circuits, the FG 504 is a "clean," 50 Ω source of pulses to less than 20 ns wide and up to 30 V p-p. Post-attenuator offset enables

For Technical Data circle 32 on Reader Service Card.
For Demonstration circle 33 on Reader Service Card.

use of the full ± 7.5 V offset range with small signals.

The FG 504 features independent push-button selection of many of its operating modes, so you are not limited to typical "either-or" decisions. Special modes of operation can be set up through independent or simultaneous use of such functions as AM, VCF (fm), SYMMETRY, VAR RISE and FALL, SWEEP, Waveform HOLD, and TRIGGER or GATE or PHASE LOCK.



Order FG 504T (\$1350) for a stand-alone instrument operable from 100-240 Vac, 48-440 Hz.

For example, the FG 504 may be operated as a free-running burst generator by using the LIN SWEEP OUTPUT to gate the main

generator. With the generator in the SWEEP mode but the START and STOP frequencies set the same, the actual sweep will be zero. Adjusting the trigger level controls the length of the burst. SWEEP DURATION controls the period between bursts.

A special range allows the audio engineer to sweep from 20 Hz to 20 kHz, or a single internal capacitor may be replaced to change the start and stop points.

And dynamic reaction of AGC, squelch, or amplitude-sensitive circuits such as Dolby systems are easy to test with the FG 504 in a square wave or pulse am mode that varies the output between two different levels.

Order FG 504 (\$1200) for a plug-in instrument that operates in any multi-compartment TM 500 mainframe power module.

For further information or a demonstration of the FG 504 and TM 500 Instrumentation, write or phone: Tektronix, Inc., P.O. Box 500, Beaverton, Oregon 97077, (503) 644-0161 ext. 5542. In Europe: Tektronix Limited, P.O. Box 36, St. Peter Port, Guernsey, Channel Islands. U.S. Sales: Prices FOB Beaverton, Oregon



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technical excellence

MCM6810AP—\$4.50 in lots of 100—will run 450 nanoseconds, and a 350-ns version will sell for \$4.95.

Communications parts. Also coming on April 5 are a synchronous serial-data adapter that will allow up to a 600-kilobit-per-second serial bit stream, and a 2,400-baud modulator that, when coupled with a digital-to-analog converter, modulates the digital bit stream for telephone transmissions. A third n-channel part, a 2,400-baud demodulator, is expected in the fourth quarter.

In linear devices, Motorola recently introduced the MC6880, a quad three-state bidirectional line-driver/receiver and has begun supplying samples of a triple bus switch in three versions. These are three single-throw, double-throw switches with a center-off position for multiplexed microprocessor systems that need to switch either a common bus or a common memory. Also in the works is a priority-interrupt controller that allows up to eight interrupts with direct vectoring to subroutines.

By the end of the year, Toon says, Motorola will also offer several more memories, including a 1,024-by-4-bit static RAM, and a 1,024-by-8-bit erasable read-only memory. □

Stripped-down 8-bit microprocessors bow

With the opening up of mass markets for dedicated microprocessors, semiconductor houses are moving to reduce parts count, simplify the instruction sets and cut the systems assembly costs. It's already begun in the four-bit world [*Electronics*, Feb. 10, p. 132] and now makers of eight-bit machines are getting into the act.

The first of these low-cost, minimum-chip eight-bit devices will come from Electronic Arrays Inc., Sunnyvale, Calif. What's more, Intel Corp. is readying a stripped-down, redesigned version of its 8080—called the 8048 [*Electronics*, Feb. 5, p. 25]. And Motorola is reported to be in development on an eight-bit n-MOS processor that also will contain RAM, ROM, and input/output

circuitry on a single chip.

Dense. Designated the EA9002, Electronic Arrays' 28-pin n-channel MOS unit is a single 5-volt supply, depletion-load device, containing the equivalent of more than 10,000 transistors on a 200-by-201-mil chip. Intel's three-power-supply 8080, by comparison, contains 4,500 on a 192-by-192-mil chip.

Besides a 12-bit program-address counter, a seven-level 12-bit subroutine-address stack, an eight-bit ALU, eight general purpose eight-bit registers, eight four-bit page registers, a 12-bit parallel bus driver and three-state eight-bit parallel bidirection data bus, the 9002—like the Motorola and Intel one-chip versions—contains 512 bits of scratch memory. But unlike those devices, a minimum 9002 system requires an 8-k ROM and two MSI devices for parallel input/output ports.

"The emphasis in our design was on simplicity," says William Wickes, manager of microprocessor development at EA, "and on a machine totally dedicated to the low-end microcontroller market." Key to this concept is a clearly defined and unambiguous instruction set, he says, so the 55 instructions provide complete control capability.

Timing for each instruction is straightforward and consistent. "Every cycle is the same, whether it's an internal operation or a data transfer," says Wickes. "All peripherals are treated as memory locations, so instructions or data can be obtained from any external device."

Available in sample quantities now, Wickes says, the 9002 will be in production by mid-year. Additional members of the family will be available by year-end. □

Automotive

Test program tough for spark advance

Asserting that its new analog "lean-burn" spark-control computer is one of the most sophisticated assemblies



Lean design. Chrysler dealers will use microprocessor controlled diagnostic system.

on a production car, Chrysler Corp.'s Huntsville Electronics division has set up an assembly line test program that would be the envy of any aerospace operation.

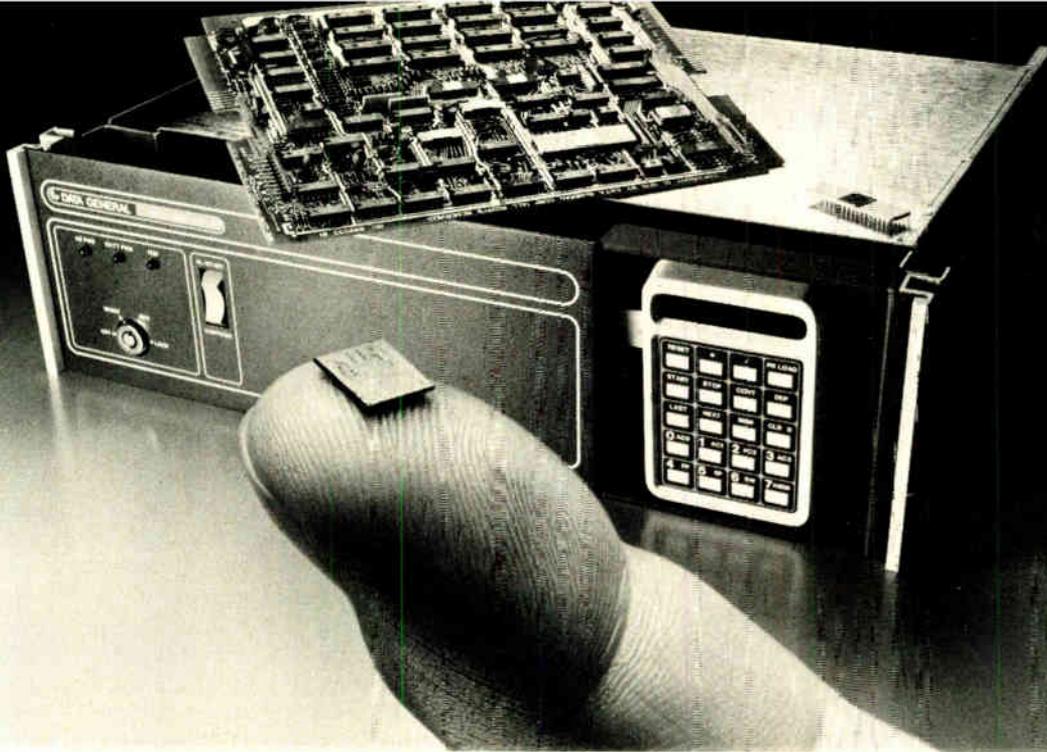
"When Chrysler Huntsville was a space and defense contractor, we didn't have as much testing under computer control with real-time information available to us as we do now with the spark-control computer," says John Webster, manager of electronic-product development there.

Besides testing each unit, the system sends information from the assembly-line test heads to a mini-computer with a disk memory that spews up complete quality-control reports for each work shift. Using this real-time system, engineers can pinpoint excessive components failures or production snags fast.

Simultaneously, the Huntsville division has begun production of a lean-burn system analyzer for use by auto dealers for testing the units in the field. During the 1976 model year, Huntsville will produce about 109,000 lean-burn spark-control computers for the 400-cubic-inch, four-barrel engine [*Electronics*, April 3, 1975, p. 38].

Better burn. The system, after evaluating such parameters as engine speed, temperature, and cylinder vacuum, advances or retards the spark to ensure the efficient burning of fuel at the leanest setting of the carburetor. This adjustment minimizes emissions so that they comply with Government standards without the use of catalytic converters.

Each spark-control computer contains more than 200 parts. The key electronic components are quad operational amplifiers supplied by RCA, Texas Instruments, and National Semiconductor. During as-



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Which was no small accomplishment.

For those who need more than a chip, there's the microNOVA computer-on-a-board. A complete, fully-buffered microcomputer that comes with 2K or 4K words of RAM on a single 7½" by 9½" board. You can add on more RAM in either 4K or

8K increments, or PROM boards with up to 4K words. Plus terminal interfaces, general purpose interfacing boards, card frame, power supply and PROM burner.

And for those who need more than a board, there's a fully-packaged 4K word MOS microNOVA mini. It comes with power supply and turnkey console. In 9 and 18 slot versions. Into which you can place as much as 32K words of RAM or PROM. And still have plenty of room left over for I/O.

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Electronics review

sembly, the computers are tested five different times on the line, and twice after completion. The most stringent test is a 10-hour temperature-cycling that checks performance of each unit through to 185° F.

The entire test operation is controlled by three minicomputers—two General Automation 16/45 models on the production floor and one General Automation SPC 16/66 monitoring the whole show from the engineering department.

One of the production-floor minicomputers, coupled to a video display, handles the three assembly tests, which include measurements before and after automated laser trimming of resistor networks. This machine, which checks laser-trimmer status, as well, records the number of hours the laser lamp burns. In all, a production supervisor can call up from this station some 50 test reports on virtually every component. A second production-floor minicomputer tracks tests performed on finished assemblies.

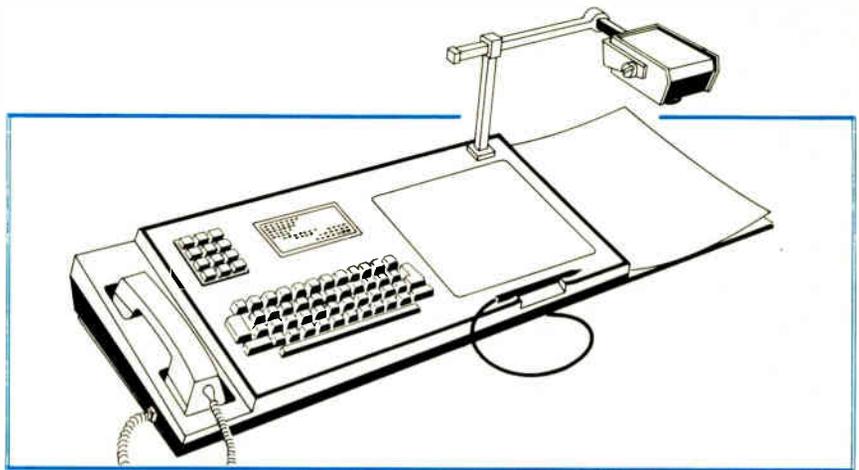
Dealer aid. Built around a Motorola 6800 microprocessor, the dealer diagnostic unit costs about \$700. It tests the entire operation of the lean-burn system via a set of programmed procedures. A mechanic has to follow only 12 instructions printed on the front of the unit and read results from three light-emitting-diode display windows. Two windows show performance readings, and the third shows a diagnostic code number if a part fails.

A major advantage of the 6800 microprocessor in this equipment, according to Chrysler, is that the program and the test parameters can be changed as newer lean-burn systems come out. □

Communications

Bell's visual terminal uses plasma display

Despite their troubles with Picturephone, the thinkers at AT&T haven't given up on two-way visual commu-



New image. Latest lab version of Picturephone sports alphanumeric keyboard, plasma screen, light pen, and solid-state camera. Output hooks into hard-copy printer.

nications. They're merely revising their approach—from emphasis on the speaker's image to terminals designed to send and receive local and computer-generated alphanumeric and graphic data.

To do this, Bell Laboratories researchers Ralph C. Brainard and Thomas P. Sosnowski in Holmdel, N.J., are developing a visual communications terminal around a microprocessor. It has a flat-screen ac plasma-display panel, television camera, telephone data set, keyboard, and a light pen for the interchange of sketches, text, and continuous-tone pictures. Equally important, the experimental terminal uses the existing switched telephone network; Picturephone requires special broadband lines.

Sosnowski says the microcomputer, based on Intel Corp.'s 8080 microprocessor, also contains 8 kilobytes of programable read-only memory and 8 kilobytes of random-access memory for programing flexibility and experimentation. The plasma display panel, 8 inches by 8 inches, contains over a quarter of a million discrete light points or sites arranged in a 512-by-512 matrix-addressed array. Produced for Bell Labs by Owens Illinois Corp., the display has inherent memory and is bilevel; that is, a site is either on or off with no provision for intensity modification.

With the present system, an image is picked up by the camera and displayed in 8 seconds. "The display architecture is such that a minimum of 5 seconds is required to illuminate the entire panel," says

Brainard. The overriding constraint is the time required to send a quarter of a million bits of picture information (one bit per picture element) to a remote telephone over the phone network. Brainard believes this will be acceptable to many users who would want to take their time studying data.

The computer can control the display in several ways. Fundamentally, all that is required by the panel is an 18-bit address (9 bits each for X and Y) and a code to write or erase the site. The keyboard is used for alphanumeric input as well as control ASCII-coded characters, as well as pictures, can be transmitted between terminals. This would permit access to interactive computer systems, for example. □

Military

Seafarer tests may start next month

Despite uncertainty about where Project Seafarer's transmitter and huge transmitting antenna grid will be located, the Navy may begin tests next month that would mark the first use of the extremely-low-frequency (elf) band for communications. Seafarer is the new name for the former Project Sanguine [*Electronics*, Oct. 2, 1975, p. 41], the Navy's attempt at an elf communications system that would permit continuous communications with deeply submerged submarines without exposing them to detection. The

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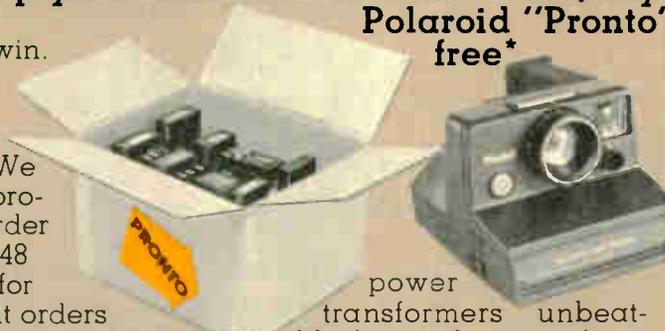
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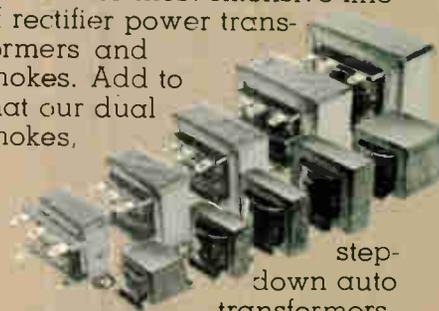


power transformers unbeatable for applications from 2.4 to 60 VA. Here, we not only used bobbin instead of layer winding to save 40-50% in winding space, we developed a special "split" bobbin to eliminate inter-winding insulation and crossover of primary and secondary leads. What's more, we made the core of grain-oriented steel instead of ordinary silicon steels—resulting in a 40% reduction in required turns, and still more weight/cost savings for you.

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SCIENCE/SCOPE

The first recipient of Sweden's new L M Ericsson International Prize for "an especially important scientific or technological contribution to telecommunications engineering" will be Harold Rosen of Hughes. King Carl XVI Gustaf will award the prize of 100,000 kronor to Dr. Rosen on May 5 "for his foresightedness -- against an originally contrary current opinion -- in proposing the introduction of geostationary communications satellites, and for his eminent scientific and technological contributions to their development, design, and operation."

The first scanning optical microscope that will inspect large-scale integrated circuits while they are being operated has been developed by Hughes. It has demonstrated its ability to detect, localize, and identify flaws in complex devices. Unlike other scanning microscopes, which scan only one logic state at a time, the Hughes microscope will effectively superimpose many logic states at one time to "characterize" or inspect the microcircuit. The completely non-destructive instrument scans with a modulated laser and was specifically designed under sponsorship of NASA's Marshall Space Flight Center to meet the high throughput requirements of manufacturers of high reliability microcircuits.

A Roland missile intercepted a jet drone at White Sands Missile Range, N.M., recently, as the U.S. Army began testing two West German-built Roland all-weather short-range air defense systems. Roland, first major foreign-designed weapon system selected for deployment with U.S. forces, protects battlefield troops and equipment and high-value rear-area emplacements against high-speed, low-level air attack. Hughes is prime contractor to the Army Missile Command for the U.S. Roland program.

Hughes needs systems-level engineers. Sonar Systems: design and develop surface and subsurface ASW tactical and surveillance sonar systems for Underseas Systems Laboratory....Communications Systems: define architecture and signal processing concepts for next-generation TDMA communications systems. Requirements: BS or higher degree, U.S. citizenship. Please send resume to: Engineering Employment, Hughes Aircraft Company, P.O. Box 3310, Fullerton, CA 92634. An equal opportunity employer.

New products from Hughes include a CMOS/LSI digital frequency synthesizer that provides up to 1,021 output frequencies from a single crystal; it is designed to increase the performance and reduce the cost of industrial, military, and citizens' band communications equipment; circuit contains an adder for transmit/receive frequency offset adjustment....an X-band traveling-wave tube for military satellite communications earth terminals; using modulation anode control techniques, it operates efficiently at 600 and 1200 watts -- highest power to date for a PPM-focused, X-band CW, air-cooled TWT -- and is available as a component or in a power supply package.

Creating a new world with electronics



Electronics review

uncertainty about the site (see "Seafarer sites and schedule," p. 40) stems from environmentalist concern over impact of elf transmissions on wild life, even though the Navy has presented substantial data showing them to be harmless.

First shipments. GTE Sylvania next month will ship the first elf receiver scheduled for fleet testing under terms of a \$3.9 million contract that's part of Seafarer's propagation-validation study. In all, Sylvania's Communication Systems division, Needham Heights, Mass., is to deliver 15 receivers. Along with associated test equipment, 13 of these will be used "to determine the signal-to-noise ratio of the elf signal when we get the receivers at sea," says George L. Downs, Sylvania's manager of elf operations. "We want to get operational experience

because we're pioneering in elf."

The band between 72 and 80 hertz was chosen for Seafarer because it suffers little from atmospheric attenuation and penetrates sea water about 10 times more effectively than the very low frequencies now used for communications with submarines. A submarine must not only slow down to receive vlf signals, but also trail an antenna uncomfortably close to the surface. With the elf transmissions the antennas can remain hundreds of feet below the surface.

There's nothing exotic about the elf receivers, says David A. Boots, Sylvania's technical director for the Seafarer program. They will be mainly digital, using transistor-transistor logic, and have analog amplifiers and filters. The transmissions are buried in noise, Boots points

News briefs

Motorola, MOS Technology settle patent suit

MOS Technology Inc. of Norristown, Pa. has agreed to withdraw its MCS6501 microprocessor from the market and to pay Motorola Inc. \$200,000 in damages as part of the settlement in the patent-infringement and unfair-competition litigation between the two companies. In addition, MOS Technology and eight former Motorola employees have given back, under court order documents that Motorola contends are confidential.

Although MOS Technology is dropping the MCS6501, which was pin-compatible with Motorola's MC6800, it will continue to produce and market five microprocessors in its 6500 family, which are not pin-compatible with Motorola's MC6800. Furthermore, both companies have agreed to a cross license relating to patents in the microprocessor field.

Cox steps down at Intersil

After 14 years in the electronics business, Intersil president Marshall Cox says that he is stepping aside to decide what to do with the next 14. The 40-year-old Cox announced his resignation, which is effective April 1, last week. No successor has been picked, and a company spokesman estimates the replacement process, which Cox will participate in, may take up to two months. Cox will remain a consultant to Intersil and will retain his equity in the company.

Bell Labs executives move up

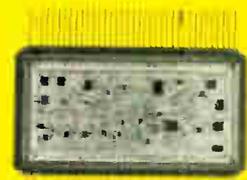
Ian M. Ross, Bell Telephone Laboratories' vice president of network planning and customer services since 1973, has been named executive vice president of Bell Labs, succeeding Morris Tanenbaum, now vice president for engineering and network service at American Telephone & Telegraph Co. [*Electronics*, Feb. 19, p. 84]. Also, Solomon J. Buchsbaum, executive director, transmission systems, succeeds Ross at Bell Labs.

Nigeria buys \$150 million Westinghouse balloon-borne system

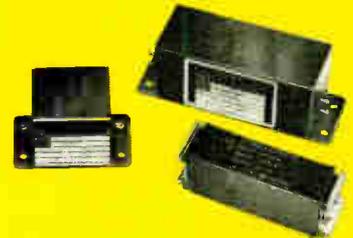
The Federal Republic of Nigeria has awarded a contract for close to \$150 million for a balloon-borne telecommunications and fm television and radio broadcasting system to TCOM Corp., Columbia, Md., a subsidiary of Westinghouse Electric Corp.

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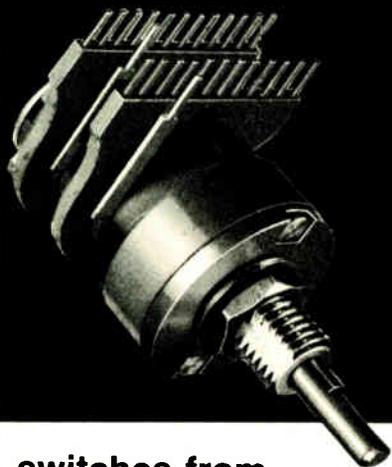
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Electronics review

Seafarer sites and schedule

The Navy and GTE Sylvania still have a long way to go before Seafarer gets afloat, even if fleet tests turn up no extremely-low-frequency propagation problems. For one thing, sites in Nevada and New Mexico [*Electronics*, Oct. 2, 1975, p. 41] are not as desirable as the upper Michigan peninsula. The Navy first became interested in Michigan for Project Sanguine, as it was then called, in 1972 and still prefers it, claiming some \$300 million could be saved by locating the transmission complex there.

But depending on the outcome of the Navy's investigation of the environmental impact of such a grid on upper Michigan, that state may rule itself out of consideration. The National Academy of Sciences, at the Navy's request, will produce an interim report in August covering the biological and ecological implications of Seafarer. And this report will be factored into the environmental impact statement the Navy must deliver the following May.

The Defense Department anticipates making a site selection next April. Actual construction will begin in May 1979 if the program is approved more than 20 years after the concept originated in 1958. Meanwhile, Seafarer watchers are also awaiting Senate Armed Services Committee action on a fiscal 1977 request for funds to start still another proposed elf research effort. It's called Pisces, for Pacific Intertie Strategic Communication ELF System, and would use an existing 851-mile power line from Dalles Dam, Ore., to Los Angeles as a test antenna.

out, and the propagation-validation system "will measure how well the signal gets to the submarine."

Reeling. The sub's antenna, an insulated wire ¾ inch in diameter, is reeled out several hundred feet so it is as far as practical from the vessel's own electrical noise. The elf signal sensed by uninsulated electrodes fitted to the wire, is boosted 80 decibels by a preamplifier.

Both analog and digital filters are used in an analog-to-digital converter stage to further isolate the signal from the noise. High-pass and low-pass filters get rid of those portions of the signal above 80 Hz and below 72 Hz, while notch filters cancel out any 50–60-Hz noise from the sub's electrical system. Another filter handles the sea water's spreading of high-energy noise spikes.

The superficial band-spreading, applied to the transmitted signal to simulate the anti-jamming techniques that an operational Seafarer system would use, can be eliminated. A cartridge recorder and AN/UYK-20 computer for digital processing are part of the validation system, too. The recorder collects such data as noise levels, highest interference signals detected, and the phase of received signals. Ultimately, the transmitted message is printed on a teletypewriter. □

Standard computer set for October

After a slippage of one month and a change in project leadership, the Naval Air Systems Command is calling for quotes on the first 70 preproduction models of its standard airborne computer, the AN/AYK-14(V). Selection of a winner from among the seven expected bidders has also slipped by two months to October, say sources close to the program.

The delays follow a dispute between the Naval Stacom project office and the office of H. Tyler Marcy, assistant secretary of the Navy for research and development. At issue, Navair sources report, was the insistence of Marcy's organization that AN/AYK-14(V) bidders effectively benchmark their systems against Sperry Univac's AN/UYK-20, which is already in the Navy inventory. Stacom program manager Ronald S. Entner reportedly opposed the requirement and was replaced by Navair's Thomas Smith. Entner, still in the Navair branch, did not respond to queries about his removal, while Smith said, "I prefer not to say anything about it."

The Stacom program is now re-

They chose Motorola's M6800 Family to handle tough systems jobs



TEKTRONIX

Tektronix designers were given the challenge of developing a reasonably priced, locally controlled, compact computing system combining convenient storage, large-screen graphic display, and a general purpose interface bus. All goals were achieved with the 4051 BASIC Graphic Computing System. Tektronix chose the MC6800 for the central processing unit.

FASFAX

FasFax was looking for the way to package their P.O.S. system more compactly with a smaller power supply and fewer devices, yet increase system capability. They found Motorola's M6800 Family just what was needed to produce "STANLEY." They're satisfied it's the most cost effective, most adaptable, P.O.S. system now in the restaurant business, and the MC6800 controls up to six units.



CODEX

Implementation of Codex Corporation's 6000 Series Intelligent Network Processors required a microprocessor to perform network management and centralized control, terminal intermix, data compression, and several additional functions in a uniquely executed multiprocessor architecture. Codex elected to use the M6800 Family for its microprocessor performance and its compatible I/O, ROM, and RAM.



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Electronics review

garded as more than a mere gap-filler pending the availability of the All-Applications Digital Computer [*Electronics*, Dec. 25, 1975, p. 52]. Transferred to the Naval Material Command from Navair earlier this year, AADC is still at the research and development stage and years from procurement.

Meanwhile, Stacom is being pushed for use in the Navy's new F-18 fighter and the antisubmarine-warfare helicopter program known Lamps—for Light Airborne Multipurpose System. And in its request quotations on the AN/AYK-14(V), Navair says that beyond the first 70 preproduction models, vendors can expect fixed-price incentive options in fiscal 1978 and 1979 for "various step-ladder quantities [of] from 30 to 720" systems. If the Navy buys the 800 F-18 fighters it says it wants, procurement will run to at least 1,600 machines in view of plans to install two on each aircraft [*Electronics*, Nov. 13, 1975, p. 32].

In addition to Sperry Univac, companies ready to respond to the June 1 bid deadline include Control Data Corp., General Motors' Delco Electronics division, International Business Machines Corp., Lear Siegler Inc., Teledyne Ryan Aeronautical, Westinghouse Electric Corp., and probably Rolm Corp.

What troubles some contenders is the Navy's requirement that any off-the-shelf systems being bid must use "new microprogramming techniques to emulate the AN/UYK-20 Government-furnished support software." As part of the evaluation, the Navy says, each competitor will be required to perform "an equipment demonstration by execution of an AN/UYK-20 object code" limited to the system's instruction set.

Software. Anxious to get the AN/AYK-14(V) on board the F-18, the Navy chose to specify the existing software of the AN/UYK-20, rather than the variant CMS-2 higher-order language identified earlier by Entner, sources said. Entner had also proposed that Stacom's support software be Fortran. He believed that it offered ease of handling and system logistics. □

NATIONAL ANTHEM

A Review of New Products and Literature

from National Semiconductor

WOW! 12-Bit Binary and 3-Digit BCD DACs

Brand new from National . . . a family of precision, low-cost, digital-to-analog converters for a wide range of industrial and military applications. Our DA1200 Series is self-contained in a 24-pin DIP (molded or hermetic); just turn them on and they're ready to go to work for you.

General features of the new DACs include both current- and voltage-mode outputs (0-2 mA, 0-10 V, ± 10 V); $\pm 1/2$ LSB (binary), $\pm 1/2$ LSD (BCD) linearity; 1.5- μ s current-mode, 2.5- μ s voltage-mode settling times; precision, buffered, internal references (10.240 V binary, 10.000 V BCD); standard power supplies (± 15 V, +5 V); TTL/CMOS-compatible, complementary binary or BCD input-logic formats; and expandability to 14 or 16 bits.

The DA1200/DA1201 are the 12-bit binary devices; the DA1202/DA1203 are the 3-digit BCD devices. Terrific specs, terrific parts. Ask for the data sheet and you'll see what we mean. #

LM117-Series Regulators Adjust to Your Needs

Announcing the one and only three-terminal adjustable voltage regulator on the market today! To change an output voltage, change a resistor value. Our LM117/217/317 regulators are exceptionally easy to apply, easy to mount (TO-3, TO-5, and TO-220 packages), and feature line and load regulations better than those of fixed regulators.

Look at these specs:

- Output adjusts from +1.2 to +37 V
- Guaranteed 1.5-A output current
- Line regulation, 0.01%/V, typ.
- Load regulation, 0.1 %, typ.
- Ripple rejection, 80 dB, typ.

And the chip includes full overload protection—current limiting, thermal overload, and safe-operating-area circuitry—which remains functional even when the adjustment terminal is disconnected. Even high-voltage supplies may be regulated because the chip floats; don't exceed a 40-V output-input differential and you stay in business.



So . . . ECL users, TTL users, supply didders (optimize, optimize!) . . . let purchasing stock *one* part—the LM117—and everyone will come up smiling. #

TELEPHONE RELAY DRIVERS



Our high-voltage/high-current positive and negative voltage relay drivers have many features that make them nearly ideal devices. For example, we specify output leakage, over temperature, at an output of ± 54 V (the polarity depends on the device). Again, we specify over temperature and at 5 mA, the minimum output breakdown; there's even an internal reference, which doesn't allow the output breakdown latching you'll find in all other

Our LM148: The Only True Quad-741 You Can Buy

Yep, we've gone and done it and, in this case, more is definitely better. Our LM148 Series is a true quad-741. It consists of four independent and well-isolated (120 dB), high gain, compensated op amps, and provides functional characteristics identical to

relay drivers, and which generally eliminates the usual need for external, inductive-transient protection; yet the output transistor is still *fully* protected.

TTL/DTL/CMOS compatibility; high-impedance pnp inputs; low power dissipation (typ., 90 mW with both outputs on); high output-voltage breakdown (typ., ± 65 V); high output current (300 mA, max.) . . . all these and more make our DS3686 (positive) and DS3687 (negative) voltage relay drivers ideal in telephone relay applications. Check them out for yourself; see what you've been missing. #

those of the 741. Yet the LM148's total supply current (2.4 mA, typ.) is less than that of a single 741; and input offset and bias currents are very much less (typ., 4 nA and 30 nA respectively) than those of a 741. Input offset voltage of the LM148 is 1 mV (typ.); all inputs and outputs are overload protected; and it's pin-compatible with the LM124.

The gain-bandwidth product of the LM148 is 1 MHz at unity gain; the other member of the family, the LM149, is a decompensated, wideband amp—4 MHz for $A_{v(\min)} = 5$. Stability? Super; 60° phase margin for both parts at the gain figures just mentioned.

To sum up: the LM148 and LM149 quads are just what you've been waiting for. Multiple-741 and 1558-type amplifier applications cry out for this part. Wherever you need matched amps or a high packing density, the LM148/LM149 will do the job; where before you had to settle for one or the other, now you get both. That's a little like having your cake and eating it too. Really. #

A Review of New Products and Literature from National Semiconductor

New Bipolar PROMs Ideal for High-Speed Systems

Our new 1024-bit PROMs are Schottky clamped; and this means high speed—a 30-ns (max.) enable-to-output delay and a 50-ns (max.) address-to-output delay into 30 pF across 300Ω. Organized as 256 4-bit words, the memories feature pnp inputs to reduce input loading, and two memory-enable inputs to control the output states.

When both enable inputs are at logic zero, the outputs present the selected

word; if either or both of the enable inputs are at logic one, all four outputs go to their high-impedance (off) state.

The memories are available in a Tri-State® version as the DM54S287/DM74S287, and in an open-collector version as the DM54S387/DM74S387. Both versions are available as either ROMs or PROMs, and are packaged in 16-pin molded and cavity DIPs.

≠

Fast Access Featured in Tri-State®, Si-Gate CMOS RAMS

National's first silicon-gate CMOS RAMs are here. Ideal in microprocessor, minicomputer, and mainframe memory applications, these Tri-State® memories are very-low-power devices with high performance: access times are only 250 ns, maximum. Intended for operation from a single +5-V supply, the memories retain their data at $V_{CC} = 2$ V. All inputs and outputs are TTL compatible; data outputs are of the same polarity as the inputs; and on-chip registers are provided.

The MM54C920/MM74C920 (22-pin DIP) and MM54C921/MM74C921 (18-pin DIP) are organized as 256 4-bit words; the MM54C929/MM74C929 (16 pins) and MM54C930/MM74C930 (18 pins), intended for larger systems, have a 1024 x 1 organization. ≠

qualified on MIL-S-19500/195 (2N2608), /375 (2N3823), /431 (2N4091 series), and /476 (2N5114 series).

All this shouldn't surprise you. After all, we've had, for many years, a fine reputation for MIL-M-38510 parts. We're simply expanding. So whatever your REL FET needs, consider National to be your prime source. Tie in with the leader. ≠

Clock Modules



JUST WHAT THE DOCTOR ORDERED FOR A REALLY GOOD 'MOVEMENT'

National's MA1002 Series electronic clock modules get it all together for you on a single PC board; a MOS/LSI clock circuit; a bright, 4-digit, 0.5-inch LED display (hours, minutes, and blinking colon for seconds); and all necessary discrete components. All you add are a transformer, switches, and an ear-phone alarm-output to form a complete, pre-tested movement that's ideal for desk clocks, alarm clocks, clock-radios, TV or stereo clocks, instrument panel clocks, etc.

Features of the MA1002 Series include a 12- or 24-hour format; 50- or 60-Hz operation with power-fail indication; brightness control; sleep and snooze timers; alarm-on and PM indicators; and fast and slow set controls. Additionally, direct (non-multiplexed) LED drive eliminates RFI, and the module's size—0.93-inch max. thickness x 1.375 H x 3.05 W—allows the finished clock a very compact design. ≠

NSC's REL FETs: Now You Know Where to Go

National is now a REL FET supplier, a simple fact that many of you will be happy to know. We've received qualification on MIL-S-19500/428 for the JAN/JANTX/JANTXV 2N4416A, a part that's been very difficult to get ('til now).

You'll also like to know that we're qualified on MIL-S-19500/385 for the JAN/JANTX 2N4856 through 2N4861—the most popular JAN FETs in today's marketplace—and will soon be



CMOS-Compatible Interface Components

We designed our DS3631/32/33/34 Series of dual peripheral drivers to be a universal set of interface components for CMOS circuits. This means that each circuit has CMOS-compatible inputs (high-impedance pnp transistors); high-voltage outputs (minimum breakdown, 56 V at 250 μA); high-current operation (300 mA, max.) at low internal V_{CC} current levels, with base drive for the output transistor derived from the load proportionate to the needed loading—essential to reduce the load on the CMOS logic

supply; and low V_{CC} dissipation (28 mW, both outputs on at 5 V). An additional bonus is that this family of peripheral drivers is also TTL-compatible at $V_{CC} = 5$ V.

Note, too that we've made the pinouts the same as those of our very popular DS75451, DS75461, and DS3611 parts. All of which means that with our DS3631 Series you can directly convert your present systems to our MM74C CMOS family, and end up with a terrific saving of power. ≠

A Spinning Wheel Doth a Tachometer Need

While originally designed for vehicle tachometer applications, our new monolithic frequency-to-voltage converters turn out to have a wide number of uses. Besides tachometer-related applications in vehicle engines and motors in general—over/under speed sensing and control, speed switches, cruise controls, and automotive door lock, clutch and horn controls—our LM2907/17 ICs make dandy touch and sound switches, capacitance meters, and delay switches (it's not magic; to see how it's done, see the data sheet).

These circuits use the charge pump technique and offer frequency doubling for low-ripple and full-input protection. The load (to 50 mA) may be ground- or supply-referenced. Versions available offer single-ended or differential tach inputs, with or without an on-chip Zener regulator.

The transfer function of the circuits is easy to use: $V_{out} = f_{in} V_{CC} RC$, where R and C are external to the chip; linearity is 0.3% typ., $\pm 1\%$ worst case. New, interesting, and unique, our LM2907/17 will definitely make your life easier. \neq

CLOCK CIRCUITS: Night and Day, These are the Ones

These MOS ICs provide all the logic needed to build many types of digital clocks and timers, notably desk and auto clocks, alarms, clock-radios, stop-watches, timers for industrial, photographic and appliance uses, sequential controllers, etc. The circuits have four display modes—time, seconds, alarm, sleep—and interface directly to LED displays.

The timekeeping function operates from either a 50-Hz or a 60-Hz input, and a power fail/return indication is provided. Outputs consist of display drive signals, a 59-minute presettable sleep timer (which can be used to turn off a radio), and a nine-minute snooze alarm. The display format is either 12 hours (with leading-zero blanking and AM/PM indicator) or 24 hours. These circuits, designated MM5384/5/6/7, operate from a single, unregulated supply (either 8-26 V or 18-26 V), and are packaged in 40-lead DIPs.

APPLICATIONS CORNER Low-Voltage Reference Sources

We've had a number of inquiries from readers asking us how to use a three-terminal regulator as a low-voltage reference source, and how to make that source bipolar.

Our answer to these questions is that three-terminal regulators can be used in such a way, but to produce a lower-than-nominal regulator output requires a rather cumbersome amount of external circuitry.

A far better method makes use of our recently-announced LM117, a three-terminal regulator with an output that is adjustable from 1.2 V to 37 V (story on Page 1). Its use as a low-voltage reference source is shown in Figure 1.

By connecting the 'Set V' pot to a negative reference in Figure 1 becomes the bipolar source of Figure 2. Pre-loading the output to a negative voltage is necessary because the LM117 is a positive-output regulator and cannot sink current.

Still another scheme—this one taken from our LM199 data sheet and shown here as Figure 3—uses the LM199 temperature-stabilized Zener in combination with an LM108A op amp to provide output voltages to ± 6.9 V. \neq

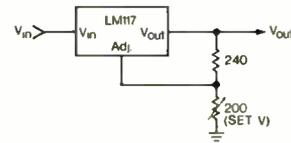


Figure 1. 1.5V to 2.0V Reference

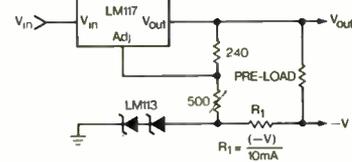


Figure 2. -2.4V to +2.7V Reference

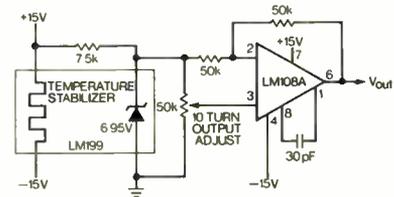


Figure 3. -6.9V to +6.9V Bipolar Reference

MM74C908/918: OLDIES BUT GOODIES

We'd like to remind you about some very popular parts we've been making. They are the MM74C908/918; CMOS, dual, high-voltage drivers that are super parts now being offered at lowered prices.

The MM74C908/918 differ only in power dissipation. At a 70°C ambient, maximum dissipation for the 908 ($\theta_{JA} = 110^\circ\text{C/W}$, max.) is 0.7 W, while the 918 ($\theta_{JA} = 55^\circ\text{C/W}$, max.) will handle 1.4 W. Both parts feature a supply range of 3 to 18 V; a low output on-



Two other 'super clock' circuits, the MM5382 and MM5383, are due very shortly. These displays feature a month-day calendar display and a unique power-failure indication: if there is a momentary ac power failure the word 'OFF' is flashed at a 2-Hz rate until the time is reset. The MM5382 operates in the 12-hour mode (and has a PM indicator); the MM5383 operates in the 24-hour mode. \neq

resistance of 8Ω (typ.); they can withstand 30 V in the off state; they will source 250 mA (min.) at $V_{OUT} = V_{CC} - 3$ V, $T_J = +65^\circ\text{C}$; and have proved invaluable when interfacing normal CMOS voltage levels to relays, regulators, lamps, etc. \neq



NEW TRI-STATE® OCTAL BUFFERS Use Low-Power Schottky Technology

Four new Tri-State® octal buffer ICs that employ low-power Schottky technology are now available in quantity from NSC. The DM81LS95/96/97/98 provide eight, two-input buffers in a single package. One of the two inputs is a control line that gates the output into the high-impedance state, while the other input passes the data through the buffer. Typically, power consumption is less than 80 mW per package with propagation delays less than 13 ns.

The DM81LS95 and 97 present true data at their outputs; the DM81LS96 and 98 invert the data. The DM81LS95 and 96 have eight common Tri-State enable lines, accessed through a two-input NOR gate. The DM81LS97 and 98 have two groups of four buffers each, each enabled by its own common line. Both commercial- and military-grade versions are available from stock.

✽



A MICROPROCESSOR HANDBOOK FOR LOGIC DESIGNERS

We've just published a new book. It's called *A Logic Designer's Guide to Program Equivalents of TTL Functions*, and it's intended to help system designers make the transition from design in hardware to design in software.

While written primarily for current and potential PACE users (PACE is National's single-chip, 8/16-bit microprocessor), much of the text will be useful to anyone unfamiliar with microprocessors and software in general.

The handbook begins with the basics of microprocessing and software, then explains, in detail, the PACE instruction set and its use. More than two dozen simulations are described from both the hardware and software points of view. These simulations range from simple logic (AND gates, etc.) to complex subsystems (digital servo, digital tach, etc.), and are written in language familiar to TTL designers. A series of appendices—a glossary, a description of the hexadecimal number system, powers of two, and many more—concludes the handbook.

A Logic Designer's Guide to Program Equivalents of TTL Functions will be a unique and useful addition to any bookshelf of modern technology. The handbook costs \$5.00, and may be ordered directly from National. ✽



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A Compendium of Recently-Issued Literature (i.e., stuff to file)

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Washington newsletter

Components trade balance down 14% in 1975, U.S. says

The U.S. trade balance for electronic components **dropped 14.1% to \$827.2 million during the 1975 recession**, according to new Commerce Department figures. Exports fell 12.3% from the 1974 level to \$1.99 billion, while imports declined 11.1% to \$1.16 billion. Semiconductors, which accounted for 69% of component imports and 53% of exports, generated a favorable trade balance of nearly \$251 million, up 12.4% for the year. Semiconductor exports totaling \$1.05 billion were off 15.6%, while imports fell 16.5% to nearly \$803 million.

The \$317 million negative trade balance in integrated circuits rose 10.4% from 1974, Commerce said, reflecting increased imports from American-owned offshore manufacturing facilities. Imports from Malaysia, for example—almost entirely ICs—climbed 28.5% to \$163.2 million, compared to exports of \$134.6 million. Japan produced the biggest deficit for the U.S., \$52.1 million, by exporting \$161.2 million of components to the U.S., while importing only \$109.1 million.

Marisat malfunction could cost Hughes incentive payments

Although the Navy is already beginning ship-to-shore service on the uhf wideband and two narrow-band channels it has leased on the first Marisat maritime communications satellite, neither Comsat General Corp., the system's majority owner and manager, nor Hughes Aircraft Co., spacecraft builder, has been able **to get the Atlantic Ocean satellite's C- and L-band commercial channels working properly**. Although tests to correct the malfunction are continuing at Hughes, the contractor stands to lose incentive payments on the program if the commercial circuits cannot be repaired, Comsat General officials indicate.

Adoption of UPC scanner systems suffers setback

Automation of the nation's supermarket checkout procedure with the laser-scanner Universal Product Code has suffered a severe setback. A supermarket industry committee has called for continued price marking of each item, **a practice that the UPC bar code and electronic checkout scanning systems would have eliminated**. "Elimination of item-by-item pricing in favor of shelf pricing was the economic key to getting stores to adopt electronic scanning," one food-industry technology specialist explained. "If item-by-item pricing must be continued, then you can forget about UPC."

Addenda

Electronics manufacturers with offshore assembly operations are mounting **strong opposition to another effort to repeal Items 806.30 and 807** of the U.S. Tariff Schedules. Both the Electronic Industries Association and Wema have testified against legislation that would repeal USTS provisions permitting importation of hardware assembled abroad with duty paid only on the value added. . . . The Navy has named General Dynamics Corp.'s Convair division **to develop its Tomahawk submarine-launched cruise missile for \$34.8 million**. . . . The Air Force has awarded two six-month contracts **for competitive study and preliminary design of the airborne electronic countermeasure system** known as PELSS—for precision emitter location strike system. Boeing Aerospace, teamed with Hughes Aircraft, McDonnell Douglas, and RCA, got \$3.9 million, while Lockheed Missiles & Space Co., teamed with IBM Corp. and E-Systems Inc., received \$3.6 million.

The unresolved issues in interconnection

When the Federal Communications Commission acted in March to drop the last barrier to interconnection of customer-owned terminals to the nation's telephone network, terminal makers were not surprised. Like a pregnant elephant, the FCC had been carrying the issue around for years. It labored long over the delivery, issuing its first ruling last fall when it favored interconnection of data modems and ancillary equipment that qualified under a new certification and registration program. Now the FCC has extended interconnection rights to the last and by far the largest categories of terminals—private-branch-exchange switchboards, main telephones, and multi-line key telephones.

But despite months of intellectual preparation, telecommunications-equipment makers were not altogether ready for the FCC's delivery of its opinion when it came. Emotions ran high for good reason. The FCC decision opens up an interconnect market that generated product sales of \$4.2 billion for Western Electric Co. alone in 1974. The American Telephone & Telegraph Co. manufacturing arm controlled about 80% of the Bell System market that year, limiting other manufacturers to sales of about \$900 million. The North American Telephone Association, which represents makers of interconnect hardware, calls the latest FCC action "the most significant and definitive since the commission's Carterfone decision in 1968."

Winning the war

But interconnection is not yet a *fait accompli*. The nation's telephone companies do not intend to surrender control over their equipment market easily. The U.S. Independent Telephone Association is challenging the FCC ruling in a Federal appeals court, while AT&T reads the decision as one more weapon with which to get Congress to rewrite the 1934 Communications Act to restore its monopoly position.

Though Congress has been unresponsive so far to the pleading of the telephone companies, terminal-equipment makers should not take interconnection for granted. Beyond the efforts of NATA, lobbying against the AT&T "reform" bill will have to be largely on a company-by-company basis since the Electronic Industries Association's Communications division is hamstrung by having AT&T as a member.

Those who favor a competitive interconnect

market would do well not to let themselves be trapped into responding to issues defined only by the telephone companies. In its fight against competition in terminals, AT&T can be expected to use much the same language as it used against competition in services. AT&T believes telephone companies are being placed in the posture "of Gulliver among the Lilliputians. Some of their most profitable business is being taken from them, and they are not allowed to respond." Eugene V. Rostow, now a Yale law professor and AT&T consultant, told Congress that "this is an absurdity which would give the nation the worst of both worlds. It is not a policy of competition, but of its opposite—market-sharing, restrictive rules, cartelization, and mercantilism."

Despite Professor Rostow's distinguished prior service as chairman of the President's Task Force on Communications Policy a decade ago, his melodramatic characterization of the issues is wide of the mark. As the president he served might have put it, "That dog won't hunt."

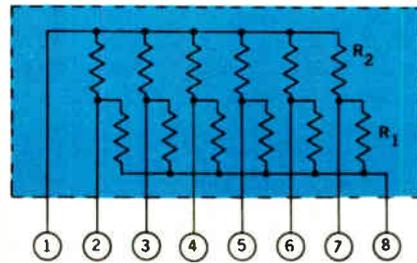
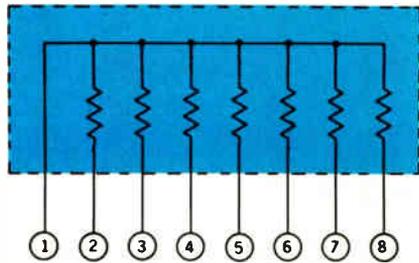
Defining the issue

The issue fundamental to free interconnection and competitive specialized services is whether the *de facto* monopoly of AT&T and the independents in the market for basic telephone service should be extended automatically to each new telecommunications service as it is developed. The answer to that question must be no. AT&T cannot possibly respond to every specialized need, be permitted to control every specialized market that comes along. The interconnection of new, special-purpose terminals to the telephone network can be expected to supplement, rather than supplant, the basic services now available from common carriers. And any telephone company that wants to compete in these new submarkets should be permitted to do so at prices that at least cover their true costs.

But as the Gulliver of the world's telecommunications industry, AT&T should not be permitted to abuse its power by destroying its Lilliputian competitors at the start. Telecommunications technology is moving faster than AT&T can economically employ it. Therefore, interconnection must be permitted to flourish if the U.S. is to maintain its position as an innovator and leader in the world telecommunications market.

—Ray Connolly

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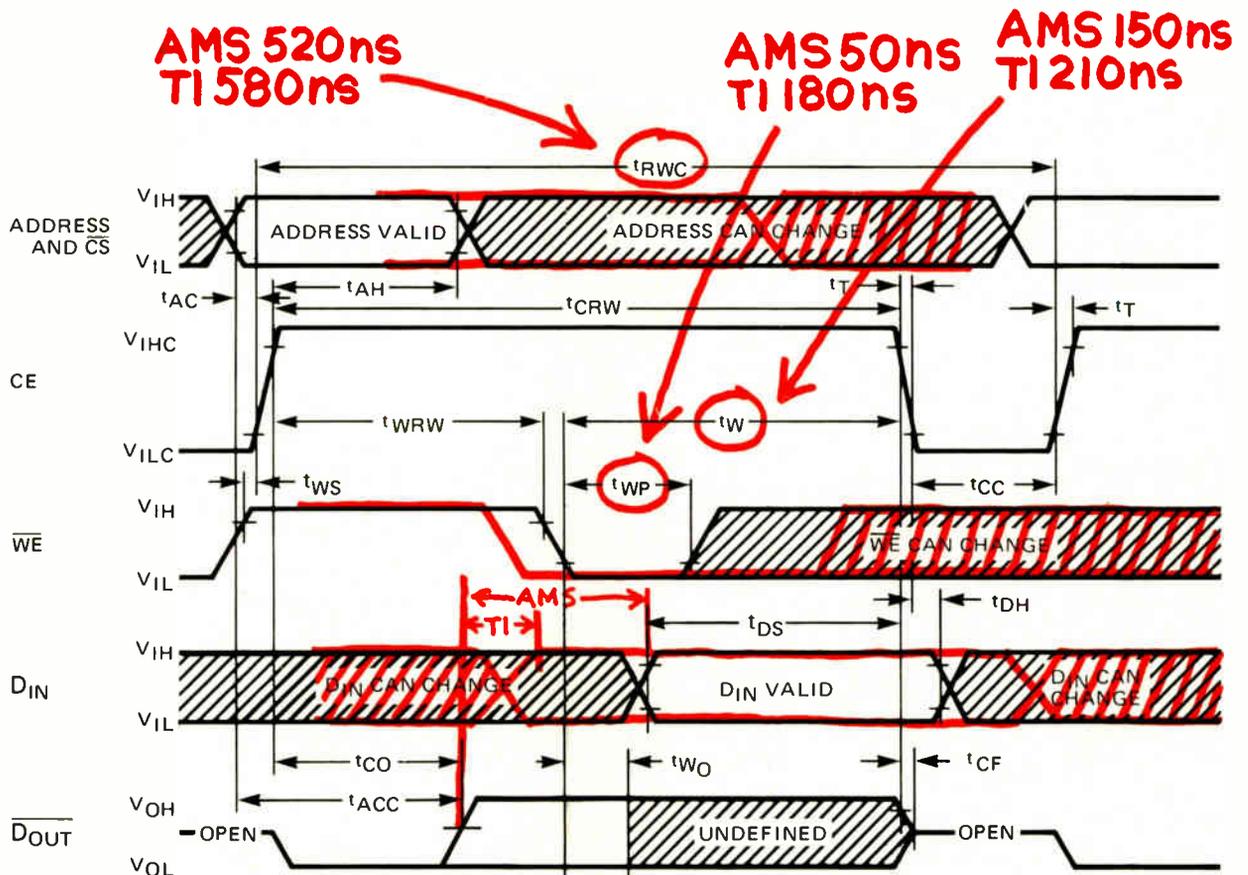
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No lack of sources.

The masks for our 22 pin 7280 and 18 pin 7270 are currently in production at National, too. Not only spec compatible, but mask-for-mask. You'll never be caught in a production/supply bind.

One of the largest RAM suppliers in the world.

We have shipped more than eleven million RAMs. In ceramic and plastic DIPs, in custom and standard card assemblies; in subsystems with or without power supplies; and in large scale memory systems. We're the largest independent add-on memory supplier in the world; and that RAM experience is ready to work for you.

Pick a spec.

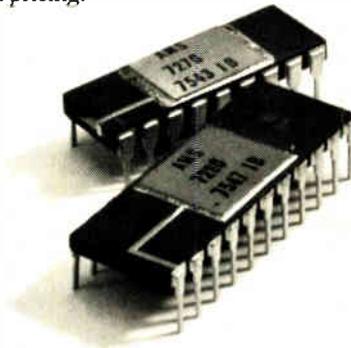
These are only highlights, but you can readily see how TI ends up in the red.

	22 pins		18 pins	
	AMS 7280	TI 4060-2†	AMS 7270	TI 4050††
t_{AH} Address & CS Hold Time	50	150	50	150
t_{WP} \overline{WE} Pulse Width Time	50	180	50	180
t_W \overline{WE} -to-CE OFF Time	150	210	200	210
t_{DH} D_{IN} Hold Time	0	40	0	40
t_{DS} D_{IN} -to-CE OFF Set Up Time	150	210*	150	210*
min RMW cycle	520	580	580	600
t_{mod} @ min cycle	20	20	80	20
t_{mod} @ TI's min cycle (580 ns)	80	20	(600 ns) 100	20

*data must be valid on \overline{WE} going low
†pin-for-pin compatible
††not pin-for-pin compatible

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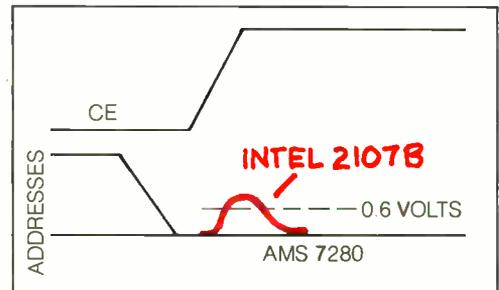
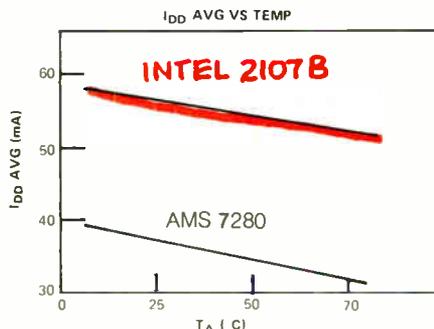
*Tri-Share is a registered trademark of National Semiconductor Corporation.

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To observe the glitch in the far right graph, hang a probe on an address line of an Intel 2107B board. Watch it shoot above 0.6 Volts. And watch it go away with 7280s.

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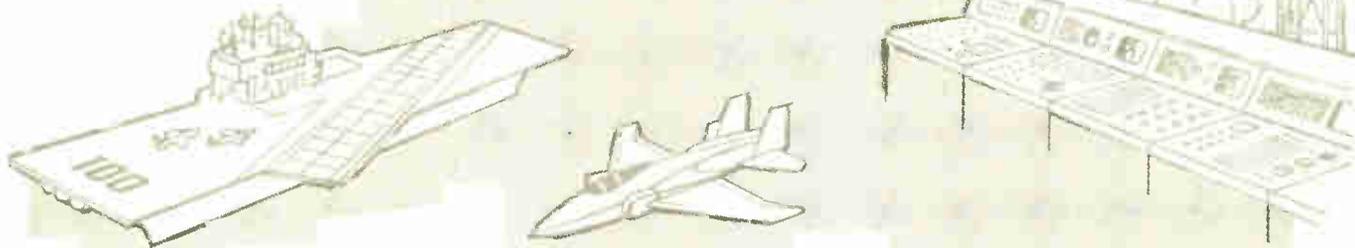
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- MIL-F-23419B
- MIL-F-5372C
- MIL-F-5373C



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Japanese system scribes images from TV camera into identification cards

An electronic engraver for quick production of pictures on identification cards combines the principles of electronic photoengraving and photography. The subject is photographed by a vidicon camera, and the image is scribed on the card by an electronic scanning process. The so-called portrait-engraving processor was developed by Matsushita Research Institute Tokyo Inc. with the aid of Dai Nippon Printing Co., which has patented the plastic cards in Japan and the U.S.

The system is much faster than photographic reproduction. Because the cards are not laminated, magnetic-tape stripes can't be erased by heating them above the curie point. And since the image is an intrinsic part of the card base, it cannot be changed, nor can a new one be pasted in. Although color photography is impossible with the system, any convenient dark color can be used. The white part of the card can also be printed or engraved.

Storage of the image in digital memory as a single TV frame also makes possible other applications. It can be read out at any clock rate below 1.5 megahertz, including slow-speed transmission over telephone lines. A similar unit at the other end can display the image on a monitor or engrave a card.

Engraving. The processor consists of a standard black-and-white TV camera with video monitor, a control-and-memory unit, and an engraving unit. When the camera picks up the image and displays it on the monitor, the operator presses a button to send the video signal to a digital memory.

In the engraver, the identification card is clamped to a table that scans the diamond cutting stylus from top to bottom, with a horizontal displacement between parallel scans spaced to engrave five lines per millimeter. The stylus cuts grooves of

varying depths on a rectangle of 17 by 21 millimeters in black or a dark color on the white plastic card.

An overscan of 20 by 24 mm prevents a dark border from appearing if the photo registration is imperfect. Light shades and white are obtained by increasing the depth of the cuts.

Automatic circuits compare signal levels over the facial region and adjust them so that the facial highlight is set at the lightest density and the darkest color is set at the darkest. The stylus reproduces 16 shades, which are adequate for positive

identification. Each picture element requires 4 bits of memory for the 16 density levels. The memory capacity of 48 kilobits is obtained by connecting in series four loops of 12 1-kilobit shift registers. The memory produces with 4-bit precision the 12,000 picture elements needed.

The picture elements are stored in horizontal lines for the video monitor, but a buffer memory stores the signal vertically to drive the stylus. The 4 bits representing one picture element are read out in parallel during a single clock cycle. □

Around the world

Plessey makes bipolar logic for blg controllers

Plessey Semiconductors is about to introduce a new bipolar-logic series of circuits specially designed to replace, almost on a plug-in basis, the complementary-MOS circuits and their interfaces in industrial controllers. The SP530 family can also replace the discrete logic in digital control systems in high-noise, high-voltage environments, but it does not directly drive large machinery. The five chips can handle crude direct-current power supplies of 30 to 60 volts, tolerate line surges up to 80 V, and have breakdown voltages up to an unusually high 90 V, the firm claims. And the chips can put out from 35 to 75 milliamperes, enough to directly drive ordinary industrial switches, unlike C-MOS circuits, which typically yield 5 to 10 mA.

Because the SP530 needs few additional components, its reliability is high. These chips, together with a microprocessor and Plessey's new SL 1200 series of extremely low-noise amplifiers, could be developed into a complete electronic monitoring and control system for automobiles.

System prevents traffic congestion on autobahn

West Germany's first computerized traffic-control system for the open road is being tested in the Rhein-Main region on all autobahns—those multilane freeways that resemble interstate superhighways in the U.S. A process computer from Siemens AG evaluates detector-derived traffic data and determines alternate autobahn routes when a particular section is jammed. Alternate routes are flashed by an attendant to remotely controllable road signs so that drivers are guided around sections slowed by heavy traffic.

The trial area, about 30 miles in diameter, is in the Frankfurt-Darmstadt-Wiesbaden triangle, where autobahn traffic from all over Germany and other parts of Europe converges. The system consists of the signs at and near the autobahn interchanges, about 600 strategically spotted inductive-loop detectors, a number of data-transmission links and the Siemens VSR 16030 computer, installed in a roadside control center.

The detectors, which are embedded in the road surface, count the vehicles, determine their speed, and ascertain whether they are trucks or passenger cars. The type of vehicle is considered when rerouting traffic on alternate routes with hills. The computer processes the detector data and displays the number, density, and speed of vehicles at the interchanges.

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World Radio History

Circle 54 on reader service card

Toshiba seeks to slash solar-cell costs by 99%

Japan's Toshiba hopes to slash costs of silicon solar cells by 99% to 50 cents a watt by its new method of vertically pulling ribbons of a silicon melt in a crucible. In the laboratory research, being subsidized as part of the Japanese government's Sunshine project, ribbons 300 micrometers thick, 30 millimeters wide, and 800 mm long have been pulled. Measured efficiency is 9%. However, by refining the controls, Toshiba hopes to get the thickness down to 100 μm . Thin ribbons and high rates of growth both contribute to low prices.

To control and minimize the ribbon's thickness, Toshiba tapers to knife edges the tops of the two inner sides of two vertical carbon plates through which the silicon is pulled. The ribbon grows straight upward through these sharp inside edges. Tyco Mobile in the U.S. also controls ribbon width by means of the top edges of two carbon plates, but the top edges are square. When the ribbon emerges, it wets the tops of the plates and grows straight upward from the outside edges, adding both plate widths to the thickness.

Bosch licenses two U.S. firms to make helical-scan VTR

West Germany's Robert Bosch GmbH has licensed RCA Corp. and International Video Corp. in the U.S. to market and manufacture the BCN-format helical-scan broadcast video-tape recorder. Developed by Bosch's Television division, the BCN equipment directly records color video signals of broadcast quality on 1-inch tape. **The equipment, only about one third the size and weight of conventional quadriplex systems, requires only a third as much current and tape.**

The licensing of the two U.S. firms closely follows a Bosch-Philips agreement to jointly promote the BCN system and to combine their engineering and manufacturing capabilities to develop it further. The PAL/Secam version of the BCN system was introduced by Bosch at the Montreux Television symposium 1975. In the meantime, the NTSC version was recently shown to the broadcasting industry at the NAB convention.

UK firm enlarges customer base in Singer buy . . .

International Computers Ltd. is increasing its customer base by more than a third with its \$2 million down payment for the overseas business of the Singer Business Machines division. The complex deal will boost ICL's overseas business by 50% and add to its 3,400 customers another 1,300 in Europe, Latin America, South Africa, and Australia.

ICL, which is to pay only for the inventory and spares it wants during the next four years, **also has the option of marketing and possibly manufacturing Singer's System-10 small business computers, model 1500 intelligent terminals, and point-of-sale equipment.** ICL is also joining the Control Data Corp. British subsidiary to form a computer-peripherals company. In the U.S. and Canada, TRW Inc. is taking over customer support for Singer's point-of-sale and System-10 equipment.

. . . and launches new medium computer

Filling a gap in its successful 2900 computer series, Britains' International Computers Ltd., is introducing the modular 2960 medium-scale system, intended for government, university, and branch-business accounting applications. The 2960, built with the "new range" architecture of the larger two-year-old 2970 and 2980, **has a main-store capacity of 512,000 to 3 million bytes and a store-block cycle time of 900 nanoseconds.** The

2960, priced at \$1 million to \$2.3 million, uses Schottky transistor-transistor logic in the central processor, instead of the faster emitter-coupled logic used in the 2970 and 2980.

Asahi Glass to sell National products

Asahi Glass Co., Japan's leading maker of television-picture-tube bulbs, has been selected as nonexclusive agent here for National Semiconductor Corp. products from the U.S. Asahi formerly represented Signetics, but when Philips of the Netherlands bought that U.S. semiconductor maker, it decided to market Signetics products directly. To service National, Asahi plans to add to its staff of experienced semiconductor salesmen.

The Japanese company will also carry notes for the small firms that have been selling National products but don't have the resources to cope with the common Japanese practice of stretching out payment for as long as four months.

Swedish firm split in try to regain profitability

The state-owned Swedish electronics manufacturer, Sonab, which lost about \$10 million last year, is being reorganized into four independently managed companies in an attempt to regain profitability. One of the new companies will make personal paging systems; another, entertainment audio equipment; the third, mobile-radio systems, and the fourth will subcontract work from the other three, as well as outside jobs.

Stig Carlsson, developer of the omnidirectional stereo speaker system that Sonab was originally created to produce, is negotiating with other electronics firms interested in producing his speaker. However, Sonab has exclusive rights to the speaker through Oct. 1, 1979. **Despite its production of what many enthusiasts claim is one of the world's best speakers, Sonab's hi-fi sales have dropped in the past two years, while the Swedish market has nearly doubled.** Carlsson charges Sonab management with wasting millions of kroner. After a feud involving unions, management, and the state holding company, Sonab managing director Staffan Haakanson has resigned.

Italian navy tries fiber-optic system for transmissions

The Italian navy has started to take a serious look at fiber-optic data-transmission systems for warships. The navy expects to have an experimental system working ashore by the end of the year. **Initially it will be operated at a slowish 35 kilohertz, then will be stepped up to 100 kHz.** The system, which will give two terminals direct access to a minicomputer memory, is scheduled to get sea trials toward mid-1977. CGE-FIAR is handling the electronics, and FACE Standard, an ITT affiliate, the optical link.

Telefunken to show digital TV tuner

West Germany's AEG-Telefunken is about to introduce a digital television-program memory that can be preprogrammed to store as many as 16 different TV channel numbers. The DSP 190, built around a phase-locked loop, will be shown at the Hanover Fair, April 28 to May 6, and be available thereafter. **For high accuracy and stability of the tuned frequencies, the PLL compares the frequency of the tuner oscillator with that of a quartz oscillator.** A channel is preselected by setting the switch to the desired channel number and pushing a "store" button.

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11/04 can now manage 28K of core or 28K of MOS memory.

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The 11/34 comes in the exact same sizes as the 11/04. 5¼" x 19" x 25". Or 10½" x 19" x 25". It's the same color, has the same number

PDP-11/34

Price: From \$6,318*

Word Length: 16 bits, plus byte parity

DMA Rate: 1.4M words/sec

UNIBUS™ Rate: 2.5M words/sec

Addressing Space: 128K words

Memory:

16K, 32K, 48K, 64K, 80K, 96K, 112K, 124K words MOS memory

16K, 32K, 48K, 64K, 80K, 96K, 112K, 124K words core memory

Contains Standard PDP-11/04 — 11/34 Features:

Direct Memory Access

Vectored Automatic Priority

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Virtual Programmer's Console

ROM Diagnostics

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UNIBUS™ architecture

More Than 400 Instructions

Operating Systems: RT-11, RSX-11M, RSX-11S

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of buttons, identical back plates, cord position and shape. The difference can't be seen.

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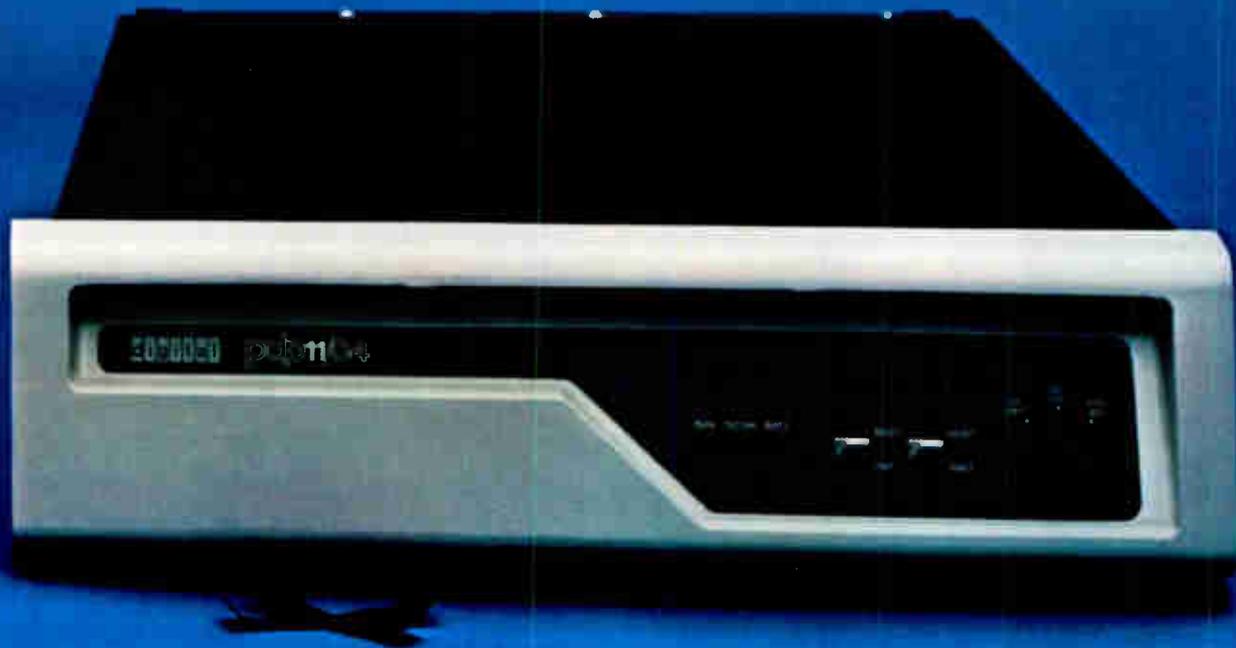
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Price: From \$2,581*
Word Length: 16 bits
DMA Rate: 1.4M words/sec
UNIBUS™ Rate: 2.5M words/sec
Addressing Space: 32K words
Memory:
8K, 16K, 28K words MOS
memory
8K, 16K, 28K words core
memory

Contains Standard PDP-11/04 –
11/34 Features:
Direct Memory Access
Vectored Automatic Priority
Interrupt
Virtual Programmer's
Console
ROM diagnostics
Power-Fail/Restart
UNIBUS™ Architecture
More Than 400 Instructions
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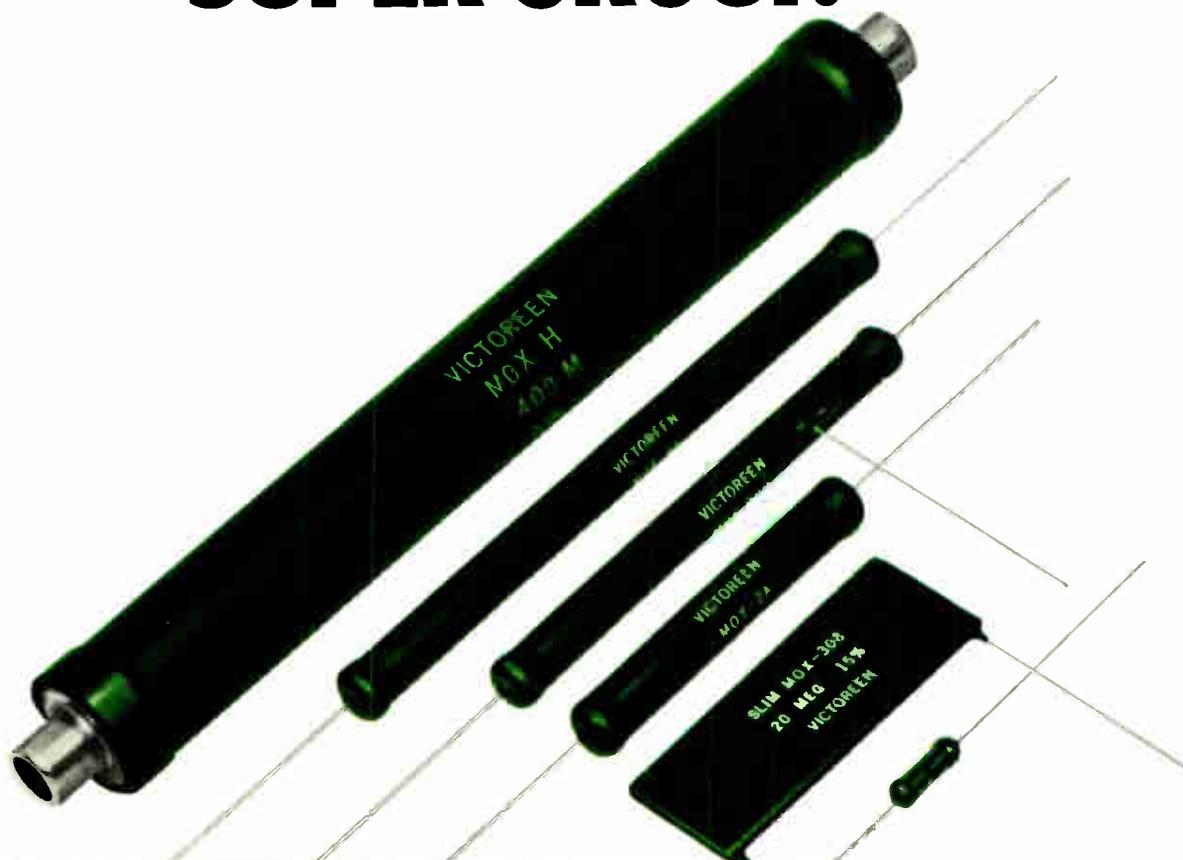
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Divider-Mox 	4.5 M Ω to 2000 M Ω	1.5 W to 6.0 W	7.5 kV to 30kV	± 100 ppm to ± 1000 ppm overall TCR Tracking ± 25 ppm	Length 2.2 to 5.2 Dia. .345
Maxi-Mox 	10 k Ω to 5000 M Ω	1.5 W to 12.5 W	7.5 kV to 37.5 kV	± 100 ppm to ± 500 ppm	Length 1.122 to 5.2 Dia. .310 or .345
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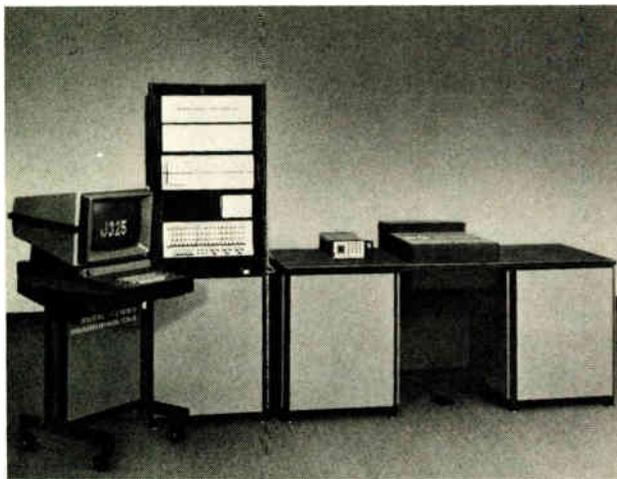
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Spring: Paris show and optimism

Signs of upturn are budding all over Europe's components business as consumer sector blooms; but recovery in other areas is still delaying

The atmosphere last year at the annual Paris Components Show was something like a wake. Any gaiety there merely glossed over the dismay about dear, departed order backlogs. But this year's show figures to be more like a coming-out party because the signs point to an upturn for the components business in West Europe.

To be sure, marketing men from the 1,000-odd companies with booths at the show (April 5 to 10) won't be chorusing "happy days are here again" as they troop into the exposition halls at the Porte de Versailles. But color-TV sales have perked up, and set makers are buying more components. That's enough to start turning the market around, since the consumer area is a kingpin in Western Europe.

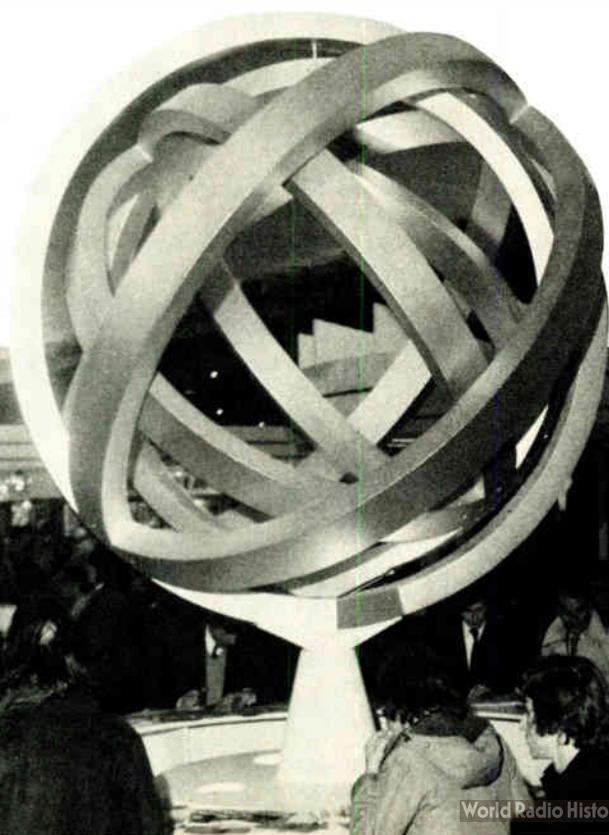
Better yet, the upturn so far has been strongest in West Germany, the pace-setting economy in the nine-nation European Economic Community. Almost all German marketing managers agree that their home components industry is on the rebound, and some foresee a stronger boom than the one in 1973-74.

Semiconductor suppliers suffered worst during the slump but are surfacing fastest now. Erich Gelder, marketing manager for integrated circuits at Siemens AG, notes that bookings have been running much higher than billings during the past few months. "The volume of incoming orders is beyond our expectations and signals a real upswing," agrees Gerhard Liebscher of Intermetall GmbH, part of the ITT Semi-

conductor group. Fritz G. Hoehne, manager for world semiconductor sales at AEG-Telefunken, reports that bookings for his firm are heavy, too.

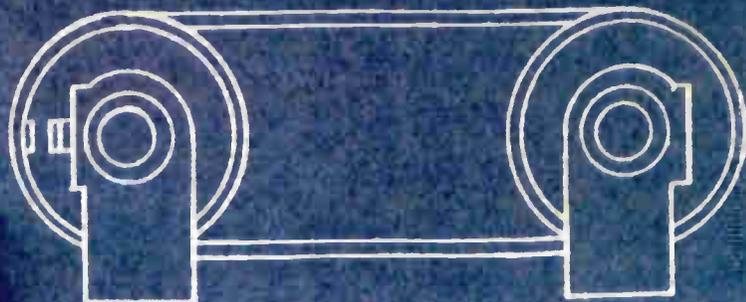
"It is primarily color TV, but also radio and hi-fi, that's providing the push," comments Hoehne. Production of color sets this year will hit 2.6 million units, predicts the electrical-electronics industries trade association ZVEI. That's up substantially from last year's estimated 2.1 million. With set builders' prospects like that, Hoehne is looking for a rise of something between 15% and 17% for semiconductor sales in Germany. Consumer analog circuits and ICs for remote controls are in for a big lift, so much so that users are now getting nervous about deliveries. Optoelectronics devices are profiting, too, since they're turning up in dials and in remote TV sound systems.

Turn from entertainment electronics, though, and the outlook dims fast. Orders from the industrial sector are "still unsatisfactory," Liebscher, Hoehne, and Gelder agree. "But it's showing signs of re-

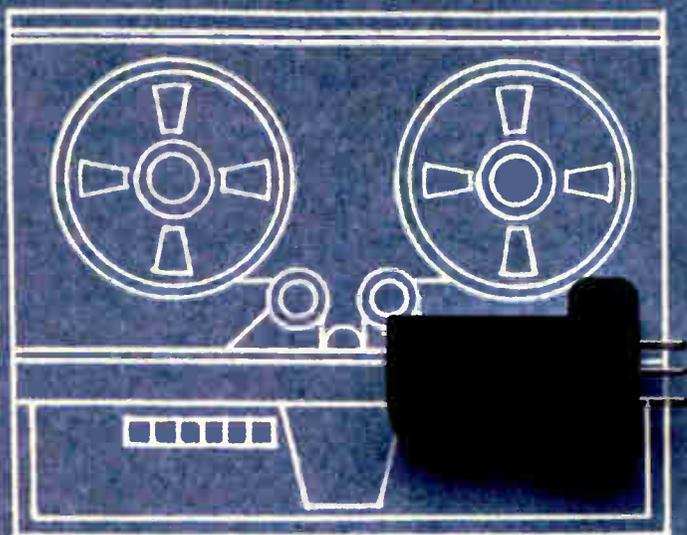


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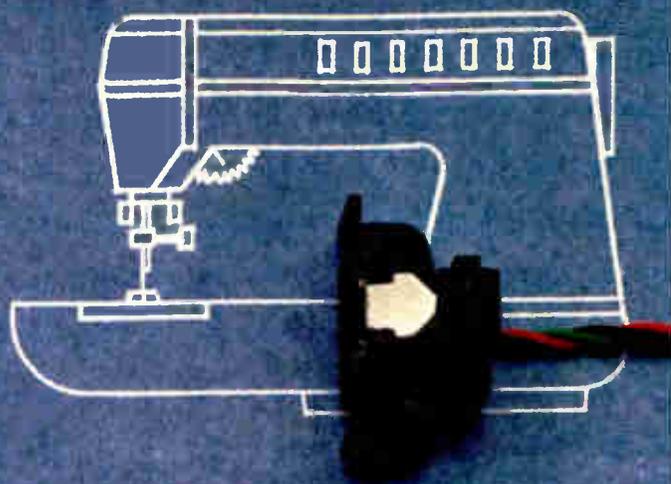
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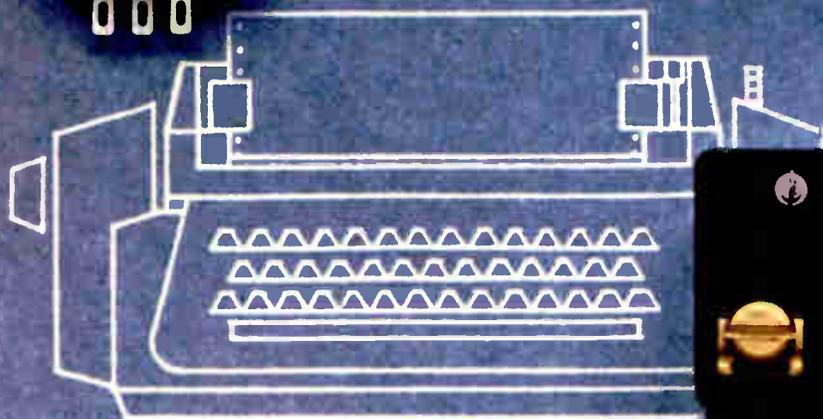
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Probing the news

covery," says Siemens' Gelder. For his part, Intermetall's Liebscher, who says the auto industry is also a plus factor, is hoping for an improvement during the second half for things like transistor-transistor logic and power semiconductors.

France's changes. Much the same outlook prevails in France, only a little less so. "We can say we have the feeling—but not the certainty—that there's been a new departure," says Pierre Mestre, who heads Thomson-CSF's components group.

For Thomson-CSF, France's largest components maker, and for most other producers as well, a new departure means an upswing after a very bad 1975. Mestre's group, for example, saw its sales dip 4% to roughly \$310 million and its profits turn to losses. (Other components-producing units in the Thomson-Brandt group—mainly for TV tubes, ferrites, relays, and transformers—did roughly \$150 million last year, so that overall the parent group was up slightly for the year in current francs, down slightly when inflation is figured in). As Mestre put it, "1975 is a bad memory that we'd like to forget." To exorcise the memory, the components group has budgeted a 15% sales rise this year.

Last year left a bitter taste in the corporate mouth at RTC-La Radiotechnique-Compélec, too. RTC, a Philips group company that ranks second in France among components makers, saw its sales slide 8.5% to roughly \$260 million.

As in Germany, the most cited signal that an up cycle has started is the strong performance of the color-TV market. At the Fédération des Industries Electriques et Electroniques, forecasters now put their estimate for color-set sale at around 950,000 sets, up a gaudy 100,000 sets from last year. Says an FIE official, "We registered a spurt starting the end of August, and we've stayed at a high level ever since."

But again as in Germany, other sectors are labelled "remain to be seen." Now that the Honeywell-Bull-CII fracas has been resolved, the computer sector should liven up. But the massive switching orders ex-

pected from the government-run phone network are still tied up in politics; the business that will eventually come from them seems unlikely to filter down to components makers much before late this year. As for the industrial sector, "We're paying for the lack of investment by industry in 1975," observes Daniel Ameline, head of RTC's Semiconductor division.

UK further away. As yet, there's hardly a wisp of smoke to signal that the fires of the British economy have been rekindled. But there's conviction in some quarters that something might start happening late this year. K.G. Corfield, deputy chairman and managing director of the ITT-owned Standard Telephones and Cables, for example, predicts a distinct upswing for the British economy in late 1976, then a takeoff in 1977 paced by consumer durables, entertainment electronics, and light industrial machinery.

A more cautious view is held by Ken Bradshaw, sales director, Plessey Semiconductors. The market is "still soft," in his view, with no chance of any basic growth "before the third or fourth quarter this year." Although there are signs here and there of an upturn, Bradshaw's "not sure whether it's salesmanship or buoyancy in the market *per se*."

Unsunny Italy. Until the crisis of the lira escalated into outright panic in mid-March, Italian component makers thought they had a real pickup going for them. Now that the value of the lira has plummeted some 30% and the government has patched together a package of restraints to cool the feverish economy, nobody's quite sure.

Just before things went from bad to worse, Enrico Villa, planning manager for the Italian semiconductor house SGS-ATES, happily reported that "for five months now the number of incoming orders has been about 20% higher than shipments." At the beginning, there was some doubt over how much of this was because of real growth and how much merely the result of stock-rebuilding. "Now we know for sure that color-TV consumption is actually going up," he said. □

Contributing to this article were William F. Arnold, John Gosch, and Andrew Heath. It was written by Arthur Erikson.

Microprocessors

European gold rush under way

Local semiconductor houses want piece of market pegged at \$150 million by 1980, while American makers rush to line up second-source deals

by Arthur Erikson, Managing Editor, International

Western Europe is a far cry from the Klondike, but all the same there's a gold rush of sorts on there among American and European microprocessor makers.

The lode they're after is formidable, indeed. Last year's sales of microprocessor chips—central processing units, memories, input/output controllers, and the like—ran something like \$10 million. At least that's the opinion of four of the heavyweights in the market. The figure will bounce up above \$20 million this year, they expect. By 1980, \$150 million looks likely, and by the mid-eighties the market should top \$750 million a year, perhaps by a lot if European entertainment-electronics producers take to microprocessors as fast as semiconductor suppliers hope they will.

Even so, the band of prospectors is so large that not everyone can

count on striking gold. So far, Intel Corp., as in the U.S., has staked out the largest claim in Western Europe, and the list of would-be claim jumpers adds up to the roster of all of Intel's major American competitors. The scramble for market shares figures to be even more hectic there than here. Lured by the burgeoning market, Europe's semiconductor houses are getting into microprocessors fast, either with their own designs or with second-source chips. At the April 5-10 Paris Components Show, for example, the Philips group, Siemens AG, AEG-Telefunken, SGS-ATES, and Thomson-CSF's Sescosem division will all have microprocessors to talk about.

Siemens has set its sights the highest. Erich Gelder, marketing manager for integrated circuits, believes the company can nail down half its West German home market

by 1980 and win "adequate" shares in other West European countries, enough to have between 20% and 25% overall.

To score that high, Siemens will field technology from both sides of the Atlantic. It has signed a contract with Intel that calls for cooperation in both microprocessor hardware and software, with second-sourcing both ways. So Siemens now offers its versions of Intel's standards—4004/4040, the 8080, and the 3002—and one of its own, the 4-bit SAB 4080. "In addition to a complete program of standard circuits, our aim is to have an array of applications-oriented devices of our own design, complete with input/output circuits," Gelder points out. Siemens has also thought about software and has gone to market with an entire programming station.

Philips Gloeilampenfabrieken, too, is convinced it can propel its wholly owned U.S. semiconductor house, Signetics Corp., into a strong market position. Far and away the largest Europe-based electronics group, with total sales of over \$10 billion last year, the Dutch firm has a components-sales force that blankets Europe. That, plus an inside shot at a big captive market, gives Signetics considerable market muscle. "As these new salesmen get trained, I think we will dominate the European market," enthuses George Rigg, Signetics' microprocessor manager in the U.S.

Back in Eindhoven, at Philips headquarters, the optimism is more muted. Kurt Noach, marketing manager for microprocessors at the Elcoma division, sees a chance for Signetics to skyrocket from next to

Think soft. Siemens, in addition to second-sourcing Intel and offering its own processor, is going to try the European market with this complete microprocessor programming station.



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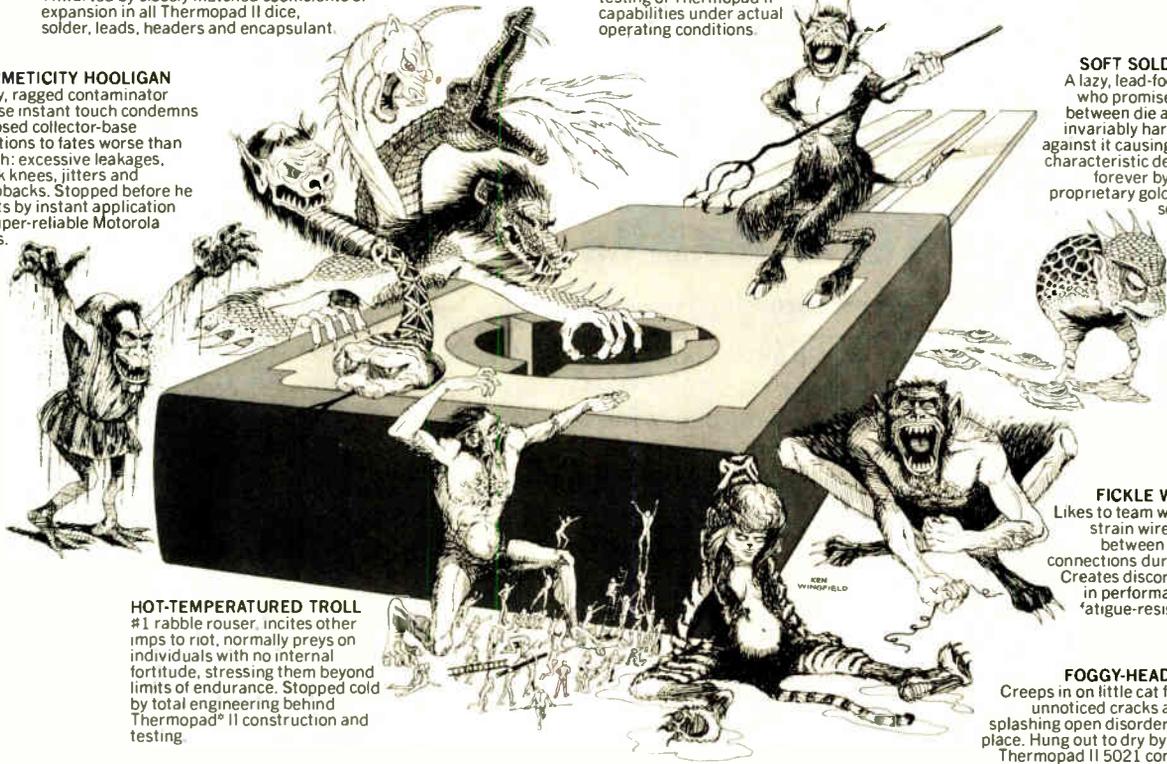
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C-MOS. Although details are sketchy, Signetics Corp. is considering a

rays, rather than the traditional resistive arrays.

Electronics/April 1, 1976

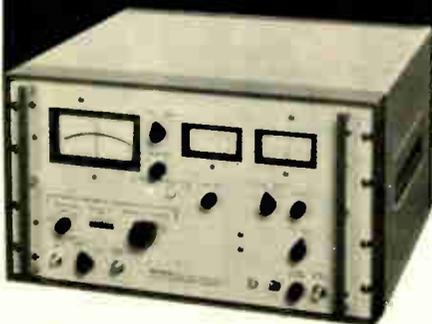
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Communications

Courts get FCC ruling

Common carriers to appeal decision that phones may be hooked to network without protective devices

by Ray Connolly, Washington bureau manager

A counterattack is forming against the Federal Communications Commission's decision to remove the last barriers to so-called foreign attachments for the telephone network. For that reason, independent telephone-terminal makers are proceeding slowly with marketing plans.

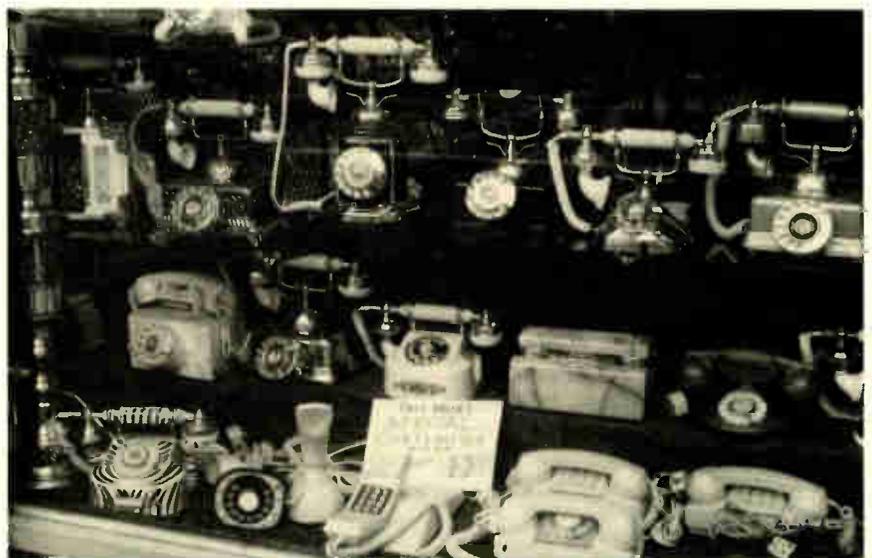
On one front, the U.S. Independent Telephone Association, made up of carriers not affiliated with the Bell System, has promised to appeal the decision in the Federal courts. And on another, AT&T chairman John D. deButts warns that AT&T would "call public attention to the fact that quality and cost of telephone service for millions of Americans will be adversely affected by this decision."

The North American Telephone Association, representing independent terminal-equipment vendors,

was predictably pleased with the ruling. NATA has estimated that competition in terminal interconnection could lead to a 50% penetration of the Bell System's market by 1980 and reduce AT&T's capital costs by \$600 million to \$800 million a year over the next five years [*Electronics*, Nov. 27, 1975, p. 40].

The FCC's 5-2 decision effectively broadens one made in November, which also is being appealed in court by Usita. The latest decision will permit interconnection of approved PBX switchboards, main stations, and key-switching telephones directly to the telephone network without protective interface devices. The decision on interconnect Docket 19528 ran counter to earlier recommendations from a Federal-state joint board of regulators. But it was not unexpected in view of last

Listening. Sellers of telephone equipment, such as this Manhattan store, would benefit from the FCC decision permitting interconnection of PBX, main station, and key phones.



November's ruling that favored direct interconnection of data modems and ancillary terminal equipment the FCC approves under a new equipment-registration program [*Electronics*, Nov. 13, 1975, p. 38].

Exemptions. Party-line and pay telephones are exempted from direct interconnection, the FCC said, but registered data modems and ancillary devices may be interconnected after May 1, and registered PBX and key systems may be interconnected after Aug. 1. But, even if the Usita appeal of the initial November interconnect ruling is reopened to permit inclusion of the latest decision on PBXs and key systems, a decision is not expected before that date.

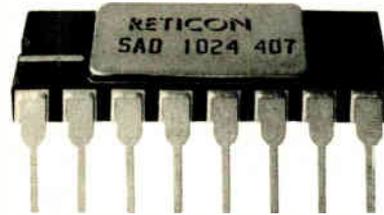
And while the FCC action elicited a minority dissent from commissioner Benjamin L. Hooks, the decision was strongly supported by John Eger, acting director of the White House Office of Telecommunications Policy.

AT&T's deButts not only promised to take his case to the public—an apparent allusion to the company's effort to gain congressional support for legislation to limit competition [*Electronics*, March 18, p. 60]—but the company has warned that the cost of service will increase. AT&T has previously suggested that residential-service charges could soar as much as 75%.

Although prices for phones vary widely across the country, in New York City, the basic charge for a residential phone is \$7.34 a month and extensions cost about \$1 a month each. A business phone there costs \$12.29.

AT&T and other carriers will be obliged to participate in the equipment registration plan, but the FCC has eased the burden of registration on carriers by exempting all items of equipment except PBXs and key telephones already connected by May 1. These may continue to be connected through Jan. 1, 1977, "and may remain connected for life, without registration, unless subsequently modified." All PBX and key telephones of the type connected by Aug. 1, the FCC said, may continue to be connected through Jan. 1, 1977, also under the same conditions. □

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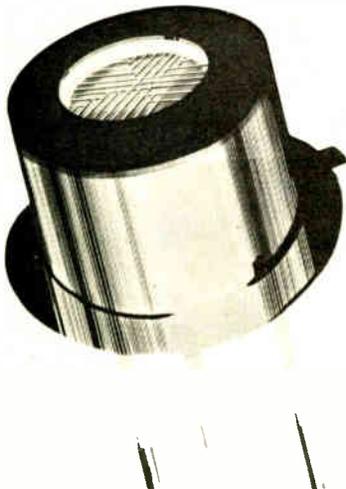
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Probing the news

You and your career

Rivers charts campaign for IEEE presidency

Board member seeks place on ballot utilizing flow chart showing 'everybody else twists knobs of engineer's career'

by Gerald M. Walker, Associate Editor

Presidential candidates can determine their positions on the issues in a number of ways. They take polls, talk to leaders, or just evolve views over the years. But Robert A. Rivers, who is seeking a spot on the IEEE ballot by petition, has a unique approach to delineating his campaign platform: a systems approach.

Rivers, the 53-year-old president of Aircom Inc. of Union, N.H., is joining Robert Saunders of the University of California, the nominee of the institute's board of directors, and Irwin Feerst, the perennial petition candidate, in this year's race for the presidency. The basis of his platform is a complex flow chart covering a 22-by-30-inch sheet of graph paper that he describes as a representation of the engineer's career development. The chart has enabled Rivers, a member of the IEEE board representing Division 4 and a member of the IEEE's manpower commission, to pick what he sees as the institute's weaknesses.

The chart shows all the governmental, industrial, and academic in-

fluences on this environment. Rivers took some three years to develop his analysis. "This chart essentially shows that we have a system operating in which the engineer is performing his services, but everybody else is twisting the knobs of the engineer's career," he says.

Influence sought. Rivers contends that the institute can become an influential force on the career environment. He has identified 67 goals in seven categories—27 of them under the heading of a lifetime career with adequate compensation.

A strong believer in the systems approach to any problem, Rivers has outlined in matrix form the various segments of the institute that must participate in implementing these goals. For example, the goal of eliminating age discrimination in engineering employment includes participation by staff and member committees in public rela-

In his hands. Robert A. Rivers holds flow chart, three years in the making, with which he illustrates EE's career development.





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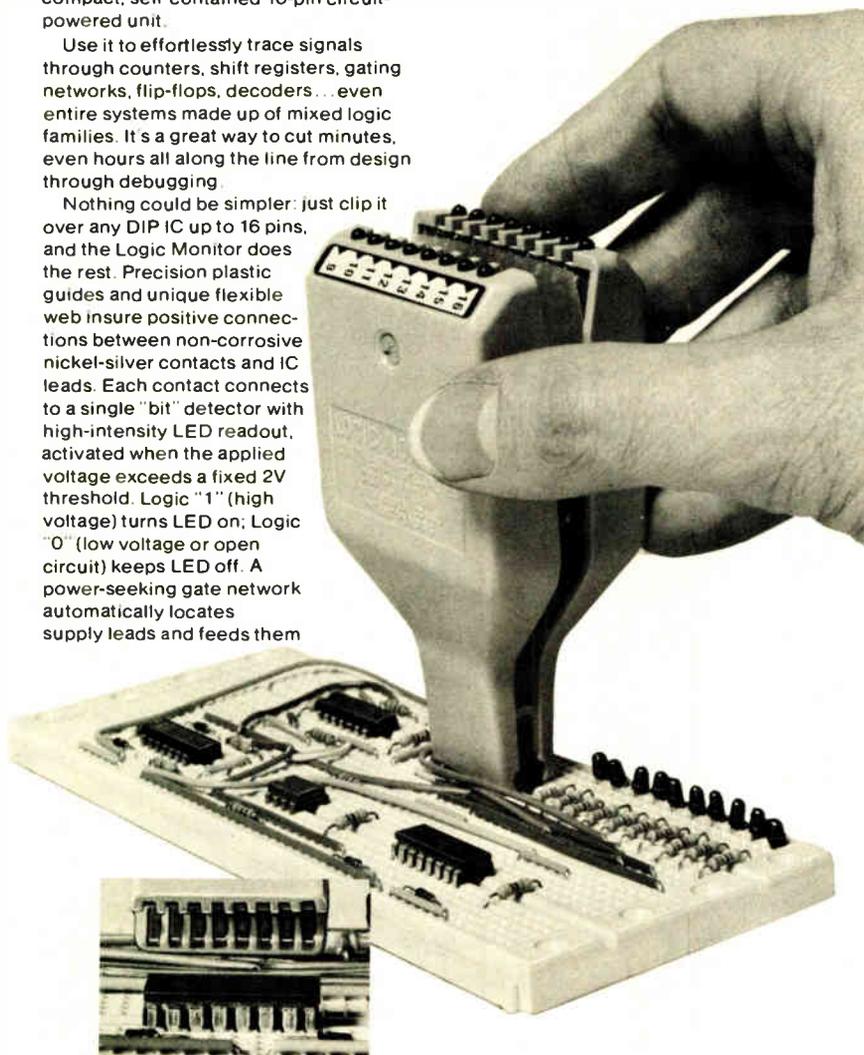
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Probing the news

tions, administrative liaison, legislative liaison, employment practices, manpower, ethics, placement, license and certification, accreditation, education, publications, member contact, technical forecasts, and survey activities.

As impressive as the career-environment chart and the goal matrix may be, do they reflect the members' needs? "Each member has different needs," Rivers remarks. "So far the institute has been in a 'survey mode:' it polls the members to find out the problems and then tries to act on the results of the poll.

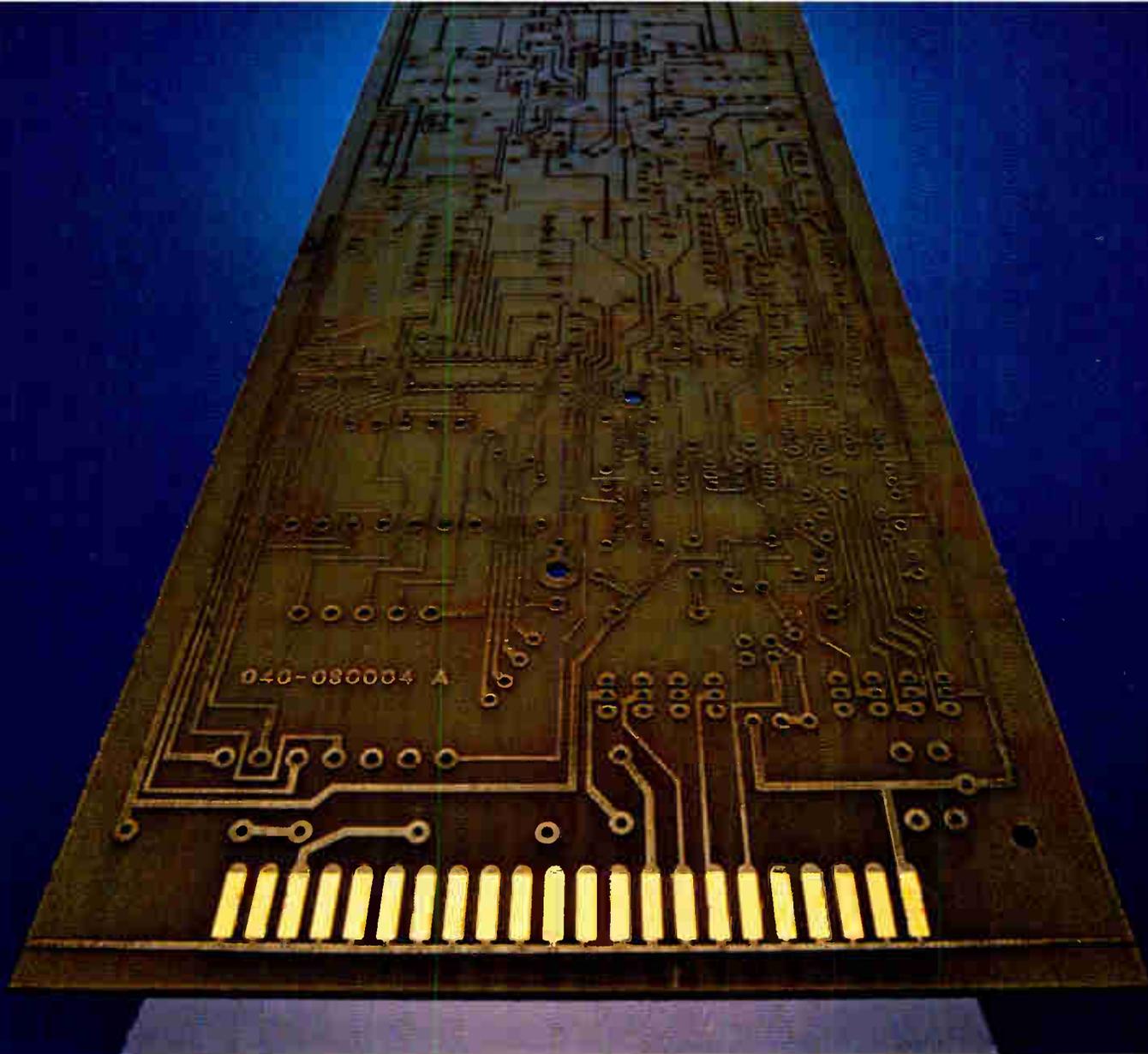
"In the 'leadership mode' that I propose to follow, the candidate for office suggests what the problems are and what the solutions are, then sees if the members want to endorse those solutions."

Issues defined. Among the most important issues is age discrimination, which definitely affects the EE's lifetime career. Rivers states flatly that the "nice guy" approach to industry—that is, friendly persuasion to try to change company policies—is not good enough.

"If you want to control the environment under which people practice engineering so it can operate in a long-term mode, there are several ways of going about it," he says. "One of the ways is to make changes in the statutes, so that companies are in equal competitive position with regard to long-term employment practices.

"Another way is to identify employers that already use employees in a long-term mode and make it easier for them to get the best engineers. The companies on the other end of the scale should be deprived of engineering services and not be able to produce a product. A third way is to support test cases of age discrimination."

Another hot issue is the control of the quality and quantity of engineering graduates. Says Rivers, "Anything that destroys the lifetime-career concept is against the best interests of engineers. Many schools have short-term interests—enrollments—when we are concerned with long-term problems." □



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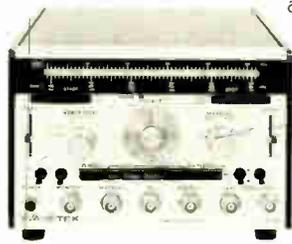
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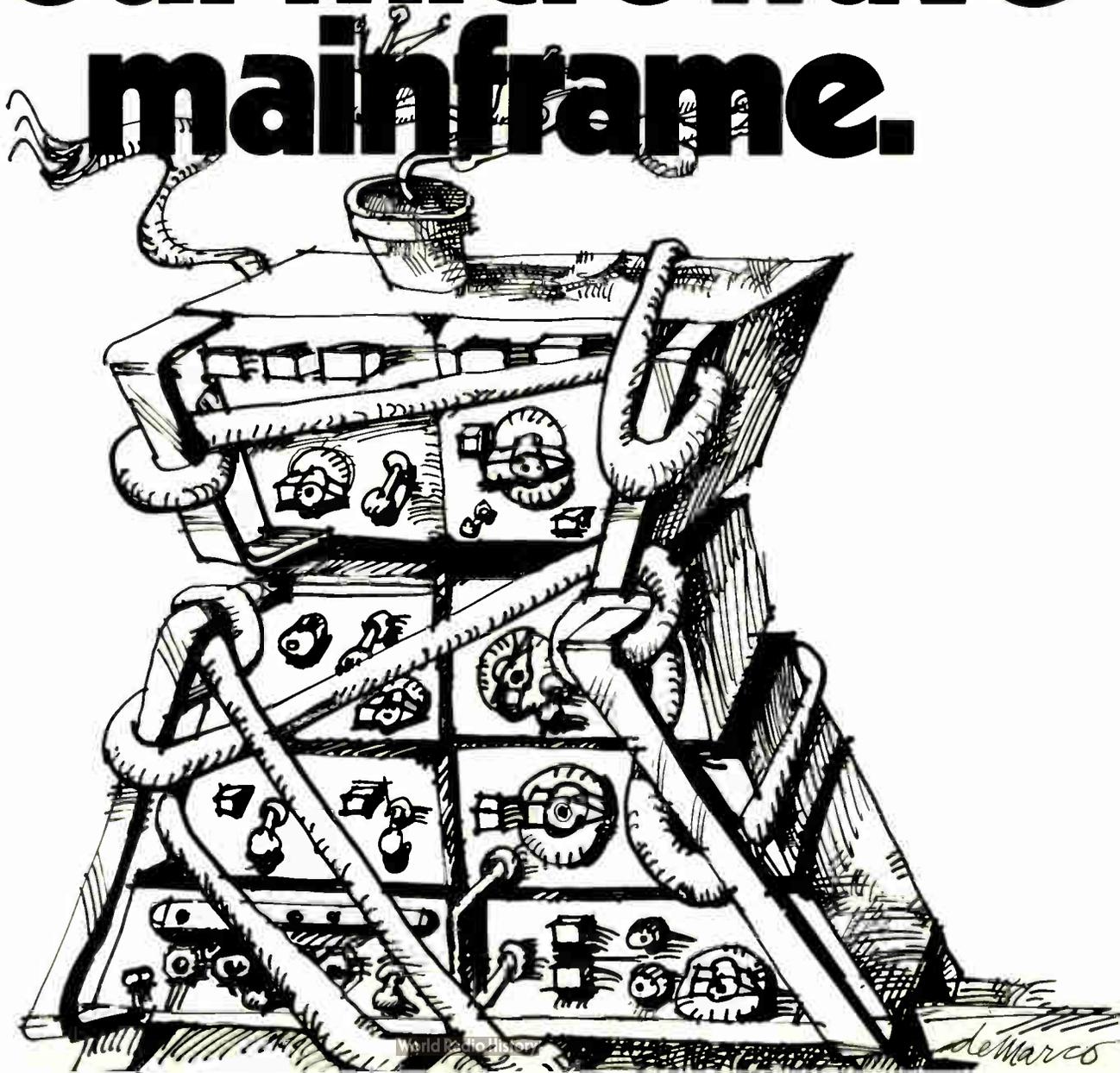
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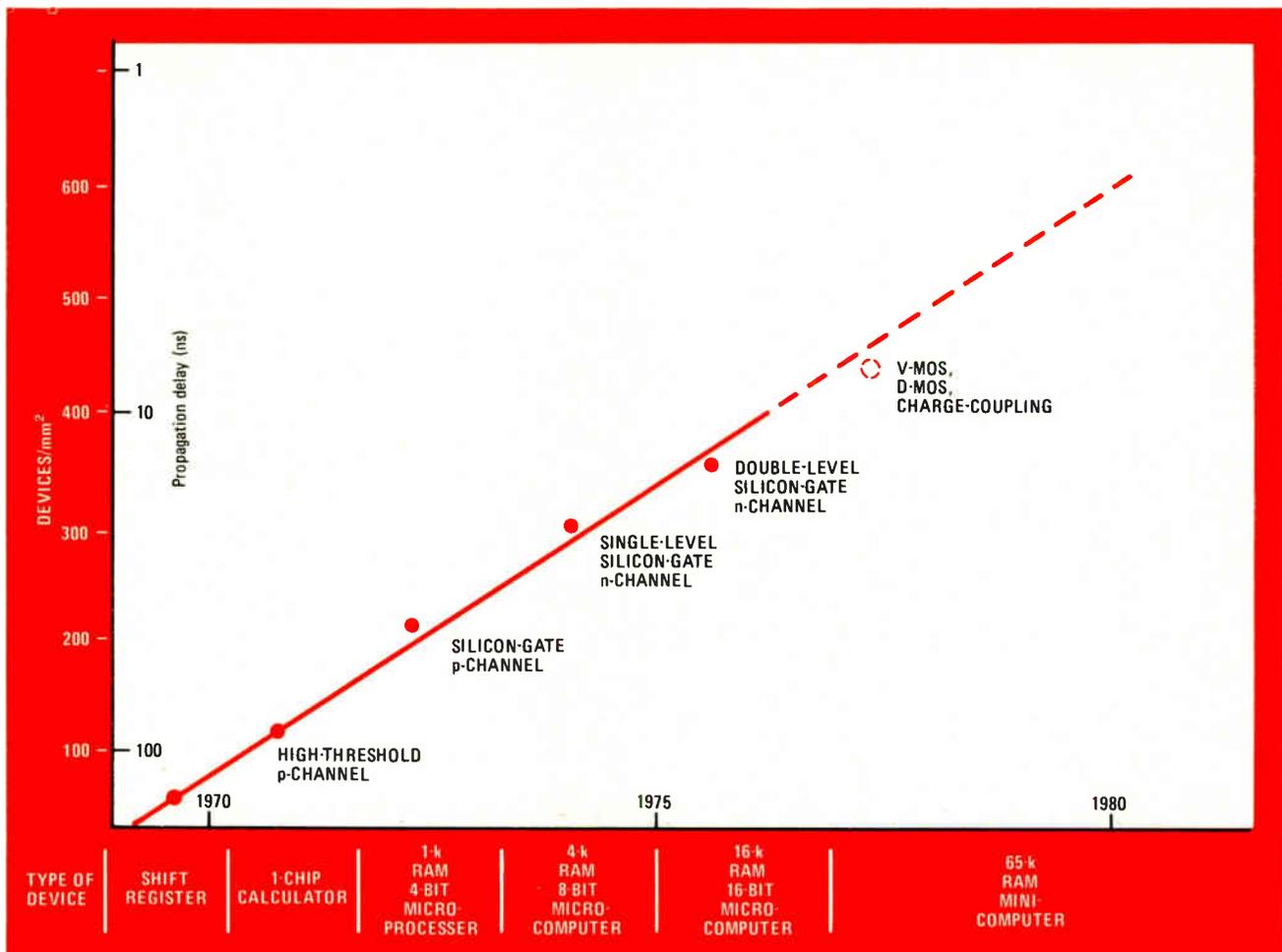
Don't tie up your microwave mainframe.



Advances in designs and new processes yield surprising performance

by Laurence Altman,
Solid State Editor





1. MOS marches on. Thanks to new inventions, MOS technology has moved up in seven years from a shift-register technique to a powerful random-access-memory and microcomputer technology. The latest tricks: double-level polysilicon, D-MOS, V-MOS and charge-coupling

□ MOS keeps popping up with new surprises. Each time metal-oxide-semiconductor technology seems to stall, designers invent new methods to carry it to higher performance levels. Now, they are calling on such techniques as double polysilicon, V notch, double diffusion, and charge coupling, to move it still higher—two to three times greater speed, five- and tenfold increases in density. These designs threaten to steam-roll over bipolar large-scale integrated-circuit designs, to make MOS the dominant digital technology.

The track record (Fig. 1) is replete with examples of this recurrent invigoration. When the high-threshold p-channel techniques developed for the single-chip hand-held calculator proved to be too slow for use in memory, designers developed a low-threshold process that brought the first 1,024-bit random-access memory and ushered in the era of programmable logic with the first 4-bit microprocessor.

When this p-channel process proved too unwieldy for denser memories, designers turned to faster and smaller n-channel structures that brought the first 4,096-bit RAMs and general-purpose 8-bit microcomputers.

Then to boost performance further, designers invented the depletion-mode technique, bringing forth MOS memories with access times of less than 100 nanoseconds and microprocessor chips capable of handling

8- and 16-bit word lengths at throughput speeds below 1 microsecond.

Now the new techniques are producing another round of devices (Table 1). In essence, the double-polysilicon process and charge-coupled designs are straight-line extensions of the existing silicon-gate n-channel technology. V-MOS and D-MOS, on the other hand, are significant departures from the standard technology.

Double poly is the process that achieves two-level memory structures in most 16,386-bit RAM designs [*Electronics*, Feb. 19, p. 116]. With two separate polysilicon levels for transistor and capacitor, designers can gain access to either of the cell's circuit elements independently. The technique cuts cell areas in half and makes possible single-chip 16-k memory arrays no larger than today's 4-k arrays.

Although some memory manufacturers, such as Texas Instruments, will introduce their first 16-k chips without double-polysilicon structures, most are convinced that it represents the best long-run approach. Since it is a direct extension of the n-channel process used in 4-k products, the process should move into production quickly and dominate mainframe-memory technology for several years.

Moreover, it sets the stage for even denser memory chips. Since the storage capacitor is essentially a bulk

silicon region and not a semiconductor junction, the two-level cell establishes the principle of nonjunction storage that surely will be required for the next RAM level, the 65,536-bit chip.

Logic designers are looking at a similar process, but for them its benefits are not quite as apparent. Although manufacturers are not talking much, apparently the trick is to let each polysilicon layer address an independent gate, so that two levels of logic can be accommodated on the chip. The independent polysilicon conductors in effect serve as a two-level interconnection system that could halve the area needed for random-logic and microprocessor layouts.

V-MOS is an adaption of the technique first used in the early 1970s to increase the density of bipolar circuit elements. A V-notch allows access to a substrate-level MOS source element, while the drain and gate structures are formed on the bias of the notch.

The vertical arrangement of these active elements permits extremely compact logic and memory. Moreover, since the structures are characterized by 1-micrometer dimensions, they significantly reduce parasitic losses and power dissipation. American Microsystems Inc. already has applied the process to a 16-k read-only memory with 200-ns access time [*Electronics*, Feb. 19, 1976, p. 105], while device designers at Electronic Arrays are working on V-notch ROMs, RAMs, and microprocessors.

The technique is also high on the list of next-generation approaches at Intel, Texas Instruments, and Motorola, where designs for 16-k and 32-k ROMs, 8- and 16-bit enhanced microprocessors, and 4-k static RAMs are under way.

Whether the notch technology, which presents a difficult topography for conventional oxide methods to

cover, will prove amenable to volume production has yet to be determined. Moreover, it requires at least one more fabricating step than standard n-channel silicon-gate process. The increased cost could offset its performance advantages to some degree, at least in the early stages of development.

D-MOS, like the notch technique, results in very small, high-performing structures without scaling their dimensions down to the micrometer level—an achievement physically beyond the capability of present photolithography. D-MOS structures do it with a double-diffused doping profile in the gate region that reduces the effective channel length to 1 micrometer or so. Since speed goes up as size goes down, the technique permits a discrete transistor to operate in the gigahertz region and transistors in large-scale-integrated circuits to operate in the 1–5-ns region.

Signetics pioneered the D-MOS approach in discrete field-effect-transistor devices and now has several low-noise FET-input amplifier products as well as discrete high-frequency transistors. The firm also has used the technique to build an 8-by-8-element crosspoint switch for telecommunication systems that is capable of more than 100 decibels of crosstalk isolation [*Electronics*, Feb. 19, p. 105].

Several Japanese manufacturers have strong D-MOS development programs in integrated circuits for memory and microprocessors. For example, researchers at Nippon Electric Co. have built a 4-bit arithmetic/logic unit that operates with less than 3-ns propagation delays.

The double-diffused technique also has been used to build power FETs. Siliconix, for example, has built a 2-ampere device and has designs for devices handling currents of 20 A and up, pointing to the time when the

TABLE 1: PROGRESS IN LSI TECHNOLOGY

	1970	1975		1977		1980	
Memory type	256-bit static	1,024-bit static	4,046-bit dynamic	4,096-bit static	16,384-bit dynamic	16,384-bit static	65,536-bit dynamic
Speed	1 μ s	150 ns	250 ns	150 ns	200 ns	100 ns	200 ns
Process	p-channel	n-channel C-MOS	n-channel single-level C-MOS I^2L		n-channel double-level	n-channel V-MOS D-MOS C-MOS I^2L	n-channel V-MOS D-MOS CCD
Microprocessor type	4-bit serial	8 to 16-bit parallel	4-bit slice parallel			16 to 32-bit parallel (active RAM control)	16-bit parallel (active RAM control)
Cycle time	50 μ s	2 μ s	1 μ s			200 ns	100 ns
Process	p-channel	n-channel C-MOS	I^2L			V-MOS D-MOS	I^2L

TABLE 2: MAJOR LSI TECHNOLOGIES (1976)

Technology	Propagation delay (ns)	Power-delay product (pJ)	Density		Chip size (mm ²)
			(Devices/mm ²)	(Gates/mm ²)	
High-threshold p-channel metal gate	80	450	150	50	7 x 7
p-channel silicon-gate	30	145	270	90	6.5 x 6.5
n-channel silicon gate	15	45	285	95	6 x 6
n-channel silicon gate depletion-load	12	38	320	107	6 x 6
n-channel double-polysilicon	10	35	525	175	6 x 6
Silicon-gate C-MOS	10	0.5	220	45	5.5 x 5.5
V-MOS D-MOS	5	20	600	225	—
SOS / C-MOS	2 - 5	0.1	650	275	5 x 5
I ² L (double level)	5 - 50	0.01 - 1	500	150	5.5

bipolar Darlington transistor pair may get a run for its money from MOS techniques.

As Table 2 indicates, the best of today's silicon-gate approaches is the double-polysilicon technique, which has a propagation delay almost equal to standard transistor-transistor logic and a power-delay product that's three times better. Its density easily exceeds that of any other standard n- or p-channel technique, rivaling those obtainable with v- and D-MOS.

Best of both

The big advantage for the notch and double-diffused processes is, of course, increased speed at double-poly densities, thus offering the best of both worlds. In fact, both approaches offer potential speed in the range of Schottky TTL, with a concomitant tenfold increase in density.

These two techniques are as fast and as dense as integrated injection logic. But I²L has a power-delay product 50 times better than any technique except silicon-gate complementary MOS and C-MOS on sapphire.

On paper, C-MOS on sapphire and injection logic finish in a dead heat with the best combination of speed, power dissipation, and density. But that's on paper. In a circuit, most manufacturers still feel that the single-channel MOS advantages of low cost and proven high-yield production continue to make up for the lag in performance, at least for present mainstream memory and microprocessor applications. (Notable exceptions are RCA and Intersil, who have commercially available C-MOS microprocessors.)

Charge coupling is based on principles used in optical imagers and serial memory devices. By combining storage and transfer into a single gate region, it reduces a RAM cell to the size of a single silicon gate, about 100 square micrometers. That's four times smaller than double-poly 16-k cells and small enough for 65-k de-

signs. Most memory suppliers are developing charge-coupled devices, with Texas Instruments providing a glimpse of some of its work [*Electronics*, Dec. 25, p. 30].

From what has been disclosed, many observers see the company's technique as elegant in principle but somewhat speculative, since the switching mechanism of the resulting structure depends on the transfer of charge between two very closely spaced storage regions. This would be difficult to control over large arrays. Moreover, logic swings in the cell disclosed are very low—1 milliampere or so—which would be difficult for present sense amplifiers to detect reliably.

Nevertheless, the problems are not insurmountable, and veteran observers feel that TI, as well as Intel, Fairchild, RCA, and others with proven CCD capability, are seriously exploring charge coupling for 65-k designs.

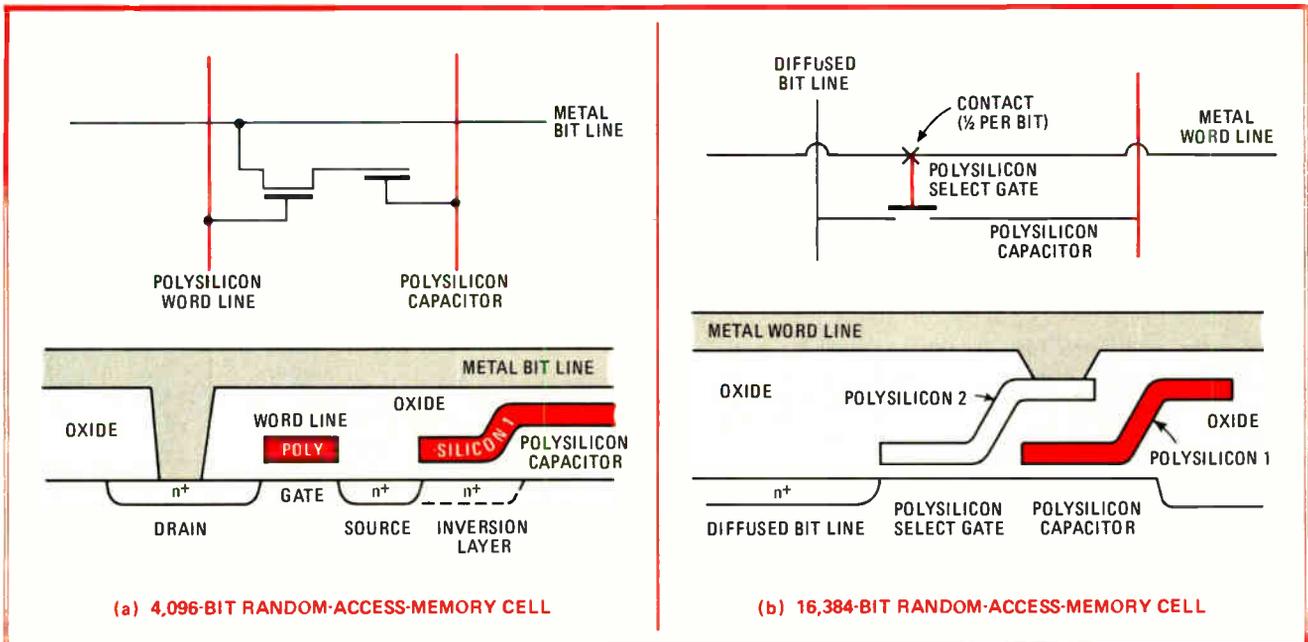
Making them work for memory

Device manufacturers are at different stages in applying these new techniques to memory, where high-volume production requirements make it the perfect place to prove a new technology. The double-poly process already has entered production for 16-k RAMs at several manufacturers [*Electronics*, Dec. 25, p. 29].

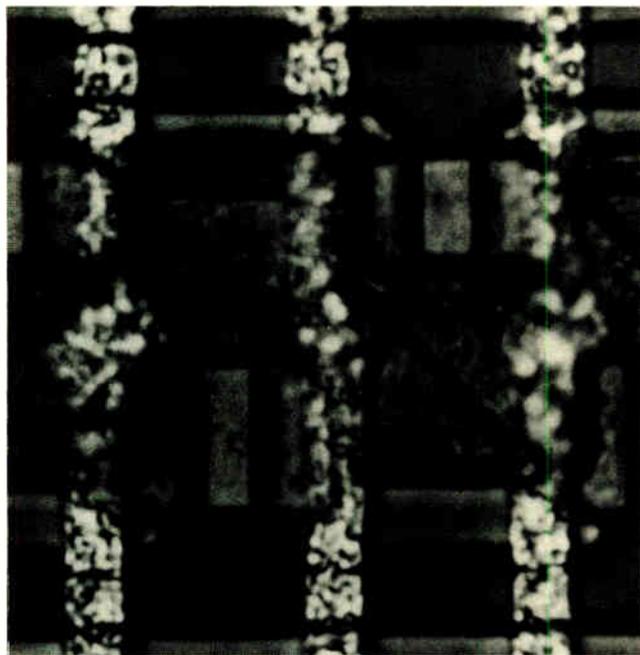
The schematic in Fig. 2 is from an Intel design, but it's typical of the two-level structures being developed throughout the industry. It saves space by placing the storage capacitor, which stores the charge corresponding to a logic state, directly under the cell's single switching transistor.

One polysilicon layer is the transistor's gate contact and interconnection while the other is the storage capacitor. In 4-k designs, transistor and capacitor had to be placed side by side. The single polysilicon layer acted as the gate interconnection as well as acting as the storage capacitor.

Since capacitor and transistor are about the



2. Two is better. Designers of 16-k RAMs went to double polysilicon levels to shrink cell sizes. In the 4-k RAM (left) one poly level made access to both transistor and storage capacitor, which were side by side. In this Intel double-level cell, two stacked lines are used.

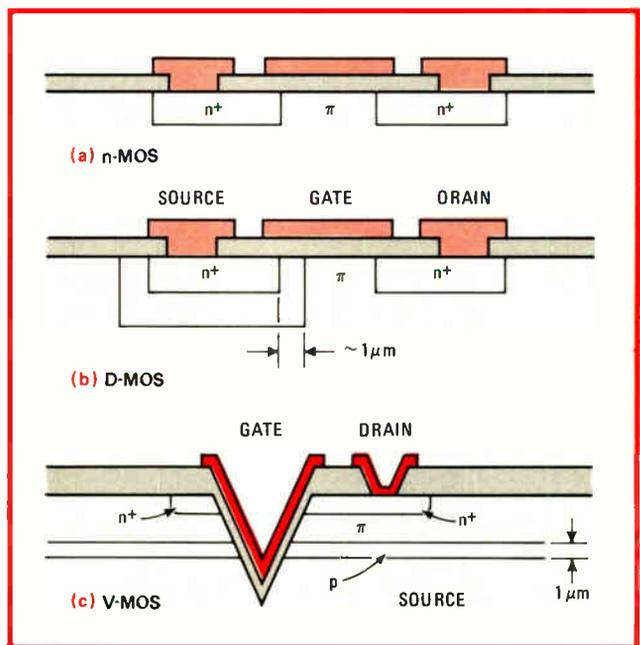


3. Small cell, large memory. This Mostek 16-k cell, which is built with a two-level polysilicon technique, shrinks cell to near $400 \mu\text{m}^2$, or less than one-half the size of 4-k RAM cells. Performance is enhanced because of lower parasitic capacitance.

same size, the two-level 16-k structure occupies about half as much silicon surface of the spread-out, 4-k one-level structure. Moreover, it operates more efficiently, because a diffused polysilicon bit-sense line has lower parasitics than the deposited metal line in 4-k designs.

The design adds up to a cell, such as the one from Mostek shown in Fig. 3, that occupies about $400 \mu\text{m}^2$ compared to 4-k cell designs typically occupying 800 to $1,000 \mu\text{m}^2$.

While the double-poly process is making 16-k RAMS



4. D-MOS, V-MOS. Double-diffused and V-notch techniques increase performance by effectively decreasing the channel lengths to 1 micrometer. D-MOS does it with a p-diffusion in the channel; V-MOS does it with a notch to access a common source.

possible, V-MOS is extending the range of ready-only-memory design, and D-MOS is boosting the speed of large-function LSI logic design. Cross sections of standard n-channel, double-diffused, and notched devices are shown in Fig. 4. In the standard structure, present photolithographic procedures must be stretched to their utmost to yield the short channels for today's products. The existing practical minimum dimensions are 4 or 5 μm especially when making big large-scale-integrated chips, where narrow lines and spaces would reduce the

yields from production lines to a quite drastic degree.

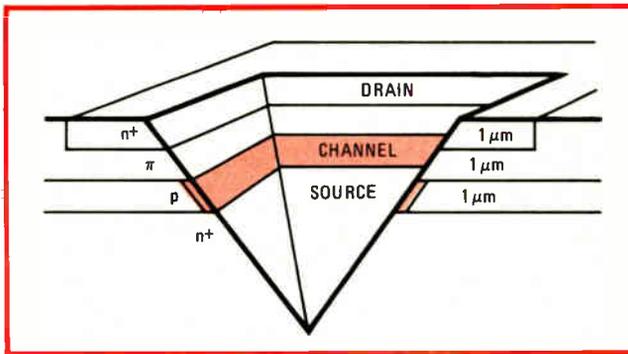
But in both double-diffusion and notch processes, narrower lines and shorter channels (hence faster, smaller devices) are produced by diffusion or implantation, rather than by squeezing photomasking limits. In D-MOS, a second lightly doped p diffusion, called a pi region, together with a heavily doped p region, are made in the channel, so that lengths can be controlled down to 1 μm .

With V-MOS, an n^+ substrate essentially is the common source for all devices, and a p layer 1 μm thick determines the effective channel depth in the saturated regions. This dimension, which corresponds to the length of n channels, can be more easily controlled than in the D-MOS process. Moreover, a pi region lowers the drain capacitance and increases the drain breakdown, so that reliable devices that have a 25-volt breakdown are being produced.

And because the source is controlled from above via the groove (Fig. 5), V-notch gate geometry is highly efficient. A typical device has a channel width of 25 μm , and yet its gate area is only 10 μm by 10 μm .

Designed by American Microsystems Inc. into a read-only memory (Fig. 6), the V-notch process produces a 16,384-bit, 5-volt static device that fits on a chip less than 17,000 mil^2 , compared to present n-channel ROM chips larger than 22,000 mil^2 . Moreover, the device's speed is at least doubled: the worst-case access time is put at 200 ns.

The process requires an extra mask for the V grooves, over the six in the depletion-load n-channel process. It can handle all the necessary circuit elements used in



5. Getting in control. In this V-MOS structure from AMI, the source is controlled from above via a groove. This makes the device extremely small. In a 16-k ROM design, it has a gate area of only 100 μm^2 , while the channel width is 25 μm .

memory design: the high-performance V-MOS transistor itself, a planar enhancement n-channel transistor, and ion-implanted load resistors. AMI's designers chose resistors as load elements, instead of depletion-load devices, because they have lower temperature dependence and better worst-case power-delay characteristics.

The V-notch process also is being applied to design of random-access memories throughout the industry, although the work is still in the early stages and manufacturers won't tip their hands. The most serious problem is the difficulty in controlling each memory cell independently and randomly, since they share a common source element lying on the substrate.

Nevertheless, the V-MOS process could make a great RAM compared to today's double-poly n-channel designs (Table 3). At first, a cell wouldn't be much smaller or result in a higher level of chip integration than do the best 16-k double-poly designs, but its 50-ns speed makes it a strong contender.

Whether V-MOS makes it as a RAM technology depends on what happens to the more established silicon-gate technology. The evolution to two-level structures suggests there's some life left in the old game yet, especially since there's not much production history with dense random-access memories for the notch process. (This also is true of I^2L —potentially as attractive for logic and memories, as Table 3 shows.) The results such manufacturers as AMI and Electronic Arrays achieve with devices now under development will go a long way toward determining just where the notch process fits into the spectrum of memory products.

In its competition with injection logic, much will depend upon the relative complexity of the processes. As Table 4 shows, there is considerable similarity. However, where V-MOS requires a gate diffusion, I^2L requires a Schottky diffusion, which generally is considered more difficult to achieve.

The moving target

Many designers of one-transistor cells are convinced the double-poly process will run out of gas short of the 100-200- μm^2 sizes needed for the 65-k RAM. But that may not be the end of the road for silicon-gate technology, because it can be applied to charge-coupled cell designs as Texas Instruments has done with its random-access cell disclosed in December. That experimental cell has the potential for achieving the 65-k size.

Unlike the single-transistor cells of 4-k and 16-k RAMs, charge-coupled cells have no transistors as such

TABLE 3: RAM TECHNOLOGY

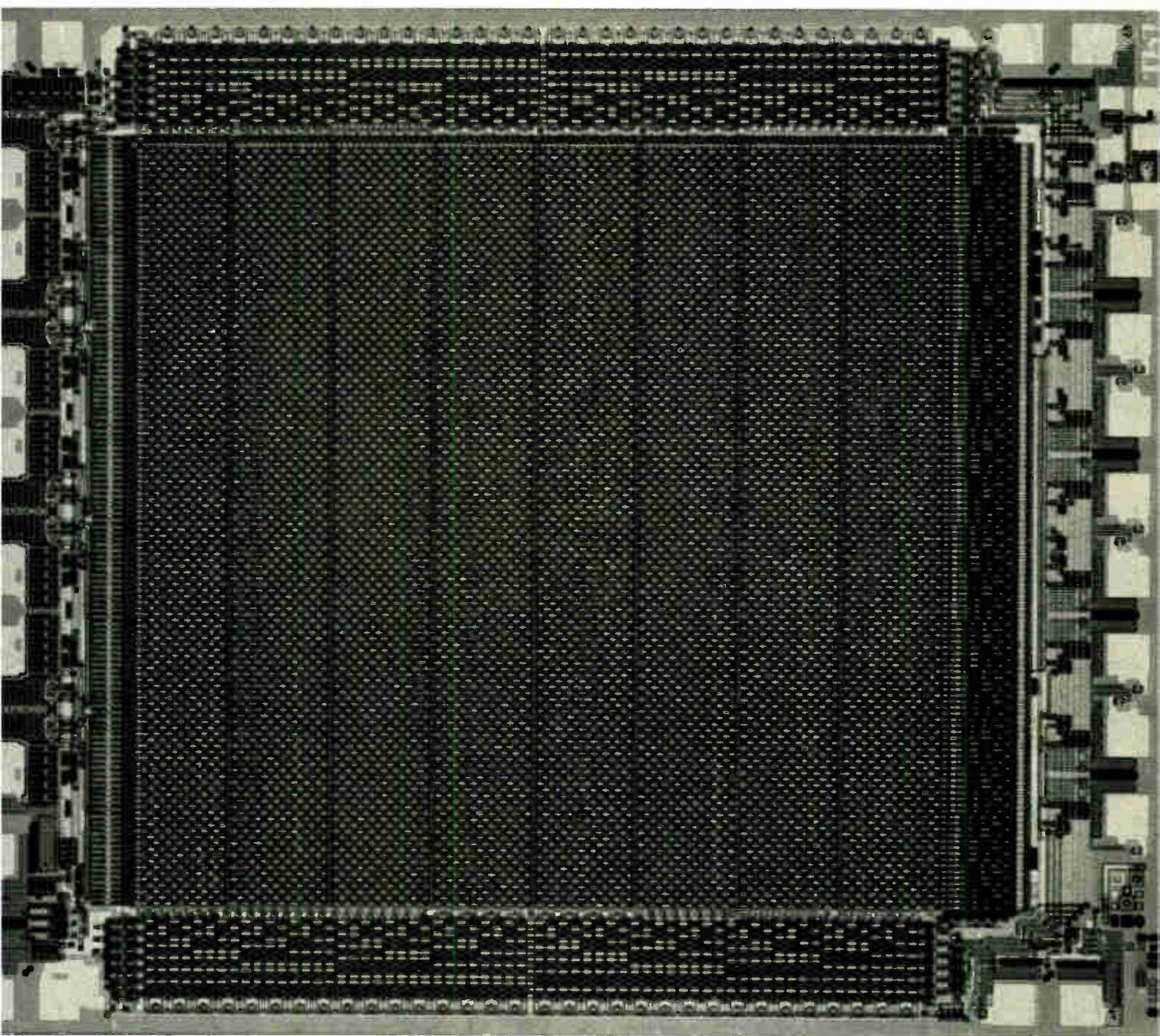
	Two-poly n-MOS (1976)	V-MOS (1977)	I^2L (1976)	
Cell size (mm^2)	400	400	800	450
Power (W/bit)	0.1	0.05	0.05	0.05
Speed (ns)	150	50	50	75
Speed power (pJ)	15	2.5	2.5	1.5
Power supply (V)	+12, -5	+5	+5	+5
Refresh	dynamic	dynamic	static	dynamic

SOURCE: ELECTRONIC ARRAYS

TABLE 4: PROCESS STEPS IN DEVICE FABRICATION

V-MOS	I^2L
1. Source diffusion (n-type)	1. Emitter diffusion (n-type)
2. Epitaxial layer (p-type)	2. Epitaxial layer (n-type)
3. Drain diffusion (n-type)	3. Base and injector diffusion (p-type)
4. Deep etch	4. Collector etch
5. Gate	5. Schottky formation
6. Contacts	6. Contacts
7. Metal	7. Metal

SOURCE: ELECTRONIC ARRAYS



6. The V-MOS RAM. This 16-k read-only memory fits on a chip less than 17,000 mil², or one-fourth less than n-channel devices. Moreover, device speed is doubled to 200-ns access time. Fabrication requires an extra mask for the grooves over the sink in the n-channel process.

(Fig. 7). Charge coupling stores data in switchable capacitor regions implanted beneath a CCD gate. An entire memory cell is no larger than a gate, which can measure less than 200 μm^2 , even with today's standard fabrication rules.

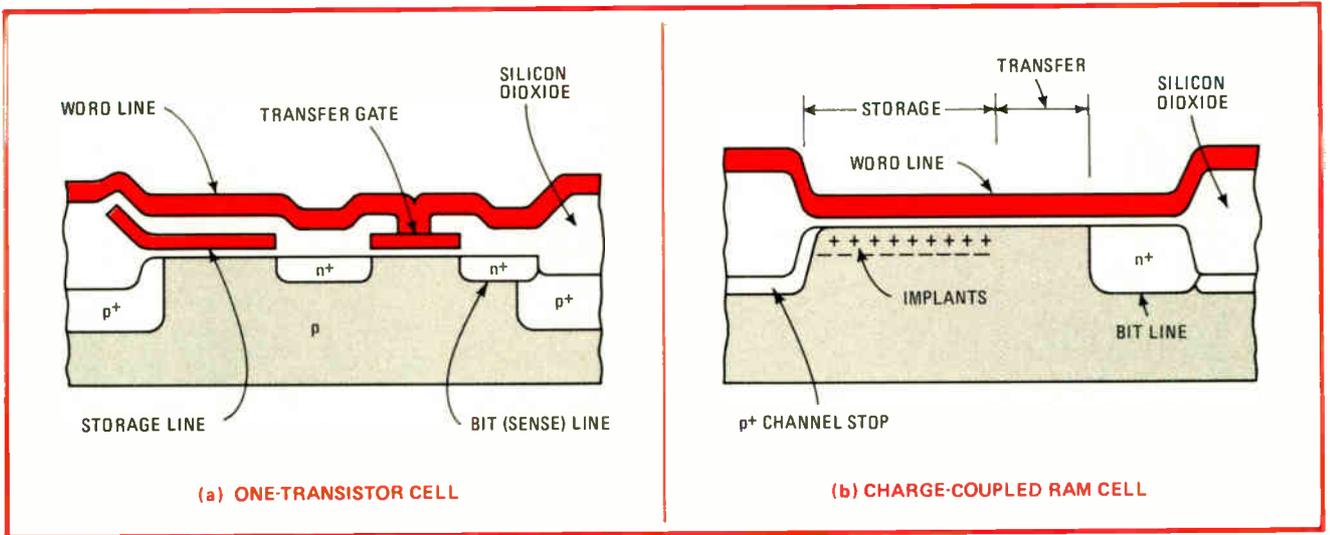
With this compact cell, CCD operation can be as fast as single-transistor cells. Moreover, combining the storage and transfer gates into a single gate region eliminates laying out two separate gate regions, which the double-poly cells have. The simplified cell structure has much more room for charge storage.

Even more chip space is saved because there are only

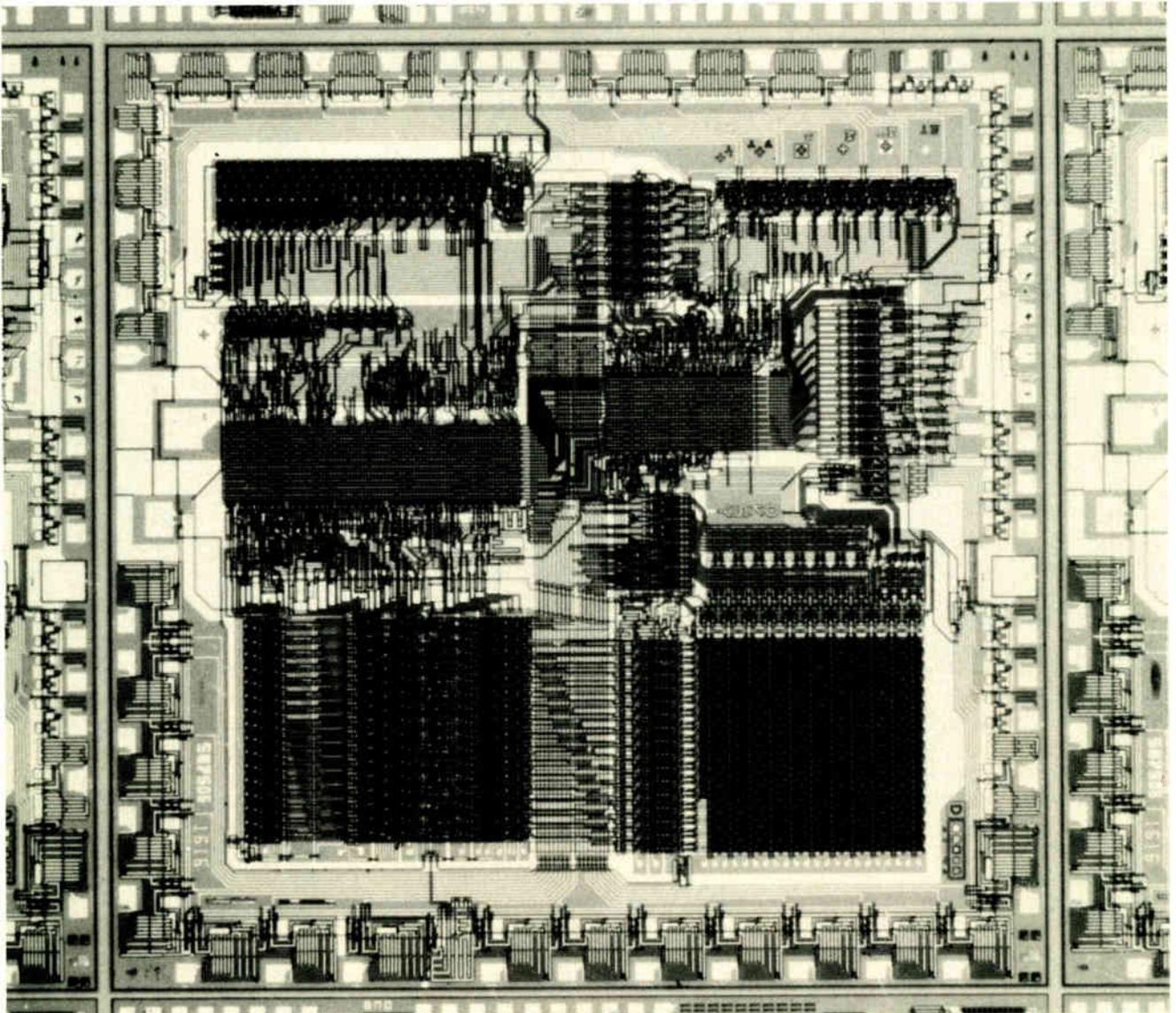
two access lines per cell—one to the storage or word line for writing, the other to a sense line for reading. Today's RAM cells need an additional line as a transfer gate.

The lower level of the storage region is achieved by a p-type implant into the silicon at a depth of several thousand angstroms, while the upper level is achieved by a shallow n-type implant. Once data is stored in one of the levels, all that's needed to sense a bit is to remove the potential on the gate and allow the charge to transfer to the adjoining n⁺ region connected to the sense line.

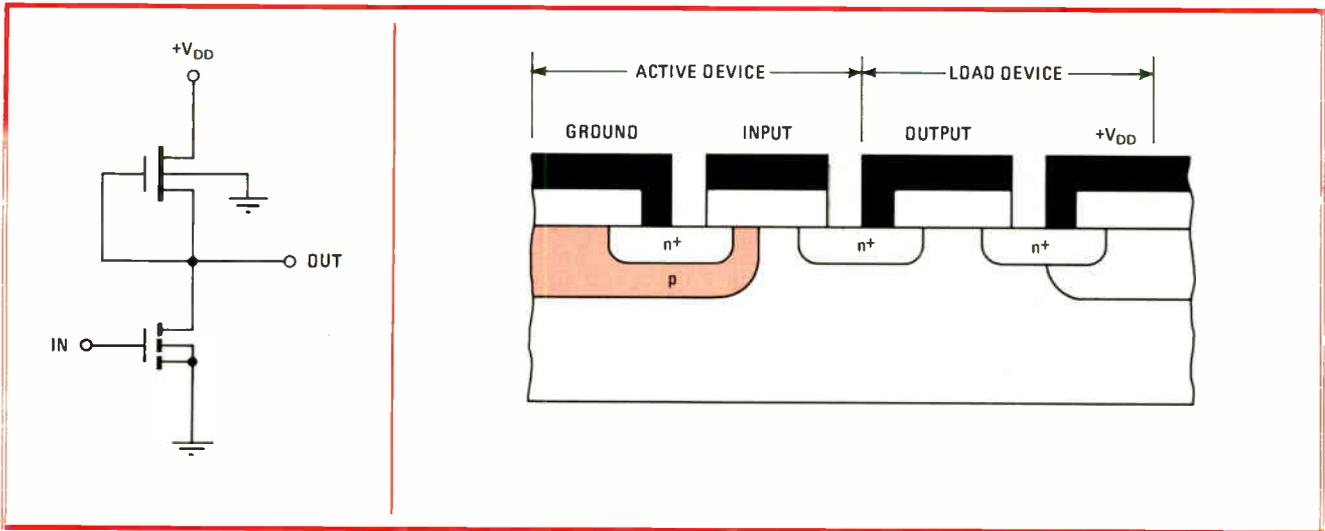
To write a 1 into the storage region, the bit-line volt-



7. **Charge coupling.** In RAM designs, charge coupling allows the switching and storage structures to be about the size of a single MOS gate, compared to the spread-out design of a single-transistor n-channel cell. This 1T1 design uses a double implanted storage region.



8. **Better gates.** Toshiba's gate-oxidation method simplifies n-channel processing so that a single mask step serves both transistor gate and field regions. Their 16-bit microprocessor has a cycle time of 300 ns and can handle 117 basic instructions.



9. D-MOS logic. This D-MOS inverter design, using depletion loads, results in a compact structure that boosts performance almost five times over n-channel designs. Fully loaded gates operate in the 1-to-5-ns region at densities of about 141 gates per mm².

age is lowered to ground or near-ground. This allows electrons to flow into a potential well that fills the region. To write a 0, the bit-line voltage is set high, and no charge enters the storage region. To achieve the store mode, the word line is turned off to complete the write operation and isolate the storage cell from the bit line.

Reading is done in the store mode with the gate voltage off and the potential well still present to store charge. When the word-line gate voltage is turned on, the surface potential in the transfer region becomes more positive than in the storage region, so that charge can flow between the bit line and the storage region. Any charge present in the storage cell is dumped onto the bit line and sensed.

Logic, too

Several Japanese manufacturers are working with new MOS structures in logic designs for microprocessor applications. A particularly promising approach is Toshiba's gate-oxidation method [*Electronics*, Nov. 27, 1975, p. 99], which boils down to increasing the oxide impurity concentration during gate diffusion when making n-channel transistors.

Simpler n-channel designs result, which are faster and dissipate less power. One mask produces the self-aligning transistors and the field regions between them. It takes two masks to get both of these elements in conventional processing.

Since the gates are more efficient, they operate with propagation delays well below 5 ns, even in complex logic configurations, such as a 21-stage 4-fanout ring oscillator. Toshiba engineers are using the method in a 16-bit microprocessor chip (Fig. 8) that achieves minicomputer performance. Capable of cycle times of 300 ns (compared to 2 microseconds for regular n-channel designs), it has 16 general-purpose registers, 15 index registers, and a set of 117 basic instructions. It can handle data in lengths of 1, 8, 16, and 32 bits and has 225 input and output ports, plus two direct memory access ports.

This 42-pin part was developed for a custom computer project but may be unbundled for the general

markets. It can address a full 64-k bytes of external RAMs, enough for most minicomputer designs.

At Nippon Electric, device designers are taking a venturesome approach with a depletion-load D-MOS technique that could outperform most standard silicon-gate approaches. With logic elements operating unloaded at 1-ns propagation delays (faster than emitter-coupled logic), Nippon engineers have built a 4-bit arithmetic/logic chip whose 113 fully-loaded NOR gates typically run at less than 3 ns at 2 v.

At these speeds, power dissipation is still only 0.71 millivolt with a power delay product of 2 picojoules. A typical density, in Nippon's inverter design (Fig. 9), is 141 gates per square millimeter using conventional 5-micrometer design rules. And that's ECL speed at MOS densities.

An interesting D-MOS application with both logic and power-train capability is Signetics' crosspoint switch for telecommunications applications. The double-diffused approach lets designers integrate on a single chip an 8-by-2 crosspoint array that before had required many discrete devices.

The technique produces an excellent switch because, unlike standard n-channel silicon-gate transistors, the device has a small on resistance and considerably lower parasitic capacitance. The result is a switching network whose interchannel crosstalk isolation is greater than -105 decibels, and whose insertion loss is extremely low.

With logic or memory, the big question is what will it cost to build? Both the v-MOS and D-MOS processes are more complicated than straight polysilicon-gate processes—single or double—and are easily as complex to build as injection logic in its highest-performing Schottky form.

Although CCD is an extension of the conventional silicon-gate process, its storage and transfer of charges in the same region may require extraordinarily precise fabrication to avoid unreliable operation. The next year will tell for these four new techniques, as manufacturers try to get some prototype-design and production experience under their belts. □

Nonlinear low-pass filter rejects impulse signals

by Barrie Gilbert

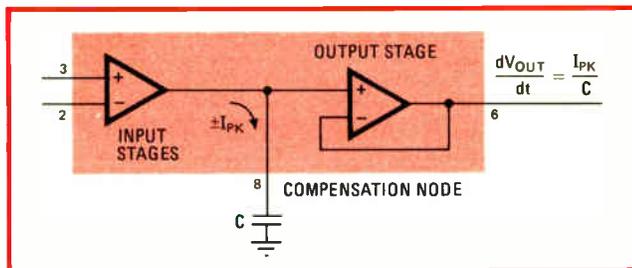
Analog Devices Semiconductor, Wilmington, Mass.

A circuit that rejects impulse signals but passes low-slew-rate signals without attenuation or phase shift can be made by connecting a capacitor to the compensation terminal of an operational amplifier. This nonlinear low-pass filtering is useful in the reduction of noise (particularly impulse noise) and in the control of glide rate between notes in electronic music equipment.

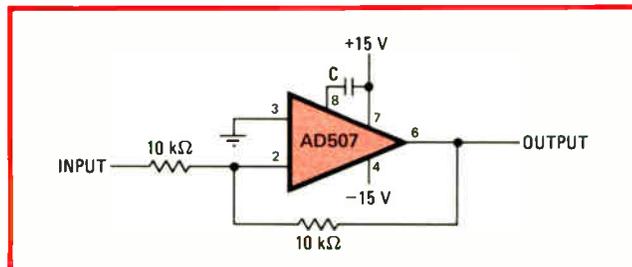
Figure 1 shows the basic configuration required of the op amp. It must have a compensation node into which it drives a stable current, I , during slewing. For optimum performance, this current should have the same magnitude in either slew direction. An external capacitor tied to the node then limits the slew rate to any value below the maximum specified for the particular op amp, because V_{out} is the voltage across the capacitor, and therefore

$$dV_{out}/dt = I/C$$

Not all op amps have the necessary configuration,



1. Nonlinear filter. An operational amplifier that has a compensation terminal with a well-controlled peak current, such as the AD504 or AD507, becomes a nonlinear low-pass filter when a capacitor is connected as shown. Because the slew rate of this circuit is limited, impulse signals and noise are strongly attenuated while low-slew-rate signals are passed without a change of amplitude or phase.



2. How to do it. The actual working circuit for the nonlinear low-pass filter shown here has the performance illustrated by wave forms in Fig. 3. Capacitor C has various values, depending on application.

and of those that do, only a few have a sufficiently well-controlled value for the peak current from the compensation node, I_{pk} . The AD507 is ideally suited to this application, having an I_{pk} that varies little from device to device or with temperature or supply-voltage variations. Its nominal value of ± 200 microamperes, which may be either measured or calculated from the compensated slew rate, gives a slew-rate of 1 volt per microsecond for a C of 200 picofarads, and reliable accuracy up to slew-rates of $10 \text{ V}/\mu\text{s}$.

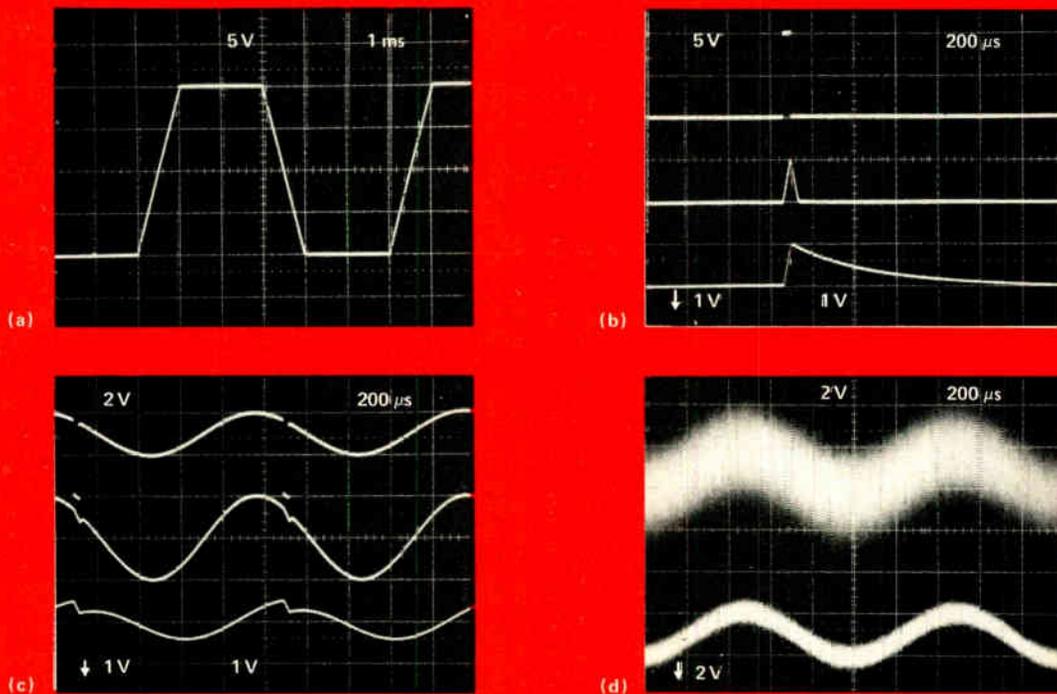
Figure 2 shows a unity-gain inverting amplifier using the AD507; with $C = 0.01$ microfarad the slew-rate is $0.02 \text{ V}/\mu\text{s}$. The response to a ± 10 -V square wave, shown in Fig. 3a, demonstrates this slew rate. Notice that C provides adequate loop stabilization, so the capacitor normally connected from pin 6 to pin 8 may be omitted. The small-signal response of this filter is determined by C , but is much higher than the full-power response; for the capacitor shown the figures are 20 kilohertz to -3 decibels (signal level of $\pm 100 \text{ mV}$), and 320 hertz for $\pm 10 \text{ V}$, respectively. Gains other than unity may be achieved simply by altering the ratio R_2/R_1 , and the amplifier may be used equally well in the noninverting mode. The slew-rate is unaffected by the absolute value of the resistors, the gain, or the mode.

The usefulness of the filter can be judged by the other wave forms in Fig. 3. In Fig. 3b a $40\text{-}\mu\text{s}$ pulse of 10-V amplitude is shown in the top trace, and the output from the nonlinear filter with $C = 0.008 \mu\text{F}$ is shown in the center trace, which for clarity has been inverted and expanded vertically. The nonlinear op-amp filter has reduced the pulse to a 1-V triangle lasting $80 \mu\text{s}$. For comparison, the response of a single-pole linear filter having a time-constant of $400 \mu\text{s}$ is shown in the lower trace. Although the amplitude has been reduced to 1 V , a tail in the response extends beyond 1 millisecond.

The capacity to reject impulse signals while passing signals of low slew rate with neither attenuation nor phase error is shown more clearly by the wave forms in Fig. 3c. Here the input is a 1-kHz sine wave of 1-V amplitude, on top of which rides a $40\text{-}\mu\text{s}$ pulse of amplitude 3.5 V representing a noise spike. The center trace is the output of the nonlinear filter and shows that the pulse has been almost eliminated while the wave form of the sine wave is preserved.

In contrast, the linear low-pass filter (again a single $400\text{-}\mu\text{s}$ RC network) more than halves the sine-wave amplitude and introduces about 60° of phase lag. Furthermore, the pulse is stretched and actually distorts the wave form, as the lower trace demonstrates.

Nonlinear filters may also be used to reduce the Gaussian noise content of a signal, since it contains occasional high peaks (there is a 0.37% probability that the amplitude exceeds three times the rms value). Fig. 3d illustrates this. Again the input is a 1-kHz sine wave with an amplitude of 1 V , to which has been added 1 V rms of white noise. The output shows an undistorted



3. Get the picture? Performance of nonlinear low-pass filter is shown and compared with that of a linear low-pass (RC) filter. In (a) the input signal is a square wave; with $C = 0.01 \mu\text{F}$, the circuit slews the output at 20 V/ms . Both (b) and (c) show impulse inputs, response of nonlinear filter with $C = 0.008 \mu\text{F}$ and response of a linear low-pass (RC) filter. Noise reduction is demonstrated in (d).

sine wave, with only 0.3-v rms noise. Subjectively (if this were an audio signal) the improvement is slight, and more care is needed in selecting the optimum slew rate to effectively reduce white noise.

Unlike in linear filters, no change in response results from cascading stages of equal gain and slew rate. Also, if stages of different gain or slew rate are used, the one having the lowest slew rate is in the driver's seat. A high-pass nonlinear filter can easily be made by sub-

tracting the low-pass signal from the direct input. In fact, the voltage at the summing node of the op amp in Fig. 2 is the high-pass function of the input.

Bandpass filters also can be constructed, by cascading a low-pass section with a high-pass section. When these have the same slew-rate the center frequency of the bandpass filter is inversely proportional to input amplitude. The practical value of the high-pass and bandpass nonlinear filters has not been established. □

FSK modem interfaces cassette and computer

by John I. Compton
Wenner-Gren Laboratory, University of Kentucky, Lexington, Ky.

Serial data from a teleprinter or serial output from a computer can be recorded and played back from cassette tape with the frequency-shift-keyed modulator/demodulator described here. The circuit may also be used as a half-duplex modulator/demodulator to store data or read it out over a telephone line. The system is crystal-controlled, operates at standard FSK frequencies, and needs no adjustment. It can be operated from 5 to 15 volts and costs less than \$15 to build.

The block diagram in Fig. 1 shows the principle of

the modem circuit, and details are given in Fig. 2.

What happens is this. A 14-stage 4020 C-MOS counter is reset to zero when one of two diode arrays, selected by the serial input, decodes a count of 424 for a logical 1 and 464 for a logical 0. In this manner the output frequencies, at Q_9 , will be the clock frequency of 950 kilohertz divided by 424 and 464, or 2,240 and 2,047 hertz, respectively. This gives 2,240 Hz for a logic 1 and 2,047 Hz for a logic 0. The resulting square wave is filtered and ac-coupled to the auxiliary input of the recorder. The 4013 D latch on the counter reset prevents false resets that might otherwise be caused by ripple outputs between leading edges of the clock.

Record and play do not occur at the same time, so that the same C-MOS counter can be used in both the modulator and the discriminator. When retrieving data, the recorder's "ear" output is ac-coupled, amplified, and Schmitt-triggered by two Norton amplifiers (National LM3900), and finally shaped by a 4013 D latch. The re-

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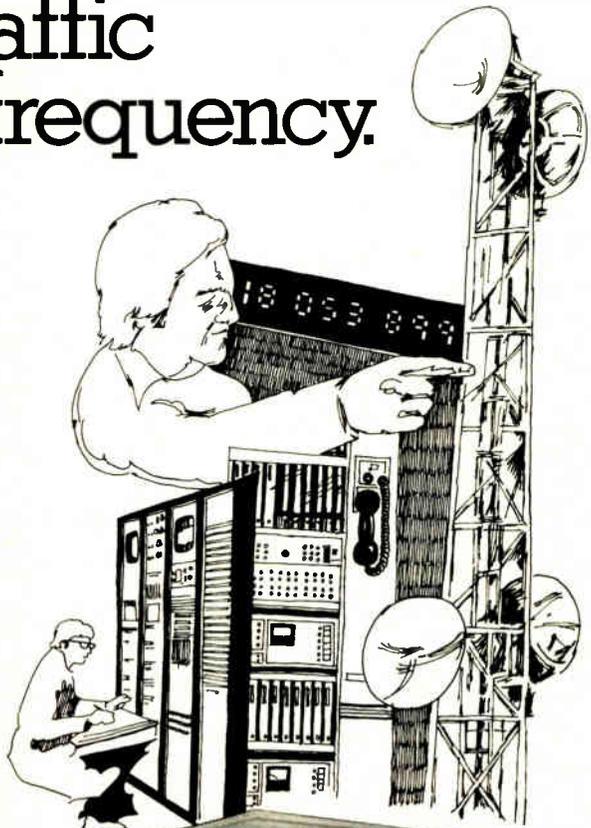
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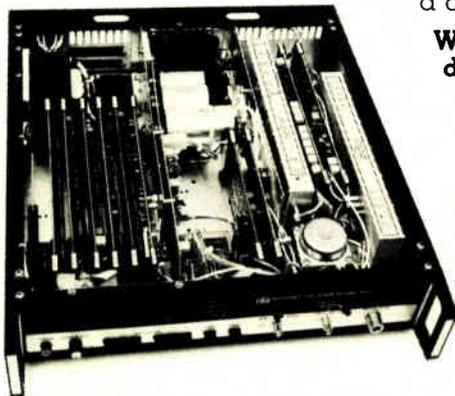
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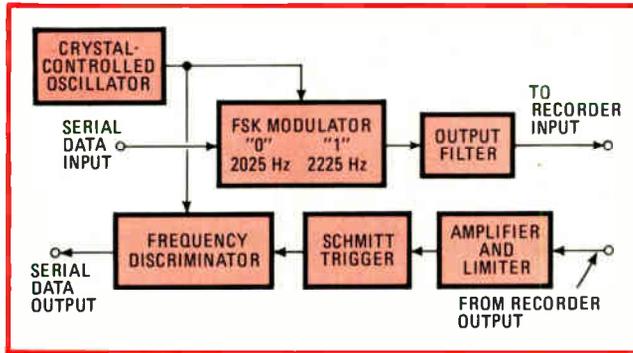
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sulting wave form is used for clocking a toggle flip-flop.

The output of the flip-flop stays high for a time equal to one cycle of the recorded data and, with the mode switch in the play position, allows the counter to count

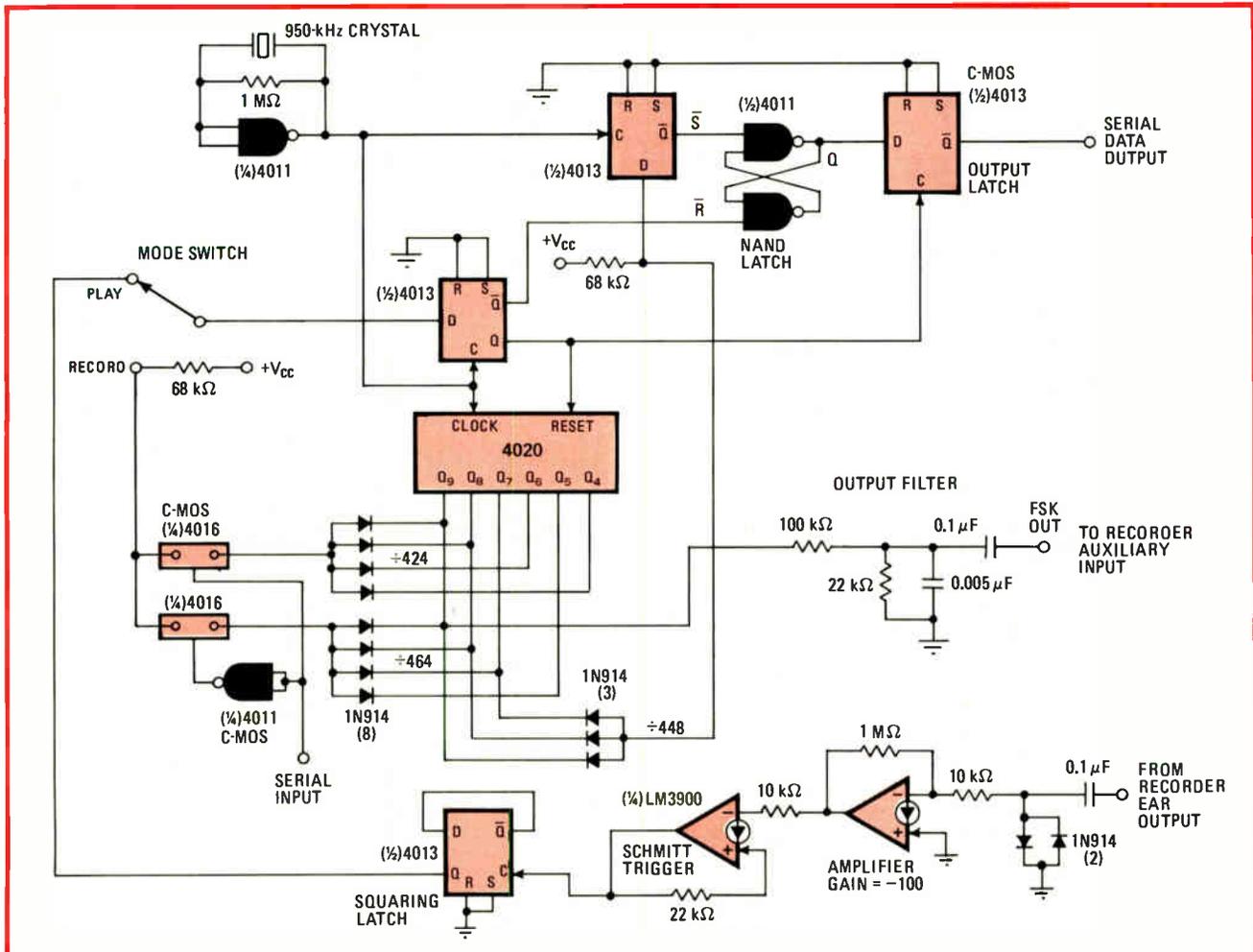
system clock pulses for one input period. Then, when the counter has counted a period longer than 472 microseconds, or a frequency of below 2,120 Hz, a three-diode array (decode 448) has an output of 1 and as a result the C-MOS NAND latch is set. At the end of the input period the output latch is set and a logical 1 will appear on the \bar{Q} output line. If the input frequency is higher than 2,120 hertz, the latch does not set, and a logic 1 will appear on the output line. The serial output, which is valid during record and play, provides automatic echo for teletypewriter use. However, the FSK output is valid only while in record mode.

This circuit has recorded and played back an 18,000-character (30-minute) test tape at 110 baud without a single error. A baud rate of 300 can be used with good-quality tape. The recorder can be low quality, however, because this frequency shift allows a 5% tape speed variation without error. Even greater speed variation can be allowed if the frequency shift is larger. □

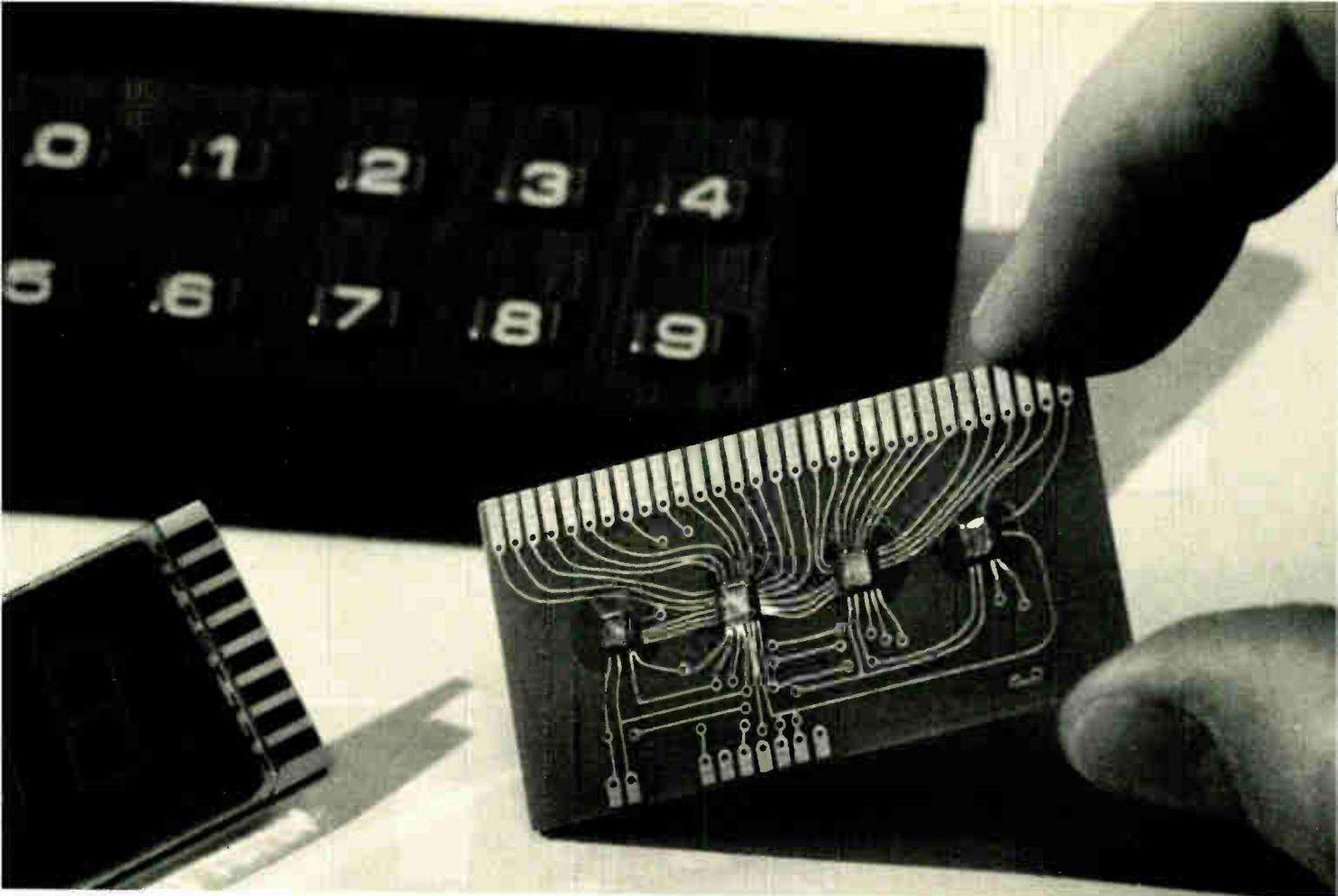


1. Taping the digits. Modem circuit converts serial digital data to FSK signal for cheap storage on cassette tape, and also converts the taped signal back to digital form for re-entry into a computer or other digital system. The FSK signal can be sent over telephone lines. This circuit can handle a baud rate of 300 without difficulty.

Designer's casebook is a regular feature in Electronics. We invite readers to submit original and unpublished circuit ideas and solutions to design problems. Explain briefly but thoroughly the circuit's operating principle and purpose. We'll pay \$50 for each item published.



2. How to modem. The heart of this circuit is the 4020 14-bit C-MOS counter. In the record (modulate) mode, the counter divides the 950-kHz crystal frequency by 424 to put out 2,240 Hz when the input digit is a logic 1, and divides by 464 to put out 2,047 Hz when the input is logic 0. In the playback (demodulate) mode, the 4020 determines the incoming frequency by counting clock pulses during one period; a long count, corresponding to the lower frequency, signals a logic 0, and a short count yields a logic 1. All C-MOS components are available from several manufacturers. Other crystal frequencies in the range 0.5–4 MHz can be used if diode code is changed to decode the proper count.



Digital television tuner uses MOS LSI and nonvolatile memory

by Lester Penner, *General Instrument Corp., Hicksville, N. Y.*

□ The phenomenal capabilities of digital technology are coming to the rescue of television-set manufacturers. The tuning of TV receivers electromechanically has been pushed to its practical limits in efforts to make ultrahigh-frequency channels tunable as easily as very-high-frequency channels. That tuning requirement has been mandated by the Federal Communications Commission. Fortunately, the versatile electronic tuner can not only handle this requirement with ease, it can accommodate a number of added frills at minimal cost.

One of the latest digital tuners is the Omega (Fig. 1). The Omega combines low-cost performance and reliability of large-scale-integrated MOS digital circuits with the analog technology of the varactor tuner and television itself. The system also capitalizes on the nonvolatile capabilities and economy of the metal-nitride-oxide-silicon (MNOS) electrically alterable read-only memory. As an indication of mix of technologies involved, the design team included representatives of General Instrument's semiconductor components, television-tuner, microelectronics, and keyboard divisions.

Not only can the electronic control accurately tune in

the 12 very-high-frequency channels and 70 ultrahigh-frequency channels, but a character generator can easily be added to display the channel number on the screen. In addition, a clock chip can be added to display the time on the screen. The smaller size of the electronic tuner offers the designer more versatility in using various types of push buttons and readouts while at the same time reducing the size of the TV cabinet.

And, although it's still on the drawing board, the programmable nonvolatile memory will enable the viewer to preprogram an entire week's viewing. In addition to being smaller and more versatile than electromechanical tuners, electronic controls, benefiting from MOS-LSI technology, are much more reliable. Yet, they are only slightly more expensive.

Even with only the 12 vhf channels to cope with, the electromechanical tuner's detent operation is susceptible to failure because of loose or dirty contacts. Reliability is jeopardized when the electromechanical control is designed to tune the uhf channels as well.

To help alleviate the problem for electromechanical tuners, the FCC has made two concessions—reducing to

1. Digital dexterity. The Omega digital tuner, shown with a seven-segment digital display and a keyboard that might be used on a TV set, is only slightly more expensive than the detent tuner.

six the number of uhf channels that must be programed into a tuner at one time and reducing the maximum deviation from the optimum frequency on any channel to ± 1 megahertz.

Although the detent tuners most manufacturers supply can fine-tune the vhf channels, that degree of flexibility can't be carried over to the uhf channels except with a cumbersome, complex device. Thus, the FCC has started manufacturers scurrying to adopt the electronic tuner, which can easily fine-tune all the channels.

The varactor tuner shows the way

Omega is not the first system to take advantage of the varactor tuner, an analog semiconductor device in which the capacitance varies with the applied voltage. Two other systems have showed promise, but proved to have shortcomings—higher costs among them. These techniques are the use of the varactor with a potentiometer array and with the phase-locked loop.

To date, the varactor is most commonly used with the potentiometer array (Fig. 2). In this arrangement, a group of potentiometers, usually 20, is connected between a stable voltage supply and ground. Each potentiometer can be adjusted to a voltage that tunes the set to one of the local channels. Some form of selector, such as solid-state switch, routes the voltage from one of the potentiometers to the tuner.

The selector may be push buttons, touch buttons, a rotary knob, or a slide lever. If a momentary button is used, flip-flop storage is included in the solid-state switch. Also, the bands must be changed, either by slide switches or by tabs inserted in the switches to indicate the channel numbers programed into each position. However, either way, someone must adjust each potentiometer for optimum tuning of all local channels.

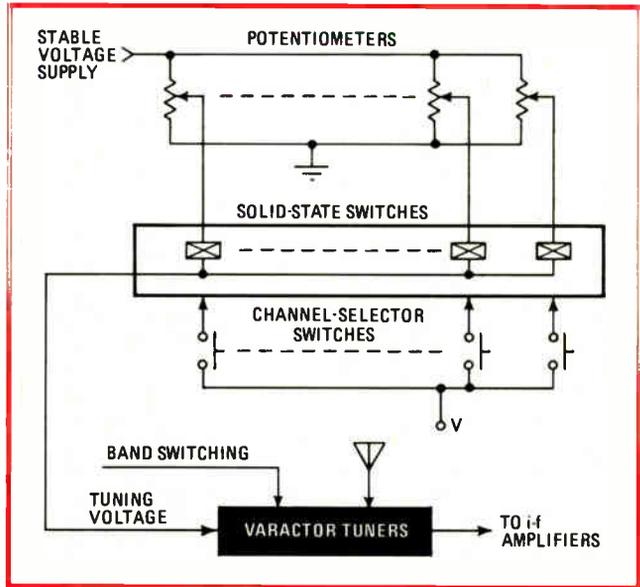
The costs of stable potentiometers and elaborate mechanical assemblies are drawbacks, regardless of the method chosen to change channels. Slide switches lack precision in tuning, and the alternative, insertion of tabs, is inconvenient.

The other common type of electronic tuning is the phase-locked loop (Fig. 3). In this technique, the fixed frequency of a crystal-controlled oscillator is divided down to a stable reference frequency and compared against a variable voltage-controlled oscillator. The VCO frequency output, f_1 , from the varactor tuner is amplified enough to drive a high-frequency digital counter. The prescaler divides f_1 by a fixed number, M , and the resulting frequency, f_2 , is then divided by a variable counter that divides by a value N , determined by the modules

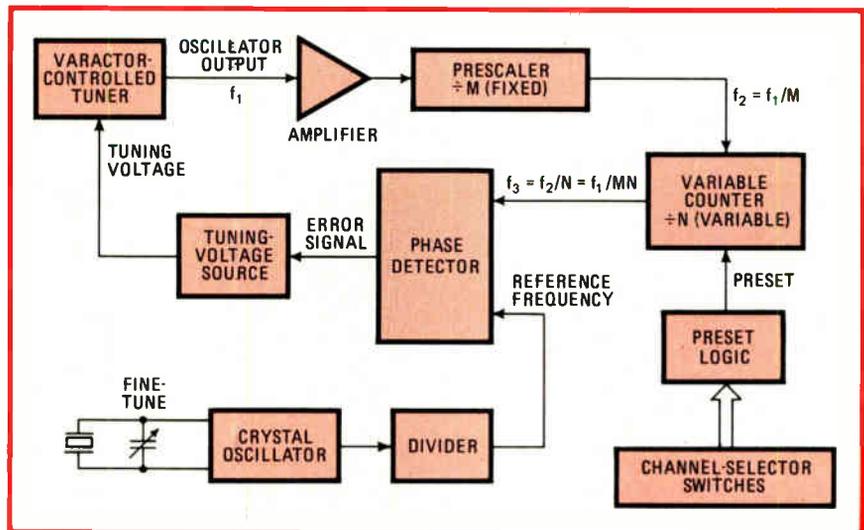
of the variable counter and the value of its initial preset.

The output of the variable counter is a frequency, f_3 , equal to f_1/MN . This frequency, in turn, is forced by the servo loop to equal f_r by putting both through a phase detector and using the output-error signal to change the frequency, f_1 , of the tuner oscillator in a direction to reduce the error between f_3 and f_r to zero. Since a subharmonic of the varactor-controlled oscillator is phase-locked to a subharmonic of the crystal oscillator, the varactor tuner will exhibit the stability of the crystal oscillator, but at some different frequency.

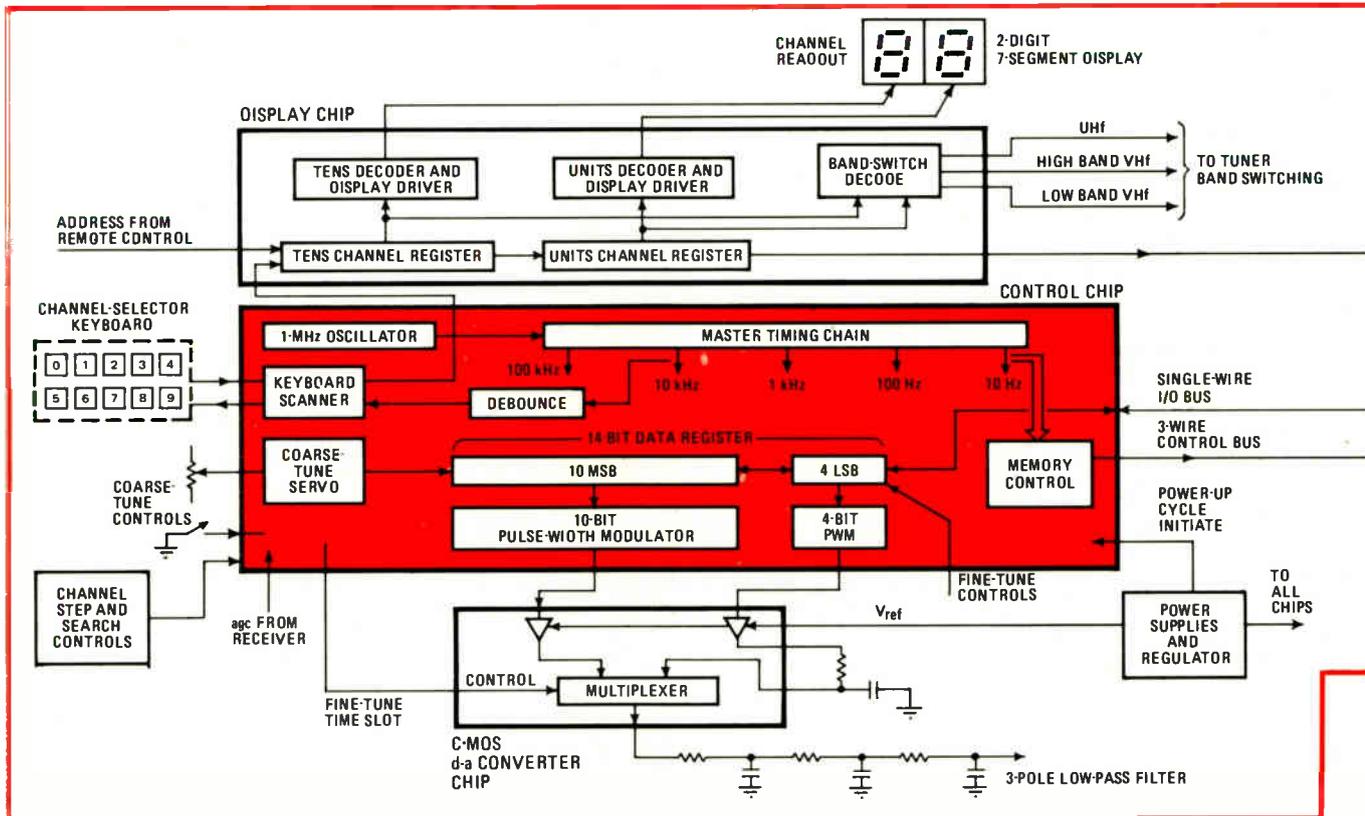
Channels are selected by changing the value of N to get the desired frequency, f_r , which is f_1/MN . The



2. Varactor gone to pot. Until recently, the varactor tuners used for TV receivers have usually included about 20 potentiometers that could be adjusted to a voltage that tuned the set to one of the local channels. This approach has worked in the early electronic tuners, but it requires stable potentiometers, which can be costly.



3. Going through a phase. A newer electronic tuner has a phase-locked loop to ensure a stable reference frequency divided down from a crystal-controlled oscillator. Phase detector measures error between reference and preset frequency f_3 ; error signal is used to adjust varactor tuning voltage. Again, cost is a problem with PLL-based tuners.



viewer operates a channel-selector switch that causes the preset logic, which may contain a read-only memory, to select a preset count-down starting point that will provide the correct value of N to tune the varactor tuner to the selected channel. The values of f_1 , M , and the range of N are all engineered to give the desired range and resolution to the tuning system. A channel can be fine-tuned by causing small shifts in the crystal-oscillator frequency, but this adjustment is not independent for every channel.

The phase-locked-loop technique is limited because the prescaler must operate at 900-megahertz input frequency, which is near the upper limit of commercially available ICs. What's more, the amplifier used to raise the low level of the tuner oscillator to drive the digital prescaler may be costly.

Omega changes the approach

The Omega all-electronic tuning system fulfills these design objectives:

- It provides an electronic memory in addition to all the best features available on mechanical tuners.
- Its nonvolatile MNOS memory eliminates the need to keep a power source operating, and its production cost is low. These two factors help keep the tuner competitive in the cost-conscious color-TV industry.
- It uses low-speed MOS-LSI digital techniques to achieve low cost, yet is compatible with LSI innovations such as on-screen channel and digital-clock displays.
- It provides the push-button inputs and digital readouts that have become so well accepted with calculators and electronic clocks and watches.
- It accommodates a variety of available varactor

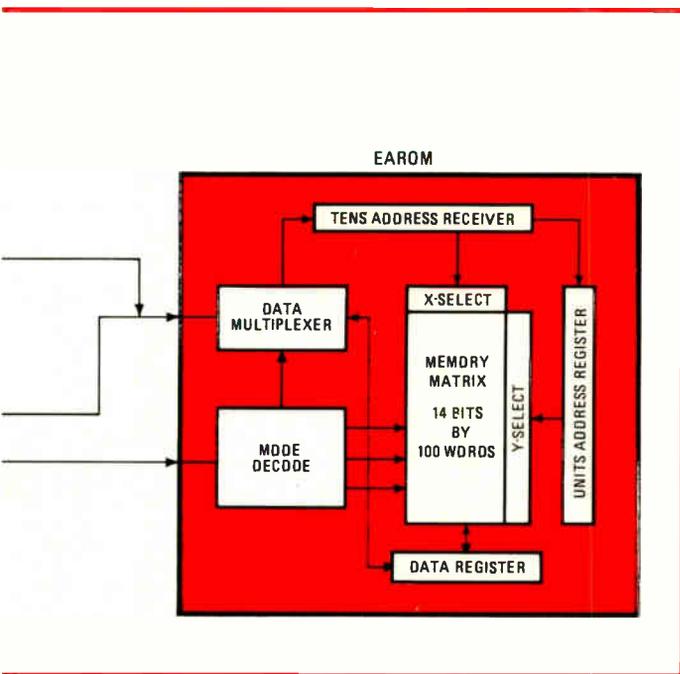
tuners, push-button selectors, display readouts, and remote-control systems.

- It accommodates the differing channel allocations found all over the world, as well as the special requirements of conversion to cable television.
- It eliminates the need for any initial setup by the customer while allowing independent fine-tuning of all 82 channels.

Cognizant of the limitations of earlier tuning systems, Omega designers built this system around four custom LSI chips fabricated with three MOS technologies. The control and display chips are fabricated with n-channel metal-gate ion-implant technology, the digital-to-analog converter chip is complementary-MOS, and the Eeprom is metal-nitride-oxide silicon.

The n-channel technology was selected for the control and display chips primarily to meet the low cost-to-performance ratio necessary for a TV tuner. The control chip (Fig. 4), which performs the timing and logic functions, contains all the circuitry necessary for interpreting input from the channel-selector buttons and mode controls. It includes some digital circuitry of the digital-to-analog converter and produces all the timing and control signals for tie-in with the other LSI chips.

In operation, the control chip scans the keys and switches at a 10-kHz rate until it senses a closure. A debounce circuit reconfirms the closure 10 milliseconds later. A switch closure might command one of several functions: two-digit random channel selection, channel stepping (by units or tens), coarse-tune, fine-tune, or search. In addition to switch closure, the control chip accommodates a signal input from a remote-control receiver, a power-up signal from a power supply to trigger



4. Omega, the tuner. This digital tuner system includes an n-channel MOS control chip, an n-channel MOS display chip, a complementary-MOS d-a converter chip, and an MNOS electrically alterable read-only-memory chip, which has a capacity of 100 14-bit words. Control chip interfaces with Earom through a single-wire I/O bus.

the last channel viewed, and an automatic-gain-control signal to terminate the search mode.

The display chip, which accepts the encoded keyboard entries from the control chip, performs the display decoding and driving, as well as the tuner-band switching. When the viewer selects a channel, the corresponding data word is retrieved from memory and held in the control chip. The data is converted from digital to analog form, and the resulting voltage is applied to the varactor tuner.

The fine-tune control can be either a double-throw switch with a center neutral position or a pair of push buttons. Using these switches, the viewer can fine-tune up or down until satisfied with the picture. This procedure actually increments or decrements the data register in the control chip. When the viewer releases the switch, the modified word is reloaded into the Earom so that any time that channel is selected, the word corresponding to the most recent adjustment is retrieved.

Each digit of the channel-number entry is encoded and sent to the display chip, where it is stored and decoded both for a seven-segment display and for band-switching. This channel number is also used as a two-digit address (0, 2 through 9, 9) for the Earom to locate the corresponding memory line. The address is sent serially to the Earom on a single-wire bidirectional bus.

Memory is slow but sure

The Earom, made by a technology licensed by NCR Corp., has a capacity of 100 14-bit words. The memory stores binary equivalents of the analog voltages for 82 channels, as well as the two digits of the channel being viewed when the receiver is switched off. An additional

16 memory locations can accommodate special frequency allocations that might be required in cable-TV.

The Earom (Fig. 5) was designed not so much for high speed as for low cost. Both write and erase take 20 milliseconds. Since memory-access time for this application may be slow, all address and data transfers are serial—a single bidirectional terminal is used to transfer both data and address to and from the memory.

Using a 3-bit parallel code to command the memory into one of its seven operating modes—input address, input data, output data, erase, write, read, and stand by—achieves another economy. In fact, the entire memory in its inexpensive package has only seven pins.

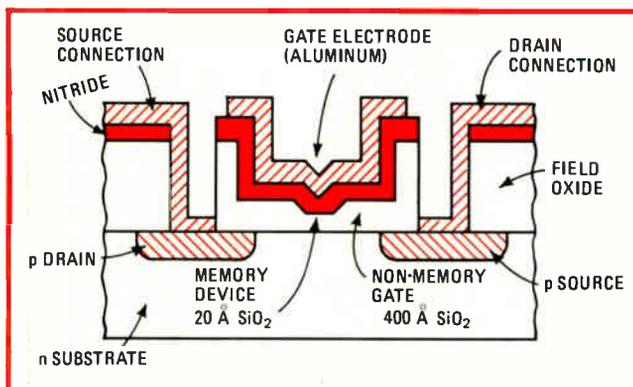
In operation, it's necessary to erase a word location to all 0s before a new word is written because the write procedure only writes 1s. This type of memory has certain theoretical storage limitations, such as storage time of 10 years between refreshes, 1 billion reads between writes, and 1 million erase/write cycles per word. However, in the manual Omega system, these are not practical limitations.

In the 100 words of memory, designated 0, 0 to 9, 9, each address is used to store the channel-voltage data corresponding to the same two-digit channel number. The single digit channels use location 0, 2 through 0, 9. Locations 0, 0 and 0, 1, which do not have corresponding channel numbers, are used for a special purpose. When a two-digit channel number is selected, that channel number is first stored in the 0, 0 and 0, 1 locations, one digit in each. Thus, when power to the set is turned off, the last channel viewed is always retained in these two locations.

When the viewer turns on the receiver, an automatic sequence, like the mechanical memory of a rotary-type selector, begins to retrieve that channel number and retune the set to the last channel setting.

In addition to the random channel selection possible with a two-digit entry, the channel number can be incremented up and down in units or tens. This form of control is useful when interfacing with remote-control systems. However, fully encoded random channel selection by remote control can be shifted into the control chip through terminals provided.

The d-a converter uses a low-pass filter along with the C-MOS device between the control chip and the low-pass



Inside Earom. In the MNOS memory transistor, voltage applied to the gate causes tunnelling through the gate oxide, 20 angstroms thick, which is the basis for nonvolatile data retention.

filter to achieve the precise, stable amplitudes required at the input to the filter. C-MOS technology provides the best way of time-gating the precise reference voltage into the filter to give a tuning voltage that suffers minimal error from variations in components.

The d-a converter had to be inexpensive, as well as highly accurate. Fortunately, the speed of conversion was not a major factor, since settling times of 100 or 200 ms when changing channels would be barely noticeable. The converter that was selected modulates the duty factor of a constant-frequency pulse train proportionally to the digital input and then filters out the average dc component of that train and uses it as the voltage input to the varactor.

D-a converter is precise

The basic converter accepts and holds the 14-bit word in the data-holding register while a counter of the same bit length is stepped by a clock of frequency f_c . The parallel outputs of the data-holding register and the counter are continuously compared. The output of the comparator is 1, so long as the count in counter B is less than that in data register A. When B exceeds A, the comparator switches to 0 until the count of B overflows, and then it begins again.

The output from the comparator, which has a period of 2^n counts, remains at 1 for a time proportional to the input value. Derived as it is from a digital LSI circuit, this signal's duty factor is precisely defined but not its amplitude. The converter must now control a solid-state switch that can increase amplitude and maintain the precise level. Because a high-voltage C-MOS device provides the best control, a special circuit and process modification was developed to perform this function.

A simple RC filter averages the area under the C-MOS-output waveform and extracts a dc component within the range of 0 v to V_{ref} for duty factors between 0% and 100%. With a V_{ref} of 28 v, the full range of most varactor diodes can be driven directly from the filter without need for amplification.

The stability and accuracy of the filter output depend primarily on V_{ref} and the duty factor. The clock frequency and the values of R and C have only secondary effects on the analog output.

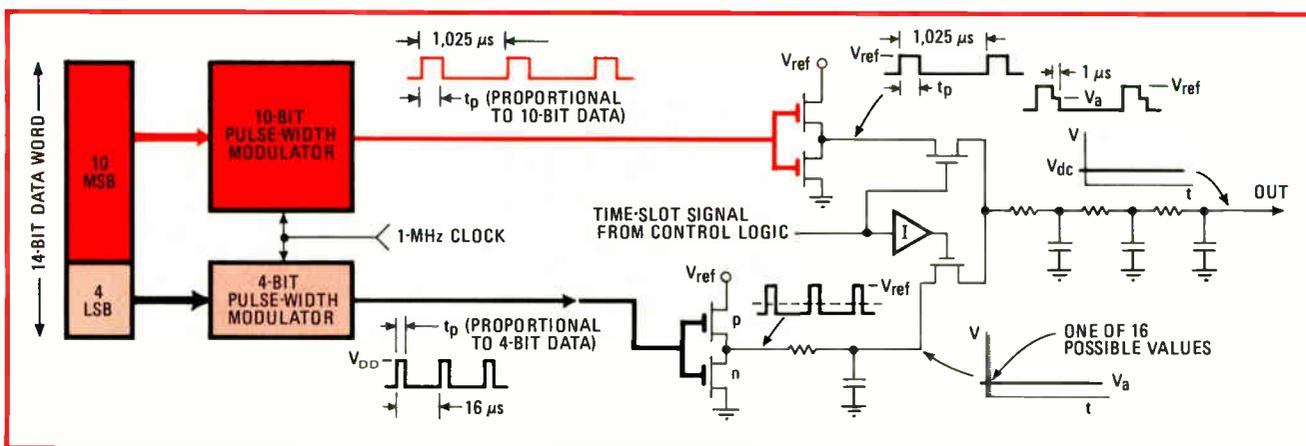
The cutoff point of the low-pass filter is a compromise between the amount of ripple left in the analog output and the sluggishness in response to a change in duty factor (a channel change). A 14-bit word was needed in the system to achieve the necessary resolution. But, since it was also desirable to keep the clock frequency below 1 MHz to minimize chip yield even with a multipole filter, such a slow period could not be filtered adequately to maintain a reasonable response time.

To overcome this problem, the converter was modified (Fig. 6) to operate only on the 10 most significant bits of the 14-bit word, as described above, while the four least significant bits are made to generate a variable-amplitude pulse that is multiplexed into the filter. A pulse 1 μ s wide, which could take on 16 possible amplitudes, multiplies the resolution of the basic 10-bit pulse-width-modulated wave form by 16 while only adding 1 μ s to the total period.

The amplitude of the pulse representing the four least significant bits is again generated by a pulse-width modulator and low-pass filter to make it relatively insensitive to component variation. Still maintaining a clock frequency of 1 MHz, the converter has a resolution of one part in 16,384 while limiting the variable duty factor period of 1,025 μ s. A simple three-pole filter reduces the ripple below the noticeable level and maintains an adequate response to channel changes.

For coarse-tuning, a rotary control is desirable so that its position can be calibrated to show a rough indication of channel number. A servo loop is built into the chip to cause the variable pulse width of the d-a converter to track the pulse width of a monostable multivibrator controlled by a coarse-tune potentiometer. Because the d-a pulse width is varied through incrementation of the data register, the digital value in this register also tracks the potentiometer position. When the push switch on the coarse-tune control is released, the memory is updated with the new digital word.

In the search mode of channel selection, depressing a switch button causes the system to select each channel in sequence. When a signal from the receiver indicates that a program is present on that channel, the sequencing stops. The effect is to scan through the channels, stopping at each active signal. □



6. Most significant. To keep clock frequency below 1 MHz but filter it adequately, converter operates separately on the 10 most significant bits of 14-bit word, while four least significant bits, operating on a different track, are made to generate a variable-amplitude pulse.

Don't pay for more performance than you need in dc-to-dc converter modules

by Daniel T. Sheehan, Stevens-Arnold Inc., South Boston, Mass.

□ Nowadays, the small, board-mountable, encapsulated module is the most widely used type of dc-to-dc converter, but selecting the right one for a given application is no easy matter. The potential user must be familiar not just with the specifications but with their interrelationships. Otherwise, he or she will be paying for unneeded performance in such parameters as noise, temperature and the related power-transfer efficiency, and input conditions.

Dc-to-dc converters, as a convenient means for shifting from one voltage level to another, were used primarily in portable standby power supplies for many years. With the development of modular converters came new uses in uninterruptible power supplies. But the modules also acquired a whole new function: as isolators for eliminating ground loops and noise problems in analog and digital systems.

There is a wide selection of modules available for powering logic devices and operational amplifiers, at prices much lower than they used to be. They always use some form of switching, although there are only two basic types of popular conversion schemes—the push-pull saturable-transformer circuit (Fig. 1a) and the switching regulator (Fig. 1b).

Making the conversion

The push-pull dc-dc converter is actually a free-running oscillator that produces an unregulated square-wave output. The dc input is chopped into complementary square waves, passed through a transformer, then rectified and filtered.

During each cycle, the transistors are driven between cutoff and saturation through the positive-feedback windings of the transformer primary. For half a cycle, the voltage across the transformer primary stays constant, as the magnetizing flux steadily increases. When the transformer core saturates, the magnetizing current rises suddenly to its peak value, the rate of rise of the magnetizing flux decays to zero, and the voltage across the primary winding collapses. This removes the base drive to the saturated transistor, turning it off.

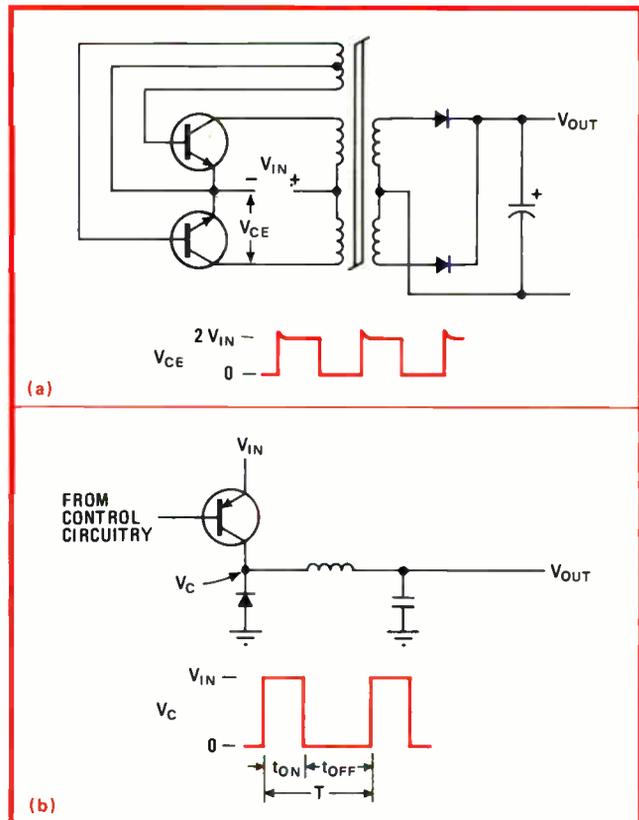
In the switching regulator, the transistor is driven on and off through its base terminal by a pulse train whose duty cycle is modulated. The catching diode, which provides a continuous path for the inductor current when the transistor is off, limits excursions at the collector ter-

minal. The output V_{OUT} of the LC filter is the average value of the switched waveform V_C . If the voltage drop across the transistor and the inductor are ignored, then:

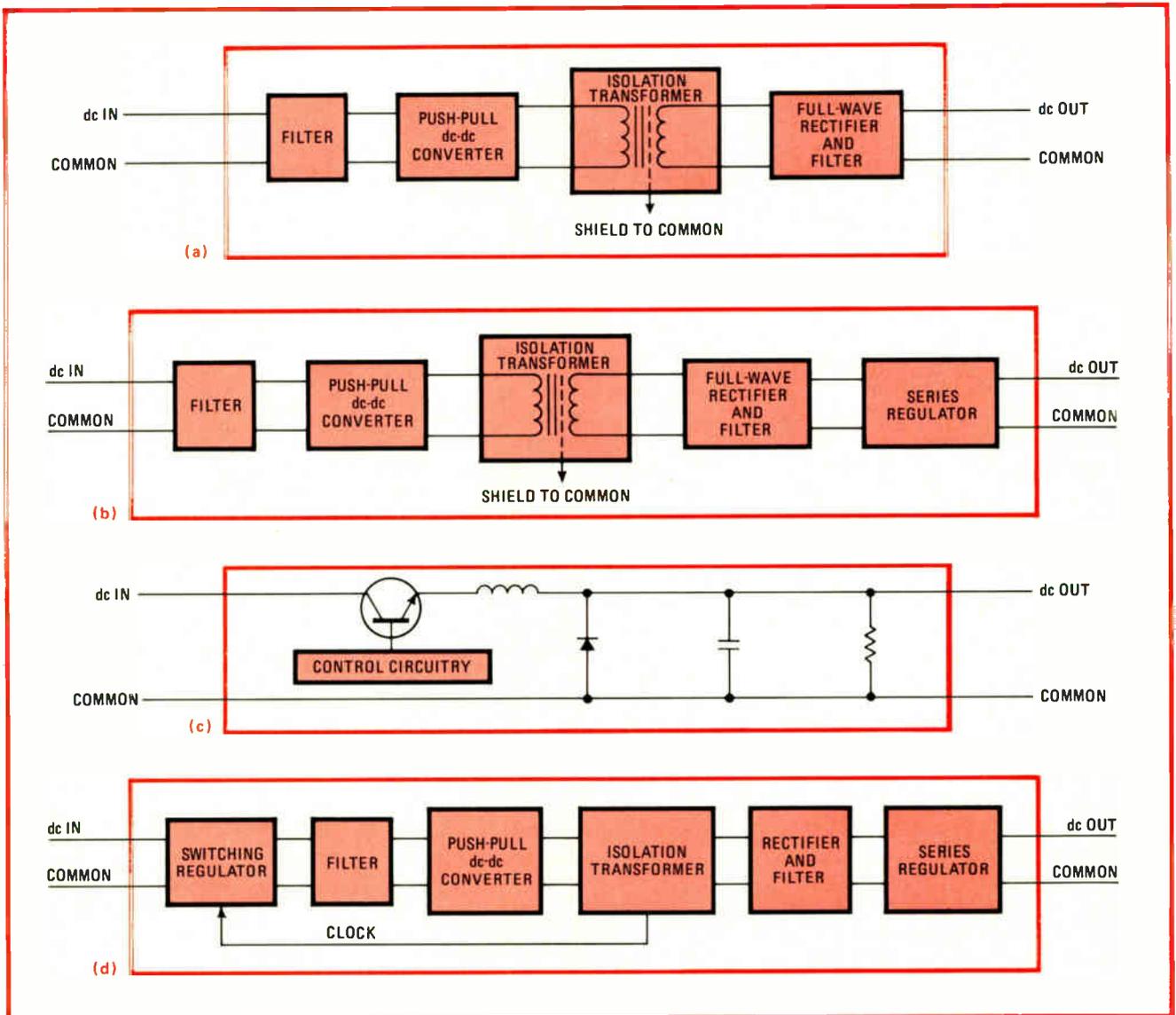
$$V_{OUT} = (t_{ON}/T)V_{IN}$$

which is independent of the load current. Here, T is the total period of the switched waveform and t_{ON} is the on-time of this period. With the switching regulator, large changes in input voltage can be accommodated by varying the duty cycle of the switched waveform.

Of these two dc-dc conversion techniques, the push-pull circuit is the more widely used because it offers inherent transformer isolation. Although its output is



1. Dc-dc conversion techniques. The push-pull saturable-transformer converter (a) chops up dc input into a complementary square wave that is then rectified and filtered. In the switching regulator (b), duty-cycle-modulated pulses are transmitted through a transistor.



2. Classes of modules. There are four basic types of commercial dc-dc converter modules—isolated unregulated (a), isolated series-regulated (b), nonisolated switching-regulated (c), and isolated switching-regulated combination (d) for extending input range.

poorly regulated—to only about 10% at best—it can provide excellent line and load regulation of down to 0.01% when it is followed by a series-pass regulator.

The switching regulator is not isolated, but it provides line and load regulation of down to 0.1% without extensive additional circuitry. Its ability to operate over a wide range of input voltage and its high power-transfer efficiency also are advantages. Since it's noisy, though, the switching regulator may require filters at its input and output. Load-recovery time is very slow—usually longer than 1 millisecond—while switching frequency is sensitive to reactive loads.

Because it contains no transformer and often no filters, the switching regulator tends to be less expensive than the push-pull converter, particularly at high power levels. Generally, the switching regulator is the more economical of the two circuits for power outputs of 50 watts or more.

The two standard forms of dc-dc conversion circuits appear in four types of commercially available modules

(Fig. 2). Two types have the push-pull circuit, one has the switching regulator, and the fourth has both. For the most part, each type can produce a single positive output for powering logic devices or a dual output (positive and negative) for supplying op amps.

Classifying commercial modules

The module of Fig. 2a is an isolated, unregulated device. It uses the push-pull circuit followed by an isolation transformer, which can be used to step up or step down the voltage and to invert it. Adding a series-regulator output stage to this module produces the well-regulated low-noise isolated converter of Fig. 2b.

The nonisolated regulated module (Fig. 2c) is simply the switching-regulator conversion circuit. To obtain isolation and the ability to operate over a wide input range, the switching regulator and the series-regulated push-pull converter can be combined (Fig. 2d). Such a module can accept inputs from 6 to 60 volts.

Circuit designers are aware that there are tradeoffs

involved in the performance characteristics of any component or device. But it is an oversimplification to say it boils down to cost versus performance—the tradeoffs are often between the performance characteristics themselves, because they influence one another.

For example, with dc-to-dc converters, increases in the surface temperature of the package are influenced by such factors as the size of the module, the amount of package power that must be dissipated (which, in turn, depends on the efficiency), and the rate at which the package can dissipate heat (which is determined by the package's thermal resistance.)

And surface-temperature rise is only one of a number of thermal factors that figure in the performance of converter modules. In fact, noise, temperature, efficiency, and input conditions are the chief determinants of module performance.

Many parameters are interdependent

Noise is influenced by converter switching-spikes, package and transformer shielding, circuit layout, and the design of the transformer, filter, and regulator sections. Converters are subject to reflected input ripple current, common-mode noise current, and output noise voltage.

Sometimes called spike-feedback current, reflected input ripple current is caused by spikes generated by the converter's power switches. If they are reflected back into the voltage source, they may falsely trigger other circuitry (such as flip-flops, gates, and counters) being supplied by the same source.

A converter module should contain a filter between its conversion circuitry and its input pins that reduces the reflected spikes by a factor of 10 or more.

Common-mode noise current consists of nanosecond-long current spikes generated by an isolated module between its input and output common terminals. Its magnitude is proportional to the converter's power rating and voltage-transfer ratios.

Common-mode noise is the least understood noise parameter and can cause mysterious systems problems.

For example, excessive noise of this type on the output common line can saturate a differential op amp that is nowhere close to approaching its true differential-input limitations.

If the transformer is not properly designed and shielded or if there are excessive leakage paths paralleling the transformer, the module's common-mode noise will be high. Moreover, if the transformer is poorly designed, the converter's isolation will be affected, and some high-frequency noise at the input may be transferred into the output common as common-mode noise current.

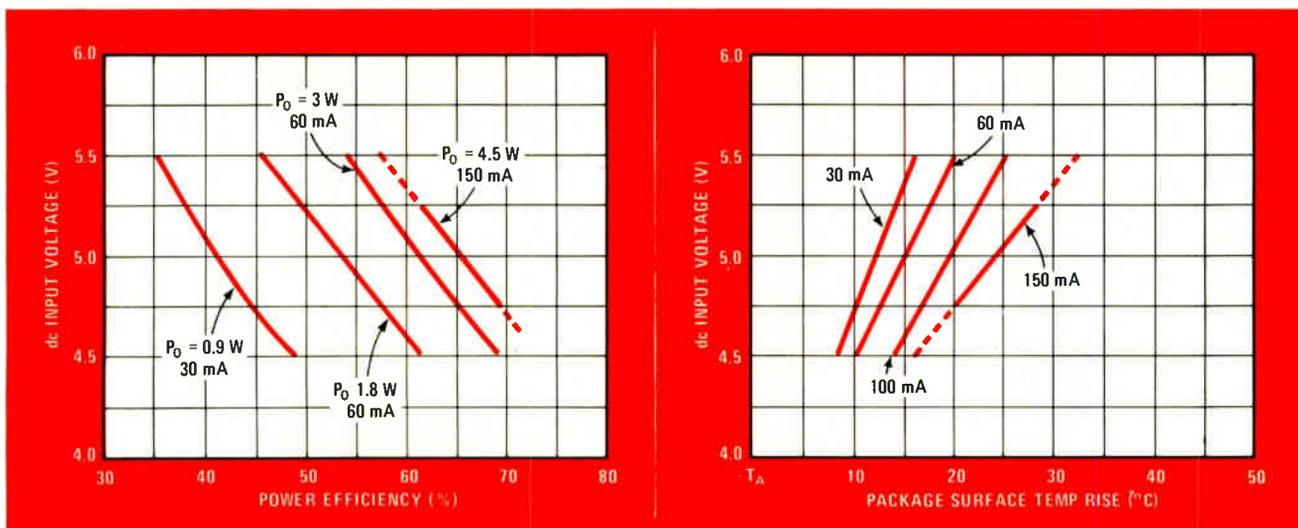
Output noise voltage (often called simply "ripple") is the ac component of the dc output voltage. It is specified as either a peak-to-peak or a root-mean-square value. Ripple voltage affects different systems to a different extent.

For instance, a narrow-band circuit may not be able to cope with more than a millivolt of true-rms noise, yet it may tolerate a short 10-mv pk-pk switching spike lasting 50 or 100 nanoseconds. Just the opposite may be true for a wideband high-accuracy circuit. On the whole, it is difficult to correlate sinusoidal behavior with switching spikes, so the user should make hardware measurements to determine a system's sensitivity to output noise voltage.

The converter's noise bandwidth, at 20 megahertz minimum, should be specified as either a pk-pk value or true rms value. If some other noise callout is used, such as an average rms value, the designer may be suitably impressed by the low number—until he tries to make a wideband true-power noise-measurement correlation. With dc-dc converters, true noise measurements require high-frequency pulse techniques, as well as appropriate terminations and ground planes.

Switching-frequency considerations

Most dc-dc converters operate at inaudible switching frequencies of 20 kilohertz or higher. Some switch at audible frequencies below 20 kHz for better power-transfer efficiency. In either case, there may be radiated ef-



3. Typical characteristics. Since the power dissipated by converter package is the difference between input and output powers, surface-temperature rise and power efficiency are related to each other, as illustrated by these curves for a standard commercial module.

PERFORMANCE RANGE OF POPULAR MODULAR DC-DC CONVERTERS

Type of converter	Output voltage (V)	Power ratings			Input voltage (V)	Regulation		Output noise voltage	Isolation voltage (V)	Unit price range	Notes
		Output (W)	Efficiency (%)	Density (W/in. ³)		Line (%)	Load (%)				
Unregulated, isolated	± 15	1 - 6	50 - 85	1.4 - 8	5 nom, -1.5, +3	none	10 - 100	30 mV pk-pk 50 mV rms	50 - 600	\$31 - \$49	filtered, raw dc output
Series-regulated, isolated	+ 5	3 - 6	40 - 70	0.73 - 3.3	12 nom, ± 2	0.02 - 0.1	0.02 - 0.3	30 - 150 mV pk-pk	300 - 500	\$69 - \$89	12-μs load recovery time
	+ 5	10 - 25	62	1.4 - 1.7	12 nom, ± 2	0.2	0.2	40 mV pk-pk	500	\$99 - \$160	external heat sink required
	± 15	1 - 4.5	40 - 65	0.6 - 3	5 nom, ± 0.25 - ± 0.5	0.01 - 0.3	0.01 - 0.1	7 - 150 mV pk-pk	300 - 500	\$39 - \$80	25-μs load recovery time
	± 15	10 - 25	55 - 65	1.3 - 1.7	5 nom, ± 0.5	0.02 - 0.1	0.02 - 0.1	40 mV pk-pk, 1 mV true rms	300 - 500	\$99 - \$165	external heat sink required
Switching-regulated, nonisolated	+ 5	5 - 10	70	1.4 - 2.8	12 nom, -3, +4	0.1	0.1	5 mV rms	none	\$31 - \$41	1-ms load recovery time
	± 15	30 - 60	85	4.3 - 8.5	19 - 28	0.1	0.1	5 mV rms	none	\$65	1-ms load recovery time

facts of electromagnetic and radio-frequency interference on other circuit components. For adequate containment of noise caused by switching, the converter should include multiple transformer and package shielding, as well as an input filter that minimizes reflected input ripple current.

If the converter does operate below 20 kHz, it should be monitored through its load conditions with an oscilloscope to assure that there is no extreme electrical or mechanical resonance point. Such a resonant condition may set up vibrations in other components that may be capable of converting them into large, intolerable noise signals. Locating the source of these signals can prove to be very difficult, because the noise occurs "all over the place."

Converter switching frequency also should be taken into consideration when noise measurements are made. A converter switching at 5 kHz will not exhibit the same amplitude of wideband noise that it does when operating at 20 or 30 kHz. Noise-measurement bandwidths should allow for such differences.

Relating thermal and power parameters

The thermal performance of a dc-dc converter module is influenced by a number of factors, such as power-transfer efficiency, package-surface-temperature rise, operating temperature range, external heat-sinking requirements, and output short-circuit protection.

So the user should carefully examine all thermal parameters for both operating and fault modes. For the

operating mode, these include surface temperature rise, derating requirements, and external cooling and heat-sinking requirements. For the fault mode, they include the conditions under which the output is protected against shorts, how long and at what temperature the output can be shorted, and the worst-case input fault-mode current.

To provide reliable operation, converters generally require extremely conservative design. For example, in the full rated operating mode, a reliable and safe converter may well consist of components operating at half or less of their limits. Such a design is necessary if the converter is to survive a long-term fault condition (say, an eight-hour output short at its upper temperature extreme and a high line-condition) and if the converter's package surface-temperature-coefficient is to be held to a reasonable level, without power derating or any external heat sinking.

Surface-temperature rise, which should be specified for the hottest point on the case, is a measure of the temperature increase (above ambient) of the module's surface for each watt dissipated in the package. It is commonly specified in degrees celsius per package-watt dissipated. The power dissipation of the package is actually the difference between input power and output powers:

$$P_P = P_{IN} - P_{OUT}$$

Power-transfer efficiency, the ratio of output power to input power, expressed in percentage, is yet another im-

portant characteristic of dc-dc converters. In portable equipment applications, it directly influences how long the batteries may be expected to last. Also, it tells the user how much power the converter's package will be dissipating, and it is linked with the device's surface-temperature rise.

Converter data sheets usually include graphs that help the designer determine power efficiency and surface-temperature rise over a wide range of input and output conditions. Figure 3 shows typical graphs for a commercial converter module. For example, for an input voltage of 5 v and an output power of 4.5 watts at 150 milliamperes, the power efficiency is 65% and the surface-temperature rise is 24°C.

Looking over input conditions

Generally, an isolated series-regulated dc-dc converter operates over a fairly narrow input range. To circumvent this limitation, the user can, of course, pay a premium to the converter manufacturer for a special unit. However, if he is able to compromise a bit on output power, he may be able to use a standard model over a wider input swing.

For instance, suppose a converter puts out 15 v at 150 mA and operates over an input range of 5 ± 0.25 v. If the output rating is reduced to 15 v at 120 mA, then the input range may be extended to 5 ± 0.5 v.

Input voltage swing can also be broadened by adding a nonisolated switching regulator between the voltage source and an isolated regulated converter.

It is essential for a dc-dc converter to receive the continuous input current it requires, so that it can operate at rated efficiency and output power. This means that the cross section of the printed-circuit conductors to the device's input pins should be big enough to permit adequate current flow.

Also, if separate sense leads for monitoring and adjusting the input voltage level are used, they should be right at the device's input pins. With such direct sense leads, the IR drop of the input power wiring will not reduce the effective input voltage the converter sees. If the required input current is not specified on the data sheet, it can be computed from:

$$I_{IN} = (V_{OUT} \times I_{OUT}) / (V_{IN} \times P_{EFF} \times 10^{-2})$$

where P_{EFF} is the power efficiency expressed in percentage. (It is usually also helpful to know the converter's no-load and full-load input currents.)

Another important input consideration is start-up after an output fault condition has been removed. A well-designed converter should restart at an input level far below its nominal requirement and operate even if its regulator section has dropped out.

Current foldback, in which the output current is reduced to two thirds of its full rated value during an output short circuit, is a common protective technique. Although it does reduce package dissipation during a fault condition, it sometimes prevents the output from returning to its normal level, even after the short has been removed. In this latchdown state, the converter simply will not recover automatically.

Also, current-foldback circuits can produce fault-

mode oscillation problems. Constant-current techniques seem to be a better alternative for avoiding latchdown problems, provided that the converter has an appropriate thermal design.

Even after the output short has been removed, some converters remain fully or partially latched down, and their input must be turned off and back on again before they recover. Other converters will not restart after an input power interruption, as long as their output is still shorted. And even if the converter does normally recover after a power interruption, large-value capacitors, distributed throughout the circuitry that the converter supplies, can inhibit startup.

So an important consideration in choosing a module is the type of fault recovery needed. Moreover, the converter should be able to restart even though a small reverse current is being fed back through the system and injected into its output—which is not an unusual system condition at all.

Of course, reliability is also a major factor in selecting a module. Since many dc-to-dc converter manufacturers burn in their units before shipment, reliability generally is good. As a rule, though, users should look for units with a mean time before failure of 75,000 hours or longer.

Selecting a commercial converter.

The user should always try to select the off-the-shelf module that most closely matches needs, without paying for performance not essential to the application. The table shows the range of performance available from standard dc-to-dc converter modules.

Sometimes, additional savings can be realized by having the vendor eliminate extraneous features. For example, output fault protection may not be essential, or the ability to operate at such typical high temperatures as 71°C or 85°C may be unnecessary.

The type of converter more or less determines regulation performance. An inexpensive switching-regulated module can provide both high efficiency and fairly good regulation in applications where isolation, low noise, and fast load recovery are not required. If low noise or isolation is a must, then a transformer-isolated series-regulated converter is the answer.

Of the more popular modules, many single-output 5-v devices operate from a nominal input voltage of 12 v, whereas most ± 15 -v dual-output units require a 5-v nominal input. An exception is the high-power dual-output switching-regulated converter, which can accommodate inputs of 19–28 v. It also provides the highest power-transfer efficiency, usually as high as 85%, and the greatest power density, up to 8.5 W per cubic inch. On the other hand, the best noise performance is obtained from the dual-output isolated series-regulated units. As a rule, isolated converter modules can provide up to 500 v or so of isolation between input and output. □

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Films dominate components conference

by Lucinda Mattera, *Components Editor*

The San Francisco gathering will also, for the first time, allot separate sessions to semiconductors, optical devices, and organic materials

□ Thin- and thick-film technologies are extending their sway and influencing all kinds of components, active as well as passive. That will be abundantly clear at the 26th Electronic Components Conference, which opens in San Francisco on April 26.

Audiences at the dozen technical sessions will hear about:

- A gas-discharge display so simple that it could be made very large—basically it's no more than a pattern of conductive thick film screened onto a glass base.
- New versions of passive components, such as an electronically tunable thin-film inductor, a thick-film automotive thermocouple, and thin-film resistor networks that push thermal printing heads to a high level of resolution.
- High-performance hybrid circuits, including an active

digitally programmable filter that uses both thin and thick films, plus a thin-film LC delay line that's fast enough for the likes of emitter-coupled logic.

Also spotlighted will be: cleverer techniques for splicing and terminating fiber-optic bundles for data transmission, methods for protecting MOS chips against damage from electrostatic discharge, and improved silicones for encapsulating optoelectronic devices and beam-lead integrated circuits.

Altogether some 71 papers will blanket all major component areas—passive, active, electromechanical, optical. That kind of coverage certainly backs up the claim of program chairman John H. Powers, Jr., that the conference is still the most broadly based of all the electronic components meetings. Powers is with IBM System Products division in Hopewell Junction, N.Y.

In other ways, though, the conference is breaking with tradition. For the first time in 15 years, it will be held on the West Coast and not in Washington, D.C. In another break with precedent, the organizers are willing to register attendees for just one day, instead of the formerly obligatory three days, because they want to attract both local attendees and visitors to the companion exhibition, Design Engineers' Electronic Components show. (For a preview of new products to be introduced at the show, turn to p. 109.) Also without precedent is the fact that optical components, semiconductors, and organic materials are each getting a session of its own.

The two new component sessions

By giving optical components equal time, the conference is intentionally granting them the same status as other classes of components, says Powers. The innovation collects developments in displays, light-emitting diodes, and fiber optics under one banner.

Large-area gas-discharge displays have been a contradiction in terms. But now Beckman Instruments Inc., Helipot division, Fullerton, Calif., has developed a screened planar technology for making low-cost large-area displays that can show hundreds of characters—both alphanumeric and special symbols. The displays sandwich the gas between a glass panel and a substrate on which a conductive thick-film pattern is screened.

High-brightness LEDs can now be fabricated several at a time on the same substrate, report researchers at Monsanto Commercial Products Co., Electronics division, Palo Alto, Calif., and many character sizes are possible. Until now, very bright LEDs have usually been discrete devices.

In fiber optics, the impulse to growth comes mainly from data communications. Both AMP Inc. of Harrisburg, Pa., and ITT Cannon Electric of Santa Ana, Calif., will be presenting papers on improved methods for splicing and connecting high-density bundles of fibers for data transmission. In each case, new adhesive and polishing techniques minimize the chances of fibers breaking during connection. Also, as shown in Fig. 1, AMP is proposing a circular plastic termination with a tapered bushing that compresses the fiber bundle for the best fit. It is intended for medium- and high-loss cables. A third company, Meret Inc. of Santa Monica, Calif., will describe transmitter and receiver modules

for sending data over the fiber-optic lines at very high speeds.

Semiconductors, too, have achieved a session of their own. Here, because of the ever-expanding use of integrated circuits, their reliability comes in for attention. To underscore that point, chairman Don Carmean of Hewlett-Packard Co., Palo Alto, Calif., notes that more than 50% of the components in an instrument nowadays are ICs.

MOS devices and thyristors both have their vulnerabilities. IBM Corp., System Communications division, Manassas, Va., is therefore reporting on a hybrid circuit that protects a large-scale-integrated MOS device against its perennial enemy—electrostatic discharge. The hybrid device is placed inside the MOS package, and to extend protection up to 6,000 volts and beyond, IBM recommends plating a spark gap on the outside of the package. General Electric Co., Static Power Component Operation, Collingdale, Pa., is concerned about thyristors with unequal recovery times and the danger of destroying the devices when connecting them in series. The firm will discuss the method it has developed for non-destructive series connection.

In a second paper, this time on Josephson tunneling logic, IBM, Thomas J. Watson Research Center, Yorktown Heights, N.Y., analyzes what electrical characteristics are best for the interconnection systems of such logic. These logic circuits (Fig. 2) must operate at cryogenic temperatures, near absolute zero, but there they offer the advantages of high speed, high density, and low power.

In another first for the conference, microwave semiconductors will be discussed. The topics are the design of microwave power transistors and the characterization of Ka-band impatt diodes.

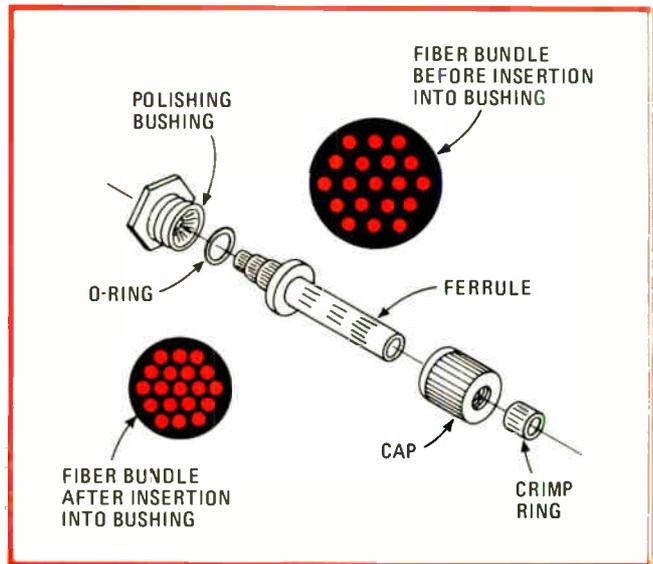
Passives shrink—but not in importance

Unlike their active counterparts, passive components have long been a mainstay of the conference and are represented by two strong technical sessions, one of them devoted exclusively to ceramic and film capacitors. "Almost all efforts nowadays are directed toward miniaturizing components," observes John J. Bohrer, who is chairing the general session on passive devices and is with the Electronics Components Group of TRW/IRC in Philadelphia.

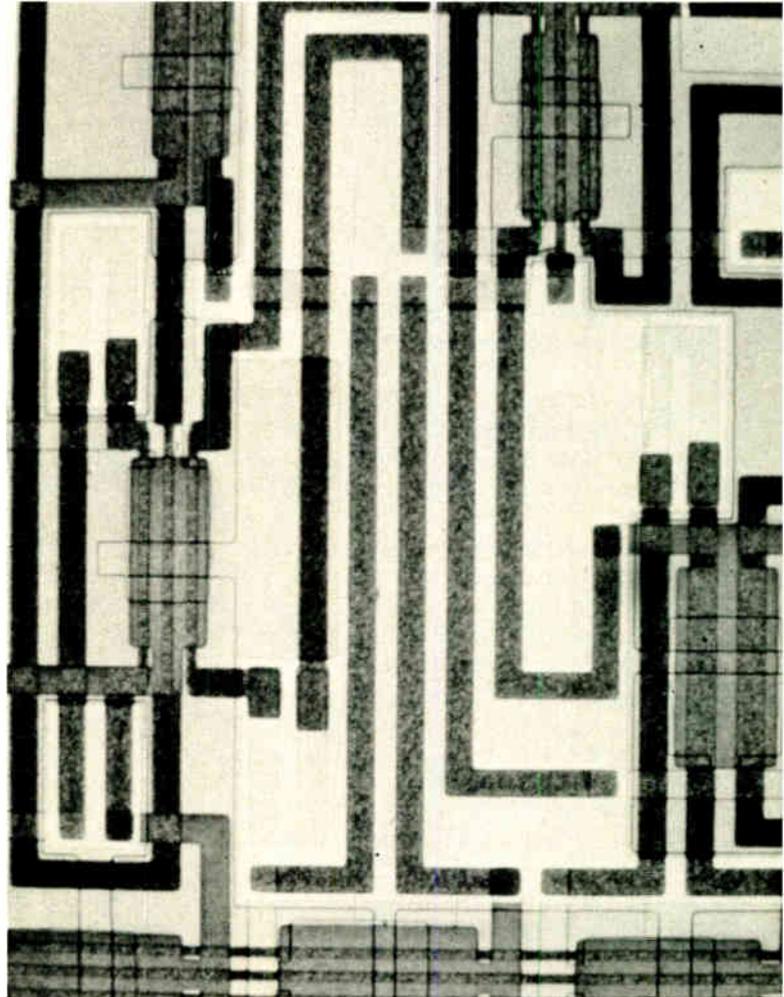
For example, a miniature thin-film tantalum-nitride resistor network, to be described by TRW Network Operations, Burlington, Iowa, has impressive power dissipation for its size—a tiny resistor measuring only 0.075 by 0.075 mil can handle up to 0.4 watt at 70° C. Intended for terminating logic lines in computers, it's packaged in a three-lead epoxy housing.

Obtaining inductance with IC techniques has been a long-standing problem. However, an IC-compatible miniature thin-film inductor, proposed by Egypt's Ain Shams University, can be tuned electronically. By evaporating a permalloy thin film onto a substrate, researchers there have built experimental devices with inductances of 4.5 microhenries in an area of only 0.5 by 0.5 centimeter. At 15 megahertz, quality factor is 65.

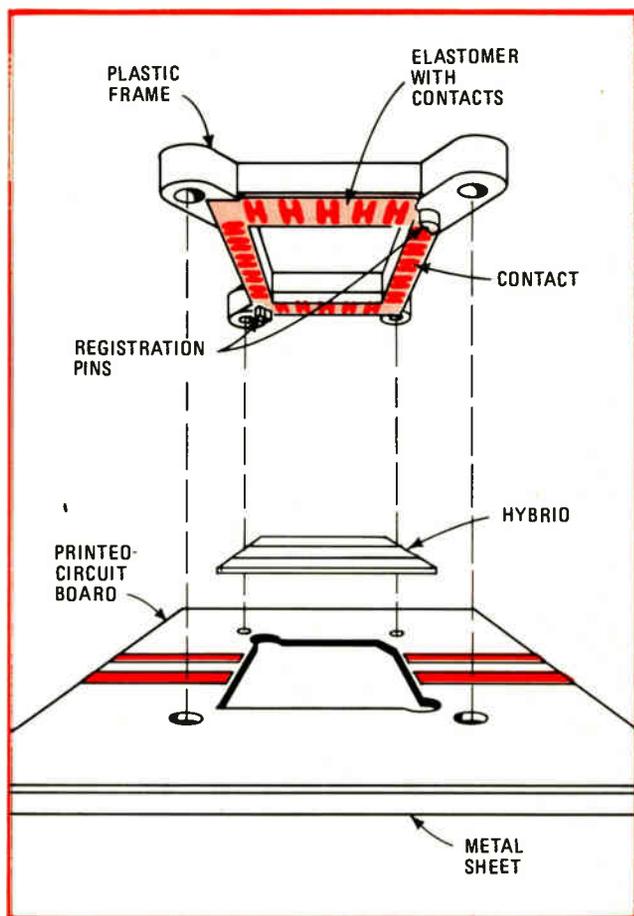
Miniaturization is also making new demands on elec-



1. Terminating fiber optics. Plastic connector from AMP has tapered bushing that squeezes fiber bundle for optimum fit without overstressing it. The technique is intended for terminating jacketed high-density fiber-optic bundles being used for data transmission.



2. At absolute zero. Besides high density, Josephson tunneling logic offers fast operating speeds and low power levels—but at cryogenic temperatures. IBM has investigated the electrical characteristics of interconnection systems for these semiconductors.



3. Hybrid Interface. Connector developed by Tektronix eliminates need for impedance matching when hybrid is interfaced with printed-circuit board. The H-shaped metal contacts, which are flexible foil-like conductors, are backed up by an elastomer.

trolytic capacitors, and Japan's Fujitsu has developed an etching method for forming aluminum electrolytics that adds dramatically to their capacitance. The process increases their effective dielectric surface by a factor of 150.

New low-cost metalization systems for multilayer ceramic capacitors are getting attention in the capacitors-only session, notes its chairman, Raj. B. Amin, from San Fernando Electric Manufacturing Co., San Fernando, Calif. Until recently, the electrode system for these capacitors used so much in the way of precious metals that it contributed heavily to their cost, says Amin. "For certain multilayer units, the electrode cost was as much as 50% of the total," he observes.

On this subject, USCC/Centralab, Electronics division, Globe-Union Inc., Los Angeles, describes a nickel electrode system that can cut device cost to as little as half that of precious-metal ceramic capacitors. The tradeoff is somewhat different electrical properties from those of the precious-metal units.

Other papers in this session will examine: methods for attaching ceramic chip capacitors, designing with high-frequency ceramic chips, low-inductance film capacitors, and spurious signal generation in plastic-film capacitors.

Connectors and contacts share the session chaired by Robert H. Van Horn of Bell Telephone Laboratories, Columbus, Ohio. His aim, he says, is "to develop an in-depth understanding [of the ability] of the connector structure to do its job over a long period of time."

Understanding the connection

Supporting this theme are two papers from Bell Labs. One, from the facility in Whippany, N.J., concerns crimp terminations for dry-circuit applications. Apparently, the crimp that gives the best electrical performance is usually deeper than the one required for greatest mechanical strength. A deeper crimp can therefore improve electrical performance yet still be strong enough mechanically.

The second Bell paper, from the laboratory in Columbus, Ohio, investigates three new metal systems for preventing dry-reed sealed contacts from sticking together. In most existing contact systems, a gold or gold-alloy layer plated over the reeds, plus extra circuitry, helps the contacts to reopen. Three proposed noble-metal systems—soft diffused gold or ruthenium or rhodium—cut overall costs by doing the job without the aid of any circuitry.

Another interesting connector development is being reported on by Tektronix Inc. of Beaverton, Ore. The company has a connector, called Hypcon, for interfacing a hybrid with a printed-circuit board. The device consists of a flexible metal conductor, almost a foil actually, backed up by an elastomer. As shown in Fig. 3, it goes around the periphery of the hybrid, interfacing the circuit with the pc board and eliminating the need for impedance matching.

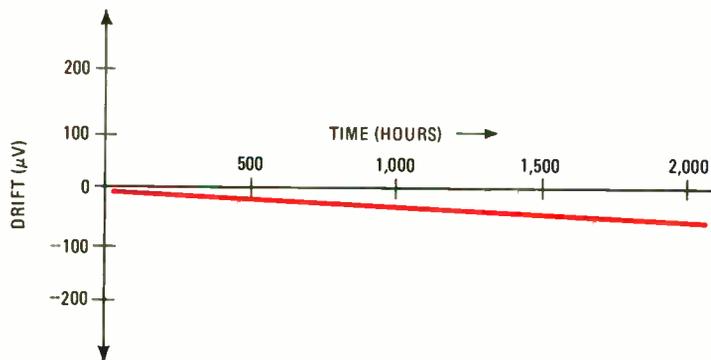
Films get better ratings

Because of the sustained growth of hybrid circuits over the last few years, thick and thin films have been flourishing. "Both technologies are maturing, so that old problems are being solved," says Robert W. Berry, chairman for the session on thick and thin films, who is with Bell Telephone Labs, Allentown, Pa. Two related sessions cover hybrid circuit applications and technology.

In thick films, resistor voltage limits are being raised and conductor costs are coming down. For thick-film resistors, E.I. DuPont deNemours & Co., Electronic Materials division, Wilmington, Del., has a new high-voltage-stable material that can withstand transient voltage gradients of 75 kilovolts per inch with only negligible change in resistivity. In thick-film conductors, Motorola Communications division, Plantation, Fla., has found that platinum-silver can be substituted for palladium-silver, the popular alloy at present. Although platinum is more expensive than palladium, less platinum is needed, so that overall cost is lower.

A rather novel use of thick film, to be described by Engelhard Industries division, Engelhard Minerals and Chemicals Corp., Edison, N.J., is a thermocouple for automotive and similar sensing applications. Ordinary thermocouples are two-wire structures, and the new device is like them—it consists of two different thick-film materials, palladium-platinum and gold-palladium. It

TEMPERATURE (°C)	OUTPUT (mV)
0	0
100	3.6
200	7.6
300	11.9
400	16.4
500	20.9
600	25.3
700	29.7
800	33.8



4. Thermocouple performance. Thick-film thermocouple made by Engelhard Industries produces an output of about 34 millivolts at 800°C, as indicated in table. Stability plot shows output drift is only 50 microvolts or so after 2,000 hours of operation.

can sense temperatures over the range of 0°C to 800°C to within an accuracy of ±1% (Fig. 4).

Another unique structure—this time a thin-film device—is a tantalum-nitride resistor network for use in high resolution thermal printing heads. Developed by Japan's OKI Electric Co., it permits a printout resolution of 5.3 dots per millimeter. Altogether, there are 512 resistors to a line (or row) over a head length of 96 millimeters.

The intent of the session on hybrid applications is to illustrate state-of-the-art circuit developments by presenting specific applications examples according to chairman Richard A. Rikoski from the Illinois Institute of Technology in Chicago. For example, Motorola Communications division, Fort Lauderdale, Fla., has developed a switchable active bandpass filter made with both thick and thin films and a custom linear IC. A digitally programable state-variable filter, it can produce any one of 12 different frequency responses.

From Japan's Susumu Industrial Co. comes the first LC delay line to be compatible with high-speed circuits like emitter-coupled logic. In essence, the delay line is a distributed LC network, made up of inductors formed by etching a copper plate and thin-film capacitors deposited by plasma polymerization. Conventional delay lines, consisting of discrete capacitors and wirewound inductors, respond too slowly and distort waveforms too much for use with high-speed logic. Response time of this film hybrid, however, is 1.5 nanoseconds to a 1-ns pulse, and waveform distortion is only about 10%.

The session on hybrid technology will emphasize new processing techniques for thick- and thin-film hybrids, notes chairman Mauro Walker from Motorola Communications division in Fort Lauderdale, Fla. Zenith Radio Corp., Elk Grove Village, Ill., reports on a technique for fabricating glass-passivated thick-film capacitors that overcomes the yield and reliability problems that plagued earlier attempts to produce the devices in volume. Zenith perfected the technique for a television varactor tuner in which thick-film capacitors and thick-film resistors are screened onto the same substrate (Fig. 5). Similarly, Honeywell Inc., Aerospace division, St. Petersburg, Fla., has developed an efficient method for attaching active elements in complex circuits, which

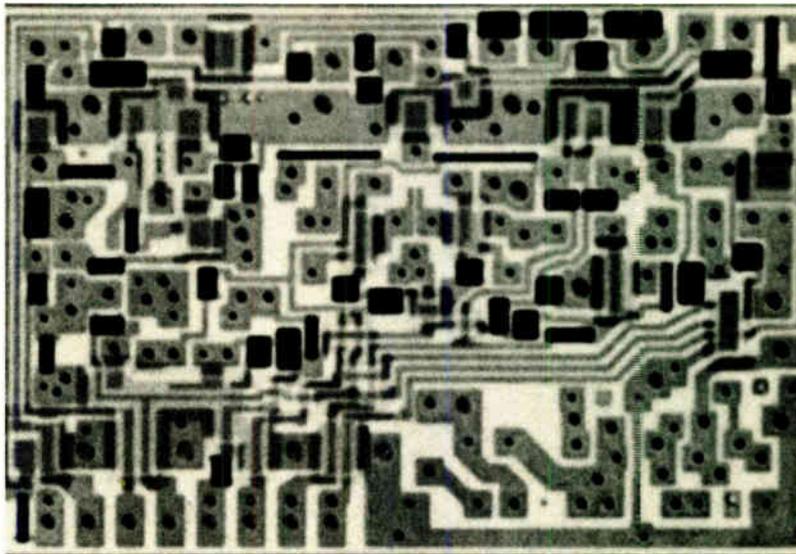
also tend to be subject to poor yields and excessive repairs.

Also in this session, Hughes Aircraft Co., Culver City, Calif., will describe a major step in hybrid development—taking hybrids up to the module level. As indicated in Fig. 6, these so-called large-area hybrids can contain both discrete and chip components, both thick- and thin-film hybrids and even multilayer thick-film interconnections. Hughes, which has built hybrid modules as large as 4 by 4 inches, can also hermetically seal selected areas of the substrate by means of a repairable process.

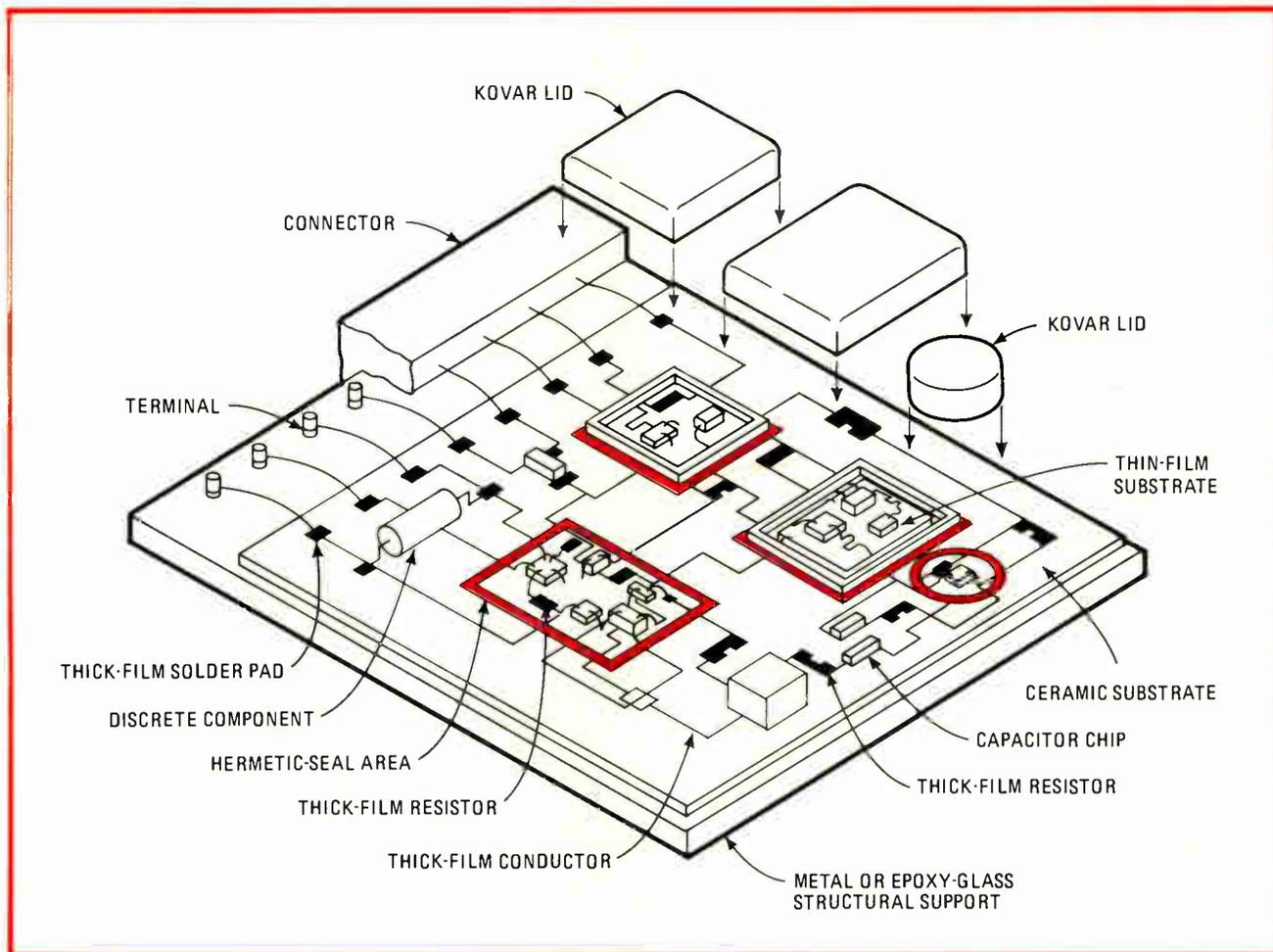
The fine art of fabrication

In the session on manufacturing technology, Bendix Corp., Kansas City, Mo., will present two related papers on improved techniques for processing two-layer thin-film circuits—specifically, those made up of a tantalum-nitride resistor system and a chromium-gold conductor

5. For TV tuner. Zenith Radio Corp. puts thick-film resistors, as well as glass-passivated thick-film capacitors, on the same alumina substrate for a television varactor tuner. Here, black rectangular areas are resistors, while dark gray areas are capacitors and crossovers.



6. Next step in hybrids. Large-area hybrid from Hughes can include conventional thick- and thin-film hybrids, in addition to discrete and chip components. Selective areas of the substrate can even be hermetically sealed. Circuits as large as 4 by 4 inches have been built.



system. One paper describes a rotating system for vacuum-depositing both conducting layers, plus their interconnects, in a single pumpdown. The other deals with a dry-film photolithographic process for obtaining the resistor patterns on both sides of the substrate simultaneously. Two "dry" sheets of photoresist are laminated to each side of the substrate. "Wet" methods require separate steps.

Another thin-film advance in manufacturing technology—an electroplating technique for depositing a thin film of gold—will be disclosed by Bell Telephone Labs and Western Electric in Allentown, Pa. Developed especially for circuits going into plastic dual in-line packages, the electroplating is done at a very high current density, to reduce the gold's hardness and improve its bondability.

A separate session on wire bonding covers parallel-gap (resistance) welding of gold ribbon to thick films, the influence of lead-frame thickness on the mechanical properties of the thermocompression bond, and the effect of nonmetallic constituents in fired thick-film gold metallization on thermocompression bonding.

Next to last, but not least, the session on reliability offers at least two noteworthy papers. Rome Air Development Center, Griffiss Air Force Base, N.Y., will re-

view several year's worth of data on how moisture affects microcircuits. The report investigates how much moisture microcircuits can withstand before failing, as well as the degree of moisture that produces certain failure modes. Another paper, to be given by Westinghouse Electric Corp., Defense and Electronic Systems Center, Baltimore, Md., describes practical procedures for discovering excessive adhesive degradation in thick-film gold-alloy solder systems.

Finally, ways to improve component reliability are also the thrust of the third new session—the one on organic materials. They're useful mainly in hybrid circuits, for bonding chips to substrates, sealing packages, and encapsulating circuits. summarizes chairman Isaac H. Pratt, Integrated Electronics division, U.S. Army Electronics Command, Fort Monmouth, N.J.

A new silicone epoxy from Dow Corning Corp., Midland, Mich., promises better reliability for molded plastic hybrid packages. Likewise, a silicone encapsulation system, developed by Honeywell Inc., St. Louis Park, Minn., can extend the life of optoelectronic devices in harsh environments. And to give Bell Labs from Allentown, Pa., the last word, its paper at this session will describe a special silicone encapsulant for beam-lead chips. □

THE MATCHMAKER

Telefile introduces the only disk system flexible enough to match any minicomputer with any of the hot, new 3330-type drives. Big disk storage at a mini price.

Telefile now has available the most flexible large capacity disk system for minicomputers on the market today. The Matchmaker. It comes two ways:

As a disk system for users (DS-16-C) where we match your minicomputer with any of the latest 3330-type technology drives you want. Telefile supplies the complete package.

As an OEM disk controller. You can order just controllers alone (DC-16-C) and mix and match minicomputers and drives to satisfy your customer's whims and storage requirements.

Either way, disk system or controllers alone, you are assured of flexibility, performance features, and price no one else can match.

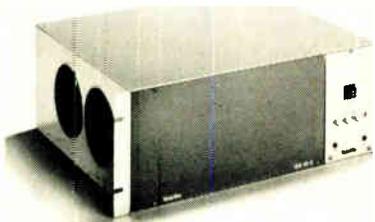
Each system stores up to 1.2 billion bytes.

You can match just the right drives to meet your storage capacity needs all the way from 13.3 million to 1.2 billion bytes per controller. Each DC-16-C Matchmaker controller handles up to four drives. Minicomputers never had it so good.

Choose any of the latest drives.

You've seen them announced one-by-one and they're coming on strong. CalComp's Trident. Control Data's Storage Module. Diablo's 400 Series. The Ampex 9000's and Memorex 677's. Each builds upon IBM 3330 technology, which means higher storage densities and new circuitry for superior reliability.

To switch drives, simply change one controller circuit board. We've timed it at 63 seconds flat!



Compatible interfaces to eleven minicomputers.

We're designing a complete line of compatible interface boards to match up to many minis: Data General, DEC, Interdata, Keronix, D.C.C., Microdata, Honeywell, Lockheed, H-P, Varian, and Cincinnati Milacron. Simply fit our tailor-made computer interface module inside your computer chassis and you're in business. If you have another type mini, we'd be glad to design one for you.

Or you can design your own interface.

Your designers may want a piece of the action. Our general interface board makes it easy. Your board will tie in directly, bringing big disk storage to any 16-bit minicomputer.

A controller so small you can even hide it.

The Matchmaker is our smallest controller yet. It is totally self-contained right down to its power supply and cooling system. It's small enough to tuck away in a drive housing or in a rack above, below, or even behind the computer. Out of sight.

We'll even make you a faceplate.

If you want to show the Matchmaker off, we'll make a bezel to match your computer panel. Private label it and call it yours. There's no end to the flexibility.

Easy "front door" maintenance.

Five circuit boards slip right in from the front of the DC-16-C Matchmaker. A disk interface board, a general interface board, a command/timing board, a memory/address board, and an optional maintenance board for offline disk pack formatting and test exercising.

Unmatched features

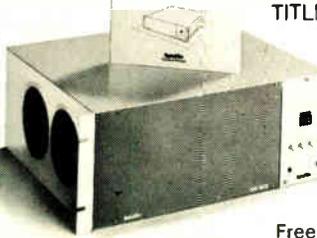
- Contains 512-byte buffer for data rate matching
- Variable data search and read
- Block transfer of data up to mini-addressing capacity
- Offset positioning and data strobe controls
- Write protection to the sector level
- Sequential or staggered sector addressing
- Defective track relocation and alternate track addressing
- Overlapping seek capability
- Multi-sector operations across head and cylinder boundaries

We wrote the book on disk controllers, and you can get it free.

For years, we've helped minicomputer users grow their disk capacities. Now our Matchmaker system is a quantum leap forward. A new in-depth, hot-off-the-presses Matchmaker technical manual gives you all the facts. Write for it. Prove to yourself that this is one disk controller no one else can match.

Telefile

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Nomograph shows bandwidth for specified pulse shape

by Franc E. Noel and James S. Kolodzey
IBM Corp., Poughkeepsie, N. Y.

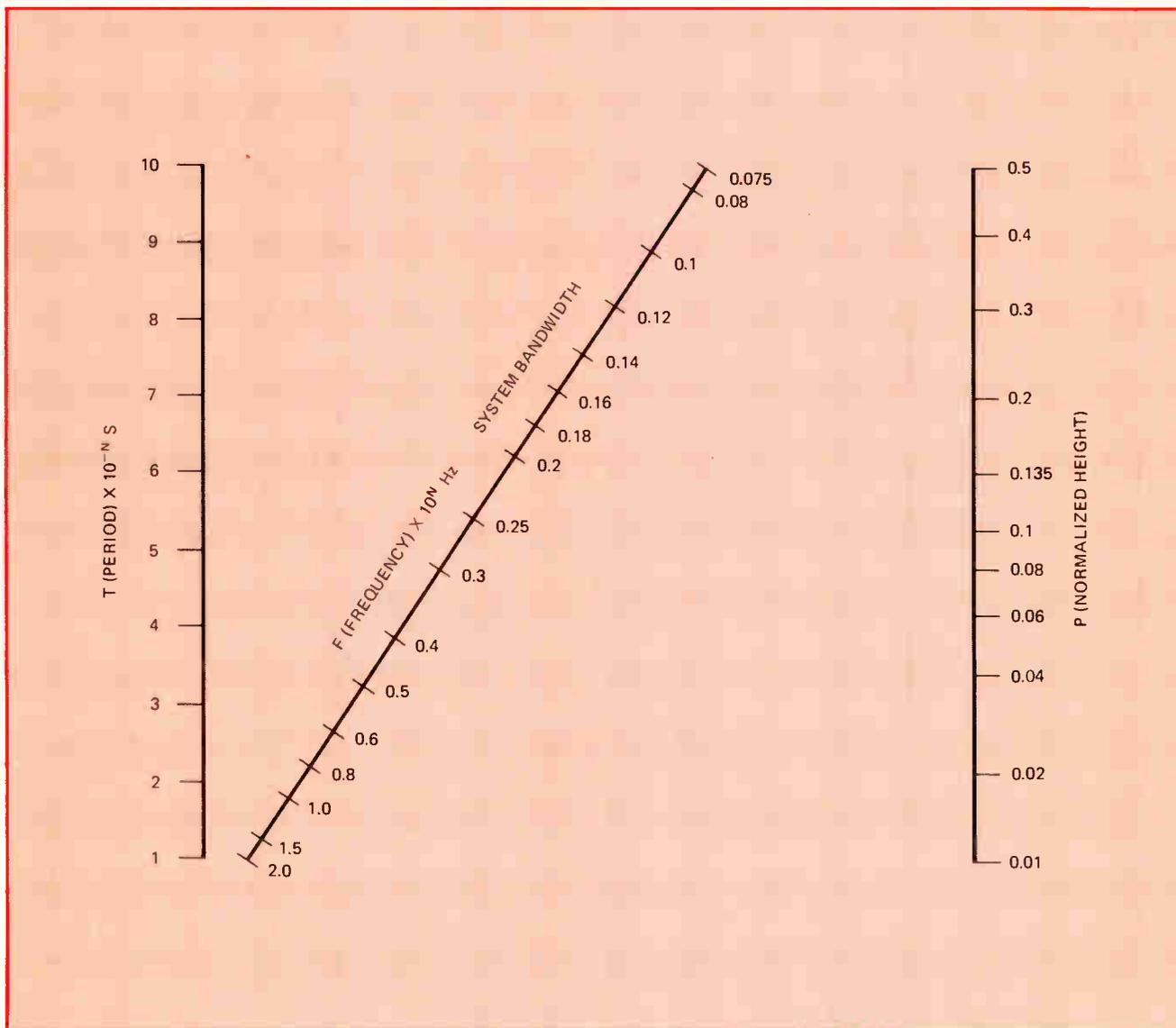
In a digital communications system, the bandwidth of the transmission channel determines the sharpness of a received pulse. For a communication channel where the received pulses may be treated as gaussian wave shapes, the system bandwidth required for a specified pulse shape is:

$$F = (2/\pi T)[2 \ln(1/P)]^{1/2}$$

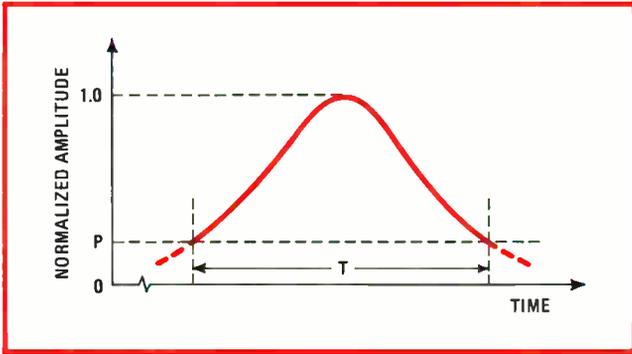
where, as in Fig. 2, T is the width of the time slot, P is the normalized height of the gaussian pulse at the ends of the time slot, and F is the 2σ bandwidth of the channel, where σ is the standard deviation of the pulse. The bandwidth that is given by this expression contains 95.45% of the pulse power.

The choice of the 2σ point is an arbitrary decision based on the fact that the frequency spectrum of the gaussian pulse is down 8.7 decibels at this point. Therefore, a linear system with a bandpass flat to this point provides a reasonable reproduction of the time-domain pulse.

The bandwidth required to pass a particular pulse is



1. **How wide the band?** This nomograph shows the bandwidth F that contains over 95% of the energy in the spectrum of a gaussian pulse, where the duration of the pulse is T and the normalized amplitude of its end points is P (as shown in Fig. 2).



2. Pulse parameters. Time-domain representation of gaussian pulse shows normalized amplitude P at edges of time slot T . A low value for P gives low spillover into next slot, and therefore low error rate, but requires large bandwidth in transmission system.

given by the nomograph in Fig. 1. The values of the time slot, T , and normalized amplitude desired at the ends, P , are connected with a straight edge to determine the frequency axis crossover. For example, a time pulse that is down to $1/e^2$, or 0.135, at the edges of a 12.5-nanosecond time slot can be passed with a system bandwidth of 102 megahertz. □

Scope-triggered register freezes data for display

by Matthew L. Fichtenbaum
General Radio Co., Concord, Mass.

As an aid in logic analysis and program debugging, a standard delaying-sweep oscilloscope, such as the Tektronix 465 or 475, may be augmented with some logic circuitry, to capture and display the data present on a data bus.

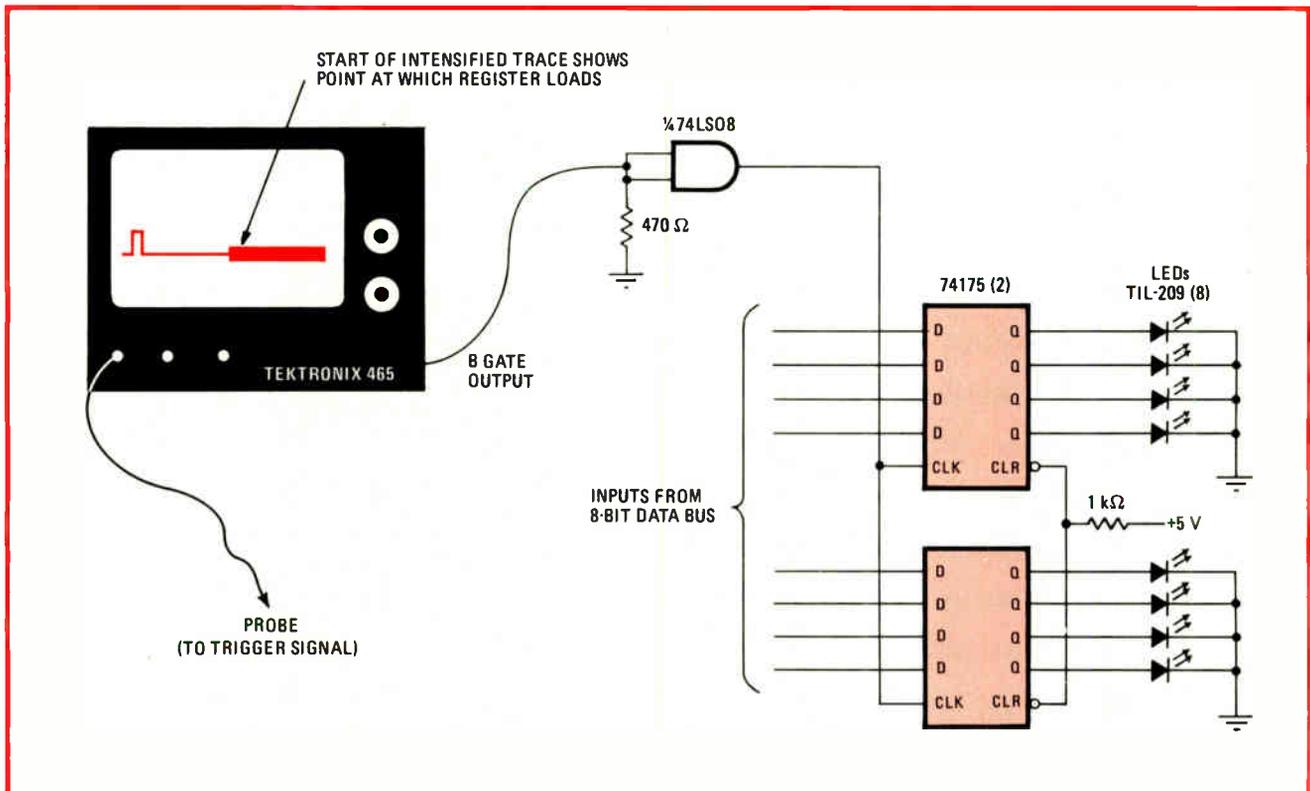
The two time bases of the scope perform trigger and trigger-delay functions, the delayed trigger clocks a reg-

ister to store the data, and light-emitting diodes display the stored levels. The data is not displayed on the face of the scope, which merely shows the timing of operations including a visual indication of the point at which the data state is stored for display by the LEDs.

The scope is operated in a repetitive or nonrepetitive mode, as appropriate. Generally the technique is used to study a repetitive process such as a program loop.

The figure shows a register of two 74175 quad D flip-flops, used to "freeze" the state of a multibit bus, such as a microprocessor's data bus, for examination. The scope trace is triggered by some major timing signal in the program, e.g., an input or output pulse, and its length corresponds to the program's duration.

The scope is operated in its A-intensified-by-B mode. Using the delay-time control of the scope, the bright-



Rudimentary logic analyzer. A lit light-emitting diode shows a logic 1 on the microprocessor data bus and a dark LED shows a logic 0—both at the moment that is determined by the setting of the delay-time knob on the oscilloscope. Scope trace indicates duration of program loop; the trace becomes brighter at the point where the data sample is loaded into the register to light the LEDs.

ened area of the trace that represents the delayed sweep is positioned to begin at the time point of interest.

When the B gate output goes high at the start of the delayed sweep interval, it loads and latches the register, which allows the LEDs to display the data levels that were on the bus at the instant of loading. As the delay time is advanced, the display lights show successive data values.

A 74LS08 AND gate is used as a buffer between the

scope and the clock terminals of the quad D flip-flops.

The register, which in this example is eight bits wide, should be as wide as the bus being examined. The ordered array of LED's is easier to interpret as a number than multiple scope traces, and the storage of the register makes it possible to examine nonrepetitive events.

If desired, the LEDs may be replaced by decoders and numeric readouts to present a binary-coded-decimal, octal, or hexadecimal interpretation of the data. □

Audio continuity tester indicates resistance values

by Calvin R. Graf,
San Antonio, Texas

A continuity tester built around a 555 timer audibly and visually indicates a wide range of resistance values. The unit, which can be assembled for less than \$10, is especially handy for testing devices without having to glance from test probe to meter and back again. However, by merely changing the value of one resistor, the tester can function as a multivibrator.

The meter can indicate by tones over a loudspeaker or a headset forward and reverse continuity conditions from 0 to more than 30 megohms for such devices as resistors, diodes, transistors, capacitors, and light-emitting diodes. In addition to the audio output, a LED serves as a pilot light and flashes when the output frequency falls below 10 to 12 pulses per second. The output is a square wave, and an audio pulse sounds each time the LED is turned on or off.

The tester can also determine the charges stored in mercury and nickel-cadmium battery cells. A full charge of 1.2 to 1.4 volts will either not sound at all or sound in only one direction, depending on the probe's polarity. However, a partially discharged cell with a potential of 0.9 V or less will create a sound in either direction because the audio frequency depends on the resistance of the cell in either direction. For the value of

resistors used an on-to-off duty cycle of about 60% is obtained. The circuit draws about 7 milliamperes from a standard 9-v battery.

The schematic shows where unknown resistor R_x is connected into the multivibrator circuit. The unknown can have any resistance value from zero ohms to more than 30 megohms. At 0 ohm, which is a short circuit across the test probes, the audio output frequency is about 7,000 pulses per second. This frequency sounds like a tone (sine wave) to the ear. At 30 megohms, the frequency from the speaker is about 1 pulse per second.

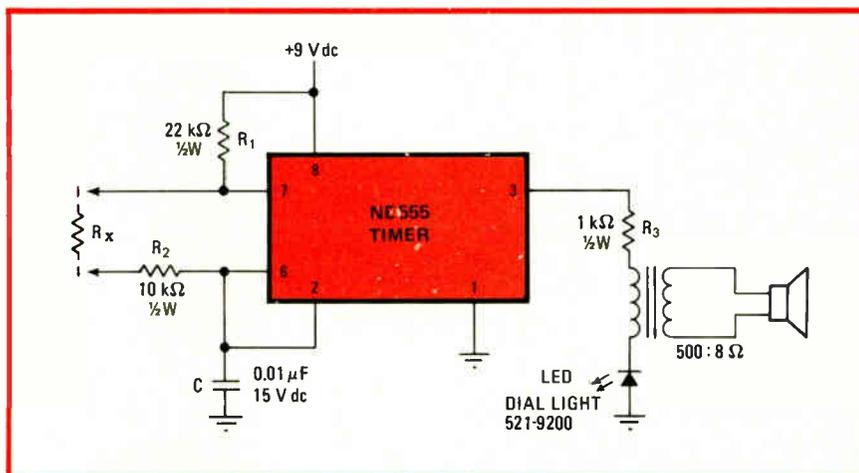
Very low current flows through the test probes. When R_x is 0 ohm, the current level through the probes is about 270 microamperes, and when R_x is 1 megohm, the current is about 9 μ A.

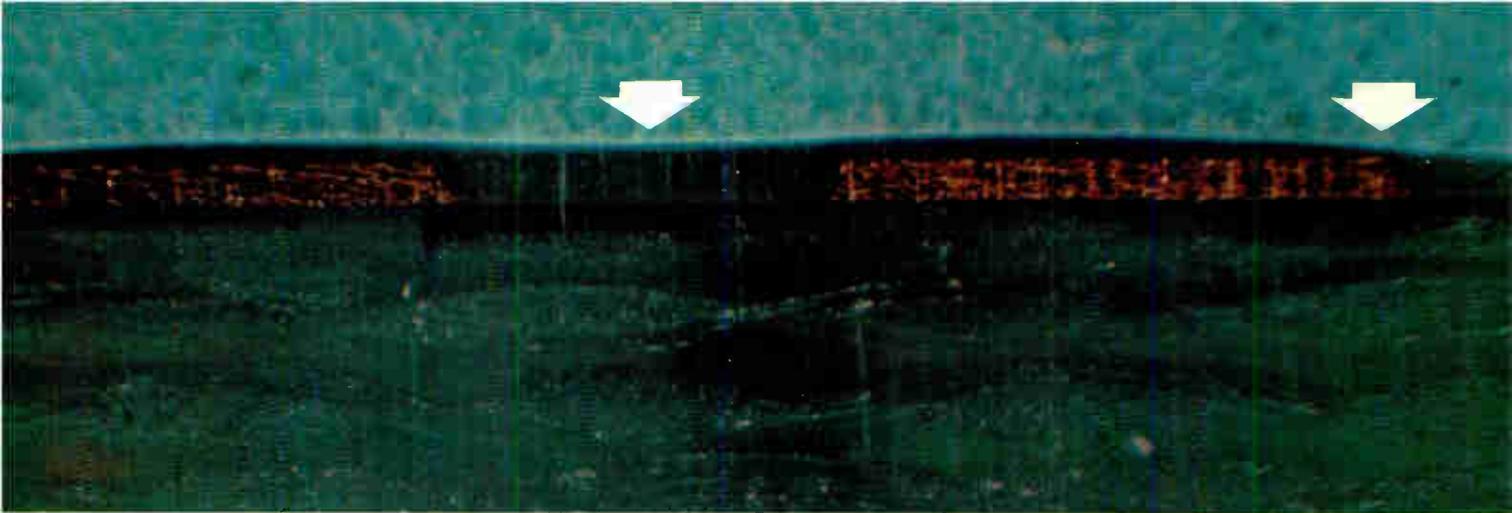
The 555 timer is operated in the astable-oscillator mode. The free-running frequency and duty cycle are both accurately controlled with three external resistors and one capacitor. The external capacitor, C, charges through R_1 , R_2 , and R_x , but it discharges only through R_2 and R_x . R_2 limits the upper frequency of oscillation to about 7,000 pulses per second when R_x is 0 ohm; otherwise, the frequency would be out of the upper range of hearing (higher than 18,000 pulses per second). The lower frequency limit of approximately 1 pulse per second is set by the value of R_x when it is above 20 megohms.

Resistor R_3 limits the current drawn through the output circuit. A value of 1 kilohm provides adequate audio volume. □

Engineer's Notebook is a regular feature in Electronics. We invite readers to submit original design shortcuts, calculation aids, measurement and test techniques, and other ideas for saving engineering time or cost. We'll pay \$50 for each item published.

Sounding off. Low-current audio continuity tester indicates unknown resistance value by the frequency of audio tone. A high tone indicates a low resistance, and a tone of a few pulses per second indicates a resistance as high as 30 megohms.





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Parylene has qualified under the stringent requirements of MIL-I-46058C; it does so with a 0.6 mil coating—parylene excels in the micro-electronic virtue of thinness.

Parylene conformal coatings have shown excellent cost effectiveness in many applications. On delicate, sophisticated and complex circuitry, in hybrid circuits and components, they may be the most cost effective answer for long term reliability.

Union Carbide invented the parylene system. The method is gas phase deposition, which is the only route to the reliability of conformal protection. Various patents apply; commercial use of the patented technology is licensed.

You can get complete information on parylene by writing for our 16-page brochure: Union Carbide Corp., 270 Park Ave., Dept. RB36, New York, N.Y. 10017. Further investigation will no doubt indicate a trial run, which we can perform at reasonable cost. If you would like to discuss that or any other related matters, please call Bill Loeb at (212) 551-6071.



PARYLENE

Industry reaches agreement on C-MOS specs

For years, selecting C-MOS products from more than one source has been very treacherous. Incredibly, there were no standards for specifying or even numbering the part types. **An ordinary C-MOS gate from one manufacturer might not have been comparable to or even compatible with a C-MOS gate made by a different manufacturer—that is, until now.** C-MOS marketing and engineering representatives from Fairchild, Harris Semiconductor, Motorola, RCA, National, and Solid State Scientific met in Austin, Texas, in January. The imminent result: industry standards on the C-MOS “B” series and an industry-wide numbering system.

How to find out what happens inside a microprocessor

Microprocessors may simplify the job of designing complex logic—but they also complicate the job of testing by making those helpful, well-worked-out logic breadboards unnecessary. The problem has not gone unnoticed. **One of the best sources of information now available on choosing logic testers for microprocessor-based systems is the “Omnicomp Handbook of Logic Circuit Testing.”** The four volumes cover in-circuit test techniques, economics, vendor analysis, and the complete brochures and data sheets for all currently available equipment. The set costs \$295 from Omnicomp Inc., 7101 N. Twelfth Place, Phoenix, Ariz. 85020.

IC regulators, too, can split dc

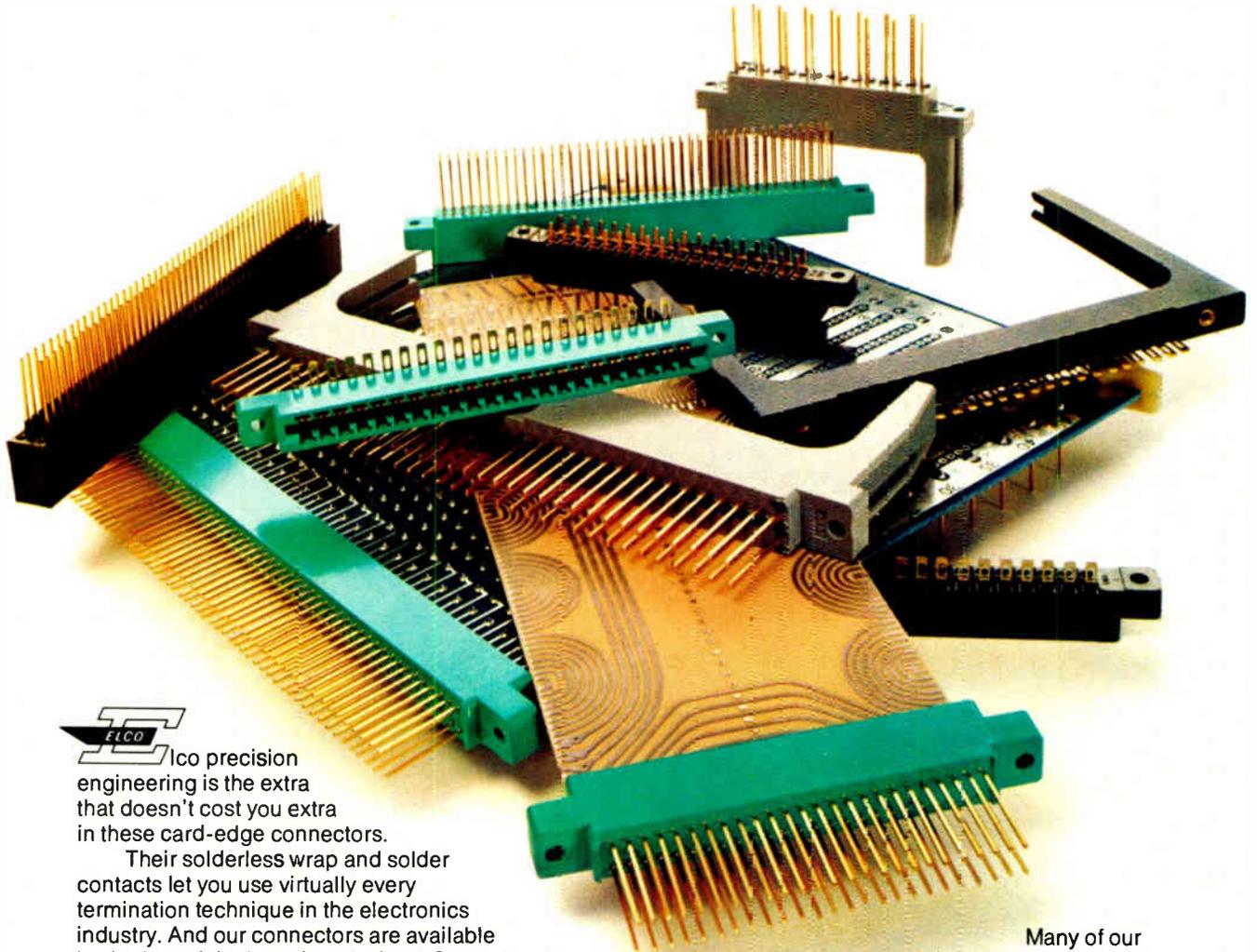
There's no need to fuss over designing a dual-output power supply for, say, operational amplifiers, if you have some of those inexpensive three-terminal IC voltage regulators around. You can split a dc voltage either symmetrically or asymmetrically using only positive regulators, says Russ Weltmer, a staff electronics engineer at McDonnell Aircraft Co., St. Louis, Mo.

To build the supply, connect a full-wave rectifier bridge across the secondary of a center-tapped transformer. Next, run a large-value (say, 3,500-microfarad) electrolytic from one midpoint of the bridge to the center tap, and run a second (same value) electrolytic from the center tap to the other bridge midpoint. Then, simply connect a pair of regulators across the electrolytics. **The positive dc voltage is obtained from the output terminal of one regulator,** the negative dc voltage from the ground terminal of the other regulator, and the common line by tying together the ground terminal of the positive-dc regulator and the output terminal of the negative-dc regulator.

Off the shelf of the IEEE press

If you're interested in computer communications, nonlinear networks, frequency synthesis, automatic test equipment, computer-aided filter design or digital signal processing—they're **all new titles in this year's IEEE Press catalog.** Of the 22 books now in print, 20 are from the IEEE Press Selected Reprint Series. For a free catalog, write to IEEE Press, M. A. Walker, 345 47th St., New York, N.Y. 10017. —Laurence Altman

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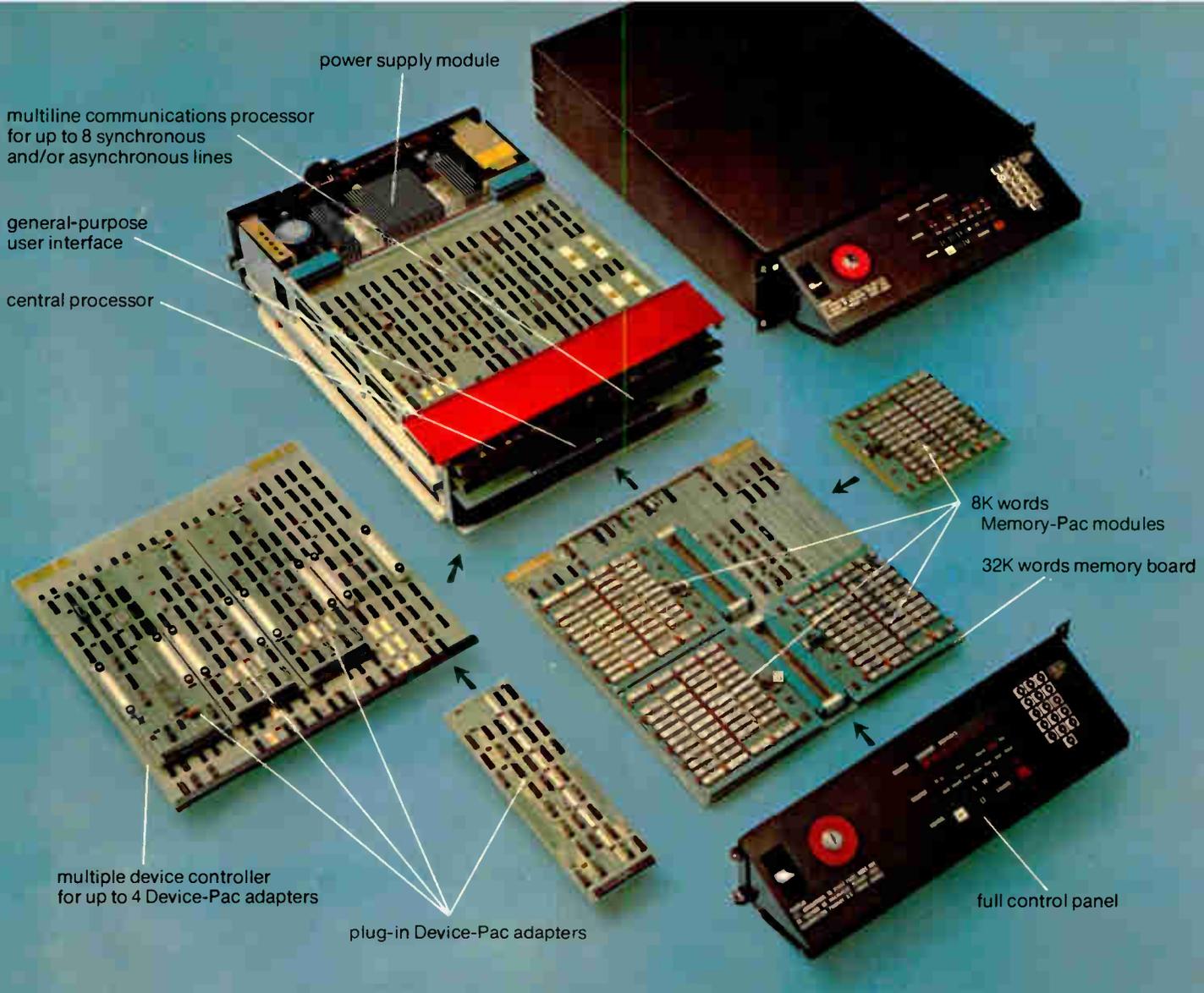
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Shown is the compact, rack-mountable Model 6/36 designed for OEM and system builder applications. The 5 1/2-inch high cabinet houses the central processor, 64K words of memory, a controller for up to four peripherals, and a communications processor which accommodates eight full duplex lines.

The inside story on Honeywell's new mini.

Honeywell's Level 6 minicomputer family offers OEM's and system builders the best of two worlds: A fully open-ended architecture that makes provisions for future developments in technology. And a unique modular packaging design that offers outstanding configurability and serviceability.

Open-ended architecture

- High-performance bit, byte, word and multiword addressing is standard.
- Each 15" x 16" board (central processor, communications processor, mass storage controller, multiple device controller) has its own micro-

processor for more efficient I/O.

- The Megabus™ supports 24 memory address bits, or 8 million directly addressable words.
- Megabus address and data paths are dedicated for bus cycle efficiency, and contention delays are eliminated by distributed control.

Advanced packaging

- The Megabus allows easy configuration by means of unrestricted positioning and addition of boards and modules.
- Modules plug into the 15" x 16" boards for maximum functional density and configurability.

- Hardware self-diagnosis simplifies servicing.

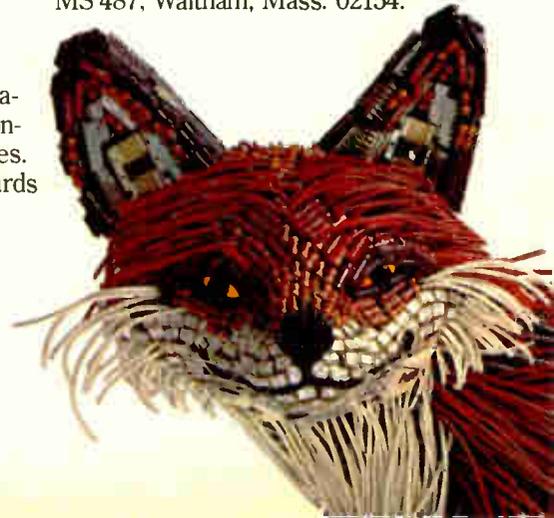
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Components show to highlight easy-to-use devices

Moving west for the first time, the Design Engineers' Electronic Components (DEEC) show will be held at the Jack Tar Hotel, San Francisco, April 26 to 28 (see p. 96). The exhibits will stress user convenience and reliability. Here are some of the more significant products to be introduced.

Standardized components simplify panel interface

Interfacing front panels to mother boards has always been a troublesome and expensive problem for the equipment designer. The lack of standardized components has caused him to intermix first-, second-, and third-generation devices, and to use mounting blocks and secondary printed-circuit cards, as well as different mounting techniques. In a complex oscilloscope, for example, as much as 20% of the total cost may be tied up in the panel interface.

Centralab Electronics division is now proposing that front-panel components be standardized as to type of mounting and device height, making it possible to mount all components on a single pc board that is 0.6 inch behind the front panel. The company, which is kicking off its campaign with a family of push-button switches, plans to supplement the system with both slide and rotary potentiometers and switches, displays, indicator lights, and perhaps even toggle, thumb-wheel, and rocker switches.

"All components will plug into a single board, which can be flow-soldered and mounted 0.6 in. behind the front panel," says Dwayne MacDonald, marketing manager for the firm's electronic-controls group. "There's no need for extra hold-down features—everything is mounted on and supported by the pc board. The panel serves only as

an esthetic cover," he points out.

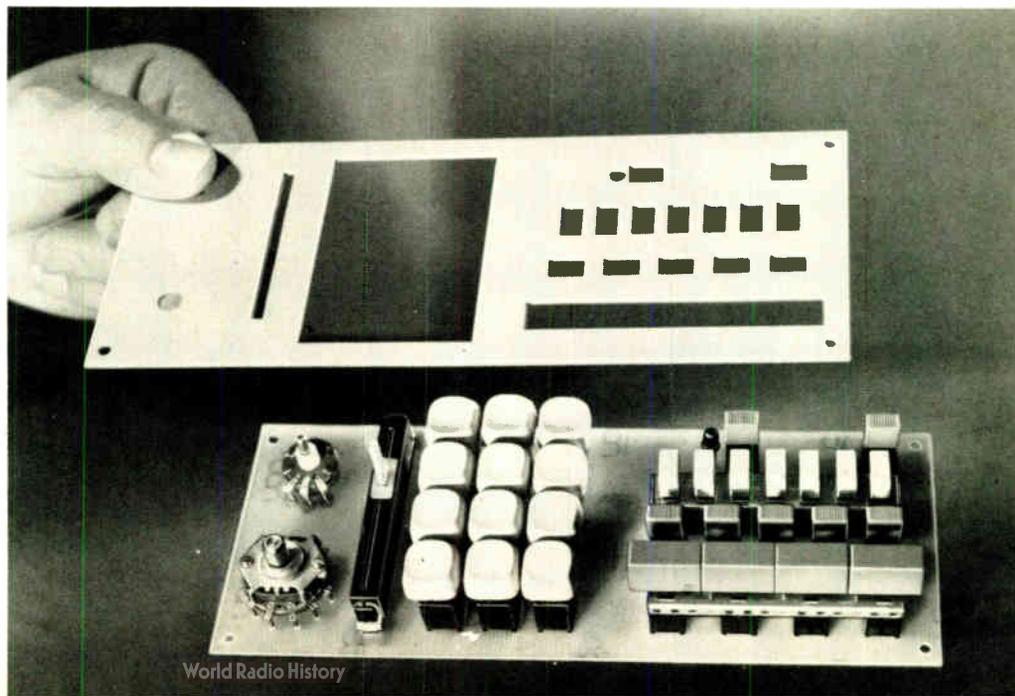
The Milwaukee-based subsidiary of Globe-Union Inc. calls its design IMPS, for integrated modular panel system. The IMPS push-button switch, to be shown for the first time at DEEC, is now available in sample quantities. It can be configured as a momentary push-push or interlock switch.

"With the trend to smart instruments, most switches will be used in keyboard configurations," observes MacDonald. Therefore, momentary-action versions of the spdt switch are designed to key-switch specifications. Total plunger travel is 0.140 in., a peak activation force is

2.5 to 3.5 ounces, and life is guaranteed for at least a million cycles. Dpdt versions have a different contact arrangement, which requires a higher activation force, and they are rated for 100,000 cycles.

The switches have square pins that can be wire-wrapped. Terminal spacing is the same as standard dual in-line packages for pc-board mounting and wave-soldering—the 0.025-in. square pins are spaced on 0.1-in. centers in rows 0.3 in. apart. Terminals are insert-molded to prevent "wicking" of contaminants into the switch when being soldered.

Price for the switches in production volume, which will be available



New products

in June, will be less than 25 cents each. They are molded from a non-flammable glass-filled polyester, and a standard ganged bracket assembly holds a maximum of 12 switch modules. Maximum contact resistance is 25 milliohms, and contact bounce is less than 2 milliseconds, according to the company.

Earlier this year, Centralab intro-

duced a microminiature rotary switch and the 700-series slide potentiometer, both of which will work in the IMPS. Also, the company is changing the terminals of its conventional 1-in.-diameter rotary switch to get the 0.1-in. standoff height, and is modifying its line of 5/8-in. industrial potentiometers. IMPS configurations of these modi-

fied products will be available in July or August.

By the end of the year, the firm will have two new rotary switches—a conventional open-range index type and an index assembly with integral lighted display.

Centralab Electronics Division, Globe-Union Inc., 5757 North Green Bay Ave., Milwaukee, Wis. 53201 [351]

Toggle DIP switch gets new look

Toggle or rocker switches in miniature dual in-line packages are among the fastest-growing segments of the electromechanical-switch market. Now, Grayhill Inc. has developed a new twist for these DIP devices—moving the actuators from the top of the housing to the side. Called the piano DIP, this side-access switch (below, right) retains the same spring-loaded, wiping, sliding-ball contact system that the company uses in its top-access models.

Unlike other DIP switches on the market, if this version is mounted near the edge of a printed-circuit board, the toggles can be adjusted without uncracking the card. Moreover, the programmed setup is visible from the edge of the card.

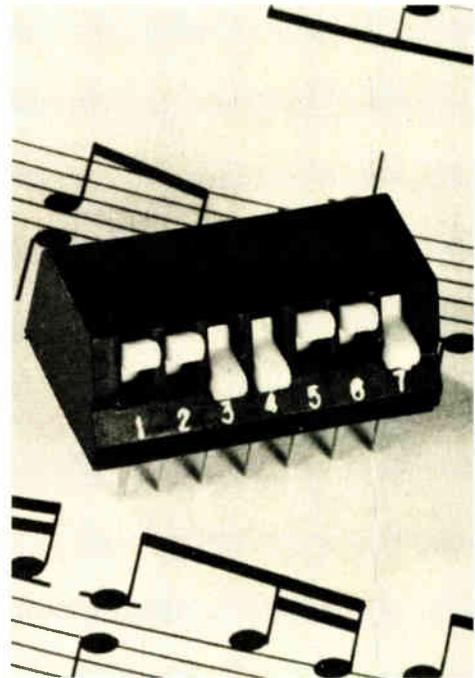
Grayhill has tooled the part with seven independent single-pole, single-throw programming switches, with actuators that are miniature toggles, instead of slides or rockers. Terminal spacing is on standard 14-pin DIP centers, and the 0.780-by-0.380-inch package stands 0.305 in. above the board.

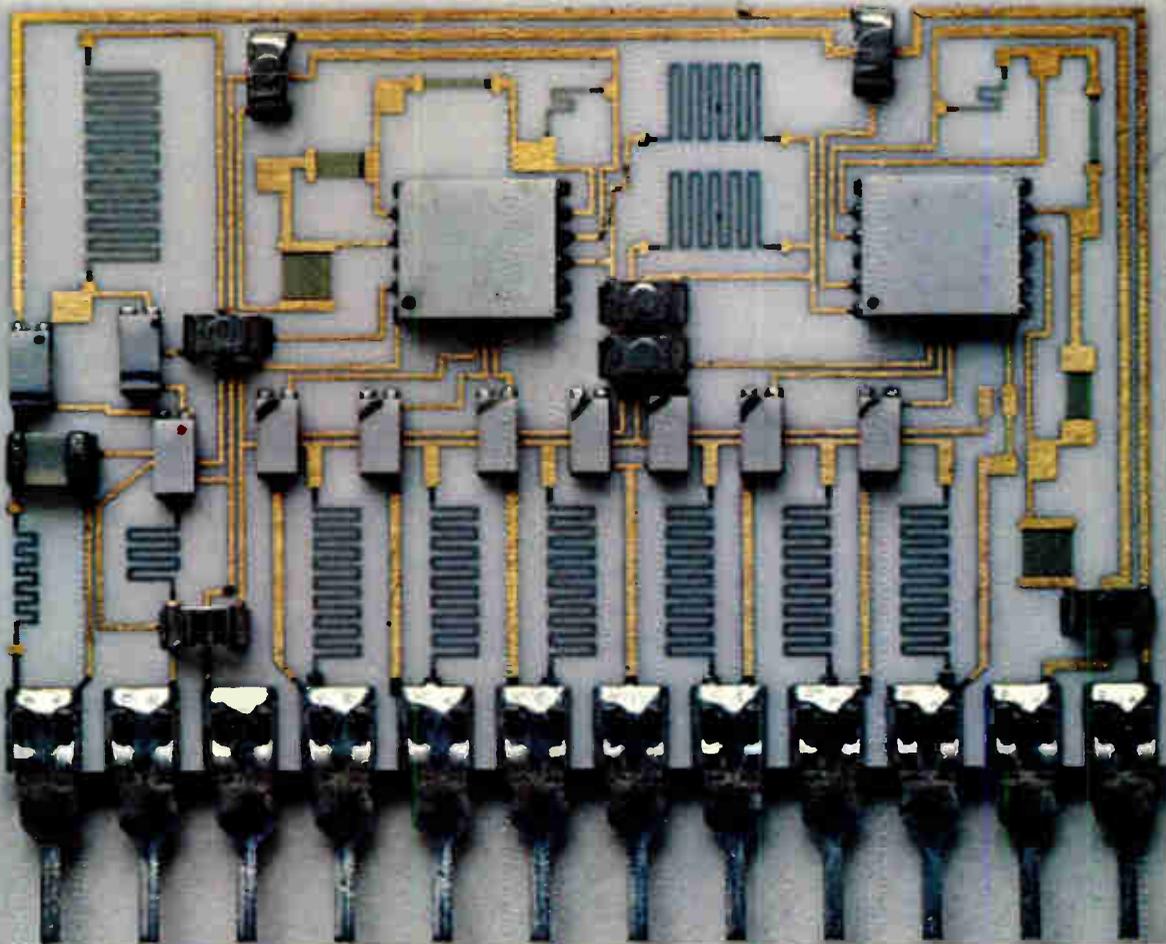
DIP switches have found broad application in the computer and telecommunications industries as logic switches on multifunction boards. At logic levels, the gold-plated contact system is rated at 50,000 operations per station. At higher loads, life expectancy is 35,000 operations at 50 milliamperes and 30 volts, or 25,000 operations at 125 mA and 30 v dc.

The first volume shipment is scheduled for mid-April. In quantities of 100 to 499, the piano DIP is priced at \$3.50 each.

Also at DEEC, Grayhill will introduce what is possibly the smallest 16-position rotary switch—a fully enclosed unit that measures only 0.562 inch in diameter and 0.617 in. behind the panel. The new 51 series switch is significantly smaller than its predecessor, which has a diameter of 1.125 in.

Aimed at the communications, instrumentation and computer markets, this miniature switch is available with shaft and panel seals, or as an unsealed model. It has solder-lug terminals and is rated at 50 mA at 120 v ac. A gold-plated contact sys-





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In addition, the system will continuously print out all test data and can be easily interfaced to virtually any device handler.

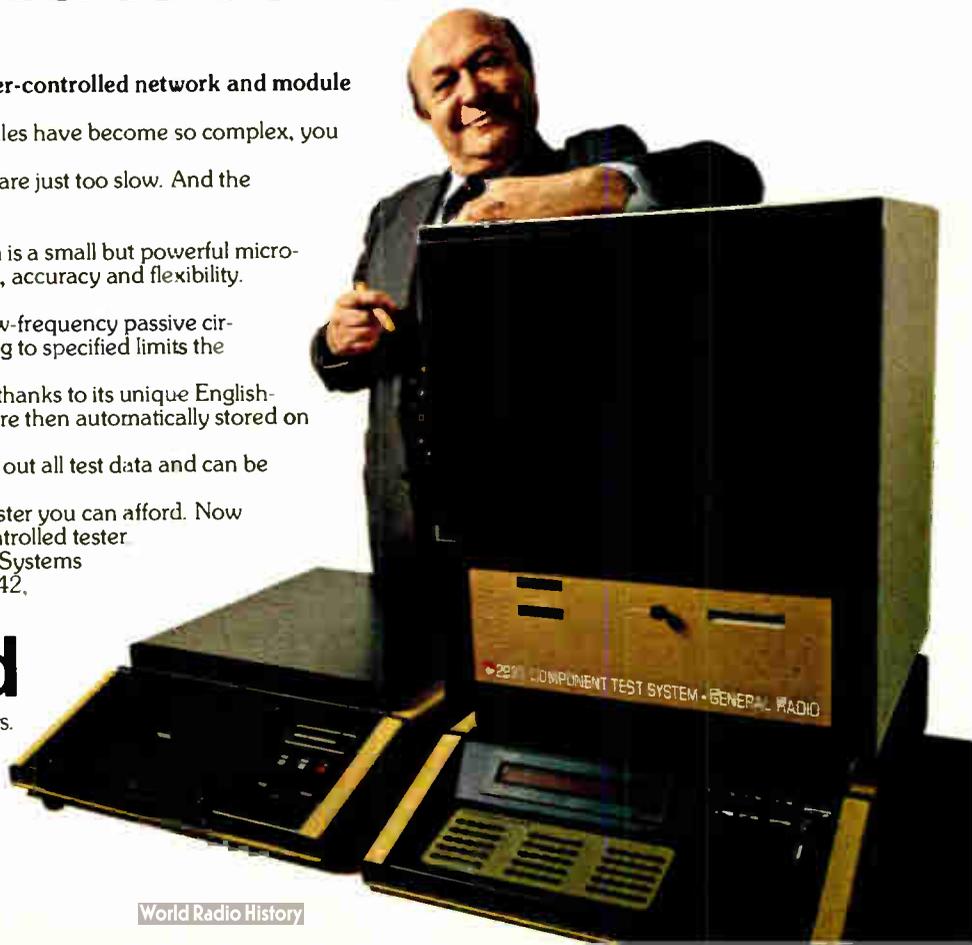
The new GR 2230. A computer-controlled tester you can afford. Now that you can't afford to be without a computer-controlled tester.

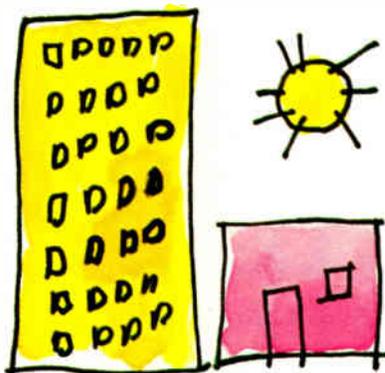
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tem ensures low contact resistance over the rated life of 25,000 cycles or 750,000 operations.

While the unit is designed to do tap switching, it can be coded by the user to give hexadecimal outputs. It is available in single-pole contacts (2 to 16 positions) and double-pole contacts (2 to 8 positions). In quantities of 100 to 499, the single-pole

version is priced at \$6 each; the double-pole type, for \$7.25. Samples are available now, and production quantities of the rotary switch will be ready after July 1, the company says.

Grayhill Inc., 561 Hillgrove La., LaGrange, Ill. 60525. For information on the piano DIP, circle No. 352 on the reader service card; for the rotary switches, No. 353.

Connector terminates optical cable

Though fiber optics hold the promise of data transmission at very high speeds over cabled lines, terminating and splicing the fiber-optic bundles is still difficult. However, AMP Inc. is now offering a circular plastic connector that simplifies terminating high-density bundles in medium- or high-loss cables. The new connector, which is billed as a universal end termination, minimizes fiber breakage and eliminates scoring of the bundle face during polishing.

The unit is constructed of a lightweight nonconductive thermoplastic, and its molded design is suitable for economical mass production techniques, says AMP. Termination is accomplished with a crimping tool, so that the epoxy normally needed for plastic fibers is unnecessary.

A plastic ferrule makes it easy to insert bundles with varying diameters. The ferrule's tapered end compresses the bundle to achieve a good

packing fraction of the fibers. Moreover, the screw thread of the cap permits continuous adjustment of the connection.

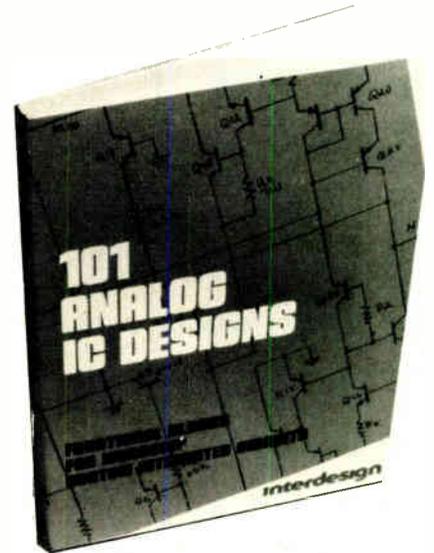
At DEEC, the firm is also showing for the first time a line of card-edge connectors (below) that eliminate the need for conventional insulating plastic housings. Designated the Laminar family, they rely on two strips of insulating film for their structural and dielectric strength. This flame-retardant film, which connects the strip of contacts, can be cut to length with hand tools.

Laminar connectors are designed to interconnect parallel-mounted printed-circuit boards. A compliant center section lets them accommodate even irregular boards.

Tin-plated contacts, rated at 3 amperes with a 30-milliohm maximum termination resistance, engage both sides of a board at the same time. At 500 volts dc, insulation resistance is 5,000 megohms.

Presently, Laminar units are

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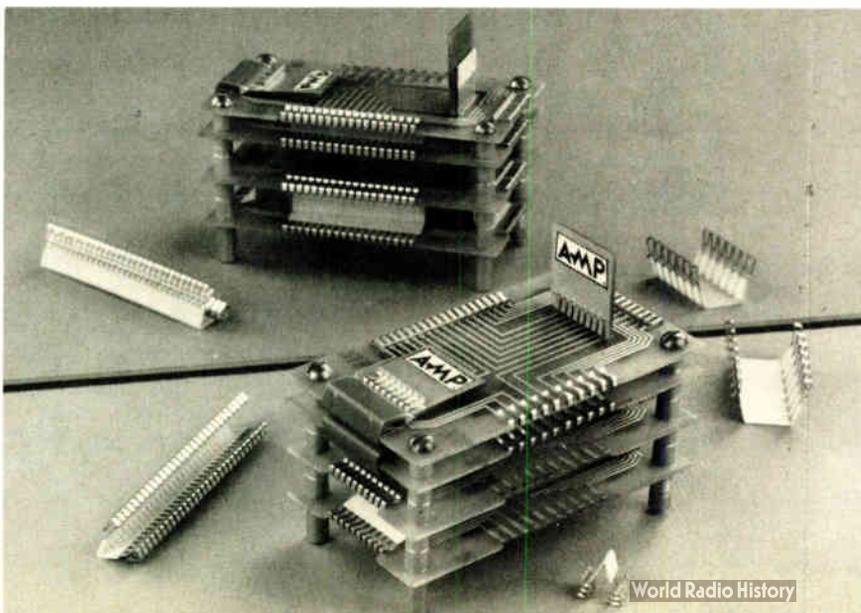
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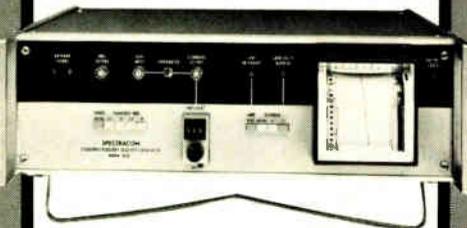
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113



TIME & FREQUENCY....



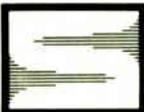
If these words are important to you then you should know more about Spectracom Corporation. We specialize in **TIME** and **FREQUENCY**, and produce test equipment that leads the industry.

For instance, our **WWVB RECEIVERS** are the finest available. Priced from about \$700 to \$2500, they all have features and performance found only in competitive equipment costing \$5,000 to \$10,000. And some of these features, such as positive go/no-go front panel phase lock indication, are available only from Spectracom. For the first time, you can install a receiver and immediately know beyond doubt that it is working, receiving a strong enough signal, and giving correct frequency calibration and time code information! The green "go" light will be on steadily, because our receivers work well under poor signal-to-noise conditions where other won't!

Another example is our **FREQUENCY DISTRIBUTION SYSTEM**, also the finest on the market. You don't need a separate cable for each remote station. You install our system by running one coaxial cable past each remote station in turn, similar to a cable TV system. Buffered line taps at each station give you the standard frequency you need there. If you want to add a station somewhere along the line, just cut the cable and insert another line tap for the desired frequency! Up to 25 stations can be driven from one base station that costs about \$650, and the cost is even less if you buy the system built into one of our VLF Receivers!

Our **FREQUENCY STANDARDS** can also be furnished with the Distribution Amplifier built in. We also have a Frequency Standard that is furnished as part of a WWVB Receiver, tracked continuously against the NBS standard frequency.

So you see, we have good reasons to be proud of our products. And the people who buy them and use them are proud of them too. For the highest quality and performance you can buy in Time and Frequency test equipment, or for special communications test equipment, call your Spectracom sales engineer.



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New products

available with 1 to 50 contact positions on 0.1-inch centers, for connecting 0.062-in.-thick boards when board-to-board spacing is 0.25 or 0.295 in.

AMP Inc., Harrisburg, Pa. 17105. Phone (717) 564-0101. For information on the fiber-optic connector, circle No. 354 on the reader service card; for the card-edge connectors, No. 355.

IC capacitors adjustable at top

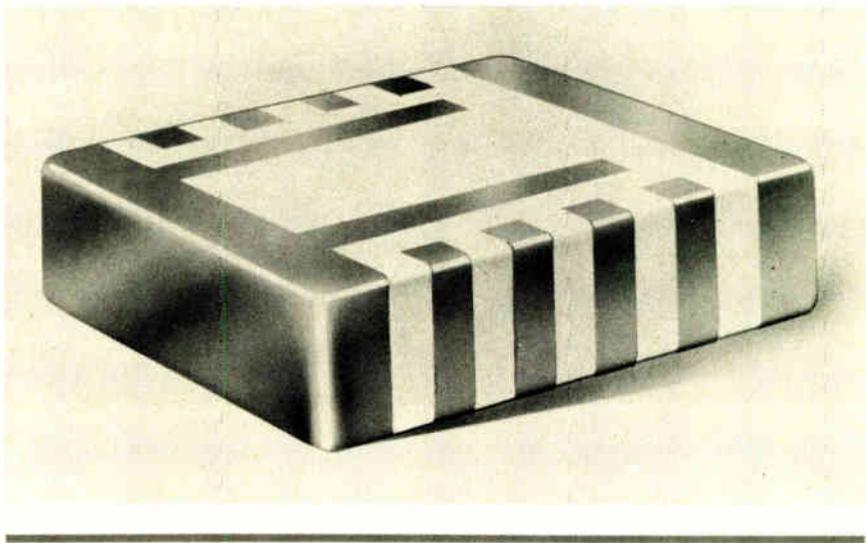
Not too long ago, ceramic chip capacitors were only available as fixed-value devices. But with the introduction of Vitramon's Vee Cal family [*Electronics*, Jan. 9, 1975, p. 125], chip capacitors became devices whose value could be varied either up or down from a nominal level. To make it easier for the user to adjust them, the company is now moving the adjustment points from the side of the chip up to the upper surface, making them more accessible and more readily visible.

As with their earlier counterparts, adjustment does not affect the integrity and stability of the VC2A versions of the Vee Cal family, claims

Vitramon. Nominal capacitance values range from 1 to 55 picofarads, and the eight adjustment increments are in 0.5-pF steps. Each chip is 0.12-inch long, 0.1-in. wide, and 0.03-in. high.

Additionally, Vitramon is introducing at DEEC a line of NPO radial-lead ceramic capacitors that provides value tolerances as tight as $\pm 0.25\%$. The new VK series devices cover the capacitance range from 10 pF to 0.51 microfarad.

Vitramon North America, Div. of Vitramon Inc., Box 544, Bridgeport, Conn. 06601. For information on the chip capacitors, circle No. 356 on the reader service card; for the radial-lead units, No. 357.



Delay lines respond in 4 ns

Besides being accurate to within $\pm 5\%$ or better, the series DDU-5 digital delay lines from Data Delay Devices offer a typical rise time of only 4 nanoseconds. The units, which are compatible with both transistor-transistor logic and diode-transistor logic, do not require any

outboarded components. Moreover, they provide 10 equally spaced taps in a 16-pin dual in-line package.

Ten different models are available, with total delay time ranging from 100 to 2,000 ns and a delay-per-tap of 10 to 200 ns. Standard operating temperature range is 0°C

to 70°C, and temperature coefficient is 100 ppm/°C.

The devices can operate from a supply voltage of 4.5–5.5 v dc. Maximum input current is 50 microamperes for logic 1, –2 milliamperes for logic 0. Output voltage is 2.5 v minimum for logic 1, 0.5 v maximum for logic 0. Maximum fanout is 20 per tap for a logic-1 output, 10 per tap for a logic-0 output.

Each 16-pin DIP measures 1.57 inches long, 0.53 in. wide, and 0.28 in. high (excluding pins). An optional extended temperature range of –55°C to +125°C is available on request.

Data Delay Devices, 253 Crooks Ave., Clifton, N.J. 07011. Phone (201) 772-1106 (358).

Glass capacitors

Glass-encased ceramic capacitors that qualify to the level M reliability of MIL-C-39014 are available from San Fernando Electric Manufacturing Co. The series CKR11 units are the first glass capacitors to meet the level M failure rate of 1% per 1,000 hours, says a company spokesman—all previous level M capacitors were molded devices.

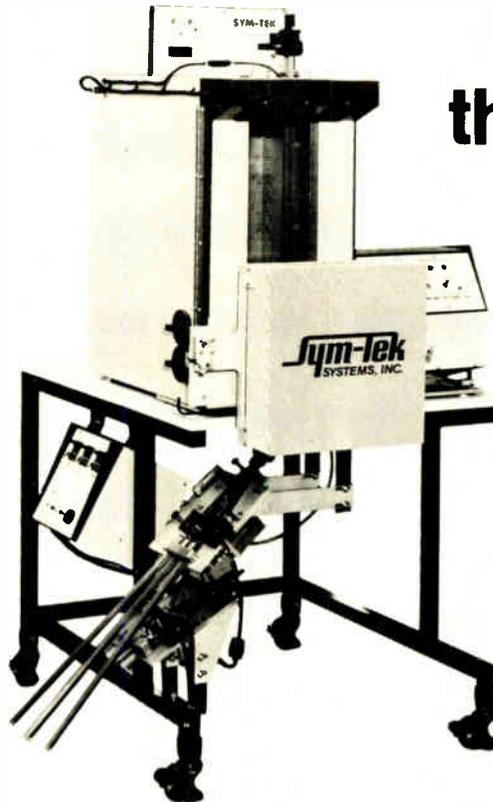
The new capacitors are hermetically sealed structures, in which the radial leads are welded to the ceramic chip. Because of the glass case, lead attachment can be inspected visually. In molded devices, lead attachments are not visible.

Besides being suitable for handling by automatic insertion equipment, the glass devices offer rugged construction. They can withstand many temperature cycles between –55°C and +125°C, as well as such high pressures as 10,000 pounds per square inch, without degradation.

Two different working voltage ratings—50 or 100 volts dc—are available in a body size of 0.16 in. long by 0.09 in. in diameter. Capacitance range for the 50-v units is 5,600–10,000 picofarads, with a tolerance of ±10% or ±20%. The 100-v units cover the range of 10–4,700 pF.

San Fernando expects to sell its

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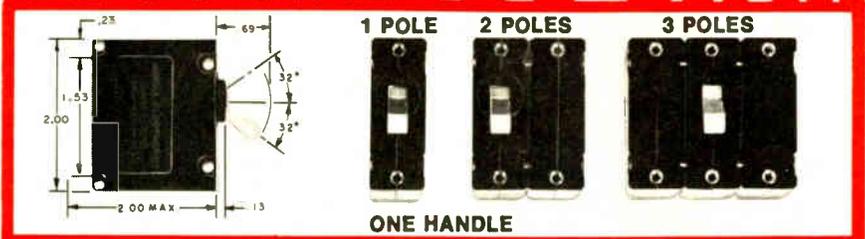
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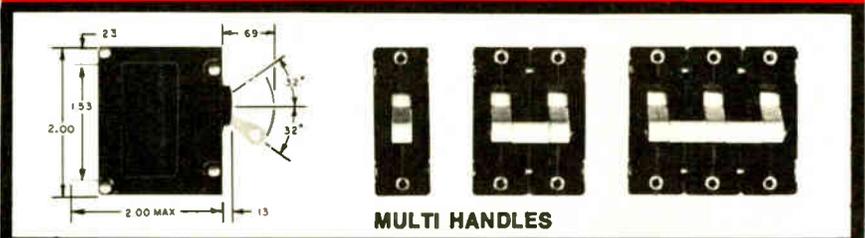
4140 Morena Blvd. San Diego, CA 92117
(714) 270-7600



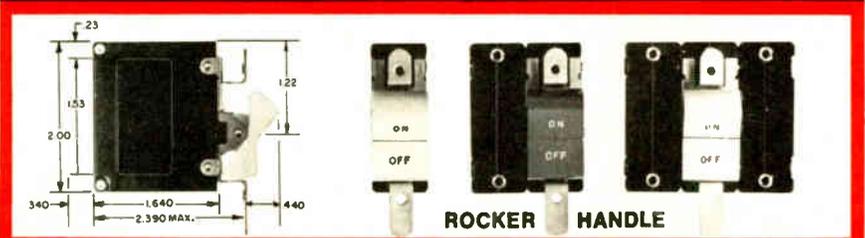
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San Fernando Electric Manufacturing Co.,
1501 First St., San Fernando, Calif. 91341
Phone (213) 365-9411 [359]

Low-loss ferrites

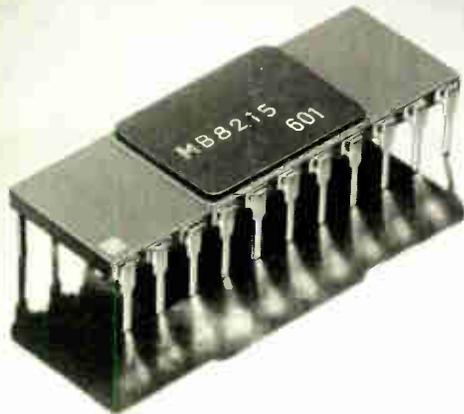
Switching-regulated power supplies are smaller, as well as more efficient, than their series-regulated counterparts, and for very high power outputs, they tend to be considerably less expensive, too. Now, new transformer ferrites from Japan's TDK Electronics Co. promise to improve their efficiency still further. Intended for the transformers used in dc switching-regulated supplies, series H7C1 ferrites can reduce power losses by approximately 10% to 15%.

The low-loss ferrites exhibit a fairly high magnetic-flux density. They are free of the usual high-frequency-response flaws found in conventional magnetic alloys like silicon steel plate. Furthermore, at high frequencies and high levels of magnetic flux density, core loss is quite low, so that the temperature of the core rises very little. As a result, transformers built with H7C1 ferrites can develop high outputs at high efficiencies.

For filters operating at frequencies of 100 to 500 kilohertz, the firm has another new ferrite family, the H6K series. These units are low-loss filter ferrites that feature good stability over both time and temperature. Since the relative-loss coefficient of the devices is low, they are suitable for realizing miniature coils with very high Q values.

Both of these new ferrite families are available from MH & W International Corp. Prices are expected to be competitive with existing products, the company says.

MH & W International Corp., 280 Midland Ave., Saddle Brook, N.J. 07662. Phone (201) 791-6277. For information on the H7C1 series, circle No. 360 on the reader service card; for the H6K series, No. 361.



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FUJITSU SEMICONDUCTORS

MOS MEMORY 4K Dynamic RAM MB 8215

When it comes to semiconductor memories, performance is the name of the game. And when it comes to 4K RAMs, the name of the game is the new Fujitsu MB 8215 N-channel silicon gate dynamic MOS 4K RAM. It's the *first* 4K to offer a typical access time of 70ns and power dissipation as low as 500mW/chip.

Bipolar comparable speed

The adaptation of N-channel silicon gate construction provides the key to obtaining bipolar-like speeds. The MB 8215 features a typical access time of only 70ns (100ns max.) and a minimum cycle time of 220ns guaranteed over 0 to 70°C. With this kind of speed, it's easy to see why the MB 8215 is fully capable of handling memory applications which, up to now, have been limited to more costly bipolar devices.

Low power consumption

The Fujitsu MB 8215 also gives you reduced power requirements for saving on power. The device has a typical dissipation of 500mW/chip (less than 0.15mW/bit), which adds up to a substantial improvement of the speed-power product.

Perfect for main memory applications

The MB 8215 is ideal for computer main memory (or similarly demanding) applications. Its 22-pin DIP design eliminates the need for address multiplexing and related timing headaches. All inputs, with the exception of the single-phase hi-level clock, are fully TTL compatible for simplified interfacing. And, Fujitsu supplies compatible bipolar linear sense amplifiers that permit the same fast access time.

Proven reliability and available now

The MB 8215 4K RAM is built by

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FUJITSU LIMITED

Communications and Electronics
Marunouchi, Tokyo, Japan

One-fiber optical cable is rugged

High tensile strength and low cost make single-fiber waveguide attractive for communications bandwidths up to 20 megahertz

by Richard Gundlach, Communications and Microwave Editor

Low-loss fiber-optic waveguides are coming closer to being competitive with conventional transmission media—wire and coaxial cable. A step in that direction has been taken by Fiber Communications Inc., which has developed a single-fiber cable, called Fiberguide, that offers high tensile strength and cost-effectiveness for bandwidth requirements that extend as high as 20 megahertz.

Fiber-optic waveguides are particularly attractive in communications applications, such as computer control of machinery and remote sensing in electrically noisy environments where electromagnetic interference is a severe problem.

Not widely used. So far, however, fibers have not been used widely for several reasons: their high cost when placed in a fairly rugged cable, their limited availability, and the fact that they have often been fragile and difficult to handle even after being placed in a cable.

And up to now, cabled fibers have been available only in fiber bundles or cables containing six separate fiber channels in one sheath. This reduced cost-effectiveness, since all the fibers in the cable might never be used. In fact, for most applications a single fiber would suffice. Moreover, most multiple-fiber cables are about ½ inch in diameter, larger than was expected for optical transmission. Also, Fiber Communications says, only moderate-strength cables with minimum bending radius have been available.

The new Fiberguide is a single-fiber waveguide encased in a ruggedized sheath with an outside

diameter of 80 mils. It offers exceptionally high tensile strength—about 475 pounds, almost four times the strength of previously available cables. But it is the combination of small size and high tensile strength of Fiberguide cables that makes possible reduced installation costs. They can be pulled through ducts

are perfect for the plastic jacket, or even fingernails will do the job. And since all segments of the cable are concentric, all alignment can be done from the outside of the cable rather than working from the small fiber.

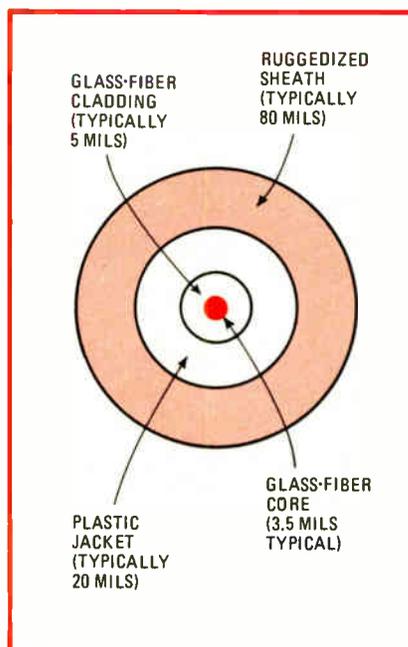
Several features of the Fiberguide cable point to its usefulness. The fiber waveguide itself is all glass. It consists of a glass core surrounded by a cladding of glass with a slightly lower refractive index. The use of an all-glass waveguide, instead of plastic cladding around a fused-silica core, helps prevent degradation of the cable's transmission characteristics in humid or underwater environments.

The step-index cable has a 0.16 numerical aperture, attenuation is below 40 decibels per kilometer, with a typical value of 25 dB/km. The cable has a recommended bending radius of 2.5 inches, comparable to large-bandwidth coax cables but it can accommodate tighter bends.

Severe tests. The cable has been extensively tested in salt water without showing any degradation. It's been put through more than 1 million cycles of flexure test under 200-pound loads with no adverse effects, the company says.

The price of Fiberguide cable is \$3 per meter (less than \$1 per foot) when purchased in lengths of 500 meters. Standard lengths of 200 and 500 meters are generally available, and lengths over 500 meters can be obtained upon special order. Lengths of 1 kilometer have already been manufactured.

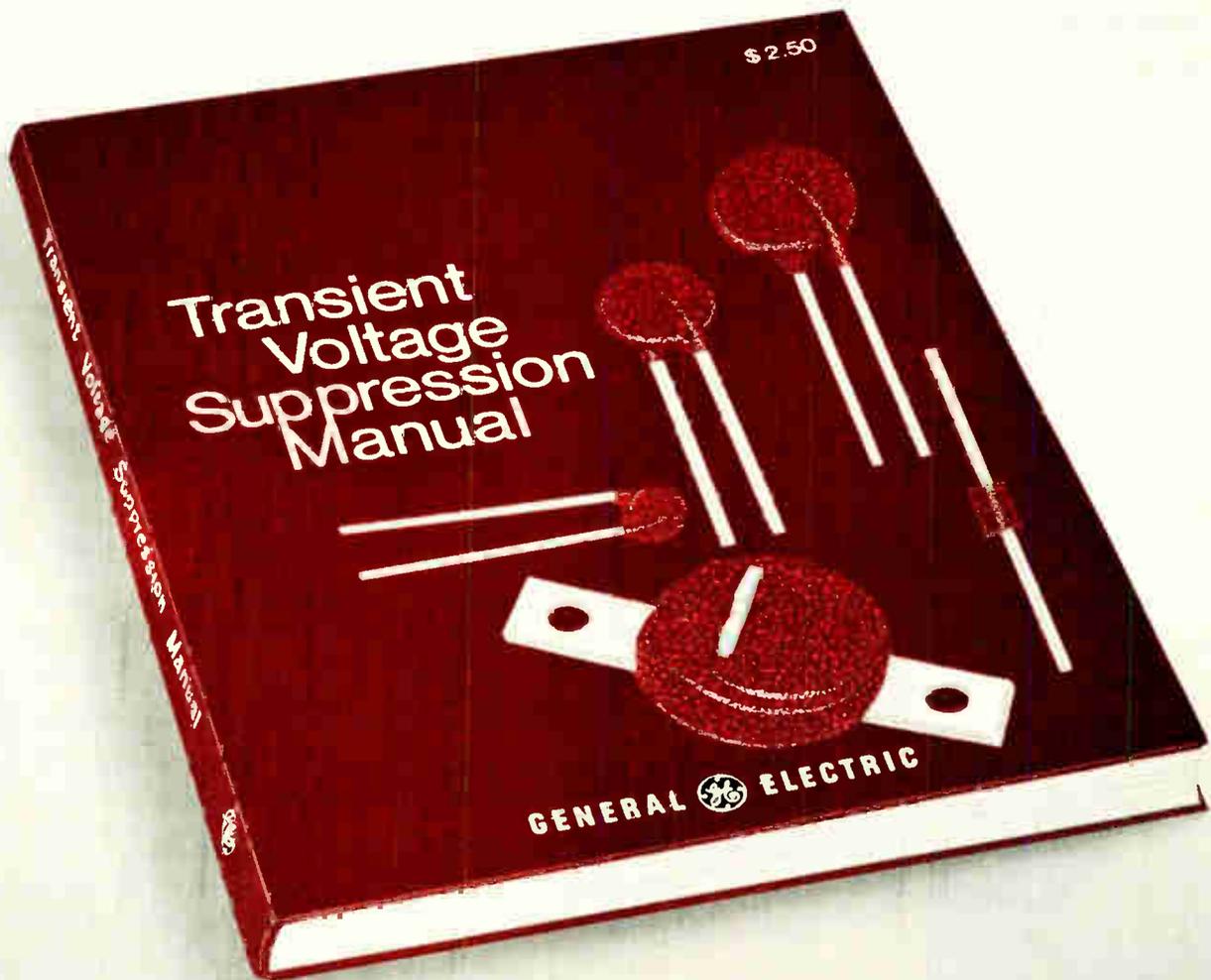
Fiber Communications, Inc., Orange, N.J. 07050 [339]



Cross section. Ruggedized sheath assures high tensile strength. All materials are dielectric, so equipment linked by the optical cable is electrically isolated.

more easily and for greater distances without any need for splices.

Further, the cable weighs only 1.5 pounds per thousand feet and can be easily stripped without specialized tools to gain access to the fiber. An ordinary pair of diagonal cutters can be used to strip and peel the outer sheath. Thermal wire strippers



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GENERAL  ELECTRIC

Programmer takes on any CPU chip

System is adaptable to any microprocessor; package includes keyboard and CRT display, permits interactive program-writing

by Stephen E. Scrupski, Computers Editor

Writing programs for microprocessors is a difficult enough task for a programmer without his having to worry about the extra time involved in loading paper tape and whether his program will fit in the available memory space. With this philosophy in mind, Tranti Systems Inc., North Billerica, Mass., has developed a system that it says is easy to use and is adaptable to any microprocessor on the market.

The system, called the μ Scope model 8000 programming system, consists of the following, all in one package: alphabetic keyboard, 10-key numeric pad and extra control keys, cathode-ray-tube display, a small alphabetic printer, a magnetic-tape cartridge, and an expandable memory that provides up to 57 kilobytes of user space. The software package is in a read-only memory and consists of three parts: monitor, editor, and assembler. Unlike off-line assemblers, which perform the assembly after the program is fully written, Tranti's system assembles the program as it is entered.

Thus, as the programmer writes his instructions using the mnemonics of the assembly language for the particular microprocessor involved, each instruction, which, of course has a one-to-one correspondence to a machine code, is translated to a stored list and the object code stored in random-access memory. The CRT displays the program as it is entered.

The model 8000 system is

built around the Intel 8080A, and the programmer can run his programs after they have been entered, edited, and assembled on the internal central processing unit. If he is using another CPU, he needs only a different dictionary of translations of assembly language to object code, which can be entered into a RAM via the magnetic tape cartridge. To run this program for another processor other than the 8080A, he must add an alternate CPU board, or bus the system memory out to his equipment.

Since the assembler is designed to convert user entries into object codes, the only additional memory required is for labels. Typically, object code and labels will consume equal amounts of memory space, resulting in 50% efficiency of memory use. Thus, a user can generate 2

kilobytes of object code with only 4 kilobytes available in user memory. Tranti says this 50% assembler efficiency is approximately 10 times better than conventional programming methods.

It gives the programmer the ability to write extensive programs without running short of memory. Also, it completely eliminates the time-consuming need to shuffle and reshuffle paper tapes.

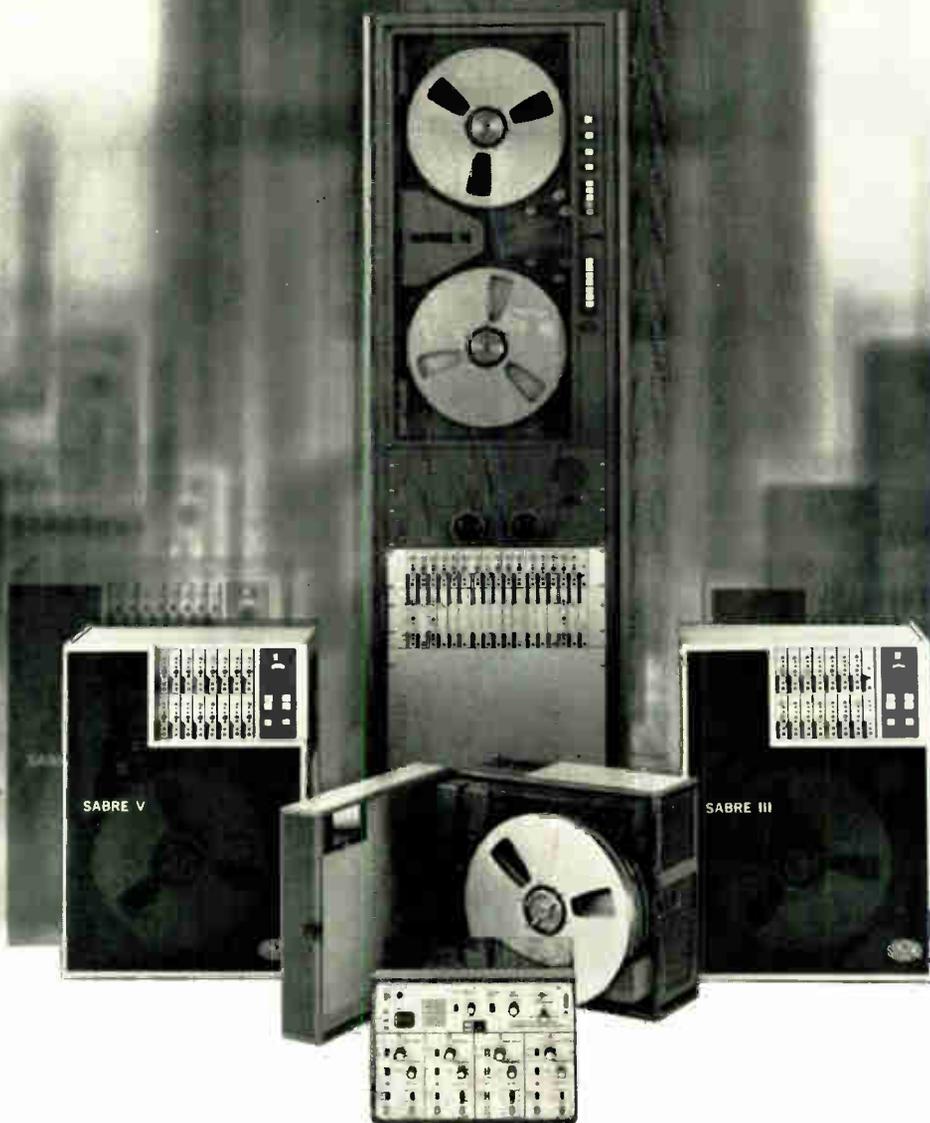
The system's monitor program reads, writes, and verifies magnetic tapes, copies and compares blocks of memory, allows direct entry or modification of any memory location, and has the capability for multiple traps or breakpoints in the program. Control keys are provided to set address, examine, backspace, clear, deposit, and run.

The editor allows the user to set program address, increment address, decrement address, label, list, move, assemble, and disassemble programs. With the printer, the programmer can selectively list portions of the program or the entire program.

The system grew out of the company's own work on cash registers intended for fast-food chain restaurants, according to president Frank Trantanella. The μ Scope has been used within Tranti for more than six months. Price of the system is \$6,995. It will be ready in June, and delivery time will be 30 days.

Tranti Systems Inc., 1 Chelmsford Rd., N. Billerica, Mass. 01862. [340]





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Communications

Tone receivers meet Bell specs

Hybrid modules are offered individually or as subsystems on pc boards

Bell's Touch-Tone telephone signaling involves the transmission of pairs of tones to represent the dialed numbers. Series 883 hybrid tone-receiver products are designed to receive these tone pairs and decode them. Available as individual modules and as pretested subassemblies mounted on printed-circuit cards, the series includes the model 883-1 dial-tone reject filter, the 883-2 low-band filter, the 883-3 high-band filter, the 883-4 dual limiter, the 883-5 quad tone detector, and eight band-pass filters (883-6 through -16). The hybrid circuits all meet Bell System requirements for use in central-office equipment manufactured by Western Electric.

Two additional products are the 883-107 and 883-108 tone-decoder subsystems. These are assemblies of series 883 hybrid modules, together with other components, mounted on a pc card. The 883-107 is designed to decode the four-by-three tone matrix (two-of-seven code) put out by standard Touch-Tone telephones. The 883-108 can handle the full Touch-Tone four-by-four matrix (two-of-eight code). In addition to the hybrid modules, the subassemblies include an input

transformer, coupling capacitors, bypass capacitors, timing and reference capacitors, and a pair of zener diodes.

Because it uses a limiter approach rather than automatic-gain control, series 883 equipment can decode tone-burst repetition rates as high as 25 bursts per second without degrading key parameters.

Unit-quantity pricing on the hybrid modules is as follows: the 883-1 through 883-4 sell for \$22, \$18.50, \$19.60, and \$12.75 respectively. The 883-5, two of which are required for a complete tone decoder, sells for \$25.25. The 883-6 through 883-16, of which seven or eight are required, sell for \$10.25. The 883-107 subassembly is priced at \$288.90, while the 883-108 goes for \$300.75. Beckman Instruments Inc., Technical Information Section, Helipot Division, 2500 Harbor Blvd., Fullerton, Calif. 92634 [401]

Portable level generator has very flat response

Designed primarily to provide the extremely stable tones needed to test frequency-division-multiplexed carrier systems, the model AT-607 level generator is portable. Its out-



put power varies less than 0.12 decibel from its value at 10 kilohertz over the frequency range from 200 hertz to 4.5 megahertz. This flatness and all other specifications become valid over the generator's rated temperature, line-voltage, and line-frequency ranges immediately after turn-on.

The instrument has an output power range from 0 dBm down to -70 dBm, switchable in calibrated steps of 10 dB and 1 dB. In addition, the level can be varied continuously over a 1.2-dB range. Frequency resolution is 10 Hz, and the maximum

frequency error is less than 30 Hz, the company says.

The large five-digit incandescent frequency numerals can be read even in direct sunlight.

In addition to its main 75-ohm coaxial output, the AT-607 has three balanced outputs: 124 ohms, 135 ohms, and 600 ohms. All four outputs are compatible with standard Western Electric and equivalent plugs.

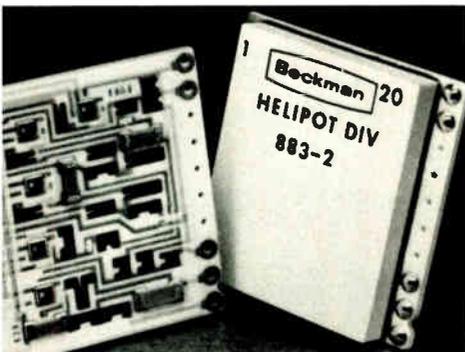
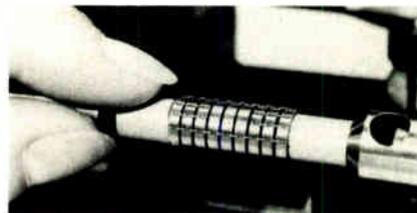
The level generator can be combined with the manufacturer's recently announced model AT-608 selective and wideband level meter [*Electronics*, Jan. 22, p. 129] to form a frequency-response measuring system. When coupled, the two instruments operate synchronously so that it is necessary merely to tune the receiver and the generator automatically tracks it.

The level generator, which weighs about 20 pounds, sells for \$2,775.

W & G Instruments Inc., 119 Naylor Ave., Livingston, N. J. 07039. Phone Ken Chipman at (201) 994-0854 [403]

Citizens' band SSB filter is extremely stable

A single-sideband mechanical filter designed for the citizens' band radio market will typically shift a maximum of 35 hertz over the temperature range from -30°C to 50°C. Designated the model P/N 526-9897-010, the filter is an upper-sideband device for an i-f of 455 kilohertz. Either end of the filter may be used as the input or output, but only one end is balanced. Both ends should be terminated in an impedance of 2,700 ohms shunted by 360 picofarads. Custom-made ferrite transducers keep the insertion loss low. The result is a very stable filter. The P/N 526-9897-010 sells for \$11 each



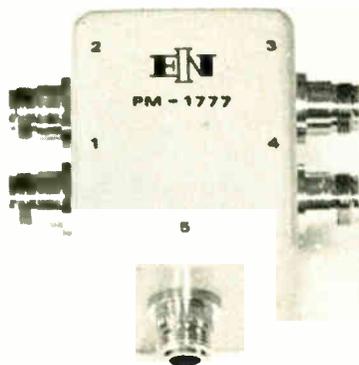
New products

when it is ordered in lots of 5,000.

Collins Radio Group, Rockwell International, 4311 Jamboree Rd., Newport Beach, Calif. 92663. Phone Jim Campbell at (714) 833-4632 [405]

Broadband multicoupler can handle a kilowatt

Covering the frequency range from 20 to 80 megahertz, the model PM1777 four-port multicoupler is capable of combining or splitting power levels as high as 1 kilowatt over an ambient temperature range of -50°C to 90°C . The latest addition to a family of 1-kw multicou-



plers, the PM1777 has a maximum insertion loss of only 0.2 dB, a maximum VSWR of 1.25, and a minimum isolation of 22 dB. The entire coupler is immersed in a silicone-oil bath and heat-sunk to its hermetic brass case. For high-power operation, the case should be mounted to a good heat sink. The multicoupler sells for \$349 in quantities of one to four and for \$295 for five to 49. Delivery time is 30 days.

Electronic Navigation Industries Inc., 3000 Winton Rd. South, Rochester, N. Y. 14623. Phone L. M. Salmen at (716) 473-6900 [406]

Spectrum analyzer spans 25 megahertz

Useful for both routine monitoring and troubleshooting of frequency-division-multiplexed communications systems, the model 236 spectrum analyzer spans the range from 100 hertz to 25 megahertz. The unit's 100-Hz resolution makes it suitable for baseband work as well as for the analysis of i-f and rf signals. The model 236, priced at \$3,800, has a dynamic range of 60 decibels and sensitivity of -105 dbm. It is offered with an input im-

pedance of either 50 or 75 ohms. A high-impedance probe adds \$155 to the basic price. Delivery time is 30 days.

Nelson-Ross Electronics, 5 Delaware Dr., Lake Success, N. Y. 11040. Phone (516) 328-1100 [404]

Equalized multi-modem operates at 4,800 b/s

Intended for multipoint polled operation, the 48/Multi data modem is an eight-phase differentially coherent unit that requires no operator intervention at the distant station. Able to operate at 4,800 bits per second over unconditioned lines, the modem has a built-in

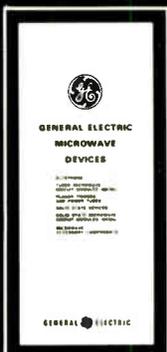


equalizer that needs adjustment only when the unit is installed. An integral signal-quality meter makes an oscilloscope unnecessary for equalizer adjustment. The 48/Multi

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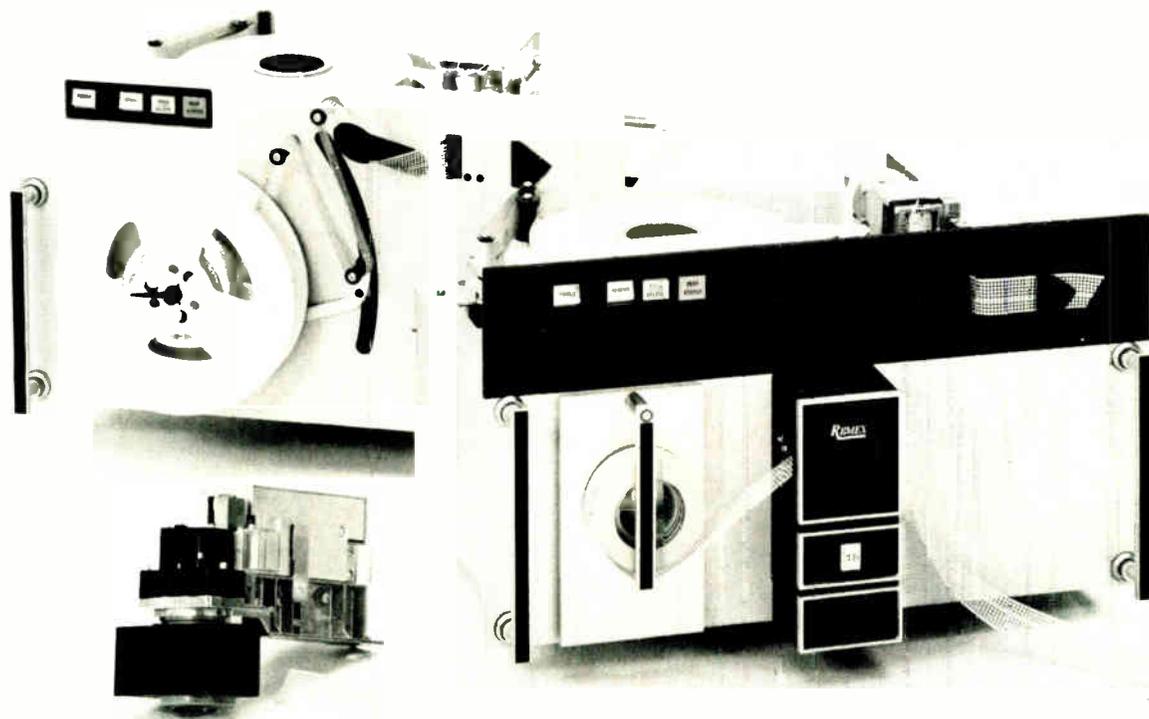


For complete information on General Electric's line of microwave tubes and devices, use the Reader Service Card to order Condensed Catalog . . . or contact:

Microwave and Imaging Devices Products Section, General Electric Company, 316 E. Ninth Street, Owensboro, Kentucky 42301. (502) 683-2401.

GENERAL  ELECTRIC

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All 6120 perforators have motor driven tape feed and sprocket drive design giving longitudinal registration within $\pm .015''$ in 5" of tape. No registration adjustments are required for the life of the product.

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Penril Corp., 5520 Randolph Rd., Rockville, Md. 20852. Phone Bill Myers at (301) 881-8151 [407]

16-channel multiplexer is microprocessor-controlled

A microprocessor-based multiplexer, the model M1308, accommodates up to 16 asynchronous or eight synchronous data channels, or a combination of both. A complete basic 16-channel, point-to-point multiplexing network sells for less than \$8,000. The multiplexer can operate at rates up to 9,600 bits per second over a single voice-grade telephone line. The unit's programmable microprocessor allows it to handle a wide mix of remote-job-entry terminals, synchronous CRT controllers, and interactive asynchronous terminals. The price of a model M1308 multiplexer ranges from \$1,800 to \$2,250.

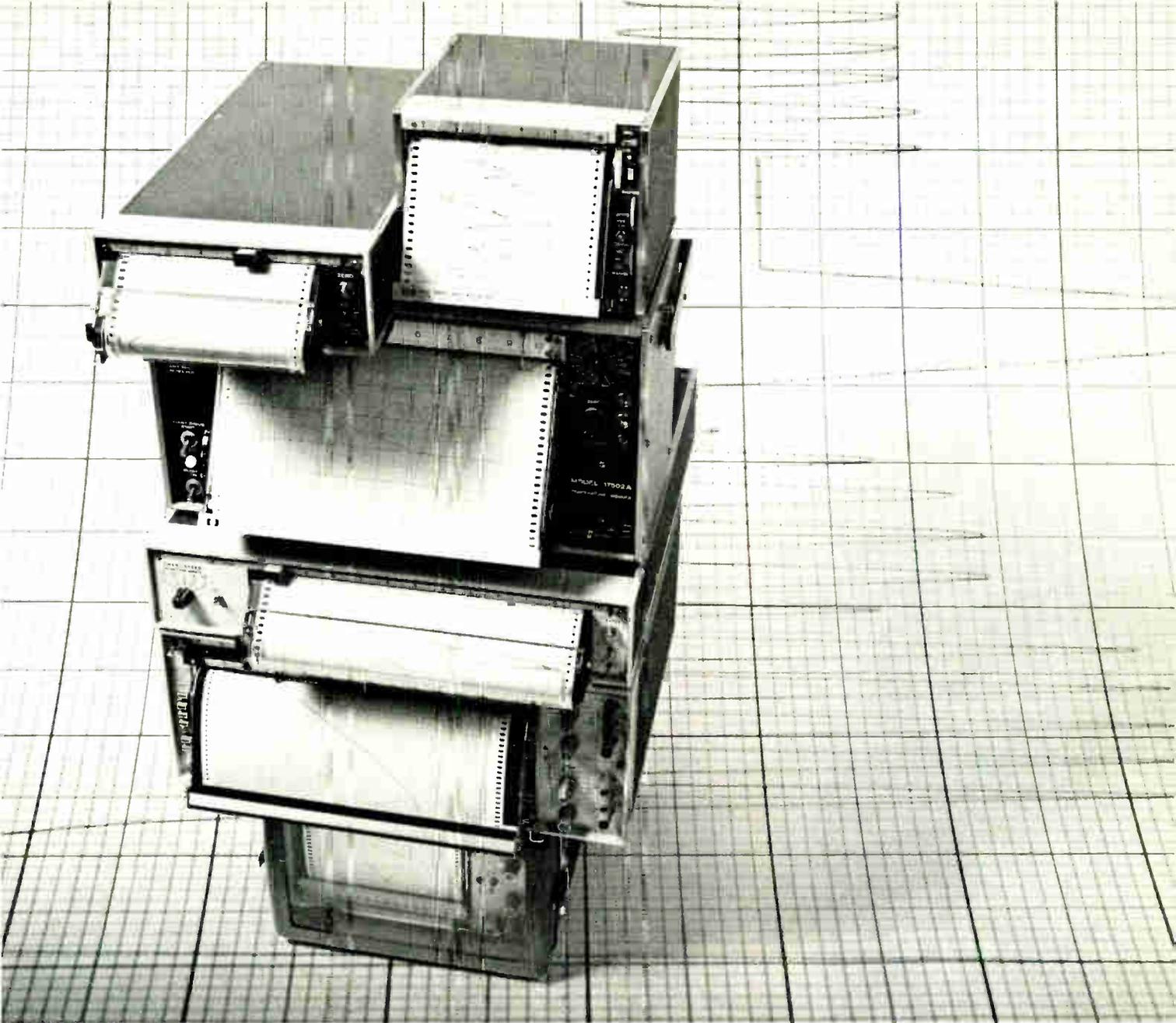
Computer Transmission Corp., 2352 Utah Ave., El Segundo, Calif. 90245. Phone (213) 973-2222 [408]

TOPICS

Communications

Solid-State Communications Inc., Hayward, Calif., has acquired a line of dc remote-control products from **Alpha Electronic Services Inc., Stanton, Calif.** The line includes the AR-7 remote-control unit, and the XR-4 extended local-control unit.

Syntech Corp., Rockville, Md., has developed an automatic control device, the ADRS, that allows its modems operating on the dial network to adapt themselves to a variety of other modems. **Penril Corp., Rockville, Md.**, and **Tektronix Inc., Beaverton, Ore.**, have entered into an agreement whereby Penril will provide Tektronix with modems for display terminals.



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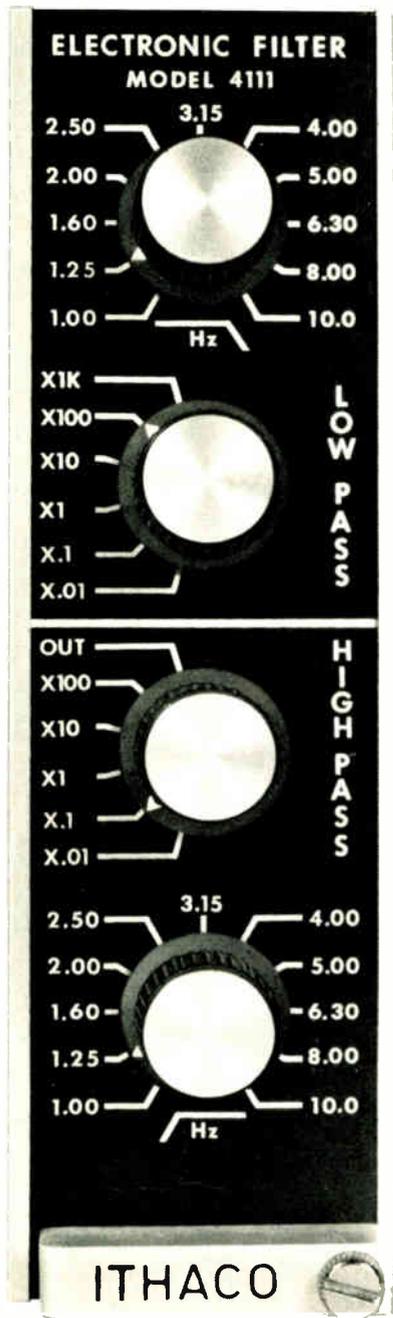
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ITHACO

New products

Subassemblies

Dc-dc converter puts out 120 W

Compact modules use high-efficiency power-transfer circuit

Power supplies are often the limiting factor in miniaturization of equipment. That's why small units that are high in power density take on special significance.

Housed in compact modules measuring only 4 by 6 by $2\frac{1}{4}$ inches, a line of dc-dc converters from Etatech provide up to 120 watts of output power at voltages from 5 to 48 v dc. The new units employ a high-efficiency power-transfer circuit called Univerter, and all have a power density of 2 w per cubic inch.

There are six converter models in all, with nominal outputs of 5, 12, 15, 28, 30, or 48 v dc. Output current is 20 amperes for the 5-v model, 4 A for the 30-v model, and 2.6 A for the 48-v model. Minimum efficiency ranges from 71 to 80%, over an input line range of 26 ± 6 v dc to 44 ± 10 v dc.

The converters are primarily intended for use in battery-operated standby power supplies for high-reliability avionics and ground-support and communications gear.

Combined line and load regulation is 0.4%, while peak-to-peak output ripple is 100 millivolts max-



imum from all sources. All units include remote error-sensing, as well as protection from output short circuits, output overloads, input over-voltages, and reverse input polarity. Recovery time from transients is 250 microseconds; filters and noise-cancellation circuitry reduce noise.

In 100-unit quantities, price is \$295 each for most models. Delivery time is four to six weeks.

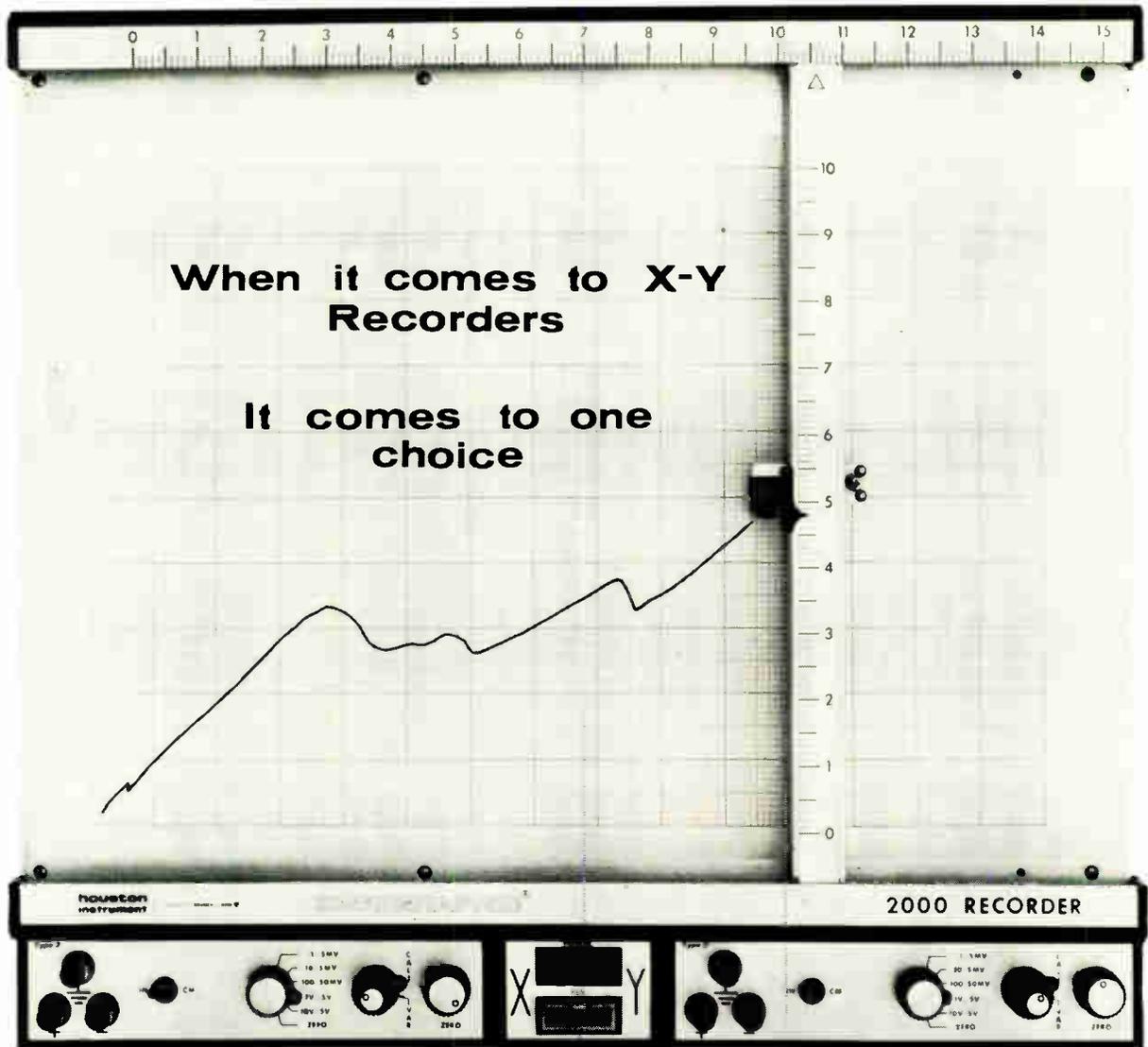
Etatech Inc., 187-M West Orangethorpe Ave., Placentia, Calif. 92670. Phone (714) 996-0981 [382]

16-bit hybrid d-a converter has reference and op amp

The model MN3300 16-bit digital-to-analog converter, a hybrid device, comes complete with an internal reference and an output op amp and is housed in a 24-pin hermetic dual in-line package. Having the output op amp inside the package eliminates contact-resistance and



grounding problems that could degrade the unit's accuracy and linearity. Maximum nonlinearity is specified as less than half a least significant bit over the operating temperature range from 0°C to 70°C . Four laser-trimmed analog voltage ranges are user-selectable by external pin connection. They are: 0 to -5 volts, 0 to -10 v, 0 to +5 v, and 0 to +10 v. The negative ranges use true binary-coded-decimal inputs, while the positive ranges use complementary BCD. The MN3300 consumes a maximum of 420 milliwatts and has a maximum settling time of 35 microseconds. It can be driven by either C-MOS or TTL levels. Typical applications include weighing systems, thumb-wheel interfacing, and process controls. The converter sells for \$149 in small quantities, drop-



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Micro Networks Corp., 324 Clark St., Worcester, Mass. 01606. Phone (617) 852-5400 [383]

Modular dc supplies have fixed outputs

A family of modular dc power supplies from Semiconductor Circuits is directly interchangeable with equivalent series LZ made by Lambda.

These new supplies, also called the LZ series, can operate over a temperature range of -25°C to $+71^{\circ}\text{C}$ with no power derating. Furthermore, they are packaged in 2.5-by-3.5-inch cases, having heights of 0.875 to 1.56 in. The Lambda units, however, have adjustable outputs, whereas Semiconductor Cir-

cuits' supplies are fixed-output devices.

At this time, there are eight models from which to choose. All models can accommodate inputs of 105-125 volts ac at 50-440 hertz. The fixed output can be 5 v dc at 1 or 2 amperes, or ± 12 or ± 15 v dc at ± 100 , ± 200 , or ± 300 milliamperes. Output



protection against short circuits is standard, and output ripple and noise is held to 1 millivolt root-mean-square.

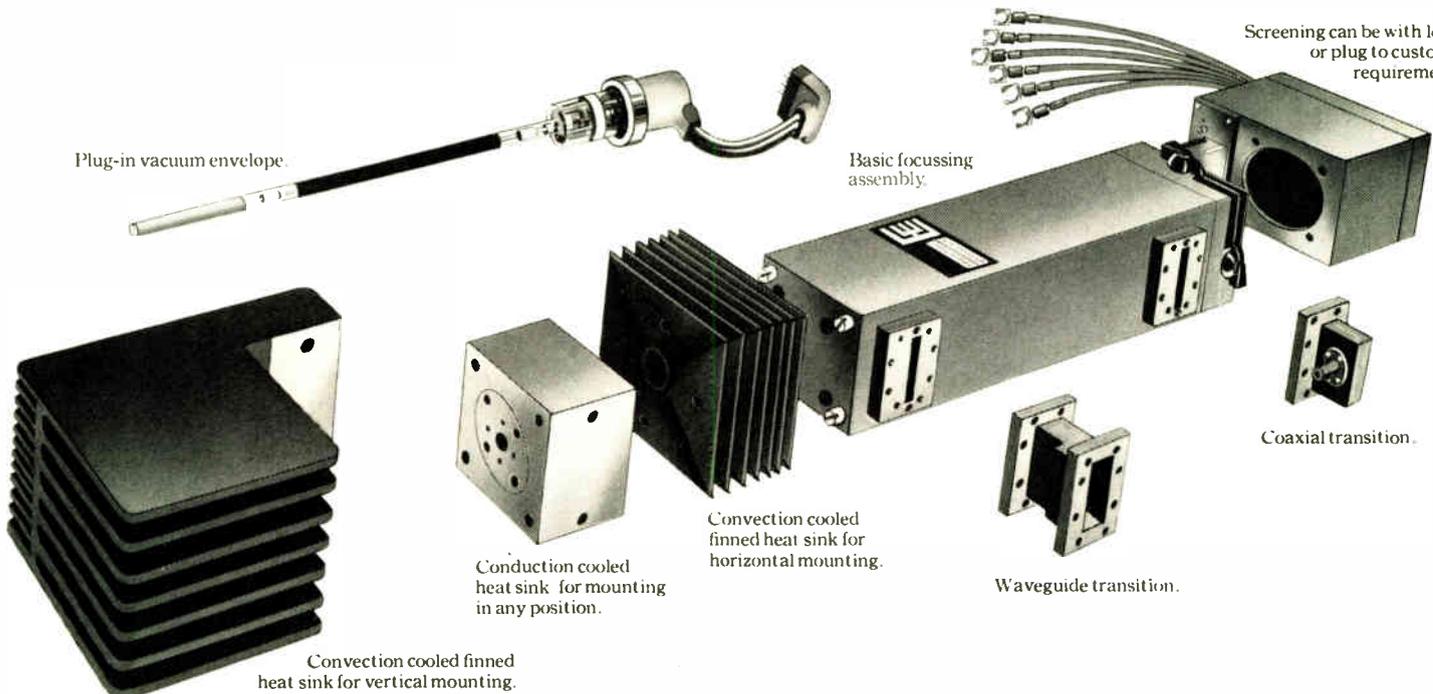
Under high-line and full-load conditions, efficiency ranges from 47% for the 5-v models to 55% for the dual-output models. Line and load regulation is 0.5% for all the single-output units, 0.2% for most of the dual-output ones.

Moreover, to aid heat removal, the supplies have plastic cases made from a thermally conductive epoxy. At 25°C , their mean time before failure is specified as 150,000 hours, the company says.

Single-unit prices range from \$38.95 to \$84.95, depending on the model, and delivery is from stock to within two weeks.

Semiconductor Circuits Inc., 306 River St., Haverhill, Mass. 01830. Phone (617) 373-9104 [391]

We tailor TWTs



Triple-output power supply operates from 6.5 to 40 V dc

Able to accept input voltages over the range from 6.5 to 40 V dc, the WC series battery stretcher provides outputs of ± 5 V dc at 2 amperes and ± 15 V dc at 165 milliamperes. The 15-watt supply requires only free-air convection cooling for ambient tem-



peratures from -25°C to 71°C . An 8-w companion converter is similar to the 15-w unit except that it supplies only 1 A at 5 V and 100 mA at ± 15 V. For both supplies, the 5-V output is not isolated from the input, but the 15-V outputs are isolated from both the input and the 5-V output. Regulation on the 5-V output is typically within 0.5%, and regulation on the tracking 15-V outputs is typically within 0.01%. These parameters, and all others, change very little over the full range of input voltage; for instance, the efficiency rises from 68% to 73% as the input voltage drops from 40 V to 6.5 V. In small quantities, the 15-w supply sells for \$114 and the 8-w version is priced at \$99. Delivery of production quantities takes from four to six weeks.

Stevens-Arnold Inc., 7 Elkins St., South Boston, Mass. 02127. Phone (617) 268-1170 [384]

Log converter stays within 0.5% over four decades

A temperature-compensated, dc-coupled log ratio module maintains log conformity to within 0.5% over the four decades of input current from 10 nanoamperes to 100 microamperes. When operated over its full six-decade range, from 1 nA to 1 milliamperere, the unit still holds log conformity within 1%. And unlike earlier designs, the model 757 does not restrict the relative magnitude of the two signal inputs, which may be either currents or voltages. Offered in two versions—one for positive inputs, the other for negative—the 757 sells for \$69 in small quantities. Delivery is from stock.

Analog Devices Inc., P.O. Box 280, Norwood, Mass. 02062. Phone Lowell Wickersham at (617) 329-4700 [385]

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Convection cooled finned heat sink for vertical mounting.

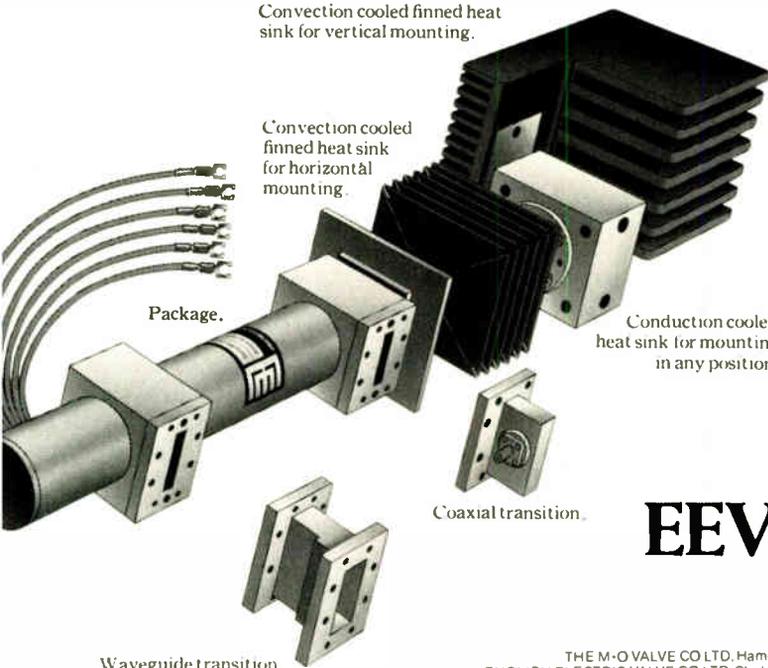
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New products

Industrial

System protects against outages

Uninterruptible power equipment restores voltage to within 5% in 50 ms

As power loss becomes a critical factor in more and more computer-based equipment, there's a rapidly growing demand for power systems that provide protection against outages.

With that in mind, Topaz Electronics of San Diego, Calif., is introducing a line of uninterruptible power systems. Standard models are available in ratings of 3, 5, 10 and 15 kilovolt-amperes. The Topaz systems, called the 81000 series, consist of isolation transformer, rectifier, battery bank, static inverter and transfer switch. When ac power is normal, the transformer eliminates line transients, the output is converted to dc by the rectifier, and then the static inverter reconverts it back into ac. The battery charger maintains the battery bank in a fully charged state. If the ac voltage falls more than 15% below nominal, a silicon controlled rectifier connects the battery bank to the inverter input, so that no discontinuity results. When ac power is restored, the SCR

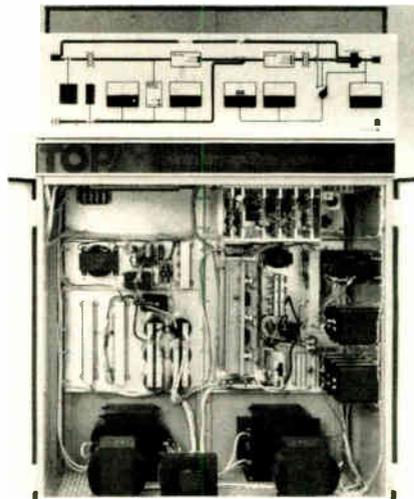
turns off the battery charger.

Minimum efficiencies for the rectifier, charger, and inverter are said by the company to be 90%, 85% and 85% respectively. The incorporation of separate battery charger and rectifier modules improves the input power factor, resulting in lower operating costs and reduced heat losses. The company claims output voltage variations do not exceed 10% of the steady-state value for step-load changes of 50%. Output voltage recovers to within 5% in 50 milliseconds.

A major feature of the 81000 series, Topaz says, is a status monitor and control panel for continuously displaying operational functions. The Topaz units are designed for a 20-year life, with company calculation indicating a mean-time-between-failures in excess of 20,000 hours. Options include a static transfer switch, an audible alarm and acknowledgement switch, output frequency meter, and battery ammeter. The new line is constructed in NEMA type 2 front-access cabinets. Topaz also manufactures a complete line of uninterruptible power systems in rack-mounted packages, with ratings from 500 voltamperes through 10 kVA, single phase.

Prices start at \$5,000 for the 3-kVA model. Delivery time is one to four weeks.

Topaz Electronics, 3855 Ruffin Rd., San Diego, Calif., 92123. Phone Richard Wheelock at (714) 279-0111 [371]

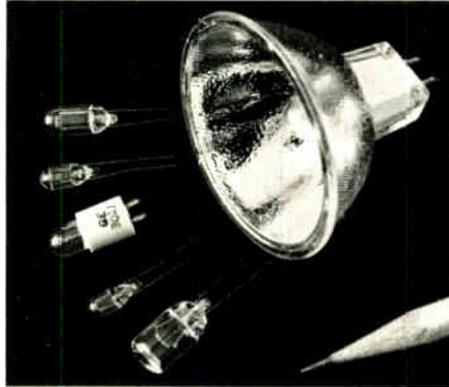


Ultrasonic liquid level sensor wraps around pipe

Using a nonhazardous, low-energy ultrasonic signal for detection of liquids, the Sensall model 621S level sensor has a pair of small encapsulated transducers that are curved to fit around a tube, pipe or vessel. There are three versions, which wrap around 1/2-inch, 3/8-inch, and 3/4-inch diameters. Other sizes may be specified as options. In operation, a remote control unit generates an electrical signal that is converted to

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They're ideal for you if you're designing applications such as optical systems, instrumentation, illuminators, fiber optics, card readers, displays and aircraft navigation. A variety of terminals are offered.

For updated technical information circle the number below or write GE for Bulletin #3-5357.

**These GE wedge base miniature lamps offer
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These lamps are ideal for applications such as indicators, markers and general illumination where space is at a premium. Their wedge-based construction makes them easy to insert and remove. They don't require bulky, complicated sockets. And because the filament is always positioned the same in relation to the base, you get consistent illumination from lamp to lamp.



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To send for updated wedge base lamp technical information, circle number below or write GE for Bulletin #3-5259.

**These three free GE catalogs include
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#3-5169

June '75 Miniature lamp catalog features 40 pages and 500 data changes for complete 500-lamp line.



#3-6252R1

Feb. '75 Sub-miniature lamp catalog features 24 pages and 91 changes for more than 210 lamps.



#3-6254R

Dec. '74 Glow Lamp catalog features 8 pages and 50 changes for 83 Glow Lamp Indicator and Circuit Component lamps.

For up-to-date technical information on any of these items write: General Electric Company, Miniature Lamp Products Department #3382-M, Nela Park, Cleveland, Ohio 44112.

GENERAL  ELECTRIC

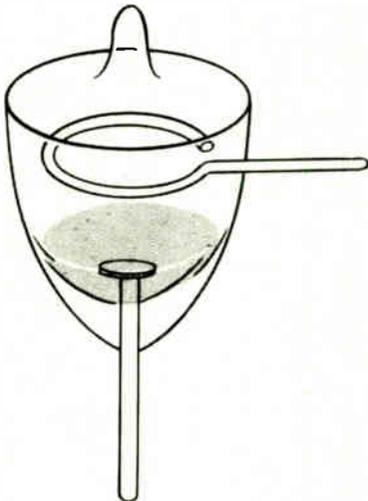


an ultrasonic signal at the transducer. The ultrasonic signal passes through the wall of a pipe or vessel to the opposite transducer only when liquid is in the signal path. It then closes the electrical circuit, activating a relay in the control unit.

National Sonics Division, Envirotech Corp.,
250 Marcus Blvd, Hauppauge, N.Y. 11787.
Phone Nicholas Poulis at (516) 273-6600
[373]

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tical. This is accomplished through use of a halo-shaped electrode and a bridging mercury pool that contacts a part of the halo at the same angular displacement, regardless of its direction. Suitable for applications in positioning, as a detector and in alarm systems, the switch is only 0.7 inch high and 0.4 in. in diameter. It is rated at 1 ampere at 115 v ac and 30 v dc.

Mack Electric Devices Inc., Wyncote, Pa. 19095. Phone Ken McKinney at (215) 884-8123 [377]

IR photoelectric devices can see around corners

A through-scan infrared photoelectric device for industrial and commercial environments can operate in almost all kinds of light and even see around corners, thanks to its optional right-angle beam-deflectors. The fully threaded, modulated light source, the MLS4A, is fully compatible with logic-level circuitry and requires no additional amplification for the 120-milliamper output to drive a relay directly. For barely accessible places, parallel positioning of the integrated-circuit units is also possible. The two-unit MLS4A works at speeds up to 125 operations per second and has an input voltage of 12-16 v dc. Both the emitter and receiver are potted in a vibration-resistant, 3/8-inch-diameter aluminum package. Price is about \$120.

Micro Switch, Division of Honeywell, 11 W. Spring St., Freeport, Ill. [374]

Rotary limit switches are programmable

Designed to replace electromechanical cam-operated switches, the series DLS103 line of programmable solid-state limit switches consists of a rotary electromagnetic transducer connected through a six-wire cable to a digital programming unit. Used for control of repetitive or sequential operations, the switches can be

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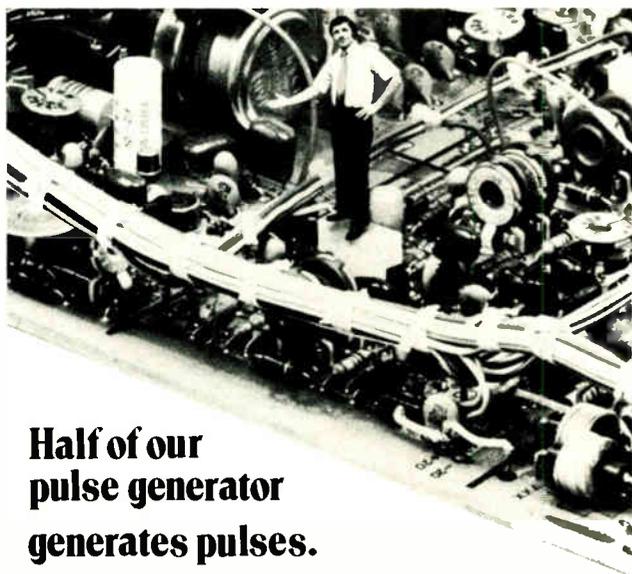


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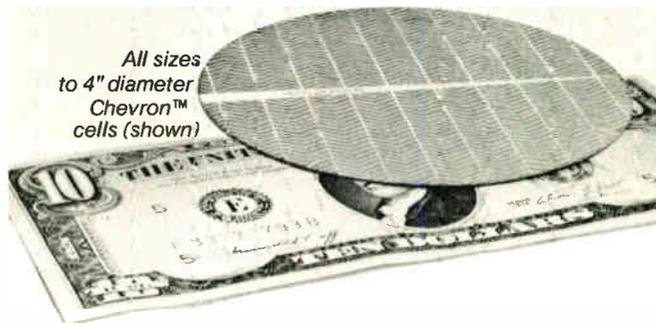
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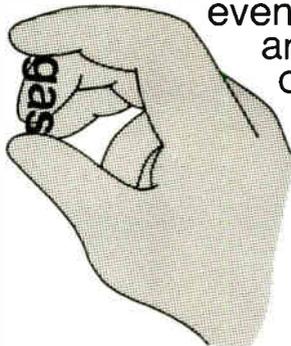
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C & A Products Inc., 37-12 58th St., Woodside, N.Y. 11377. Phone (212) 779-4303 [375]

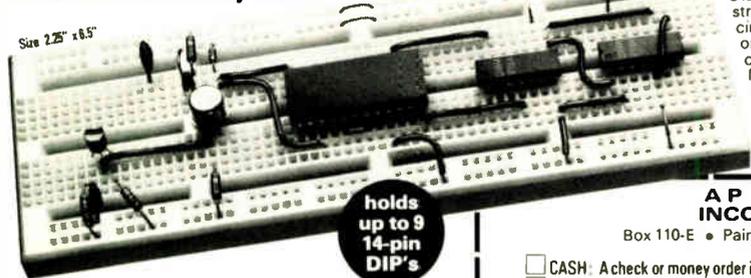
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Veeder-Root, 70 Sargeant St., Hartford, Conn. 06102 [379]



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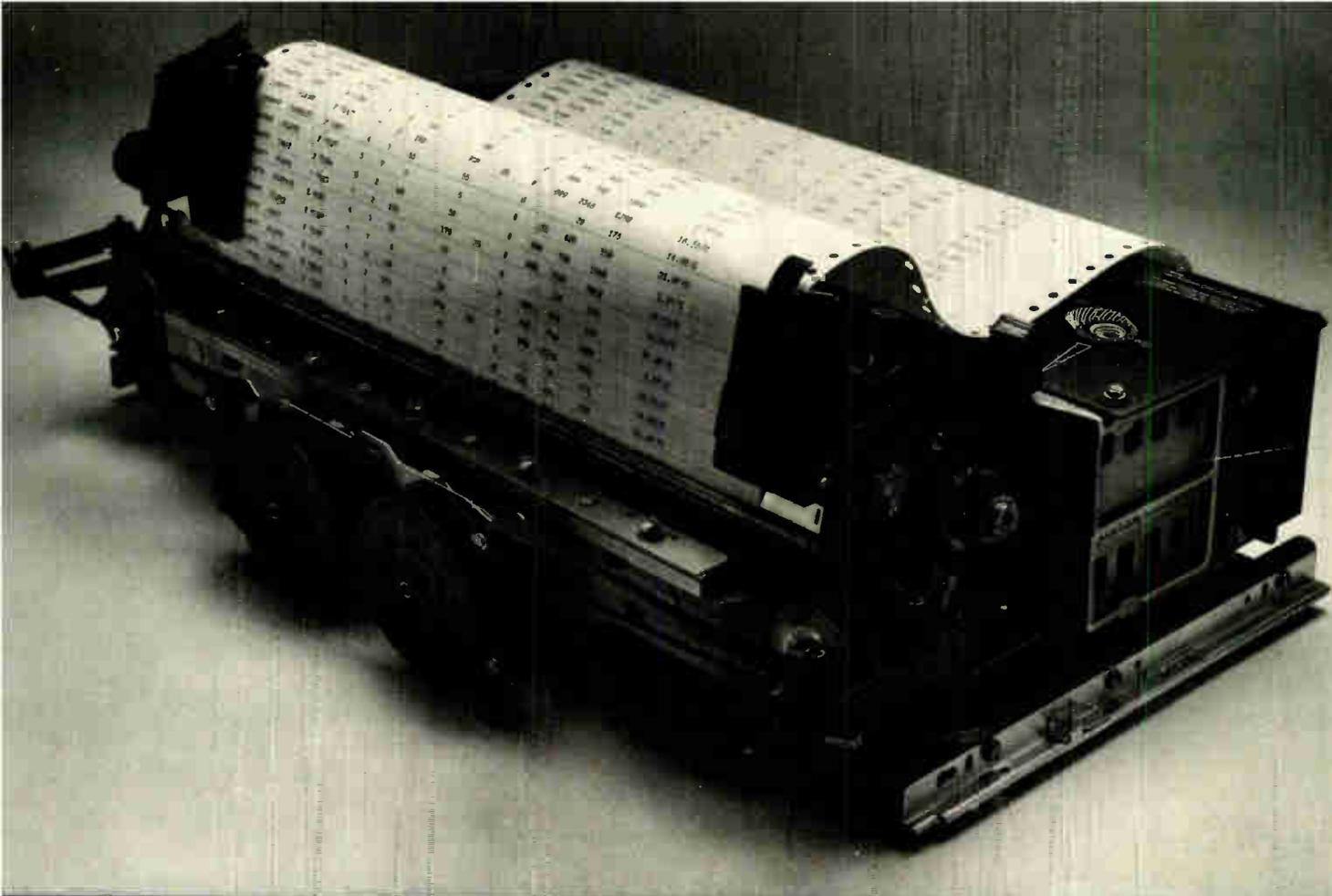
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Circle 137 on reader service card

New literature

Circuit design wall chart. A technical wall chart containing data, graphs, circuits, and formulas often used in the design of analog circuitry is offered by Intronic Inc., 57 Chapel St., Newton, Mass. 02158. The chart includes a circuit for the computation of vector phase and magnitude, tables and curves showing thermal noise in resistors, and Fourier spectra and crest factors of common waveforms. Circle reader service number 421.

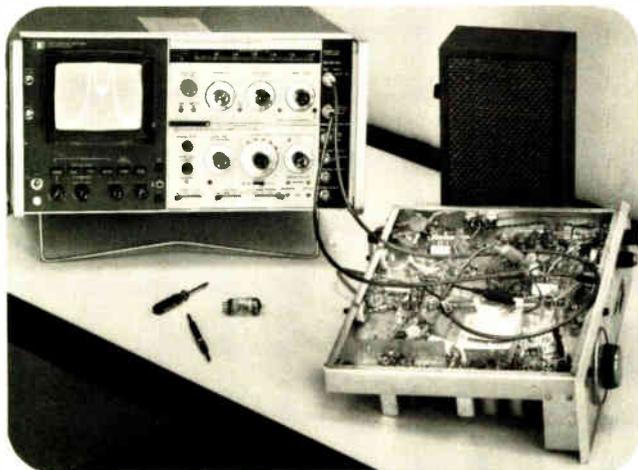


Camac standard. IEEE Std 583-1975 is an internationally accepted instrumentation interface standard for Computer Automated Measure-

ment and Control (Camac). The standard deals with both mechanical and electrical quantities. Camac systems can be connected to digital data systems built in accordance with IEEE Std 488-1975 by means of an interfacing module. Copies of the Camac standard are available from the Institute of Electrical and Electronics Engineers, 345 East 47 St., New York, N. Y. 10017. The price of IEEE Std 583-1975 is \$10 per copy, post paid; IEEE members may

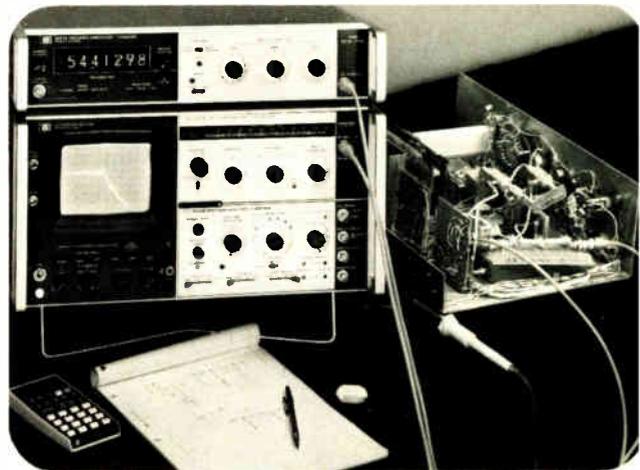
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order a single copy of the publication for \$7.50.

Measuring noise. A 16-page brochure from Ailtech, 19535 E. Walnut Dr., City of Industry, Calif. 91748, includes an introduction to noise-figure measurement, a page of useful formulas, including a nomograph for finding noise figure or effective input noise temperature, and catalog data on the Ailtech line of noise-figure measuring equipment.

Some of the equipment spans the range from 10 MHz to 40 GHz. [423]

Speed-sensing switches. A line of zero-speed and speed-sensing switches along with a series of rotary-motion detectors is described in a new product brochure put out by Autotech Corp., 859 Westgate Ave., Addison, Ill. 60101 [424]

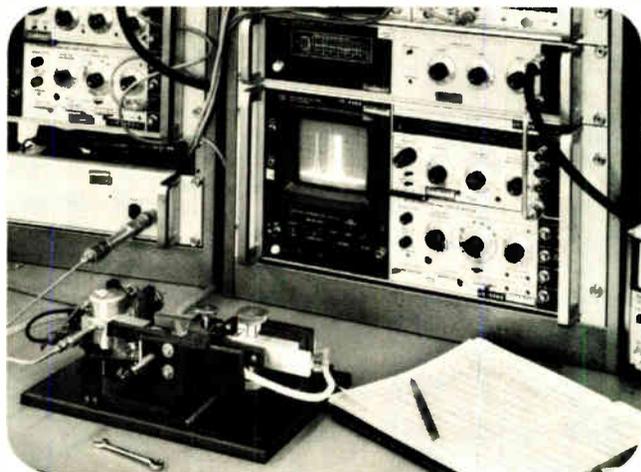
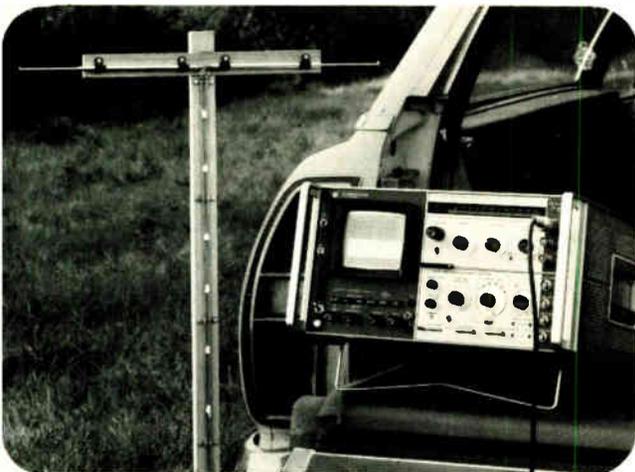
Power supplies. Catalog 1976-1977 covers laboratory benchtop power

supplies with single, double, and triple outputs; miniature supplies in both pc-board and chassis-mounting versions; and rack-mounting high-current supplies. Also included are general-purpose, plug-in, premium-performance, narrow-profile, and unregulated units. The catalog can be obtained from Acopian Corp., Easton, Pa. 18042 [425]

Indicator lamps. A 15-page catalog from Solico, 530 Oakwood Ave.,

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New literature

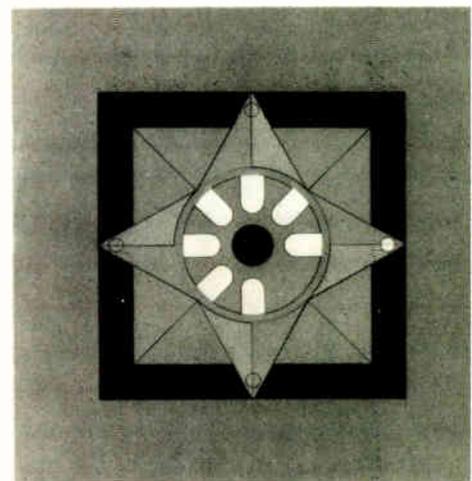
West Hartford, Conn. 06110, gives details of the company's line of incandescent, glow, and LED pilot and indicator lamps. The booklet also contains a discussion of the physical characteristics of glow lamps and a glossary of technical terms. [426]

Semiconductor chips. A broad line of discrete transistor, diode, and zener-diode chips is described in catalog CN-164D, which also supplies information on Sprague's Oxsil MOS capacitor chips, which are used in thin-film hybrid circuits. Copies of the catalog may be obtained from Technical Literature Service, Sprague Electric Co., 35 Marshall St., North Adams, Mass. 01247 [427]

Interconnection devices. Specifications of terminal strips, tube and transistor sockets, plugs, receptacles, wire-grip terminal board assemblies, and similar electromechanical interconnection devices are outlined in a 28-page catalog put out by Malco, a Microdot Co., 12 Progress Dr.,



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Montgomeryville, Pa. 18936. Ask for Electro Mechanical Assemblies catalog #801-1. [428]

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Circle 159 on reader service card

Processing analog signals?

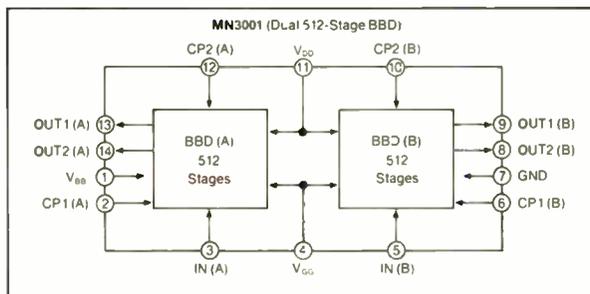
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Circle 213 on reader service card

New literature

and photo reproduction lenses are listed in a catalog of unusual items published by Electro Science Mart, 119 Foster St., Peabody, Mass. 01960 [429]

Phase measurement. A 28-page illustrated publication entitled Applications Handbook of Precision Phase Measurement is available for \$2.75 from Dranetz Engineering Laboratories Inc., 2385 South Clinton Ave., Plainfield, N.J. 07080. The handbook begins with two introductory sections, one on how today's phase meters work and the second on why phase measurement may often be preferable to gain or bridge measurements. The third section discusses and illustrates various types of measurement in device work.

Signal sources. Circuit techniques for optimizing the performance of various types of phase-locked sources are described in a 12-page applications bulletin, "Recent Advances in Solid-State Phase-Locked Microwave Signal Sources." The bulletin first describes six types of sources and the principal reasons for employing the phase-locked technique. Then comes a section on the theory of operation, and the final section is devoted chiefly to methods and devices for improving the performance of phase-locked signal sources. The bulletin is available from Communication Techniques Inc., 1279 Route 46, Parsippany, N.J. 07054 [430]

Vacuum coating. The first in a series of application notes on coating is available from Varian Palo Alto Vacuum Division, 611 Hansen Way, Palo Alto, Calif. 94303. The eight-page bulletin describes details of the deposition of aluminum silicon alloy with high-rate S-Gun sputtering sources. The recipe for a typical metalization process is defined and microphotographs showing step coverage are reproduced. The bulletin deals with application of coating techniques to silicon-gate MOS and silicon-gate nitride MOS devices [431]

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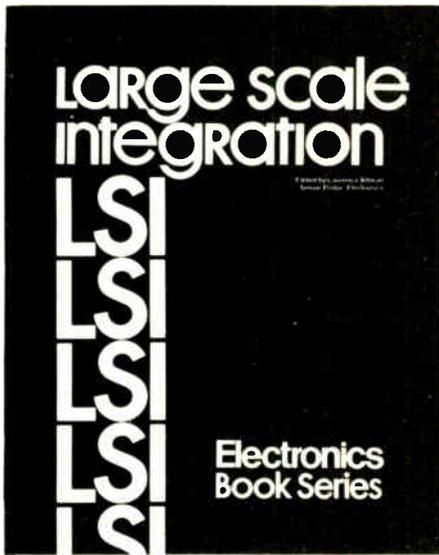
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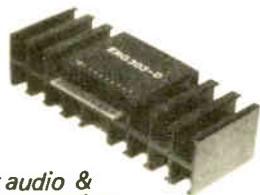
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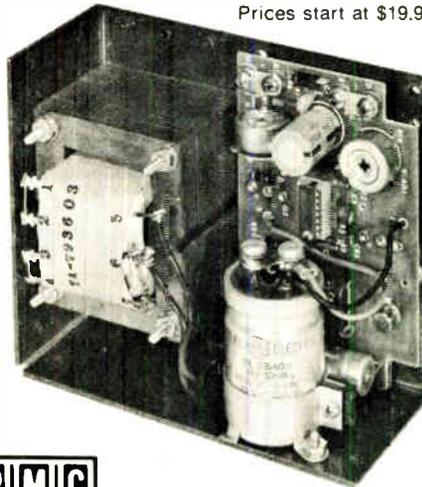
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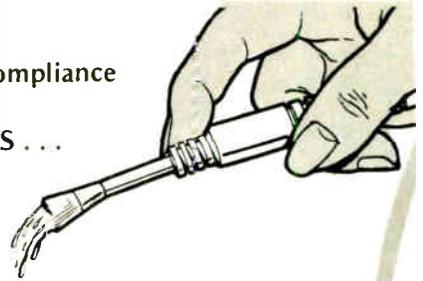
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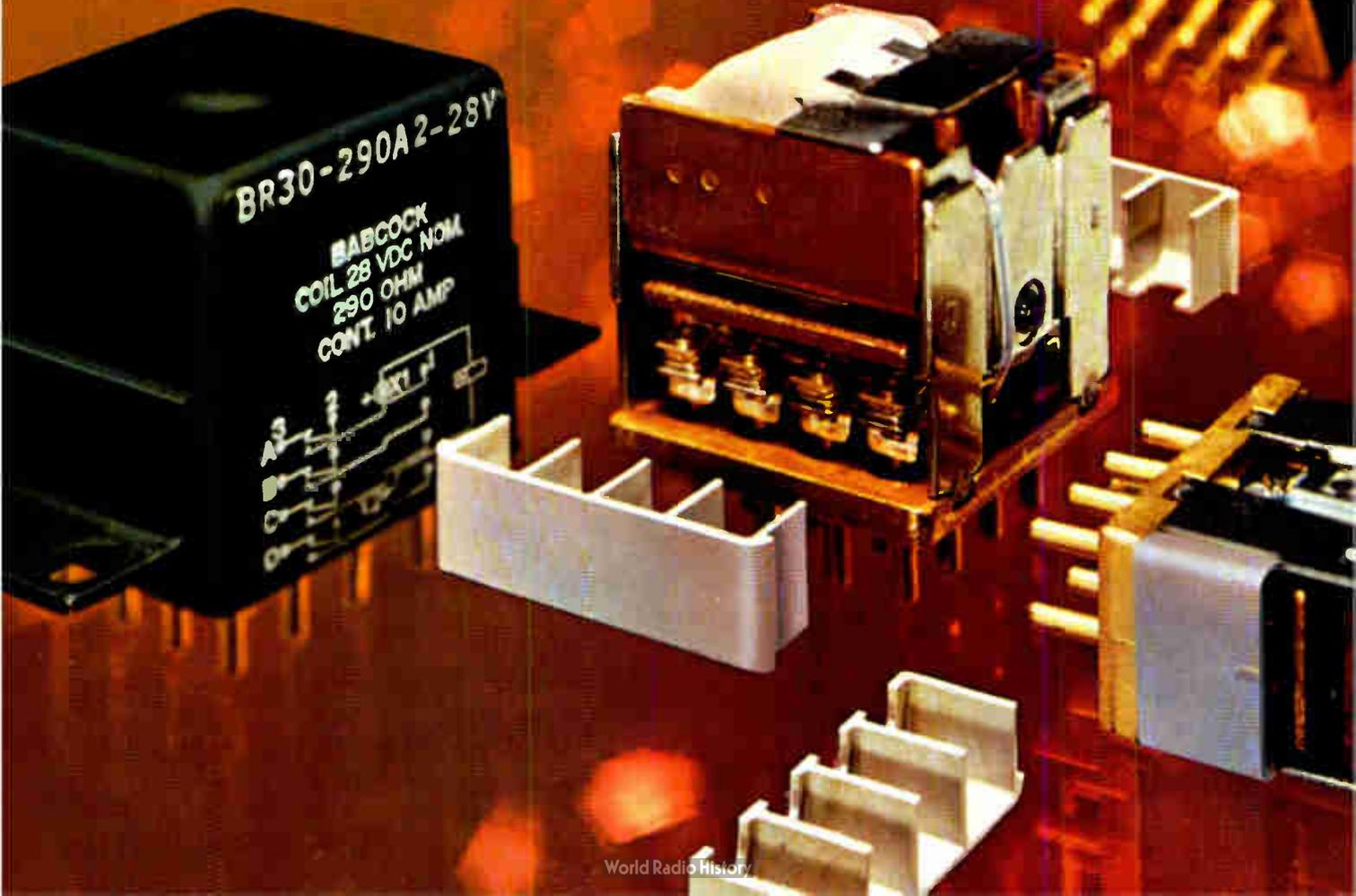
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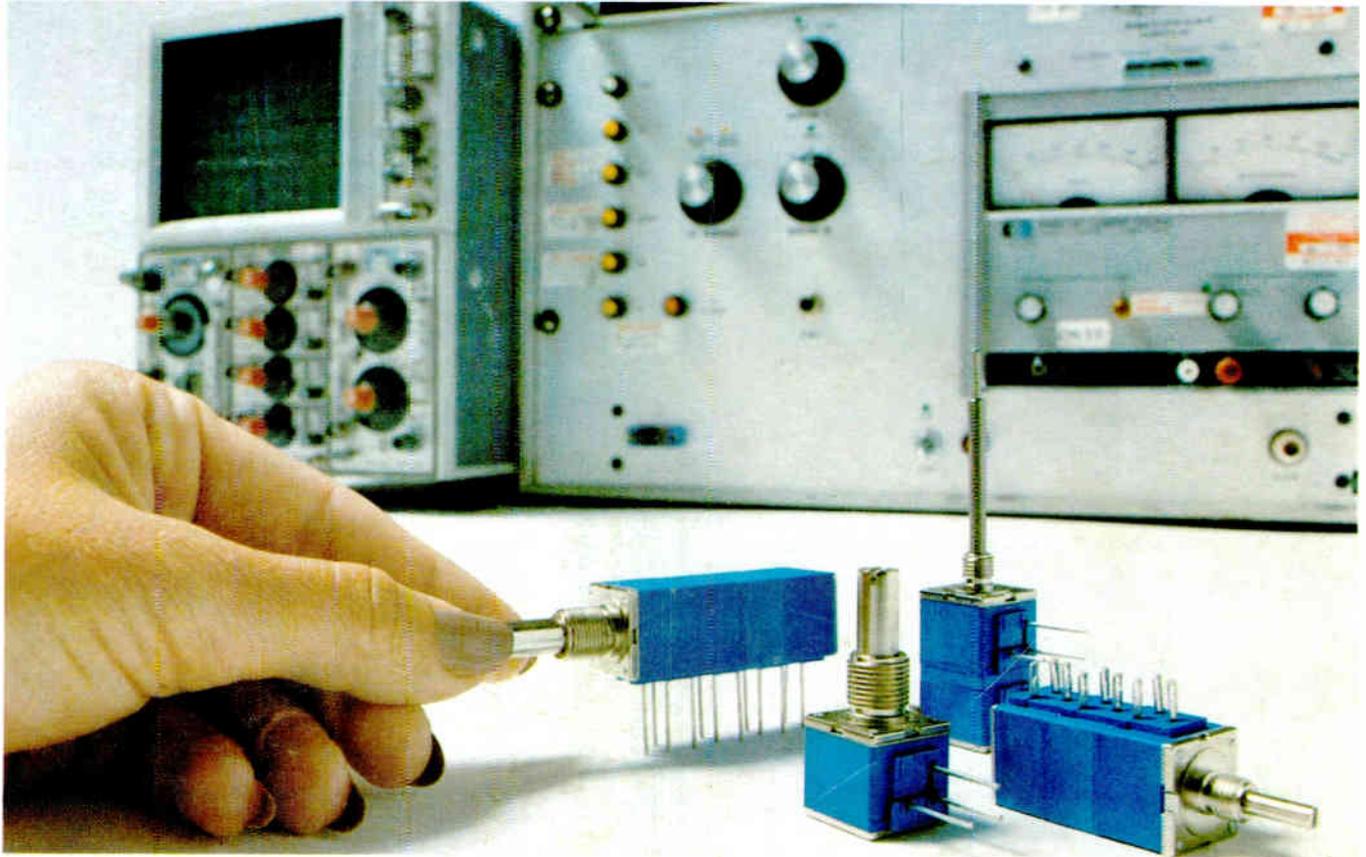
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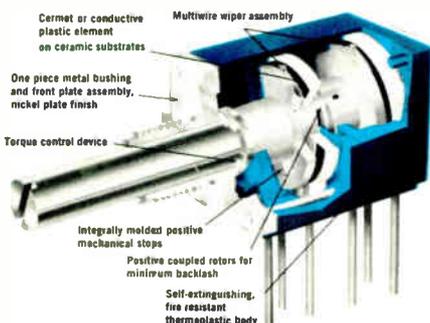
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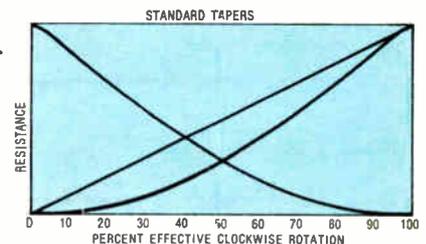
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