

## EXCLUSIVE

 REVIEW-
# Much more than just kits quite simply the best way 

 to make music...Powertran have been designing and manufacturing high-quality electronic kits for more than a decade. Thousands have been purchased and assembled by constructors throughout the UK and world-wide. Many of our regular clients have built the entire range - several times! A Powertran kit makes an excellent gift for the electronics enthusiast; and is a gift that, when constructed, may be given again.

Our reputation rests on these unshakeable foundations - we use the most imaginative and ingenious designers; we use high grade components subjected to rigid quality control; our kits are complete, even screws and wire are included; we take care with packing and despatch; our instructions are clear and always fully comprehensive .... and if that weren't enough we back it up with our money-back guarantee. Powertran care and your skill gives you that something special.

Among the most popular of our kits are the fabulous 'Transcendent' range of synthesisers. Designed by the expent In the fleld, Tim Orr, and featured in Electronics Today International - those kits represent the zenith in both constructional Ingenulty and musical performance. Thanks to our fully illustrated, carefully dlagrammed 30 pages plus of constructional notes the 'Transcendent' range is comfortably within the capability of most enthuslasts. A great many 'first time builders' have completed them without difficulty and are justifiably pleased with the results.



Check your reactions p. 79



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## EDITORIAL AND ADVERTISEMENT OFFICE <br> 145 Charing Cross Road, London WC2H 0EE. Telephone 01-437 1002/3/4/5.

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## NEWS:NEWS:NEWS:NEWS:NEWS:NEWS:NEWS



## In-Car Care

The high price of installing in-car sound systems is often due to the large proportion of cash earmarked for so-called expert installation. But Greens at Debenhams have cume up with a less expensive alternative. They have introtluced Track One - a range of in-car entertainment systems with full installation instructions, backed by a 24 -hour

# Game, Set And Match 

Extending the series of features Eon their C80 watch. Casio have decided to include alarm with tone control, day and date indication with time display, and a combat style electronic game. The two new models incorporating these features are called the CA90 and CA901. The first has a black resin case and bracelet; the second. a stainless steel coated case and solid stainless steel bracelet. Recommended retail prices are £ 22.95 (CA90) and E34.95 (CA901). Thev should be available from your friendly neighbourhood Casio stockist or in case of ditficulty contact Casio themselves at 28 Scrutton Street, London EC2A 4TY

## New Connections

Following up on our article last month about the new TelecommuniI cations bill. we have found British I elecom are now pulling their proverbial receivers out. For example, a scheme designed to enable telephone sockets to become as commonplace as power outlets has recently been announced by BT. The scheme will be launched nationwide this autumn and follows a successful trial programme in Taunton and Carlisle which has been running since May this year. Instead of having telephones fixed permanently, customers now will be able to unplug them and move them from room to room wherever sockets have been installed. This idea will radically change the installation of the telephone service in and around the home, also simplifying the sale of some Telecom equipment, when phones will be available from some 40 shops by next A pril, mostly within department stores. These will be a vailable for sale to customers to take home and plug in. As supplies of the new plugs and sockets become available, customers asking for extension phones witl have sockets fitted, and will be supplied with a telephone of their choice, with a plug alreadv connected to it Engineers will also adapt existing phones on the same line to plug and socket connections. This will replace existing extension arrangements. Customers requiring an extension telephone will pav a connertion charge of $£ 25$ with additional extensions provided at the same zomef for only E10. the rental \& Il be E 265 tor b: residential extension, complete with staindard telephone and additional sockets on their own will be charged at 15 p a quarter. All new installation work on residential and single business lines will incorporate the new system. Extranote: This means that all telephones approved after the new liberalisation comes into eftect will be candidates for this kind of plug and socket connection.

Further update: Ferranti have just won a contract to supply British Telecom with the new ZN470AE Microphone Amplifier Integrated Circuit. This will be incorporated in the new linear electret microphone manufactured by AP Besson Ltd, which will replace the familiar carbon type, thus offering improved speech quality and long-term reliability

## Telecom Turnround

It seems that there has been some confusion about the use of the word 'illegal' in the article entitled Telecom Turnround published last month. 'Illegal supplier' and 'illegal equipment' were not clearly defined. In fact, there is nothing illegal about supplying or selling telephone equipment, and likewise the units themselves are not illegal. The law is only broken when an independent supplier publicly states that the equipment he se ${ }^{1 \prime}$, can be connected to the UK network when it hasn't been approved by British Telecom. And again, the equipment is only illegal once it has been connected and in this case it is the buyer of the equipment who breaks the law, not the supplier.

We apologise to any readers (especially suppliers!) who may have been inconvenienced by this misinterpretation.

## FM Main Man!

Here is a letter we received from the Electricity Coun

## Dear Sir

FM Mains Remote Control
The feature in the October 1981 edilion of Electronics Today describing a control device which utilises the electrical installation as the mediun the possibility of between separate rooms of a house aiso mentioned the possibility of utilising the power distribution system between
separate premises. eparate premises.
Would you please point out to your readers that under section 27 of the the owner or occupier of Act 1899 there is provision which states that if the owner or occupier of premises uses the energy supplied so as to interfere 'unduly or improperly' with the efficient supply of energy to any other person the Board may discontinue the supply. be used in the manner you describe power distribution system were to restricted to the circuit beeween dhe the injected signals would not be broadcast throughout the surrounding lowful neighbours' but would be therefore be 'received' by all the other electricity consumers connected to that network. Any detrimental elfects the signals may have on any item of equipment being used by those other consumers must clearly be the responsibility of those involved in generating the signals regardless of whether they have spilled over from the insiallation or whether they have been deliberately injected into the network directly. Yours faithfully,
D.V. Iord

Head of Distribution Engineering


## EXPERIMENTOR BREADBOARDS

No soldering modular breadboards, simply plug componenis in and out of letter number identified nickel-silver contact holes. Staft small and simply snap-lack boards together to build a breadboard of any size
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## EXP 600 f6. 30 Mosi

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spacing so is perfeci for
MICROPROCESSOR applications

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No. 11 DIGITAL ROULETTE
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## No. 12 EGG TIMER

How do you like your eggs done, hard or soft, just set the timer and it will sound when the egg is done to your liking. Long battery life because it switches itself off automatically. So get cracking now!
Want to get started on building exciting projects, but don't know how? Now using EXPERIMENTOR BREADBOARDS and following the instructions in our FREE 'Electronics By Numbers' leaflets. ANYBODY can build electronic projects. For example, take one of our earlier projects, a L.E.D. Bar Grapt):


You will need: One EXP 300 or EXP 350 breadboard 15 silicon diodes
6 resistors 6 Light Emitting Diodes
Just look at the diagram, Select R1, plug it into the lettered and numbered holes on the EXPERIMENTOR BREADBOARD, do the same with all the other components. connect to the battery, and your project's finished. All you have to do is follow the large, clear layouts on the 'Electronics by Numbers' leaflets, and ANYBODY can build to us, and you will receive the latest 'ELECTRONICS BY NUMBERS' leaflet.

If you have missed projects, 1, 2 and 3, or 4,5 and 6 , or 7.8 and 9 , please tick the appropriate box in the coupon.

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## NEWS:NEWS:NEWS:NEWind

## Books, Books, Books...

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nce again it's time to get to grips with the ever-increasing pile of review books that threaten to take over ETI's corner of the ASP of-

The first one comes from Hodder and Stoughton, and is the latest Teach Yourself book in their Computer Science Studies range. Entitled Microelectronics and Microcomputers', the book is intended to provide a general background and introduction to microcomputers for people who want to come to terms with the increasing impact of computers both at home and at work. All aspects of the subject are covered, although briefly; the book starts with such basic topics as types of electronic components, the use of various number systems (decimal, binary, hex and so on), and simple logic of a microcomputer, peripheral devices and how to use them, programming, system development, data transmission and instrumentation techniques. The final five chapters describe numerous applications in the fields of industry, transport, consumer goods, education and business. No prior knowledge is assumed, although some experience of electronic theory would make the first half of the book easier to grasp The a uthors have packed a great deal into the book's 225 pages, and it can be recommended as an introductory text for bewildered businessmen and for people interested in the forceful effects that computers are having on our society. 'Microelectronics and Microcomputers' is written by I.R. Carter and E. Huzan, and costs E1.95.

The rest of the books come from Bernard Babani. 'Audio Projects' ( $£ 1.95$ ), by $F: G$. Rayer, gets down to the nitty-gritty very quickly - a brief introduction and then it's straight into the projects, over 30 of them, rang ing from preamps, mixers and power amps to tone control networks, test gear and a simple tuner. Although constructional details are rudimentary and in some cases non-existent, none of the circuits should be at all difficult to build. '50 Simple LED Circuits Book 2', by R.N. Soar (E1.35), speaks for itself - the circuits are simple, they use LEDs, and there are 50 of them. A good buy for the absoluie beginner; no constructional details are given but anyone should be able to build these circuits on Veroboard without any trouble. If you think BASIC is beyond you, 'An Introduction to BASIC Programming Techniques', by S. Daly, provides a simple guide to this popuiar high level computer language. The author covers all the statements you're ever likely to meet, keeping the examples simple and pointing out possible machine-dependent variation. In fact the book recommends that you try programming an actual computer as soon as possible - and so do we. This way you quickly find all the quirks of a particular BASIC, and the hands-on approach is definitely the fastest way to learn. 'An Introduction to BASIC Programming Techniques' will cost you E1.95. That's it for the time being; more reviews will follow, word blindness permitting!
 add to the protection they can of-

## Pack Up Your Troubles

mhof-Bedco, the electronics packaging specialists, have just launched a range of 'camera craft' security cases. Of course, they needn't just be used for carrying cameras, as they are just as usefu! for carrying any sort of delicate equipment - test gear perhaps? They feature an aluminium frame and facing on rigid wooden panels, combining strength and smart appearance with light weight. Lockable toggle catches, robust hinges
fer. The cases can be supplied in three different sizes complete with carrying straps and shock absorbent foam inserts. The foam is easily cut to shape with the knife provided, so it can accommodate various shapes and sizes. There is also a briefcase version fitted with PVC lining and document wallet in the lid, just in case you have any briefs in need of protection! The cases are available ex-stock and prices start at E22. Further details are available from Mike Young Imhof-Bedco Standard Products Ltd, Ashley Road, Uxbridge, Mid. dlesex UB8 2SQ (telephone Uxbridge (0895) 3712.3).

## Mail Order

Toolmail are offering two new helpful aids to the hobby enthusiast. The first of these is a hob; by service case, available for the first time in this country. It has a robust metal frame containing 16 clear styrene drawers (each $51 / 2 \times$ $23 / 4 \times 1 \frac{1 / 2^{\prime \prime} \text { ) for storage of com- }}{(1)}$ ponents and small parts, and one strong base drawer ( $11 \times 51 / 2 \times 31 / 4$ ") for the storage of tools and other large or heavy items. The front of the tough vinyl outer case folds down to provide a useful working surface. The case is $12^{\prime \prime}$ high with a comfortable carrying handle. It is available for a limited period at the introductory price of E29.95
which includes VAT and free delivery ( $R$ RP is E34.95). The second offer is an electronics service wallet designed for work with computers, video and audio units. It includes a selected range oi 25 precision miniature tools and is contained in a fitted zipper wallet. The tools include miniature soldering iron, desolder braid, solder, soldering tools, range of screwdrivers pliers and cutters, wire strippers, IC extractor, tweezers, scissors and contact cleaners. The kit costs E39.95 including VAT and free postage anywhere in the UK. Both of these items are available mail order from Toolmail Lid, Parkwood Industrial Estate, Sutton Road, Maidstone, Kent ME15 9LZ.

## Heavy Levy

The Government is likely to face strong opposition from Britain's 25 million blank tape users if it goes ahead with its plan to put a levy on the sale of blank tapes. The levy is meant to compensate performers for their loss of sales and royalty payments due to home taping. The six main UK blank tape manufacturers will also be adding their weight to the argument through the auspices of the Tape Manufacturers' Group. The Group has been formed to fight the proposal reported to be contained in a Green Paper reviewing the whole area of copyright law, which is expected to be published shortly. The TMC includes representatives from BASF. 3M Maxell Memorex, Sony and TDK, and Mr Bill Fulton, the Group's Chairman, maintains that the levy plan is unworkable and impractical and that the problem of home taping has been overstated. He also believes that any levy would penalise all tape users, whether or not they were breaking the copyright law, and that the levy would be like imposing a tax on blank tapes therefore effectively subsidising the record companies. The basis on which the levy has been proposed is that the British Phonographic industry claims that it is losing E1 million a day through breaches of copyright. But the TMG say they haven't produced any hard evidence to back up this fact.



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## NEWS:NEWS:NEWS:NEWS:NEWS: ${ }^{1 \text { ºso }}$

B-B-Books from B-B-Babani
Dernard Babani Publishing Ltd will be happy to send copies of their new 1982 catalogue of Radio, Electronic and Computer books to anyone who cares to send them their name and address. So if you want one, write to them at: The Grampians. Shepherds Bush Road, London W6 7NF.


## Hot Stuff!

Sinclair Electronics has just announced the launch of the Thandar TH301 hand-held digital thermometer. It features a large readout LCD display, a wide temperature range of $-50^{\circ} \mathrm{C}$ to $750^{\circ} \mathrm{C}$ and $1{ }^{\circ} \mathrm{C}$ resolution. It also incorporates the latest technology and over 1,000 hours of battery life is obtainable. The unit is housed in a strong Thandar case and is supplied complete with battery and fast response bead thermocouple. The price is $£ 59.50$ including VAT and Sinclair offer a range of

thermocouples as optional accessories, covering a wide range of applications, including mineral filled, hypodermic, right angle and surface. These all come complete with plug and flexible interconnecting cable. For further details contact Sinclair Electronics Ltd, London Road, St Ives, Huntingdon, Cambridgeshire PE174HJ.

## ELCB

R \& R Relays have broadened their range of combined 13 A Earth $D$ Leakage Circult Breaker (ELCB) socket outlets with the introduction of a new wall mounting version. Based on the successful HO4 portable ELCB with integral 13 A socket, the new model is designed for mounting directly on a convenient single of double outlet box. The new ELCB is simple to install - anyone capable of wiring up a standard socket can do it, be it at home, in the office, workshop or factory. The unit is available in sensitivities ranging from 10 mA to 30 mA , and a test button allows operation to be checked at any time as well as every time the device is switched off. Special socket styles can be supplied to ensure that particular equipment is always plugged into the ELCB and not a non-protected socket outlet. Further information can be obtained from B \& R Relays Ltd, Edinburgh Place, Harlow, Essex CM20 2DJ.

## Bright Flatpacks

Derdix Components Ltd have just announced the release of two new incandescent digital displays for their Aurora line. The new FFD-71 ( $0.472^{\prime \prime}$ character) and FFD-81 ( $0.614^{\prime \prime}$ character) displays operate from 3.5 V DC with a low current drain of 7 mA while maintaining extremely high brightness levels. The units also feature TTL compatibility, are multiplexible, can operate from $A C$ or $D C$ power and can be filtered to almost any colour. For further information contact Perdix Components Ltd, 98 Crofton Park Road, London SE4.

## Drilling Holes

O
$K$ Machine \& Tool (UK) Ltd have launched a lightweight electric drill for drilling, grinding and polishing which is particularly useful on printed circuit boards. The PCB-258 drill is powered by a high-speed 220.240 V motor and measures 175 mm long $\times 44 \mathrm{~mm}$ diameter. Four different collet sizes are su pplied to handle 0.4-3.2 drills. Optional extras include tungsten carbide cutter sets, grinding points, cutters, sanding discs and various drills. A drill stand is also available with a springmounted arm which provides good stability and can be used with circuit boards up to $\mathbf{2 8 0} \mathbf{~ m m}$. Further information and prices can be obtained from OK Machine \& Tool (UK) Ltd, Dutton Lane, Eastleigh, Hants SO5 4AA.

## New Improved DMMs

Cluke are now manufacturing a new series of digital multimeters. They are intended to replace the existing 8020A series by providing more features at even more competitive prices. The new features include four models instead of the previous three, three of which include a high-speed continuity bleeper as standard, improved calibration specification with two-year calibration guarantee, two-year parts and labour warranty, heavy duty 600 V fuse system to provide greater protection against high energy inputs and improved mechanical design with non-slip feet, tilt bail and easier-to-use layout. This new ' $B$ ' series will be available direct from Fluke at Watford or through their nationwide network of distributors. Further information from Fluke (GB) Ltd, Colonial Way, Watford, Herts WD2 4TT.


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24 TUNE ELECTRONIC DOOR BELL Normal Price $£ 19.70$ £12 Plays 24 different tunes with separale soeed control and volume appropriase lune for your visitor, with appropriate tunes for difterent tomes of

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and
bleepers (fis each) this facitity can be
extended 10 colleagues and members of the family. Using a C90 siandard cassette you can record as many as 45 messages The announcement can be up to 16 seconds long and the incoming message up to 30 seconds lond.
the machine : easy to install and comes with full instructionis it is easily wired to your junction oox with the spade connec tors provided ot alter nalively a gack plug can be provided to plug into a juck socker Most importint. of course. is the tact that
il is folly POST OFFICE APPROVED The price of $£\{35$ linc VAT) includes in The price of $\%$ machine. an extra light remate call-in Bleeper, the microphone message iape a $C$ mains ardaptor The uny is
$g_{3} \times 6^{" \times 2} 2^{\prime \prime}$ and is fully guarameed for 12 months The telephone can be placed direcily un the unit - no addilional desk £135

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## THE EXHIBITION FOR THE ELECTRONICS ENTHUSIAST

 COMPUTERS • AUDIO • RADIO © MUSIC $\bullet$ LOGIC• TEST GEAR $\bullet C B \bullet G A M E S \bullet K I T S$Wednesday 11 th November 10 a.m. -6 p.m. Thursday 12 th November 10 a.m. $-8 \mathrm{p} . \mathrm{m}$. Friday 13th November 10 a.m. 6 p.m. Saturday 14 th November 10 a.m. 6 p.m. Sunday 15 th November 10 a.m. -4 p.m.


COMPONENTS • DEMONSTRATIONS • SPECIAL OFFERS • MAGAZINES•BOOKS

Any one of the 17,000 people who thronged the RHS for the Breadboard exhibition last year will need no introduction to this year's premier show for the electronics enthusiast. They already know all about the demonstrations, bargain sales, bookstalls, games, kits, computers and music machines to be found at BREADBOARD 81. They could name you all the leading companies who were there to see - and to buy from, at fantastic prices.
Even thuse lucky 17,000 would be surprised to hear that this year we've improved BREADBOARD still further! More stands, more demonstrations and wider gangways to make it all easier to enjoy!
3READBOARD 81 is the place to be from November 1 lth to 15 th at the RHS Hall. Why not come and find out for yourself how much you missed last year? We can promise plenty to see and do at BREADBOARD 81. Close to Victoria Station and NCP car parking facilities.

## Cost of entry will be $£ 2.00$ for adults and £1.00 for children under 14 yrs and O.A.P.s. ORGANISED BY ARGUS SPECIALIST PUBLICATIONS LTD., 145 CHARING CROSS ROAD, LONDON WC2H OEE. <br> ROYAL HORTICULTURAL SOCIETY'S NEW HALL, GREYCOAT STREET, WESTMINSTER, LONDON S.W.I.

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To apoid queueing, advance tickets will be available from
                                    Advance Tickets BB '81,
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                                    London WC2H OEE.
            *Special Advance Booking Price**
    Adults £1.75 Children under 14 yrs and O.A.P.s 80p
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## NEWS: NEWS: NEWS: NEWS: NEWS: NEWS: NEWS

Defence Digest

This new regular feature is devoted to defence electronics, its equipment techniques and application. Defence remains one of the largest growth areas in UK industry, with much of the real innovation and investment taking place there.

Defence Digest will thus act as a news (and views) section, containing up-to-date information and explanation of some of the happenings in the different sectors of the defence industry.

Companies with information and articles for these columns are invited to submit them direct to Defence Digest at our editorial address. Indeed, anyone with anything to say on the subject, be it information or opinion, is a potential contributor and should not refrain from putting pen to paper.

## Acceleration

The picture shows Mr C.H. Davies (left). Director of Aircraft Production, Ministry of Defence (Procurement Executive), receiving from Mr Lester Ceorge, Managing Director of Ferranti Instrumentation Ltd, a presentation model of the Ferranti FA2 accelerometer to commemorate the production of the 9,000th instrument. Ferranti inertial grade ac-
celerometers have been in full series production since 1968 and currently exist in five variants. Since that time they have been fitted in the inertial guidance platforms of all British military aircraft including the Nimrod, Jaguar, Buccaneer, Phantom and Lornado. Other applications have included use in airborne and shipborne radar antennas and sonar arrays. Satellite and rocket applications include Exosat, Skylark. Black Knight and Ariane


## Tracking Helmets

Eerranti is to supply its advanced Helmet Pointing System (HPS) as part of the British Aerospace Dynamics Group Tracked Rapier Missile System for the British Ar$m y$. The HPS, which was first seen at last year's Farnborough Air Show, is a revolutionary target sighting system which can direct weapon aiming sensors towards any target at which the wearer is looking. The HPS was originally conceived as a pilot's aid but it has received its first major contract as a land-based system. The Ferranti Helmet Pointing System is a very lightweight, simple system which improves normal weapon aiming. Basically, the entire system consists of the helmet-mounted sight and sensor, a radiator (mounted on a convenient nearby fixed object), a signal processing box, a control unit and an appropriate source of electrical power. The observer's sight is light and compatible with the latest protective masks. In the Tracked Rapier situation, the commander (observer) with his head
out of the cupola, searches for possible targets. The sight on his helmet has an illuminated aiming mark, focused at infinity representing line-of-sight. A tiny sensor, also on the helmet, continuously monitors the angle and position of the Commander's head (and hence his precise sight-line) relative to the radiator fixed to the vehicle, with high accuracy. Once the observer has spotted a target, he overlays it with the aiming mark, as a means of designating his target. At the press of a button, the line-of-

## Direct Hit

The first guided launch of the Hughes Aircraft Company's Advanced Medium-Range Air-to-Air Missile (AMRAAM) was a success with a direct hit on a drone aircraft target. It was tested in Holloman Air Force Base, New Mexico, and fired from a US Air Force F16 aircraft against an F-102 drone target. It closed in on its target using its radar guidance system, thus proving the missile's capability with this particular aircraft. The next generation AMRAAM missile will pack higher performance into an airframe which is only about half the weight of the missile it will replace - the AIM. 7 Sparrow. The missile provides 'launch-and leave' capabilities enabling the pilot to break away immediately after launch to engage other targets. Hughes is one of two contractors selected for a 33 -month prototype validation programme; selection of a winner is expected in October and the winning contractor will then start full-scale development. The AMRAAM program is a joint US Air Force and Navy effort to develop an advanced all-environment missile for operational use between 1985 and 2005.

sight, as measured by the HPS, is transferred to the optical tracker, directing it to the target, which then appears in the tracker operator's sight. The operator then follows normal procedure through weapon release to target impact. In general use the Ferranti HPS can be used where an observer's sightline needs to be transferred to equipment that must be directed to the same 'target' - whether it be in low-flying aircraft, a ground target or in a commercial application.


## Hughes Hues

Here a Hughes Aircraft Company engineer uses a full colour display to test a stand-off airborne system for detecting and tracking massed armour and other forces. The system is called Pave Mover and displays targets and their movements in full colour on a cartographic base, showing roads, railway lines, airfields and rivers. As many as 4.096 colour hues can be displayed. The Pave Mover uses airborne radar to relay target information via data link to a mobile ground-based Data Processing Control Station. Computers in the DPCS process the information and display the target data. Pave Mover's radar can also guide missiles or tactical aircrait to designated targets. Guidance commands and targeting information are supplied by the DPCS. The Pave Mover system is part of a broader Assault Breaker programme for neutralising enemy armour before it reaches the forward edge of the battle area. The system is being developed by Hughes under contract from the US AIr Force and the Defense Advanced Research Projects Agency. It is being evaluated at White Sands Missile Range, New Mexico.


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## Meter Beater

If the bane of your life is the not-so-lovely Rita the Meter Maid then you need our all-singing, all-dancing project. The unit is the same size as a Barclaycard (though you can't get credit with it) and fits neatly onto your keyring. It has an LED display which indicates whether you're setting it for $20,40,60$ or 80 minutes with the touch switch operation, and when you are nearing the time to beat Rita back to the limo, it beeps. We've set it so that for a 20 minute wait, it will go off three minutes before at 40 , it goes off six minutes before, and so on. We reckoned that the longer you were leaving the car the further away you might be, cunning eh?

## Robotics Today

This month we have an in-depth study on the hobbyist approach to robotics written by one of our readers. It covers just about everything from thoughts on mechanical construction, through data processing and programming to experimental ideas for sensors. For a really down-to earth approach to the exciting subject of Robotics read on......

## DC Control of Audio

Taking still further our favourite theme of remote controlling everything in sight, Keith Brindley follows up his article on Veltage-Controlled Audio with this little offering. Using another of Mullard's chips to demonstrate, he delves into the murky depths of voltage-controlled volume and tone for use in preamps and to help put the theory into practice there are loads of good circuits to play with.


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For centuries the Swiss have had a virtual monopoly on the world's timekeeping. A combination of geography, climate and national spirit have by some inexplicable coincidence led to a thriving cottage industry dedicated to high precision mechanics. Up until only 10 years ago it was a virtual certainty that most watches sold throughout the world, particularly those in the middle to upper price bracket, would be Swiss-made.

However, there can be little doubt that the Far Eastern countries, particularly Japan, Hong Kong and Taiwan, now dominate the electronic watch industry, a logical development of their undoubted expertise in miniature electronics. The Swiss, like any country with a international reputation for a particular product, had two choices: they could either resign gracefully, like the British motorcycle industry, or they could re-invest and at least try to regain that which was once theirs. The Swiss have for obvious reasons, chosen the latter path, but along that path have made some very shrewd and hopefully correct predictions about the future of personal timekeeping.

## Time Zones

The most obvious decision was not to take on the Far East with purely digital watches. The LCD watch is now made almost exclusively in Hong Kong. No other country could even attempt to compete in the lower price bracket multi-function watch market. The Swiss have an enviable history of being able to work with micromechanics so the logical step was for them to marry the two technologies - mechanics and electronics - to produce watches with analogue displays, hands and faces but with the timekeeping controlled by integrated circuits and quartz crystals.

Fortunately for the Swiss the cottage industry that has developed around watch manufacture lends itself to a kind of co-operative operation where, for instance, a small area might have several factories producing different parts for different watches. Such an arrangement has existed, in fact, since 1939 when a number of companies got together to form ASUAG, which by the time it is translated into English, stands for the General Corporation Of Swiss Horological Industries Ltd.

'Mr Mouse' assembling electronic watch chassis. The picture on the previous page is of the 'Mr Beaver' robot.

Within ASUAC are familiar names like Longines and Eterna, plus a dozen or so other companies which most of us will never have heard of but are nonetheless well known within the watch industry. ASUAC was primarily designed to rationalise the production of watch movements, thus making it easier for the industry as a whole to respond to changes in demand and fashion. As the age of the electronic watch dawned it became necessary for ASUAC to respond by producing electronic watches, so within the group certain companies changed their production from purely mechanical parts to wholly electronic parts. Within these companies diversification into the development of automated watch assembly has led to some exciting developments in robotics.

## Swiss Success

The micromechanic expertise of one company, SSIH, has been channelled into the development of high precision robots. The term 'high precision' refers to the robots' ability to place miniature parts into assemblies with an accuracy that is measured in microns. So far SSIH have 20 working development machines with a further 200 planned for next year. The two types of robots (known as Mr Beaver and Mr Mouse!) use a combination of electronic and pneumatic power to articulate the arms. The mechanics are controlled by built-in microprocessor systems running on Swiss-developed software.

Although these robots were designed primarily for watch assembly, SSIH see a promising future of Mouse and Beaver in any situation that calls for high-precision, repetitive work. Such work might include the assembly of cameras, another area that would benefit from SSIH development

At the moment SSIH have the field virtually to themselves. Robotics has, so far, concentrated more upon heavy industrial usage, car assembly and the like, where ultra-high-precision is not required. Doubtless the ability of the Swiss to produce mechanical systems to such high precision will keep other robot manufacturers out of the market for some time. It is tempting to speculate that SSIH have an eye on the Japanese and Hong Kong markets where much of the assembly for digital watches and similar items is still highly labour intensive. It would indeed be ironic to see the Swiss succeed in this area; Swiss robots assembling Far Eastern watches.

## Time In Hand

If you're sweating over a hot keyboard, trying to finish your program for our Armdroid competition before the October 31st deadline, then panic not. There has been a hold-up in the supply of the driver boards to customers, so on the fairly reasonable basis that you can't test the software without the hardware, the closing date for entries has been extended to December 31st. This will allow those of you who want to see what's involved to visit our stand at the Breadboard Exhibition (see page 12) and watch our demonstration. We hope to have two Armdroids running, one under program control and the other with a manual control box so you can play with the system and get the feel of things. There may even be a competition to find the most dextrous manipulator amongst our readers.

## Meet The Mouse

On the other hand, if you can't make it to Breadboard (in which case we'll never speak to you again!), cast your eyes across the page and you'll see details of the ACC's conference on robotics. One of the attractions will be this year's winner of the Micro-mouse competition (together with his designers, of course). We'll be providing some of the speakers at this meeting together with demonstrations of projects old and new ${ }_{\perp}$ See you there!

This new series will provide a stage upon which our readers may display their robotics achievements. It is intended to cover the practical application of robots in Britain today, be it at hobbyist level or in industry.

Readers in either category are invited to write to the editor of ETI, detailing their experiments, projects, application or usage of robotics. Any articles published will be paid for at commercial rates. It is also hoped to run an 'Ideas Forum' wherein readers can exchange views and ideas but that depends upon the response of our readers - you!

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- Up to 26 FOR/NEXT loops.
- Randomise function - useful for games as well as serious applications.
- Cassette LOAD and SAVE with named programs.
- 1K-byte RAM expandable to 16 K bytes with Sinclair RAM pack. - Able to drive the new Sinclair printer.
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## Built: $56 \%$, $\underline{5}$

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You'll be surprised how easy the ZX81 kit is to build: just four chips to assemble (plus, of course the other discrete components) - a few hours' work with a fine-tipped soldering iron. And you may already have a suitable mains adaptor -600 mA at 9 VDC nominal unregulated (supplied with built version).

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# MICROCOMPUTER EXPANSION SYSTEM 

> Treat your home computer to extra memory and more peripherals with this versatile and simple expansion project. With the modules to be published you'll be able to custom-design your system to meet your requirements - and change it all around at a later date should you so desire. Design by Steve Wilding.

Home computers, like hi-fi, generally have a habit of starting out modestly (assuming your finances are anything like ours), and then growing steadily as you discover a burning need for more memory, more I/O, a printer, and finally the sound generator that turns the system into an all-singing, all-dancing electronic marvel. (Actually, computers almost never dance). To help out those of you in the upgrade market, we present this low-cost, flexible expansion system designed for a number of popular microcomputers on sale in the UK - ones based on the 6502 and Z80 microprocessors.

The system is made up of a motherboard and a range of expansion cards that plug into the motherboard as and when your needs dictate. For example, if your need is purely for RAM memory expansion then a motherboard and four RAM cards will give you an extra 32 K of RAM. The constructional and application details are given for the motherboard and the 8K RAM card in this article - in subsequent articles details of the remaining expansion cards in the range will be provided. These will include an EPROM program-mer and an EPROM card for use with 2516 and 2532 single rail EPROMs; a sound board utilising up to three of the popular AY-3-8910 programmable sound generator chips; a parallel I/O card providing two 6520 s for uses such as a parallel printer driver (Centronics) and a low cost disc interface.

Two 40 -pin input sockets are provided (SK6 and SK7) - these are wired in parallel so that two or more motherboards may be linked together. This allows you to use a larger number of those modular which use less than 8 K of memory.

## Vive La Difference

Obviously there are differences between computers and the expansion system must be capable of adapting to meet varying demands. The important difference for this application is the first free memory location available for expansion in your computer's memory For RAM expansion to be effective it must run consecutively with the existing RAM. Table 1 gives the first free location for some popular computers.

For the more technically minded an explanation of how compatibility is achieved is given in the How It Works section. Suffice to say here that the system is capable of being moved around in memory to fit the particular computer's requirements. This is achieved by means of selective soldering of wire links as described later in the article.

## Construction

Construction is best achieved using a 15 W soldering iron with a fine bit and 22 swg solder. First solder in the four wire links to select the correct location in the memory map for your computer (see Table 2). Solder the DIL sockets into the board taking care not to make any shorts between pins. Next fit the five edge connectors and lastly the ceramic capacitors - these can be held in place whilst soldering by slightly spreading their legs under the board. The PCB has plated-through holes and so it isn't necessary to solder a connection on both sides of the board; but allow enough time when soldering for solder to run through the hole as this ensures a good connection. Construction of the motherboard is now complete.

Follow the same instructions when constructing the RAM card - solder the IC sockets in first, then the eight ceramic capacitors and construction of the RAM card will be complete.

Finally insert all ICs according to the overlay, taking care not to bend any IC legs.

## SPECIFICATION

Motherboard: This is the main board of the system. It allows up to five expansion cards to be used at once socket 5 being a duplicate of socket 4 , allowing smaller expansion cards to be used without tying up a whole 8K block each. Both the control bus and address bus are buffered by the motherboard but the data isn't, as this is already done by many micros. Power requirements are 5 V at 100 mA .
RAM Card: A static RAM expansion card using 16 2114L 300 ns RAM chips. Power requirements are 5 V at 650 mA . EPROM Card: Available for either 2516 ( $2 \mathrm{~K} \times 8$ ) 5 V single rail EPROMs or 2532 $(4 \mathrm{~K} \times 8) 5 \mathrm{~V}$ single rail EPROMs.
EPROM Programmer: For programming 2516 or 2532 single rail EPROMs for use with the above card. Sound Card: Allocation for up to three AY-3-8910 three-channel sound chips, allowing the generation of complex waveforms.
PIO Card: This board contains two PIO chips for 32 individual inputs or outputs. 12 of these are used for a Centronics-compatible parallel printer driver, for use with Superboard/UK101 and Watford's WEMON chip. Three connections are for use with a light pen and a further six are for power output applications.


TABLE 1

| TABLE 1 |  |  |
| :---: | :---: | :--- |
| GROUP | FIRST FREE MEMORY <br> LOCATION | COMPUTERS |
|  | 2000 | UK101 |
| 1 |  | Ohio Superboard |
|  |  | 8K PET |
|  | 4000 | Microtan 65 |
| 2 | 6000 | 16K PET |
| 3 | 8000 | 16K TRS-80 |
|  |  | 16K Video Genie |
| 4 |  |  |

The photograph above shows a close-up of the wire links around IC4. This board was in use with the 8 K PET, a group 1 computer. (Check the links against Table 2 and Fig. 1).


ETI DECEMBER 1981
NEXT MONTH: Connection details for more machines, details of the modifications for use with Z 80 micros, and the second of the plug-in modules.

| TABLE 2 |  |  |  |  |
| :--- | :--- | :---: | :---: | :---: |
|  | GROUP1 | GROUP 2 | GROUP 3 | GROUP 4 |
| Link W to: | A | B | C | D |
| Link X to: | B | C | D | E |
| Link Y to: | C | D | E | F |
| Link Z to: | D | E | F | G |

Fig. 1 Table 2 shows which of the lettered holes around IC4 to link in order to locate the expansion system at the correct point in your computer's memory. The 'thin' holes are not through-plated; the

" rest are.

Fis. 2 Circuit diagram of the motherboard. To avoid large numbers
of confusing lines, the buses are shown as thick black lines instead
of eight or 16 thinner ones. SK 7 is not shown - it is simply connected
in parallel to SK6 to allow further expansion.


## SжуОМ ІІ Мон



 eight decoder (IC4) is used to decode the
top three address lines (A13-A15) into 8K
 one for each expansion card connector
socket. The fifth socket is a duplicate of socket four so as to enable the use of two ex-



The 8 K blocks are located at $0000-1 \mathrm{FFF}$, 2000-3FFF..E000-FFFF (Hex), so if the
system is to be conficured to start at 2000 system is to be conficured to start at 2000
(group 1), then blocks 2 to 5 must be used. If



 more 7415138 s (ICs $7-10$ ). The bus signais
and these 1 K decodes are then brought to
the expansion socket connectors. The DD
 X







Fig. 3 Circuit diagram of the 8 K RAM card.


$\square$
-

Fig. 4 (Above) Overlay for the motherboard.
Fig. 5 (Below) Overlay for the 8K RAM card,


## PROJECT : Computer Expansion

EXPANSION SOCKET PINOUTS

| + VE | 1 | 2 | + VE |
| :---: | :---: | :---: | :---: |
| +VE | 3 | 4 | +VE |
| +VE | 5 | 6 | +VE |
| BD0 | 7 | 8 | +VE |
| BD1 | 9 | 10 | +VE |
| BD2 | 11 | 12 | +VE |
| BD3 | 13 | 14 | +VE |
| BD4 | 15 | 16 | +VE |
| BD5 | 17 | 18 |  |
| BD6 | 19 | 20 | BA3 |
| BD7 | 21 | 22 | BA4 |
| BAO | 23 | 24 | BA5 |
| BAI | 25 | 26 | BA6 |
| BA2 | 27 | 28 | BA7 |
|  | 29 | 30 | BA8 |
| RM | 31 | 32 | BA9 |
| CSO | 33 | 34 |  |
| CS1 | 35 | 36 |  |
| CS2 | 37 | 38 |  |
| CS3 | 39 | 40 |  |
| CS4 | 41 | 42 |  |
| CS5 | 43 | 44 |  |
| CS6 | 45 | 46 | Earth |
| CS7 | 47 | 48 | Earth |
| $\emptyset 2$ | 49 | 50 | Earth |
| NM | 51 | 52 | Earth |
| IRQ | 53 | 54 | Earth |
| Earth | 55 | 56 | Earti |
| Earth | 57 | 58 | Earth |
| Earth | 59 | 60 | Earth |
| BD0-7 | Buffered data lines |  |  |
| BAO-9 | Buffered address lines |  |  |
| NMI | Non-maskable interrupt |  |  |
| CSO-7 | Chip select lines |  |  |
| RW | Buffered Read/Write |  |  |
| Ø2 | Buffered clock |  |  |
| $\overline{\mathrm{IRQ}}$ | Interrupt request |  |  |

Each chip select line enables a 1 K block in the memory. CS0 is the lowest and CS7 the highest within the 8 K area assigned to that particular socket.

Table 3 (opposite) gives the connection details for SK6 (the motherboard input socket) for people who wish to use this project with other systems.

Fig. 6 Block diagram of the complete system.


Top: You probably can't see it in the photo, but the screen of this 8 K PET reads ' 15359 BYTES FREE' - courtesy of the expansion system. This is plugged into the PET using sockets J4 and J9, on the right of the PET at the back (above).


TABLE 3

| PIN | FUNCTION | PIN | FUNCTION |
| :---: | :---: | :---: | :---: |
| 1 | IRQ | 40 |  |
| 2 | NMI | 39 |  |
| 3 | DD | 38 |  |
| 4 | D0 | 37 |  |
| 5 | D1 | 36 | D4 |
| 6 | D2 | 35 | D5 |
| 7 | D3 | 34 | D6 |
| 8 |  | 33 | D7 |
| 9 |  | 32 | RMW |
| 10 |  | 31 | $\emptyset 2$ |
| 11 |  | 30 |  |
| 12 | A2 | 29 |  |
| 13 | A1 | 28 |  |
| 14 | A0 | 27 | A15 |
| 15 | A3 | 26 | A14 |
| 16 | A4 | 25 | A13 |
| 17 | A5 | 24 | A12 |
| 18 | A6 | 23 | A11 |
| 19 | A7 | 22 | A10 |
| 20 | A8 | 21 | A9 |

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# CROSSOVER NETWORKS 

> Put down that inductor you're winding on an old cotton reel and read this article on crossovers and loudspeaker design from KEF Electronics. It'll tell you why you've been wasting your time.

The basic requirements for a high-quality loudspeaker include on the one hand a smooth and uncoloured response maintained over an angle of radiation wide enough to cover the listening area, and on the other, freedom from audible non-linear distortion, together with a combination of efficiency and power handling capacity adequate for the conditions of use. For each drive unit in a multi-way system, there is only one frequency band over which all these requirements are simultaneously satisfied; outside this band there will be regions in which some of them cannot be met. A low-frequency drive unit, for example, if allowed to operate in the high-frequency range, would introduce colouration through diaphragm resonance. Again a high-frequency unit, if allowed to operate at low frequencies at which the necessary diaphragm excursion exceeds the linear limit, would introduce distortion products. To avoid degradation of the overall sound quality by such unwanted contributions, it is therefore essential that the output from each drive unit outside its working frequency range should be reduced to a sufficiently low level by adequate attenuation in the crossover filter.

## Filtering Through

Filters in practice cannot have an infinitely sharp cut-off, so that there is an overlap region around the nominal crossover frequency in which the total sound output is made up of contributions from two different drive units. Ideally, the combined characteristic of each unit working in conjunction with its associated filter network should be such that the sum of the two contributions gives a flat response over the entire transition region; in addition, if the frequency characteristic of a unit within its working band is not quite flat, the network should be designed to rectify this. Each filter has therefore to be tailored to suit the response of its associated drive unit both in the working band and in the nominal cut-off region; moreover, it must be designed to operate into the input impedance of the unit, which will in general be complex and will contain additional components associated with the fundamental resonance of the diaphragm. Finally, the impedance presented by the filters to the power amplifier must be kept within prescribed limits which apply not only to the magnitude or modulus, but also to the relationship between the resistive and reactive components.

To measure the phase shift in a loudspeaker has been until recent times a very difficult operation, largely because of the additional - and much greater - phase shift associated with the time taken for the sound to reach the measuring microphone; this phase shift depends on the distance of the microphone from the acoustic centre of the drive unit, ie that point within the unit at which the sound appears to originate. The exact location of the acoustic centre is initially unknown but can be readily determined by the pulse test method developed by

KEF; a short electrical impulse is applied to the unit, and the complete frequency response, in both amplitude and phase, is derived by computer analysis of the resulting transient sound output. This technique allows the phase shift introduced by the drive unit to be separated from the multiple phase rotations associated with the distance of the microphone from the acoustic centre, so that the position of the latter can be accurately calculated.

## On Target

In designing crossover filters to suit individual drive units, the method adopted by KEF is to consider the overall electroacoustic response of the network and unit together, and to make this conform as closely as possible to some known filter function that gives adequate attenuation in the cut-off region together with a smooth transition at crossover; the response/frequency relation to be aimed at is referred to as the Target Function and is represented by the symbol $T(f)$. The response/frequency function of the drive unit alone, already measured under working conditions, is represented by $S(f)$. The next step in design is to compute the frequency characteristic $H(f)$ of a filter that will convert the existing response $S(f)$ into the wanted reponse $T(f)$; the functions $T(f), S(f)$ and $H(f)$ are in linear units, not dB , so that the conversion is a multiplication process, ie

$$
T(f)=H(f) \cdot S(f) \text { and } H(f)=\frac{T(f)}{S(f)}
$$

In specifying the function $T(f)$ we can use any of the known forms of filter response, ignoring however the circuit configurations conventionally associated with these. The form commonly adopted is that of the classical Butterworth filter. Figure 1


Fig. 1 Butterworth high-pass filter characteristics. (a) 1st order, maximum slope cut-off 6dBloctave; (b) 2nd order, 12 dB/octave; (c) 3rd order, 18 dB/octave.
shows three high-pass filters of this type; the corresponding lowpass characteristics are the same but reversed left to right. All these curves are of the type described in filter theory as 'maximally flat'; this means that the attenuation within the pass band is kept as small as possible down to the nominal cut-off frequency $f_{3}$ - at which the loss is 3 dB - without introducing peaks or ripples in the characteristic. The curves in Fig. 1 represent Butterworth characteristics of the first, second and third order; the higher the order, the greater the cut-off slope - which in the three cases illustrated rises to a maximum of 6 dB and 18 dB per octave respectively - but also the greater number of circuit components required.

## Cross Over Choice?

Although a first-order crossover network exhibits such desirable characteristics as unity amplitude and zero phase shift at all frequencies, the relatively slow cut-off rate of 6 dB/octave gives rise to a number of practical difficulties and such designs are not used. Crossover networks of the second order were at one time favoured but now have little application in high-quality systems. The overall frequency reponse obtained is not flat in the crossover region, but exhibits either a crevasse or a hump, depending on whether the drive units are connected in the same or opposite polarity; moreover, the cutoff slope of $12 \mathrm{~dB} /$ octave is still insufficient for many purposes.

Third-order crossovers, on the other hand, satisfy many of the requirements and are widely used. Figure 2 shows a commercial high-frequency drive unit fed through a conventional


Fig. 2 High-frequency drive unit with convèntional 3rd order Butter* worth high-pass filter.
third-order Butterworth high-pass filter having a nominal cut-off frequency of 3 kHz , and Fig. 3a the measured amplitude and phase response of the filter unit together; Fig. 3b represents the theoretical response of the filter alone when loaded with a resistor numerically equal in value to the nominal impedance of the unit. Comparing curves (a) and (b) it will be seen that the response of the filter/unit combination deviates substantially from that which the filter was intended to produce. At high frequencies the characteristic is modified by the voice coil inductance, which resonates at 5 kHz with the second capacitor of the filter. From 3 kHz downwards, the cut-off slope, which for a


- Fig. 3 (a) Measured amplitude and phase response of the Fig. 2 circuit; (b) Theoretical amplitude and phase response of the filter alone, terminated with the correct resistive load.
third-order filter should be $18 \mathrm{~dB} /$ octave, starts off at $12 \mathrm{~dB} / \mathrm{oc}-$ tave and below 1.2 kHz - the fundamental resonance frequency of the diaphragm - increases suddenly to nearly $30 \mathrm{~dB} / \mathrm{oc}$ tave. This large change in slope is reflected in the phase shift in the cut-off region, which far exceeds the proper value; the disparity extends up as far as the crossover frequency and would have a significant effect on the overall loudspeaker response in the transition region.


Fig. 4 (a) Computed Acoustic Butterworth filter designed to compensate for the non-flat response and complex input impedance of the high-frequency drive unit; (b) Practical realisation of Acoustic Butterworth filter.

Figure 4a shows the same high-frequency unit with a new network computed by taking the theoretical filter response of Fig. 3 b as the target function; Fig. 4b illustrates a different but equivalent circuit configuration adopted for greater convenience in manufacture. The new network compensates for the electro-acoustic characteristics of the drive unit, including the effects of the voice coil inductance and the fundamental resonance. The voltage at the terminals of the unit varies with frequency in such a way as to produce the acoustic response shown in Fig. 5a; over most of the range from 500 Hz to 20 kHz this response conforms closely to the theoretical Butterworth characteristics, reproduced in Fig. 5b, the residual deviations being within $\pm 1 \mathrm{~dB}$ in amplitude and within a few degrees in phase.


Fig. 5 (a) Measured amplitude and phase response of the highfrequency drive unit with the filter shown in Fig. 4; (b) Theoretical 3rd order Butterworth filter characteristic (as in Fig. 3b).

## Avoiding Interference

For maximum horizontal distribution of sound without interference, the drive units in a multi-way loudspeaker should be mounted one above the other. Because of the unavoidable separation between the units, some interference effects mustthen occur when the listener is located above or below the design axis and thus no longer equidistant from the different sound sources; the amount of this interference sets a limit to the angle above and below the axis within which the response can be maintained substantially constant.

This situation is further complicated by the phase shift necessarily associated with the high- and low-pass characteristics of the individual filter/unit combinations. The high-frequency drive unit, which at crossover normally has a phase lead over the low-frequency unit, is commonly mounted above the latter; what happens then is illustrated by the polar diagram in Fig. 6, which shows how the loudspeaker response at crossover varies with angle in the vertical plane. It will be seen that the main lobe of the polar $90^{\circ}$ characteristic, instead of coinciding with the axis of zero inter-unit time delay, is tilted downwards and has a maximum amplitude 3 dB above the on-axis response; a great deal of sound energy is thus directed away from the listening area and towards the floor, producing unwanted frequency-dependent reflections which modify the relationship between the direct and reflected sound in the room. Worse still, there is a region, just above the axis, where the outputs from the two units are beginning to get out of phase and at one angle almost cancel each other; as a result, a small vertical displacement produces a large change in the response of the system around crossover, and hence in the spectrum of the reproduced. sound.

diagram of two-way speaker system. X-Y indicates axis of zero inter-unit time delay.


One way of dealing with this situation is to mount the lowfrequency drive unit (or mid-range unit in case of a three-way system) above the high-frequency unit, this turning the polar diagram upside down; the main lobe is then directed away from the floor and the cancellation region placed where it can do little harm. This arrangement is adopted in the KEF Calinda and Cantata loudspeakers. A more radical solution, applied in the KEF Model 105 loudspeaker, is to choose for the target functions a form of filter characteristic that keeps the acoustic outputs from the high- and low-frequency drive units in phase over the whole frequency range, so that the main lobe of the polar curve remains symmetrical about the axis of zero inter-unit time delay. The crossover networks used to achieve this end are of a special type of fourth-order filter which is equivalent to two second-order Butterworth filters in cascade and thus gives a cutoff slope of $24 \mathrm{~dB} /$ octave.

## Time Travel

Before leaving the subject of interference, it may be noted that the acoustic centre of a high-frequency drive unit usually lies approximately in the plane of the panel on which the unit is mounted, while that of a low-frequency or mid-range unit is located further back, a short distance in front of the voice coil. The resulting difference in time delay can be allowed for in the physical positioning of the units in the loudspeaker assembly. It is however possible in some cases to achieve the equivalent result electrically by modifying the amplitude response, and hence the phase shift, in the crossover filters in such a way as to introduce a compensating time delay, while still satisfying the basic requirements of flat overall response and adequate cutoff slope. The target functions adopted then differ from the classical forms illustrated above - for example, the high-and low-pass characteristics at crossover may not be of the same order; given the necessary computational facilities, a number of useful variants of this kind can be evolved to meet particular design requirements.

## FEATURE : Crossover Networks



The KEF Calinda (Jeft) and Cantata (far left) loudspeakers avoid interference effects by putting the mid-range drive unit above the high frequency unit. The KEF 105 II (lead picture) employs a more radical solution.

## Network Synthesis

The design of the KEF Model 105.2 loudspeaker provides a good example of modern methods of network synthesis. The mid-range filter only is considered here: a similar procedure is adopted for the high-and low-frequency networks.

The first step is to examine the frequency response curves of a large number of mid-range drive units, measured under standard production test conditions, and to select one specimen, the characteristic of which coincides with the mean of the production spread. This unit is then mounted in the enclosure designed for the complete loudspeaker system, and its response under these conditions measured without a filter, ie with constant voltage applied to the input terminals.

Since the filter has to be designed to operate into the complex impedance presented by the input of the drive unit, this impedance must now be measured. For the purpose of network synthesis however it is convenient to represent the result by an equivalent electrical circuit with specified component values rather than by a series of resistance and reactance figures at a number of frequencies; this approach makes it easier to calculate the effect of certain parameters of the unit.

The next step is to decide what circuit configuration will produce the best fit to the desired network response curve while using the minimum number of components - taking into account the complex load imposed by the drive unit and the need to present an acceptable impedance to the power amplifier. The order of network required can usually be deduced by comparing the slope of the frequency characteristic for the drive unit alone with that of the target function representing the desired overall response curve. In principle, a number of alternative circuit configurations could be considered at this stage, but in the light of the designer's experience the choice will usually be narrowed down to one or two.

Details of each network to be investigated, the response characteristic required and the equivalent circuit for the drive unit input impedance are now fed into a computer; this is pro-
grammed to carry out an optimisation routine, which determines the network component values giving the best fit to the desired response curve and also the degree of accuracy achieved. The optimisation process is initiated by assigning approximate values to the various circuit elements; the computer then calculates the effect of making small changes in each element, and retains any of the new values that bring the response nearer to the ideal. This operation is repeated - possibly a thousand or more times - until the residual error in the curve fitting cannot be reduced any further. With the component values thus arrived at, the input impedance of the network is then checked to ensure that it remains within acceptable limits throughout the working frequency range.

The above procedure is repeated, if necessary, for alternative types of network so that a final choice of the optimum circuit configuration can be made.

## Choose Your Components

At this stage the designer has to consider ways of utilising readily available circuit components, avoiding the need for non-standard values and close tolerance limits, both of which add considerably to the cost. The computer program is accordingly re-run with the calculated values of capacitors and resistors replaced by the nearest preferred values. Provided that a sufficiently accurate fit to the target response was achieved in the original calculation, the effect of these changes can be offset without appreciable detriment to the performance of the filter by altering the inductance values in the circuit - a simple matter since the coils are in any case wound to suit the individual design.

The process is now extended to allow for the production spread in component values. By arranging that the deviations of different circuit elements from their nominal values have opposing effects on the overall performance of the filter, it is possible to utilise stock components, having normal commercial tolerances, with very little wastage. The known manufacturing variation in component values, expressed in statistical form is fed into the computer, which calculates the maximum percentage of stock items that can be utilised in this way while keeping the filter characteristics within tolerance. Finally, permissible combinations of component values are worked out and incorporated in the instructions for assembling the networks on the production line.

## Experts Rule OK?

It will now be clear why the standard of reproduction potentially attainable with a modern high-quality system cannot be realised by the home constructor with an assemblage of ready-made networks and drive units selected simply on the basis of their nominal frequency range, impedance and sensitivity ratings. Attempts have been made to ameliorate this situation by publishing descriptions of complete loudspeakers incorporating commerically available drive units, and giving circuit details of the filters to be used. The success of such designs however depends on the extent to which the author has taken into account all the factors referred to and has been able to measure the electro-acoustic characteristics of each type of unit specified allowing for manufacturing tolerances before attempting to determine the appropriate network parameters.

On the other hand, those manufacturers who have good facilities for acoustic measurement are usually well aware of the various pitfalls in filter design, and by means of computerised data-handling methods are able to produce the components of a multi-way loudspeaker in matched sets. These techniques ensure that the end product - whether in the form of a kit for assembly in an enclosure of prescribed construction such as the V3 speakers in the October ETI, or a complete system represents the best combination of performance and costeffectiveness that modern technology can provide.



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I$n$ the cut-throat world of consumer electronics, one of the questions designers apparently ponder over is 'Will anyone notice if we save money by chopping this out?' In the field of domestic TV sets, one of the first casualties always seems to be the sound quality; small speakers are used for cheapness and to reduce the size of the set, tone controls are rare, and as for separate bass and treble drivers overdrafts at the ready if you want them.

This is a pity really, as the TV companies do their best to transmit the highest quality sound possible, using a FM modulated sound carrier. Perhaps the TV manufacturers think that viewers are going to be so engrossd by what's on the screen that they won't notice the inferior sound reproduction. Obviously they haven't watched television lately.

## Get In Tune

Naturally the more discerning members of the public (ETI readers,for example) will be dissatisfied with this state of affairs, and the easiest remedy is to build this TV Sound Tuner. The unit is designed round a ready-built and aligned UHF tuner module which is soldered directly onto the PCB containing the IF detector circuitry. The IF board filters out the sound carrier from the video, which is ignored funless you want to try building a colour television, with Teletext, Prestel, remote control. oh alright, forget it), and the demodulated audio signal can be fed directly into your hi-fi, free from the abuse it normally receives at the hands of your telly

Since TV sound is broadcast in mono (at present, anyway), an optional 'stereo simulator' based on a Mullard preamp module has been included in the design. With an eye on the future, four tuning pots are provided so that the fourth TV channel can be catered for when it arrives. The required tuning voltage is fed to the varicaps by a fourway selector switch.

The signal from the aerial has to be split so as to feed both the TV and the Sound Tuner, and first we toyed with the idea of fitting the tuner with 'aerial in' and 'aerial out' sockets, like a video recorder. But splitting a UHF signal involves, among other bits and pieces, the use of a balun coil and winding one of these just isn't worth the effort. Do what we did and buy an aerial splitter from your local accessory shop

- it's cheaper, neater, easier and quicker. Ours cost £1.53.


## A Classic Case

The kit supplied by RTVC (see Buylines) includes the PCB (which you will have to drill yourself), all the components for this board including the pre-aligned UHF tuner; a rotary switch for channel selection and the mains transformer. If required, the stereo simulator preamplifier module (which is supplied ready-built) can be obtained for an additional charge. Items which are not supplied include aerial and audio sockets, wire, the potentiometer for the stereo blend, mains switch, power-on neon and case. The case we used was from West Hyde Developments' Classic II range everything fits in quite neatly.


Assembly of the main board is straightforward. Take the usual precautions with the orientation of the ICs, diodes and electolytic capacitors. To make life easier the PCB is overprinted with the component positions.
The tuner module and the coils will only fit the board one way round, with. the exception of $L 6$. This coil should be soldered so that the printing on the can faces away from the tuner module.

L2 is to be wound by the constructor using the 18 swg wire supplied in the kit; it consists of $33 / 4$ turns around a 10 mm former (which is removed before soldering the coil to the PCB).

The values of C9 and C16 are not critical and the components supplied with the kit will depend on availability.

We mounted our PCB on stand-off spacers so that the varicap multiturn coritrol pots are positioned level with holes drilled in the side of the case these pots should only need to be set up once, using a small screwdriver. The position of the transformer, preamp board and the other hardware can be seen from the photographs, and the wiring diagram shows how to connect up all the various bits.

## Alignment

As only the sound is to be extracted, the only equipment required is a non-metallic tuning tool. While monitoring the audio output tune L6 to receive maximum noise (or station if you were lucky enough to receive it the first time). Tune the selected multiturn pot to receive a station and adjust L5,L4, L3 and L1 for maximum output. Note that tuning of $\mathrm{L} 1, \mathrm{~L} 3$ and L 4 will appear 'flat', particularly in high signal strength areas. (R1 may be reduced should overloading occur).


This is the aerial splitter that we bought - much easier than making one. The aerial plugs into the socket on the right, while those on the left connect to the TV set and the tuner.


Fig. 1 Block diagram of the TV tuner.


Fig. 2 Circuit diagram of the ETI TV Sound Tuner. The optional 'stereo simulator' board is supplied ready-built.

## HOW IT WORKS

The ECL 1043 is a ready built and aligned varicaptuned UHF tuner. Its 38 MHz IF output contains AM modulated video and FM modulated sound carrier frequencies. Pin 1 is the AGC input which is not used; R1 and R2 form a potential divider to preset the gain. The 38 MHzoufput is connected to IC1 via input-trapping circuitry. IC1 is an IF amplifier/video detector chip tuned by a single coil 1.5 . As the sound carrier is spaced 6 MHz from the vision carrier frequency, the output on pin 12 contains the demodulated video (positive) and the 6 MHz FM modulated sound carrier frequency. The video is removed by using a 6 MHz ceramic filter. The filter also sets the operational frequency of IC2, which is an FM IF amplifier/ quadrature detector chip. Detection alignment is obtained by adjustment of a single coil L6, which provides the quadrature signal to the coincidence gate detector. Audio output is recovered at pin 14.

In preference to a multi-secondary transformer a readily available $7 V 5$ single secondary transformer is used, at the expense of a few extra components. The supply voltage to the ICs and tuner is derived from a full wave voltage doubler. The tuning voltage is further quadrupt ed, filtered and regulated by IC3.

The stereo simulator is simply a stereo preamplifier with a bass control in one channel and a treble control in the other. A dual pot is used to control the cut and lift of these tone corr trols, so as the pot is rotated the high and low audio frequencies are directed to opposite speakers and a 'stereo' effect is obtained.

## BUYLINES

A kit of parts containing those items listed in the text will be available from RTVC Ltd, 21E High Street, Acton, London W3 6NG (mail order only)The TV tuner costs $£ 11.45$ plus $£ 1.50$ p\&p; the transformer is $£ 1.50$ plus $£ 1.50$ p\&p (p\&p free on transformer if ordered with kit); and the LP1183 preamp costs $£ 1.95$ plus 75p p\&p. The case is available from West Hyde Developments order as CL2 AEL.


Fig. 3 Wiring diagram for the tuner project. Make sure you use shielded cable for the audio connections and UHF coax for the aerial connection, as shown, and don't forget to make the earth connection to the case of the tuner module.



Fig. 4 Component overlay of the tuner board. IC2 comes in a quadir-line package, so you'll have to solder it directly rather than use a socket. Do this quickly and carefully! Also make sure that L6 goes in the right way round - with the printing away from the tuner module. CF1 can be fitted either way round.


PARTS LIST

| Resistors (all $1 / 2 \mathrm{~W}, 5 \%$ ) |  |
| :---: | :---: |
| R1,9 | 22k |
| R2 | 5k6 |
| R3 | 150R |
| R4 | 100R |
| R5 | 47k |
| R6,7 | 680R |
| R8 | 470R |
| R10,11 | 2k2 |
| Potentiometers |  |
| PR14 | 100k varicap multiturn controk |
| RV1 | 220k dual linear pot |
| Capacitors |  |
| $\text { C } 2,4,6,7$ | 120p polystyrene |
| 10,11,14,15 | 10n polystyrene |
| C3,23 | 47u 16 V PCB electrolytic |
| C5,8,18 | 10u 25 V PCB electrotytic |
|  | 33-39p polystyrene (see text) |
| C12 | 220us 16 V PCB electrolytic |
| C13 | 4 n 7 polystyrene |
| C16 | 18-22p ceramic (see text) |
| C19 | 56 n mylar |
| C20,21 | 2200u 16 V PCB electrolytic |
| C22,24,25 | 470 25 V axial electrolytic |
| C26 | 40040 V PCB electrolytic |
| C27 | 470n polyester |
| Semiconductors |  |
| IC1 | TDA440 |
| IC2 | TAA661B |
| IC3 | TAA550B |
| D16 | 1N4001 |
| Coils |  |
| 11 | D1 (Toko) |
| 12 | see text |
| L3 | D3 (Toko) |
|  | D819 (Toko) |
| 15 | D10 (Toko) |
| L6 | 34721 (Toko) |
| Miscellaneous |  |
| SW1 | 1-pole 4-way rotary switch |
| SW2 | miniature DPDT mains switch 6 MHz ceramic filter |
|  |  |
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# Don't they ever sleep at Casio? No sooner do we review the FX 602P than its big brother the FX 702P drop through our letterbox. Peter Freebrey has been probing at the push-buttons to make this report. 

First the Casio FX 502P, more recently the FX 602P and now we have the FX 702 P . If my memory (unfortunately somewhat intermittently volatile!) serves me correctly the FX 502 P was hailed as 'better than sliced bread'; sounds a bit crusty now but it helped persuade me to buy one of those little beauties back in 1980! The FX 602P was also rated highly and 'recommended to anyone seeking a powerful portable machine, which has comprehensive control over memory and data space'.

The FX 702P (RRP £134.95) follows in this fine Casio tradition, offering a lot more than its predecessors in that it has moved away from being an uprated programmable calculator (such as the FX 602P) to what is Casio's answer to the Sharp PC1211 - a fully fledged pocket computer. The FX 702 P offers full alphanumeric capabilities and according to the instruction manual 'uses BASIC program lanugage'. Although this may appear to be true at first sight, Casio's BASIC does vary more heavily from the norm on this score. It is generally perfectly understandable to anyone with a knowledge of another dialect of BASIC and would be mastered just a quickly as any other form of BASIC by the newcomer. A number of instruction words are used in a shortened form, for example: PRINT becomes PRT, INPUT is INP, GOSUB is GSB, RETURN becomes RET, and so on. This minor variation was presumably brought about by a desire on Casio's part to save on display space (not to mention keeping the small keyboard clutter down to a minimum), and to follow the successful pattern of their previous programmable calculators. Reasonably logical on such a machine (machine?!) but is it still BASIC? I suppose one must concede that it is but do we look to a future generation of pocket computers sporting such commands as PT, GB, RT, IN? There are also one or two small anomalies; on the FX 702PRND (is an instruction to round
off a number whereas in some other BASICs RND (is a call for a random number. Also, GET on the FX 702P is a tape handling command only ... not an instruction to'get' a character from the keyboard, a function which is performed on the FX 702 P by Casio's KEY command (not unlike other dialects' INKEY).

All in all slightly different from other BASICs, but perfectly workable once you have handled it for a relatively short time. Having struggled for a long time to familiarise myself with the standard (ugh!) QWERTY keyboard layout, I now have to relearn the positions of the letters of the alphabet!

From the above the FX 702P clearly shows its programmable calculator anticedents; it also has some fairly heavy guns on the statistical analysis front. The obvious question must be how it will compare with the Sharp PC1211. It offers more facilities than the Sharp and is in some ways more versatile. The comprehensive program library supplied with the FX 702 P contains mainly scientific applications, many of them rewritten for use on the FX702P from the previous FX 502 and FX 602 libraries. It will undoubtedly find many supporters from existing Casio programmable owners and ! think its popularity will grow as its full capabilities are realised. The review model performed perfectly without a hitch and appears to do all that is claimed of it.

The instruction manual supplied, like many otherm anuals, is not all that one might expect. In this instance it suffers on two counts; the slightly stilted and occasionally unnecessarily involved English is probably due to inadequate translation facilities, and although all functions are explained somewhere there is no comprehensive index of list of them - so, if I have got it right, treasure the table of functions/commands that l've provided!

The FX 702P, like the FX 602P, offers the user the option of
defining the available memories at the expense of program steps. You can choose from 26 memories and 1680 program steps to 226 memories and 80 program steps. As an indication of steps required for a program:

```
10 PRT,10 GSB and1000 PRT
10 PRT""
1 0 ~ P R T " H O W M A N Y S T E P S " '
use four steps uses six steps uses 22 steps
```

So it would seem to be something like two steps for line number, two steps for BASIC command words and one step for each character.

The LCD display is very clear and a control for the contrast of the display is provided. 62 characters may be written on one line, 20 of them being displayed at any one time. The display scrolls to the left to enable long strings of text to be read. The characters are made up of a $7 \times 5$ dot matrix and no confusion arises between any two characters.

Also following previous practise, the FX 702P has a MODE key which defines the current status or mode that the machine is executing, thus:

MODE 0 ... RUN, manual and program calculation mode MODE 1 ... WRT, program writing, checking and editing mode
MODE 2 ... TRACE, program RUN line by line in debugging mode
MODE 3 ... TRACE off
MODE 4 ... DEG, unit of angular measure will be degrees MODE 5 ... RAD, unit of angular measure will be radians MODE 6 ... GRA, unit of angular measure will be grads MODE 7 ... PRT, print output mode if printer connected MODE 8 PRT off

In MODE 0 the FX702P can be used either in the direct mode as a calculator or will RUN any currently stored programs from any one of the 10 'program areas' designated P0-P9. In direct mode and using the minimum number of memories each memory is assigned a label A-Z. You may therefore assign these memories by keying $A=2, B=5, C=1.234$ etc. Should you use these characters as variables in a program they will either have the value already entered ( $A=2, B=5$ etc), or if reassigned within the program the original value stored will be lost. Quite straightforward but you must make a note of what variable names and memory locations you have used. Which is normal practise, is it not ......!

All the normal operators $(+-1 * \hat{\imath}=\leq \geq \neq<>)$ and punctuation (,,$;:$ ? ! that you would expect to find on a BASIC language computer are available, together with a large number of predefined functions/command words. These are selected either by keying one of the two function select keys F1 and F2 followed by one other key, or by keying in the appropriate keyword using the alpha keys. So the PRINT command may be obtained either by pressing F1 and ; or by pressing PRT. Both result in PRT displayed.

Not only but also - there are some commands available only by keying in the appropriate keyword, for example CLR, CLR AlL, CNT, MX, MY and so on - wow!

## Find The Function

With all computers there are usually a few functions/commands that are either missing or behave in a manner that is not what the user wants or expects. With computers above a certain complexity it is normally possible to persuade them to do what you want even though the specifications would have you believe that a particular facility is not available. The FX702P has a few such grey areas so perhaps the following hints will help.

To utilise MID the string to be operated upon can only be assigned one name/label - $\$$. This rnay be up to 30 characters long but must be called $\$$. String variables $\mathrm{A} \$, \mathrm{~B} \$ \mathrm{C} \$$ etc may.on-
ly be up to seven characters long so you cannot directly extract a string of over seven characters from the possible input of 30 . Should you wish to do so try this: check for the length of the string using LENC, if it is over seven characters long use MIDC to extract a portion of this string and assign this to $A \$$. Take the next portion of this string and assign to $B \$$, the next $C \$$ and soon. When you need to display or use the overlength string for further string handling call up $\mathrm{A} \$+\mathrm{B} \$+\mathrm{C} \$$ either as PRT $A \$+B \$+C \$$ or $\$=A \$+B \$+C \$$. This is called string concatenation.

Missing from your BASIC vocabulary is VAL, the BASIC command that returns the numeric representation of a string; if the string is not numeric a zero is returned. This means that if you use the command KEY to enter a character from the keyboard that character can only be string variable and although it may be a numeral $0-9$ you may not perform árithmetic on it directly. One way to overcome this is to use a series of IF commands so:

```
10 A$ = KEY:IF A$ = '"' THEN 10
20 IF As = "1";X = 1
30 IF AS = ''2":X = 2
```

and so on. You now have $X$ assigned to the numerical value obtained by using the KEY command.

Other common functions missing from Casio's BASIC are REM, READ, DATA and ON...COTO. At first sight the missing REM is a nuisance and it means you cannot include any nonoperative program information within the program. Fear not, where there is a will there is a way (sometimes!). How about a program line like this:

## 20 COTO 30:THISISA HIDDEN REM

On executing the GOTO the computer ignores the text after the colon - so who needs a REM command! I'll let you think of ways around any other missing statements!

## Tape Measures

A cassette tape recorder in conjunction with an FA-2 adaptor may be used to store programs or data on tape. The paragraph concerning this in the manual is a wonderful example of a (presumably) Japanese/English translation inferring that a magnetic tape recorder may be used to store important programs and data but that another type of recorder can also be used for recording (alto, bass, tenor?)

One could go on for some time praising and explaining the functions and capabilities of the FX 702P, which like all computers has characteristics unique to itself. The proof of the pudding is in the eating: I certainly enjoyed my feast with this latest offering from Casio. Look at the table of Functions/Commands and judge for yourself...


| COMMAND | KEYING SEQUENCE |  | RESUIT |
| :---: | :---: | :---: | :---: |
| EXE | EXE |  | EXEcute, instructs computer to action current instruction, enters program line. |
| MODE | MODE |  | Selects operating MODE - RUN, WRT, debug TRACE, unit of angular measure and printer operation |
| F1 | F1 |  | 1st Function key (coded red) |
| F2 | F2 |  | 2nd Function key (coded blue) |
| C | C |  | Deletes character to left of cursor |
| CLR |  |  | Deletes current program area |
| CLR ALL |  |  | Deletes all program areas |
| AC | AC |  | Clears display, terminates RUNing of program, will switch computer back on after auto shutdown |
| STOP | STOP |  | STOPs execution of program |
| CONT | CONT |  | CONTinues execution of STOPped program |
| ANS | ANS |  | Displays result of previous calculation |
| STAT | STAT |  | Input mode for performing statistical calculations |
| ASTAT | F1 ANS |  | Displays results of statistical calculations |
| SAC | F1, |  | Clears statistical summation memory |
| INS | F1 C(clear) |  | Inserts space at cursor position |
| DEL | F1 STAT. |  | Deletes incorrect statistical data |
| HOME | F1- |  | Positions cursor to left of display area |
| 4 | 4 |  | Cursor movement left |
| $\rightarrow$ | $\rightarrow$ |  | Cursor movement right |
| COMMAND | KEYING SEQUENCE | EXAMPLE | RESULT |
| FOR | F2" | FORK $=\mathrm{n}$ TOm | Increments from $K=n$ TOK $=m$ during which time program lines up to NEXT $K$ are repeated |
| TO | F2 \# |  | See FOR |
| STEP | F2 \$ | FORK $=n$ TOm STEP p | Optional increment in FOR...NEXT loop |
| NEXT | F2: | NEXT K | Used in conjunction with FOR... |
| PRT | F2; | PRT A | Displays value of A |
|  |  | PRT AS | Displays string A\$ |
|  |  | PRT "TEXT" | Displays string enclosed within ""r |
| IF | F2 A | IF $X=Y$ THEN... | Decision/comparative instruction |
| THEN | F2 B | IF.... THEN 200 | In conjunction with IF, in example if comparison true jump to line 200 |
| GOTO | F2 C | COTO \# COTO 200 | Jump to execute program area P5 Jump to line 200 |
| GSB | F2 D | GSB 500 | Jump to subroutine at line 500 |
| RET | F2E | RET | End of subroutine RETURNs to program line following associated CSB |
| INP | F2 F | $\begin{aligned} & \text { INP X } \\ & \text { INP X } \$ . \end{aligned}$ | Assigns value of keyboard INPUT to variable, numeric or string |
| WAIT | F2 C | WAIT 100 | Determines display time when using PRT command, WAIT $100=$ approx. 5 seconds |
| SET | F2 K | SET E, $n$ | Defines number ( $n$ ) of digits displayed - |
|  |  | SET E, ${ }^{\text {S }}$ ST | Defines number ( $n$ ) decimal places displayed Cancels SET command |
| VAC | F2 L | VAC | Clears data use memory. |
| STOP | F2 M | STOP | Suspends execution of program |
| END | F2N | END | Terminates execution of program |
| SAVE | F2 0 | SAVE[\# ${ }^{\text {"filename" }}$ ] | Command to SAVE program area $n$ on tape under specified filename |
| LOAD | F2 P | LOAD [\#n"filename"] | Command to LOAD from tape sepcified program to program area $n$ |
| PUT | F2 Q | PUT ["filename" $]$ A,Z | Command to save data variables to tape |
| GET | F2R | CET [ "filename"] $A, Z$ | Command to read data variables from tape |
| VER | F2S | GET ["filename"] | Verifies program or data written to tape |


| DEFM | F2T | DEFMn |
| :--- | :--- | :--- |
| PASS | F2U |  |
| RUN | F2V | PASS "password" |
| LIST | F2W | LIST $n$ |
| KEY |  | F1N |

Increase number of memories available by 10 times $n$
Designation of password to protect program
Executes program in specified program area
In WRT MODE displays specified program line and subsequent lines on keying EXE
Reads one character from keyboard and assigns it to A $\$$

| FUNCTION | KEYING |
| :--- | :--- |
|  | SEQUENCE |


| RPC | F2 H | $A=R P C x, y$ |
| :---: | :---: | :---: |
| PRC | F21 | $A=P R C x, y$ |
| DMS | F2) | $\mathrm{A}=\mathrm{DMS} A$ |
| RAN \# | F1 | $A=$ RAN \# |
| SIN | F1 \$ | $A=\operatorname{SIN} x$ |
| COS | F1 : | $A=\cos x$ |
| TAN | F1; | $\mathrm{A}=\operatorname{TAN} \mathrm{x}$ |
| LOG | F1 A | $A=\operatorname{LOG} x$ |
| LN | F1 B | $A=L N x$ |
| EXP | F1 C | $A=E X P x$ |
| SQR | F1 D. | $A=S Q R x$ |
| SGN | F1 E | $A=S G N x$ |
| INT | F1 F | $A=1 N T x$ |
| FRAC | F1 C | $A=F R A C X$ |
| ABS | F1 H | $A=A B S x$ |
| RND $¢$ | F1 I | $A=R N D(x, y)$ |
| DEG ( | F1 | $\mathrm{A}=\mathrm{DEG}(\mathrm{n}, \mathrm{m}, \mathrm{o})$ |
| LEN( | F1 K | $\mathrm{A}=\operatorname{LEN}(\mathrm{B} \$$ ) |
| CSR. | F1 L | PRT CSR $n ; X$ |
| MID ( | F1 M | $\mathrm{A} \$=\mathrm{MID}(\mathrm{n}, \mathrm{m})$ |

## RESULT

Converts rectangular to polar coordinates
Converts polar to rectangular coordinates
Converts decimal to sexagesimal
Generates random number where $1>A>0$
$A=$ Sine of angle $x$
$A=$ Cosine of angle $x$
$A=$ Tangent of angle $x$
$A=$ Common logarithm of $x$
$A=$ Natural logarithm of $x$
Exponential function, $\mathrm{A}=\mathrm{e}$ raised to the power of x
$A=$ Square root of $x$
$A=\operatorname{Sign}$ of $x(1,-1$ or 0$)$
$A=$ Integer part of $x$
$A=$ Fractional part of $x$
$A=$ Absolute value of $x$
Rounding to significent number of digits ( $x$ displayed to yth significant place)
Sexagesimal to decimal conversion
$A=$ Number of characters in string $B \$$
Designates location of display, PRINTs $X$, $n$ spaces from left of display
Extracts n characters from string $\$$ starting with mth character

## Functions used in performing statistical analysis

## FUNCTION

## KEYING SEQUENCE

| SDX | F10 |
| :--- | :--- |
| SDY | F1 P |
| SDXN | F1Q |
| SDYN | F1R |
| LRA | F1 S |
| LRB | F1 T |
| COR | F1 U |
| EOX | F1 V |
| EOY | F1 W |

## RESULT

Standard deviation of $\times\left(x \sigma_{n}-1\right)$
Standard deviation of $y\left(y \sigma_{n}-1\right)$
Standard deviation of $\mathrm{x}\left(\times \sigma_{n}\right)$
Standard deviation of $y\left(y \sigma_{n}\right)$
Constant term (A)
Regression coefficient (B)
Correlation coefficient (r)
Estimated value of $x(\hat{x})$
Estimated value of $y(\hat{y})$

## Statistical functions not having shortened keying sequence (ie have to be entered in full).

## FUNCTION

CNT
SX
SY
SX2
SY2
SXY
MX
MY

## RESULT

Number of data ( n )
Sum of $x(\Sigma x)$
Sum of $y(\Sigma y)$
Sum of squares of $x\left(\Sigma x^{2}\right)$
Sum of squares of $y\left(\Sigma y^{2}\right)$
Sum of products of data ( $\Sigma x y$ )
Mean of $x(\bar{x})$
Mean of $y(\bar{y})$

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| Harmonic distortion at all levels below clipping: | 0.01\% maximum |
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# AUDIOPHILE 

## Source time again. Audiophile this month goes back to disc playing machinery, giving Ron Harris a chance to assess the complete Mayware pickup system and a new cartridge range from Audio Technica.

Mayware are best known for the pickup arm, the Mark III (née Formula 4, but re-named under an international agreement). However, they also market a small range of cartridges and a step-up transformer, the T-24. I took a look at Mayware's low-cost high-output moving coil some months ago (ETI May '81), and found it a worthy product. Shortly afterwards I was tempted by an offer from Mayware to see what I could glean from a complete pickup of theirs - the Mark III arm, MC-2V cartridge and T-24 II transformer. (Overcoming temptation has never been a strong point of mine - I'm a pushover, in fact. One little exercise of the feminine wiles and logic flees the empty plains of my mind, taking any remaining vestige of willpower with it.)

Thus the Mayware pickup is duly considered herein. As an appropriate complement we have two units from a new range of cartridges from Audio Technica, the A T-3100XE and the more upmarket AT-31E. The now superseded AT-30 set a high standard for its price and I was interested to see what the AT-31E


## Mayware Wares

The MC-2V is a low output (around 0.25 mV ) moving coil cartridge with a conical stylustip - unusual even for these days of the ever-changing shape. Record wear is lower, for a given tracking force, with a conical stylus than with an elliptical tip, so the recommended 2 g downforce of the $\mathrm{MC}-2 \mathrm{~V}$ should cause no tremors of uncertainty even in the fainthearted.

The T-24 is matched to the MC-2V, or vice versa if you prefer it, such that the two constituteone working unit. The T-24 has briefly raised its head before in Audiophile, competing manfully with the incomparable Ortofon T-30 transformer, and gave a good account of itself. Matched up to the MC- 2 V it did nothing but add to an already favourable impression.

In various forms the Mark III has been around hi-fi emporiums for a while now, but it continues to offer excellent engineering at a good price and deserves more publicity than it receives. Maybe now that Thorens and Mayware share a distributor it will rise into the sight of more enthusiasts, receiving due defence in the process.

Left: The Mayware $\mathbf{P}-24$ II transformer; at $£ 69$ it is good value for money.
Below: The response curve for the MC-2V pickup.


Above: The Mayware MK III pickup arm. Note the graduations down the arm, used in conjunction with the rider weight to apply tracking force. Why not dispense with the lot and graduate the counterweight side of the pivot, thereby reducing the effective mass? The arm gave a good account of itself with most cartridges, and whilst not as universal as, say, the SME III, in my opinion it's excellent value for money and well-engineered.

## Arms And The Man

The Mark III is a unipivot design, with provision of a damping well around the bearing. Silicone fluid is employed to give a variable facility, primarily to control subsonic resonances.

An earlier headshell, which had been rightly criticised for its lack of rigidity, has been replaced here with a strong casting of greater substance. Downforce is applied, curiously, by sliding a rider-weight along the arm tube toward the pickup. Bias compensation is provided by a falling weight and thread arrangement, retained from earlier models

The counterweight is eccentrically mounted on the arm tube, to provide some lateral balance - a necessity for unipivots, as they lack the stability of designs with 'twin' bearings in this plane. If incorrectly set-up they are liable to a strange rocking mode of oscillation.

The Mark III is well presented to the new purchaser and adequate setting-up instructions are provided by the sheet accompanying the arm. Alignment and mounting protractors are also thoughtfully present and installation is very straightforward. Instructions are clear and concise, if not up to SME standards. As the arm has no sliding base, adjustments for tracking error are made by the positioning of the cartridge in the headshell itself.

## Sound Sense

Before fitting the MC-2V, I put the Mark III through its paces individually, to assess its strengths and weaknesses. Arm tube resonance is relatively minor, set at about 650 Hz , and there is a counterweight resonance at around 60 Hz . Pivot friction is commendably low, at $<20 \mathrm{mg}$ in both planes.

Using a reference cartridge of known quality, the Mayware III arm was shown to give a good basic performance with good, well-controlled bass and a clean, well-imaged, sound stage. It had a tendency towards a forward or bright presentation but not unduly so. For the price, an excellent result.

I would comment, though, that even with the low effective mass of 7.5 g , that rider weight is a strange way of adding downforce. Much better to keep it as far back towards the pivot as it will go, thus minimising its addition to effective mass, and applying tracking force by moving the counterweight forward.

Also, with low compliance moving coils, some addition to headshell mass would be beneficial, as it was to prove with the MC-2V (compliance $=8 \mathrm{cu}$ ). High compliance units give very good performances, and I tried both the Shure V15 IV and Empire 600 LAC cartridges in the Mark III with textbook results.

## Transforming Levels

The T-24 II is a well-made little unit with no frills at all, save the gold-plated phono sockets provided for I/O purposes. Finished in matt black, it is small enough to sit unobtrusively in all but the most miniscule hi-fi set-up.

On the test bench it gave an exemplary performance, proving to be flat across $20-20 \mathrm{kHz} \pm 2 \mathrm{~dB}$ and with few phase problems. It will match cartridges of between $3-40 \mathrm{R}$ impedance, although it appeared to operate best with those of low impedance characteristics, such as the MC-2V. Hum pick-up is particularly low and will give no trouble in use, I feel.

## All Together Now. . .

And so, at last, to consideration of the pickup system as a whole. Setting up was simple and the MC-2V reached its best tracking levels at around 2.1 g . It is by no means an excellent tracker, but is more than a match for most MC units in this price class. Listening tests were conducted with the system set up on a Thorens TD 160 S, feeding KEF 105 II loudspeakers via a Lecson/Monogram amplifier combination.

The overall impression was one of a well-balanced sound, but one which was not controlled tightly enough, and was a little bright overall, with a slight midrange hardness. Adding a back-plate between the MC-2V and headshell to increase the mass damping effect greatly 'tightened-up' the presentation and gave improved detail all around. The brightness persisted, however, as the only blot on an otherwise impressive performance.

Going through the components, one by one, and fitting them into other systems (as with the arm) showed that, when mounted in an SME series III the MC-2V is a fine cartridge with excellent bass and mid-range, good treble register - but a slightly forward presentation.

Put simply then, both the arm and cartridge are excellent value for money and will perform better than their respective price tags would promise.

## Match Of The Day

As a general recommendation, the MC-2V will match any of the higher mass rigid arms perfectly, and is usable in the more versatile low-mass designs, epitomised by the SME III, with the addition of headshell weight. Take care with loudspeaker matching to obtain the best from this high-quality unit, however.

The Mark III arm is ideal for all high-compliance cartridges as it stands, and has the flexibility to support the lower compliance moving-coil designs perfectly adequately. It does have


Audio-Technica's new duo of mobile coils. On the left the up-market


Above: The alignment of the motor system of the new AT cartridges. The whole system is replaced with the stylus.


Changing the stylus on the AT-31E means pulling half the body off!

Below: A potted-down' version of the test results for this month's three cartridges. As you can see, there is little to choose, on paper, between the MC.2V and the AT-31E. Personally I preferred the AT-31E for its better midrange. An opinion only, and you should listen to both for yourself, if in that field you search for perfection's touch.
this slight tendency towards brightness, though this is not serious and should not deter an intending purchaser, merely engender the requisite care over matching.

The T-24 II transformer can be unreservedly endorsed as providing value for money and a good all-round performance, distinguished by faultless bass response and outstanding transient performance. At $£ 69$ including RRP it can be said to be value for money, too.

## Technical Audio

Audio Technica have produced a new line in moving coils recently, and the AT-3100XE is a fine example of a budget unit (at under $£ 30$ ) with user replaceable stylus. (Surely one day all cartridges will be made this way?)

The AT-31E is an up-market elliptical unit, designed for the more demanding - and pecunious - enthusiast. Both are low output types, and will need step-up devices. Since the Mayware T-24 II was to hand, I used this to assess the Audio Technica units. Seemed sensible; besides which, AT hadn't sent me one of their AT 650 transformers, so serve 'em right if I don't use it!

AT make great play of their ingenious operating system; employed in this new range, in which the channel coils are wound onto separate formers and mounted in a V configuration, similar to that found in record cuitting equipment.

TEST RESULTS

|  | Mayware MC-2V | Audio Technica AT-31E | Audio Technica AT-3100XE |
| :--- | :--- | :--- | :--- |
| Frequency response (see graphs): | $20-20^{\circ} \mathrm{kHz} \pm 2 \mathrm{~dB}$ | $20-20 \mathrm{kHz} \pm 2 \mathrm{~dB}$ | $20-20 \mathrm{kHz} \pm 3.5 \mathrm{~dB}$ |
| Output voltage (at $5 \mathrm{cms}-1$ ): | 0.2 mV | 0.4 mV | 29 mV |
| Channel separation (at $\mathbf{1 ~ k H z}$ ): | 23 dB | 32 dB | within 1.5 dB |
| Channel balance: | within 1 dB | within 1 dB | 1.8 g |
| Tracking force (optimum): | 2.1 g | 1.6 g | $20^{\circ}$ |
| Vertical tracking angle: | $20^{\circ}$ | $20^{\circ}$ | 4.3 g |
| Weight: | 6.9 g | 5 g | $£ 29$ (or less) |
| Typical price: |  | $£ 56$ (or less) |  |

Claimed benefits are improved separation, better imaging and improved tracking due to reduced weight. Compliance is fairly high for moving coils and this allows a wider choice of arms than is usual. High energy (per weight) samarium-cobalt magnets are used and a spring-terminal set-up allows for userreplaceable styli.

This in itself is achieved in a novel and advantageous manner, where the generator elements are left undistributed - they are simply exchanged wholesale. Normally the stylus is changed, leaving one half of the motor, either coils or magnets, intact. Not so here, and the difference should make for more repeatable results and higher quality control standards.

## Book Covers And Judges

If appearances dictated height of fidelity, these units would be well up on the scale. The AT-31E is a striking blue and silver and the 3100 a very prominent black/white/silver. Both arrive neatly packaged on a perspex headshell, with good instruction manuals and the usual hardware (nuts, bolts, cleaning brush and so on).

The AT-30, predecessor to the AT-31E, had some problems with response in the early days which were ironed out in later samples. The AT-31E has no such troubles! As the graph shows the trace is ruler-straight except for a very slight bass rise.

Removing the cartridge from the superb packaging proved entertaining, to put it mildly. To get the unit off that nice shining headshell, you've got to pull the stylus assembly off first, else the mounting screw won't come out. Damn sneaky if you ask me, and should be explained clearly ON THE BOX somewhere. Better yet, be sensible, AT, and set it up so you don't need to be able to solve Rubik's Cube in six seconds flat to play records. Silly people

Once enthroned in a real headshell, however, the AT-31E made me inclined to forgive AT for the packaging. The imaging is excellent and the channel separation the best l've heard from a moving coil. For once, the publicity blurb is true! Tracking was above average, but not yet in the V15 IV class.

The sound quality was such that it reminded me of the Coral MC81 - only more refined! I set up a Coral for comparison and the analogy proved a good one. The AT-31E has all the Coral's strengths, in terms of mid-range detail and clarity, but none of the vices, ie slight roughness and bass extension worries.

As I was extremely fond of the MC81 (and still am, come to that) I couldn't be less than enamoured with the AT-31E. It is a fine unit and will be serious competition for the myriad other cartridges in the price bracket. Give it a listen.


Above: Safely mated with an SME headshell, the AT-31E prepares to make light of tracking and imaging problems. It matched the arm well and did itself proud in both test and living room.

## AT-3100XE

This too is a low-output design, although a higher output version - the AT-3200XE - is available. A step-up is thus required which will, to some extent, negate the advantage of low cost. We managed to try out the 3100 in a few decks costing between $£ 70$ and $£ 150$ and in the inevitable SME Series III later on. Time had gotten very short by now, but sufficient listening hours were clocked to facilitate sensible comment.

This unit too is characterised by attention to detail, especially in the midrange. It handles complex material well, although with some roughness, be it said, in the lower registers. There is a slight rise in the hf end which is not serious, but could accentuate surface noise if the cartridge is not set up PRECISELY.

One should judge against price and competitors, and on that score the AT-3100XE comes out very well. For $£ 30$ you would be hard pressed to buy a better sound anywhere.

## Man Of Letters

## Dear Mr. Harris,

Thank you for producing such a great magazine which I read regularly and with pleasure. Your recent review of the Monogram Amplifier prompts me to tilt at windmills and ask some questions. I refer to the current obsession of manufacturers for more and more watts of output! 400 watts is


Above: Response traces for the AT-31E and the AT-3100XE. The h.f. rise on the later is almost certainly due to an impedance matching problem with my test rig. It was one of those days when the gremlins ruled OK and there was nought to be done! Still, it was a repeatable result so

## NEWS : Audiophile

powerful stuff, useful perhaps for discos and Hyde Park corner, but hardly conducive to neighbourliness in domestic hi-fi.

It is recognised that Class $A$ amps are superior in range and tonal qualities to the AB, but all that heat and metalwork, as well as PSUs needing gun-carriages to move the lot around. Is it worth it all when we consider the notorious inefficiency of the standard design loudspeaker. You rarely get out of a speaker what you put into it.

My own set-up is very modest; a pair of mongrel speakers which satisfy me if no-one else. They have a power rating of 15 watts apiece. The amplifier is also a hybrid - Capricorn preamp construted from your friendly rival, HE (sorry about that), and two ILP power amps, rated output 15 watts each. I did not build the HE power arips as I did not have $£ 125$ and could not think of a reason for needing 300 watts. I doubt I am using more than $20 \%$ of my output power in domestic use. The signal source is either radio or stereo tape deck. Which brings me to my questions.

Is the advantage of these larger output amps to be found in their class A quality rather than lots and lots of watts?

With my present speakers would it even be feasible to use power amps with such large outputs?

Thank you for reading this and please could you enlighten me. If the improvement in tonal quality were to be considerable, then I will go for Class A, hot though it may be, but at a more modest price.

Fr. K. Callaghan, Clapham, London.
Oh ye of little faith! Watts the point of me extolling the virtues of good PSUs, high powers and increased dynamic range if you're gonna ignore me totally? I give up. I despair. I resign from the human race (assuming i was ever in it).

High power amps are not a luxury, they are a necessity if you are to employ anything like the same range in your music as you would experience in the concert hall. Most domestic users probably run their systems under 1 W most of the time, but once a crescendo trots down the wires, or someone hits a bass drum, something like 100 W is needed to maintain the same fidelity levels on the signal.

If your amp is underpowered, the attempt to reach such heights simply boots the output into clipping - which sounds rough and harsh compared to that which has gone before. Even with your modest speakers, higher power would make itself audibly apparent, with a sense of ease and clarity. Start saving the pennies!

Dear Sir,
Having read your column on and off for some time now 1 have come to the conclusion, reluctantly, that you are biased against Linn products for some reason of your own. Answer me straight, is this true? If not, why don't you review one of their new arms, for example?

## D. C. Chesterton, Tovil, Kent.

No it is not true. I consider the LP12 greatly overpriced for the performance it offers and refuse to concede that a Linn source is the only viable one. The Linn has its own sound, which is pleasant enough, but hardly totally uncoloured. Straight enough?
(P.S. I'd be only too happy to review the Basik (or any other product of theirs...) should Linn feel able to loan me one!)

## Stop Press

If you like the idea of owning a pair of Volt V3 speakers (see October ' 81 ETI ), but don't like the idea of chopping up the chipboard, then take heart. Wilmslow Audio are now offering a complete precut woodwork kit for this project. This news arrived so late we don't know the price yet, so for more information get in touch with Wilmslow, Cheshire (telephone 0625 529599).


## BODYWORK CHECKER

## Don't go out and buy a second-hand car without building this handy little gadget. It'll point out any problems under the paintwork. Design by Rory Holmes. Development by Tony Alston.

The purpose of this project is to help the selective second-hand car buyer detect the amount of bodyfiller used under well-disguised repair jobs. The unit gives a two-state indication of metal or plastic, ('OK' or 'BAD' respectively).

Our metal detector uses a capacitive sensing principle, which will detect the presence of any conductive object. Because of this the circuitry is much simpler and more reliable than metal detectors working on an inductive principle. It is also more suitable in this type of application where large areas of metal must be checked.

In use the device is switched on and lightly run over the car panels; if it runs over an area of body-filler the 'BAD' light will come on, otherwise it should read ' OK '.

## Construction

The case is the most important part of this project as it is also part of
the electronic sensing circuit. Take a careful look at the photographs of the finished project and you can clearly see the sensor area at the bottom rear of the case. First cut a rectangular hole $(30 \times 35 \mathrm{~mm}$ ) about 8 mm from the bottom edge of the case and 14 mm from either side - make sure to clean off any burrs from the hole. A piece of single sided copper clad board ( $24 \times 30$ mm ) is used for the sensor plate - this is centrally glued (copper side out) to a piece of plain paxolin or similar material ( $35 \times 45 \mathrm{~mm}$ ). This assembly is then glued to the case from the inside, so that the copper clad board will then be flush with case surface.

A small hole is drilled through to the copper side of the sensor plate and a short length of insulated wire, long enough to reach the main PCB, is soldered to the copper surface of the sensor plate.

The components can now be assembled and soldered to the main PCB as shown on the overlay diagram,


Fig. 1 This cutaway diagram shows the constructional details for the sensor plate.



With the protective felt peeled back to reveal the sensor, you can see how the fixing screws should be countersunk so they lie flush. In the internal shot (right), note how the trimming wire is taped to one side of the case.

making sure to correctly orientate D1,D2, IC1 and IC2 and the LEDs. Make sure to fit the link adjacent to IC1.

A short length of insulated wire is connected from the PCB to a solder tag fixed to the case - make sure this is a good connection as it forms part of the detecting circuit. The connecting lead from the sensor plate is soldered to the main PCB as indicated. A further insliated lead is taken from this same point on the PCB and held against the side of the case by a piece of insulating tape to form a capacitive trimming circuit (see photograph and refer to the setting up procedure). The LEDs are

directly mounted on the PCB and appropriate holes are drilled in the front case panel to allow these to pass through.

Finally, a piece of felt cut to size is then glued to the rear of the case, covering the sensor plate; this prevents the case from scratching the car bodywork and upsetting your friendly second-hand car dealer!

## Setting Up

Setting up the circuit is straightforward; PR1 controls the detecting sensitivity and PR2 the metal/plastic switching threshold. When altering the presets bear in mind that replacing the case lid will slightly offset the adjustments, so replace the lid after each adjustment to check the effect.

Start with maximum sensitivity, ie set PR1 to its full resistance (anticlockwise). Then place the case, sensor
side down, onto a non-conductive object. With the lid off, PR2 can now be adjusted until the switching threshold is found. When the 'OK' LED is on, back off preset PR2 until it just extinguishes and the 'BAD' LED comes on (indicating no metal). The unit can now be placed against a metal surface and the 'OK' LED should re-light.

The trimming wire capacitively couples a small degree of HF voltage into the detector, effectively altering the switching threshold. Its effect can be varied by trimming the length. By experimenting with this if necessary, together with PR1 and PR2, a suitable switching action can easily be found.

Note that the human body is a fairly good conductor - you can prove this by holding your hand against the sensor, when the 'OK' LED should come on. This resulted in one member of staff wandering round the office, checking out the female employees and reassuring them that all was well!

## BUYLINES

All ICs and other components for this project are readily available. Most mail-arder who advertise within these pages, eg Bi-Pak, will be able to supply all that is necessary. The PCB is available from our PCB Service as advertised on page 94.


## HOW IT WORKS

CMOS inverter gates IC2a and IC2b form:a high frequency oscillator of about 150 kHz . This signal is connected directly to the case, which in turn is capacitively coupled via the sensor to the high-impedance detector circuitry based around IC1. This unusual way of screening the circuit prevents the user's hand from affecting the capacitance between the detector input and the 0 V ground rail.

D1, D2, C1, and PR1 rectify the signal from the sensor and pass this voltage to the positive input of the op-amp, which is configured as a simple comparator. PR1 is used to set the input impedance and hence the sensitivity of the sensor. PR2 sets the switching threshold voltage on the non-inverting input to the comparator. When the coupling capacitance is increased, due to a conductive object lying across the case and sensor, the high frequency signal strength arriving at the detector will increase, raising the voltage on pin 3 of the comparator above the threshold, and switching the output from pin 6. fully positive.

IC2c,d are connected as a Schmitt trigger with R4 supplying positive feedback. This sharpens up the switching action coming from the comparator and further provides suitable drive signals for the two LEDs. These drive signals are buffered and current-limited by IC2e,f which power the LEDs. When metal is detected LED2 is lit and LED1 is off; the corverse is true if metal is absent.


Fig. 2 Circuit diagram


Fig. 3 Component overlay of the ETI Bodywork Checker.

PARTS LIST

| Resistors (all $1 / 4 \mathrm{~W}, 5 \%$ ) |  |
| :---: | :---: |
| R1 | 22k |
| R2 | 8k2 |
| R3 | 100k |
| R4 | 8M2 |
| Potentiometers |  |
| PR1 | 4M7 miniature horizontal preset |
| PR2 | 47k miniature horizontal preset |
| Capacitors |  |
| C1 | 4 n 7 disc ceramic |
| C2 | 470p polystyrene |
| Semiconductors |  |
| IC1 | CA3140 |
| IC2 | 4069B |
| D1,2 | 1N4148 |
| LED1,2 | 5 mm red LEDs |
| Miscellaneous |  |
| Battery and clip (PP3); diecast case, |  |
| approximate siz | $114 \times 64 \times 30 \mathrm{~mm}$ (RS $509-939$ Buylines). |

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\end{gathered}
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[^1]
# WHY CLASS A? 

## The System A has aroused a lot of interest among our readers - and a few questions too. In this article Stan Curtis explains 'Why Class A?'.

Class $A$ is a mode of operation in which all the output devices operate on the linear portions of their transfer characteristics all the time, the mean current drawn from the supply being constant irrespective of the signal. Class B is a mode in which the output devices split the positive and negative portions of the waveform between them, each device operating from an initially cut.off condition (or a low stand ing current in the case of Class AB). No matter how well engineered, this transition from positive to negative (and vice versa) will cause an irregularity or non-linearity in the transfer characteristic which in the worst case, causes a crossover dis tortion made up of high order harmonics at high peak amplitudes - harmonics which are very offensive to the ear.

The use of a small standing (quiescent) current through the ou tput stage together with the application of large amounts of overall negative feedback minimises these effects but it must be remembered that at the actual transition point the amplifier becomes effectively open loop (ie no overall negative feedback because the output is zero) and has a very low overall gain (which is dependent upon the current through the output devices); hence the intermodulation distortion of a good Class A amplifier is virtually nil at low powers and then rises gradually with increased level (see Fig. 1).

## Improper Conduct

The second major problem of Class B amplifiers is their operation at high signal frequencies. Figure 2 shows a typical Class B transistor output stage. As the voltage across the baseemitter junction of Q1 changes from a negative (forward) bias to a positive (reverse) bias, the base current of Q2 will decrease. Because of emitter-base junction capacitance the base current of Q2 will lag the baseemitter voltage of Q1. Thus when the base-emitter voltage of Q1 is zero, there will still be some charge remaining on the baseemitter capacitance of Q2. This charge only leaks away slowly since Q1 is cut off. Thus Q2 remains conducting after Q1 has been cut off and so the conduction angle of each output transistor can be much greater than $180^{\circ}$. This results in the familiar'notch' distortion, higher current drain from the power supply, lower efficiency and hence increased dissipation by the output transistors.



NONER (Watts)
Fig. 1 Comparitive distortion versus power curves for two typical amplifiers. The vertical scale shows THD in \%.

These problems do not occur in the Class A amplifier because the transistors are always on and so never have to be switched. Thus a Class A amplifier can be designed to have an extended bandwidth with a consequent reduction of high frequency distortion and increased slew-rate.

With all the output transistors conducting in the linear collector region, the distribution of the distortion harmonics is more desirable than the equivalent Class $B$ (or Class AB) amplifier because the non-linearities in the transfer curve are smoother and less abrupt. These low order harmonics (primarily second and third) are far less audibly offensive than those of higher orders. The push-pull output stage of the System A power amp results in a cancellation of the even order harmonics leaving a small amount of the third harmonic which can be reduced to insignificance by the application of a moderate amount of negative feedback.

## Heat Treatment

Another advantage of the Class A design is that of thermal equilibrium. The standing dissipation of the amplifier is between two and four times the rated output power. The output stage dissipation is lowest at full output; thus, in the case of a music signal, the amplifier will be operating near its normal running temperature (which is also its maximum temperature). This thermal stability will tend to minimise the temperature dependent variations of gain, $\mathrm{V}_{\mathrm{Br}}$, and reverse leakage current, as well as avoiding the danger of thermal shock when the signal level changes suddenly. Conventional Class AB amplifiers have their output stage biasing set by a transistor which is thermally coupled to the heatsinks; but there is a thermal lag between increase in the temperature of the output transistor junction and a proportional increase in the temperature of the heatsink. Thus following a large amplitude signal (and the consequent heating up of the junctions) the bias voltage will be tracking the wrong temperature and so, for a short time, the crossover non-linearity may be far worse than the designer intended.

## Driving It Home

Loudspeakers are not the simple resistive loads that engineers desire them to be. This is not the time or the place to go into much detail but suffice it to say, that some amplifiers are completely incapable of driving a real loudspeaker with anything like the fidelity they demonstrate on the test bench. For one thing loudspeakers store energy particularly in their resonant conditions, and this same energy can be dissipated in the form of electrical current pushed back into the amplifier. Thus the perfect amplifier needs the ability to sink a lot of current as well as source it; and it should also have a very low output impedance (the theoretical ideal would be zero).

Most amplifiers achieve a low output impedance (ie high damping factor) by applying a large amount of negative feedback. For example the open loop output impedance could be $5 R$ but apply 40 dB of negative feedback and it drops to a respectable 0.05R. But the mathematics show that the important thing is the open loop impedance so efforts must be made to keep this very low. Typical figures that I have measured on commercial amplifiers range mostly from $1 R$ to $5 R$ with a few much higher still and one or two lower at nearly $0.5 R$. The System $A$ design has the advantage of effectively having three output stages in parallel and so the output impedance of one stage is effectively divided by a factor of three. In fact (skipping the mathematics again)the open loop output impedance of this amplifier is less than 0.1 . As a result the measured 'Interface In termodulation Distortion' is very low indeed.


Fig. 2 Typical Class B output stage.


Fig. 3 Simplified diagram of the System A output stage; effectively it is three stages in parallel.

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# ENGINEER'S GUIDE TO BASIC 

# In the final part of this series, Stewart Fleming looks at arrays, techniques of structured programming, and some simple example programs. 

|$n$ this article a fourth type of variable item, the array, is introduced together with the DIM statement which is a preliminary statement usually required by BASIC before an array can be used. This is followed by a review of the naming conventions employed by different BASICs for all four types of variable. This section is concluded by a description of some problems that can occur with BASICs offering strings and floatingpoint numbers and how they can be easily and neatly overcome.(This will complement last month's issue where we looked at a limitation of BASICs which offer only strings and integers). Finally we introduce the concept of structured programming.

## Hip Hip Array

In last month's issue, the variable was introduced as a box containing a value - string, integer or real. An array simply extends this idea to several boxes, side by side, but all having the same name.


Fig. 1 Real array containing seven elements.
Fig. 1 shows an array, A, seven elements long, with each element containing a single real number. This is known as a real array. It is also possible to have integer arrays, with each element containing an integer, and string arrays, where each element contains a string; the types of array available to the BASIC user will depend on his version of the language. Figures 2 and 3 show two more arrays, $\mathrm{B} \$$ and C .


Fig. 2 String array containing four elements.


Fig. 3 Integer array containing five elements.
Eachelement in an array behaves like a single variable, and is identified by its position relative to the leftmost box using an integer value known as the subscript. The subscript appears in brackets after the variable name. Thus in the first example, the value of $A(2)$ is 4.0. Arrays are used to store data items which are
similar in some way or when we wish to carry out a particular operation on several items of data. An example is given in the section on structured programming.

## Sizing It Up

The size or length of an array is the number of elements it contains. Before an array is used in BASIC it should be dimensioned using the dimension statement DIM. Thus to create an array A of seven elements we would put the statement DIM A(6)(remember the numbering starts at zero) - preceded by a line number - in the program. If the DIM statement is omitted, an error is produced when an attempt is made to reference the array. A possible exception may occur as most BASICs (of which Research Machines' extended BASIC, PET and APPLE BASICs are examples) will create an 11 element array automatically on encountering a reference to an undimensioned array name. A dimension error is then only produced if the subscript is too big or too small.

Long, thin arrays as above are know as one-dimensional arrays or vectors. Most integer BASICs allow only onedimensional arrays but, as we shall see later, arrays may also be two, three or multi-dimensional (a two-dimensional array is called a matrix). There is a theoretical maximum number of allowed dimensions (eg 88 for Applesoft BASIC) but if you think your program needs that many you can be sure your array is awry!

A final point on arrays also concerns Integer BASICs. As a general rule, BASICs which offer floating-point numbers tend also to offer string arrays - each element of the array being. capable of holding a complete string of up to 255 characters. By contrast, integer BASICs (eg the ZX80 4K BASIC and Apple II BASIC) do not allow string arrays, and in addition some (such as Apple II BASIC) require ordinary string variables to be previously DIMensioned for the number of characters the variable is


Fig. 4 The complexity of rewriting a structured program depends on the differences between the BASICs.
likely to contain. Acorn Atom BASIC is an interesting exception since it not only allows individual strings to be stored one character at a time in previously DIMensioned variables, but also allows lots of strings to be stored - one per element - in an array, each element of the array requiring to be separately DIMensioned for the number of characters it is likely to contain!

## The Name Game

The naming conventions for all four types of variables, integers, reals, strings and arrays, varies considerably between BASICs.

Numeric (floating-point) variable names in Research Machines' Extended BASIC(Version 5) and Nascom II 8K BASIC begin with a letter and may be optionally followed by an alphanumeric (A to Z, 0 to 9). To improve readability, longer variable names such as SUM and AVERAGE may be used, but only the first two characters are significant - hence COMET and COEFF are equivalent. String variables are subject to the above restrictions but in addition the name has a dollar sign, $\$$, appended. Real arrays have the same naming conventions as real variables; string arrays have the same naming conventions as string variables. Thus $\mathrm{A} \$, \mathrm{X7}$ and XY are all valid but 7 X is not. Note that A and AS are separate variables and both may be used within the same program.

Applesoft BASIC and Commodore PET BASIC(Version 4.0) are similar to the above except that in addition, integer variable names have a percent, $\%$, appended. Hence C\% specifies an integer variable (or array).

Naming conventions for TRS-80Nideo Genie BASICs are similar to those for the PET except, in addition, ordinary (singleprecision) floating-point variable names may be optionally followed by an exclamation mark (eg D4!) and double-precision variable names must be followed by a hash symbol, \# (eg A $\#$.

With Apple II integer BASIC and Sinclair ZX80 4K integer BASIC, integer variable names start with a letter and may be followed by a number of alphanumerics (up to about 100 in Apple 1I) all of which are significant. The same applies to string variable names and array names in Apple II BASIC (string variables must also have a $\$$ appended); with the $\mathrm{ZX80}$, however, string variable names are restricted to a single letter followed by a $\$$ and integer array names to a single letter. Thus FRED, JOE\$ and ATILLA(1) could all occur in Apple II program but only FRED in a ZX80 4K BASIC program.

Acorn Atom BASIC allows 26 variables which may be used to store integers or strings. These are the letters $A$ to $Z$. If a variable is to represent a string, it will be preceded by a $\$$. Thus $A$ is an integer variable; $\$ B$ is a string variable. (There is also a variable denoted by @ and called the 'print field size'). Unlike most BASICs, the same letter cannot be used to simultaneously represent both types. Thus A and \$A cannot both be used at the same time to represent a number and string respectively. Atom BASIC has 27 integer arrays AA...ZZ and @ @. The floatingpoint extension additionally allows the user 27 real variables, $\% \mathrm{~A}, \% \mathrm{~B} \ldots . \% \mathrm{Z}$ and $\%$ @, and 27 real arrays \%AA, \%BB..... \% ZZ and \% @ @.

A final note concerning variable names: no variable name must be the same as, or contain, a BASIC reserved word. Thus, FOR and ON will be illegal variable names as also will PONY (since it contains ON).

BASIC numeric values are initially set to zero, and string variables to the null string. Note that BASIC numeric arrays may not always be initially set to zero and so it is a good practice to set them to zero prior to use.

## A Real Dilemma

Last month, we considered the limitations that can arise in a BASIC which only offers integers and strings. We shall complete this section by considering two situations that can arise when using a BASIC which offers real (floating-point) numbers.

It is not to be supposed from this that a BASIC which only offers floating-point numbers is necessarily inferior to a BASIC offering integers as well; some BASICs offering both still convert integers to reals before performing any calculations (though ones which can also perform integer arithmetic offer advantages of speed and accuracy in some instances), and the two situations described here can arise with any floating-point BASIC.
Surprise Number 1 Consider the following program;

```
10 LET T \(=1 / 10\)
20 LET S \(=0\)
30 FORI \(=1\) TO 1000
40 LET S \(=S+T\)
50 NEXTI
60 PRINT S
70 END
```

Those with some knowledge of BASIC will recognise this as a program to add up 0.1 a thousand times. What is surprising is that the computer may print 99.9991 or similar, rather than 100 , at line 60 . The reason is that the value of T, 0.1 , can only be represented approximately in floating-point form. However, the small error is accumulated 1000 times as line 40 is repeatedly executed, hence the final error. If you suspect that something like this is happening in a program, and you know that the answer should be an integer, add 0.5 to the value and take the integral part:
$55 \mathrm{LET} S=\operatorname{INT}(S+0.5)$
will do the job.
This formula can always be used to force rounding to the nearest whole number. A general formula for rounding off a value $X$ to $D$ decimal places is:

$$
X=\operatorname{INT}(X \cdot 10 i D+0.5) / \operatorname{INT}(10 i D+0.5)
$$

where $X \geq 1$ and $X<999999999$.
Actually, the PRINT instruction carries out slight rounding on your behalf, so the problem described here would not have occurred if the 1000 of line 30 had been replaced by, say, 30.

Surprise Number $2 \operatorname{In}$ the following program,

$$
\begin{aligned}
& 10 \mathrm{LET} \mathrm{~T}=1 / 10 \\
& 20 \mathrm{LET} \mathrm{~S}=0 \\
& 30 \text { FOR I }=1 \mathrm{TO} 30 \\
& 40 \text { LET } S=S+\mathrm{T} \\
& 50 \mathrm{NEXT} \text { I } \\
& 60 \text { PRINT S } \\
& 70 \text { DIM A }(3) \\
& 80 \mathrm{LET} \mathrm{~A}(2)=2 \\
& 90 \text { LET A(3) }=3 \\
& 100 \text { PRINT A(S) } \\
& 110 \text { END }
\end{aligned}
$$

the number 3 will be printed out at line 60 , but 2 at line 100 ! The reason that this occurs is that real numbers are always truncated to the highest whole number in the evaluation of array subscripts. The value of $S$ was very slightly less than 3 , so it was truncated to 2 in line 100 and the value of $A(2)$ was printed. This problem can always be remedied by adding a small number such as 0.1 to the array subscript; ie changing line 100 to

100 PRINT AIS +0.1)

## prints 3 as required.

## Structured Programs

As promised, we now briefly consider structured programming. This is a language-independent approach to programwriting in which all the tasks to be performed by the program are broken down into three types of item. Once the complete task has been specified as combinations of these three types of item in an algorithm, it may be readily programmed in a suitable language, in our case BASIC.

The three types of item are:

- Processing statements - these afe straightforward actions, eg add 1 to X .
- Decision structures - these are of twotypes: The first has the following form:
if logical expression then processing statement A The logical expression is a statement that may be evaluated as either true or false. For example, a decision structure might be

$$
\text { if } X=3 \text { then add } Y \text { to } X
$$

The logical expression here is $X=3$. If the current value of the variable $X$ is actually 3 the expression is true; otherwise it is false.

If a logical expression is true, we carry out processing statement $A$ and then go to the next part of the algorithm; if it is false, we go directly to the next part of the algorithm.

The second type of decision structure is

> if logical expression then processing statement $A$ else processing statement $B$

In this case either processing statement $A$ or processing statement $B$ is executed (but not both), depending on the truth or otherwise of the logical expression, eg
if the river is $>6 \mathrm{ft}$ wide then lwalk to nearest bridge]

## else [iump across]

The deviousness of structured progratinning begins to become apparent when we realise that the processing statements $A$ and B may themselves be lists of processing statements or even another decision or looping structuret Note the use of positioning and brackets to make the algorithin clearer.

- Looping structures - these are also of two types. When we want to perform a processing statement a predetermined number of times, say 50 , we use
loop for $i=1$ to 50 do processing statement $C$, eg
loop for $i=1$ to 50 do [add ithelement of array $A$ to $T$ ]
When the number of times the statement is to be performed depends on some factor which changes as processing statement $C$ is repeatedly obeyed, we can use the second type;
loop while logical expression do processing statement C, eg
loop while there is still food on the plate do continue eating


## Sorting It Out

An algorithm, then, is a list consisting of these three types of item. As each item in the list is obeyed, control passes to the next item in the list until it is exhausted.

Here is an algorithm to read to values into an array $A$, sort them into ascending order and print out the sorted array. The algorithm works by repeatedly comparing adjacent elements in the array and swapping them if they are out of sequence.

$$
\text { dimension the array } A \text { to size } 10
$$ put the 10 values into array $A$ -

loop for $i=1$ to 9 do. [pass through the array]. print out array A
where [pass through the array] equals
loop for $j=1$ to 9 do $[$ if jth element $>j+1$ th element
then [swap jth and $j+1$ thelements I]
Note that the processing statement corresponding to loop for $i$ $=1$ to $9 \ldots$.... itself a loop for' structure whose processing statement is actually a decision structurel

## Attention To Detail

Another feature of structured programming is that, at the lowest level, the instructions will be able to be carried out on the computer in the language chosen (it is no good asking the computer to choose its favourite colour, but quite reasonable to get the computer to pick a random number between 1 and 10). The algorithm will hopefully be 'language-independent', however - that is, understandable without reference to any particular programming language or version of a language. The

[^2]
## FEATURE : Guide To BASIC Part 3

algorithmic structure of a program may be its 'lowest common denominator, and hence may be the only basis for the conversion of BASIC programs from one version to another. If the program has been well-structured, this task can be carried out one module or section at a time, and the new module tested before the new modules are reassembled to give a program which should work first time.

## A Graphic Illustration

A particular case to consider is graphics programs or graphics modules within a program. Graphics facilities vary tremendously from one BASIC to another, as illustrated by Programs 1 and 2 which produce cartoons of a man walking. One is written for the Apple II (using Applesoft BASIC) and the other for the TRS-80Nideo Genie. Were it not for the underlying algorithm - draw man in position 1, pause, erase man in position 1, draw man in position 2 and so on - one would be hard put to know it was the same language, let alone the same task being carried out!

## Float On

We conclude this month's article with an algorithm for one of the subroutines used in last month's program to perform floating-point addition. (The subroutines make 10 floating-point variables available to the user. In the main program last month we read in two numbers and stored them in the fifth and eighth of the 10 available locations (lines 110 to 140), added them up, and stored the result in the fourth available location (line 150). Then we printed out the contents of the fourth location (line 160). However, we could have performed any number of additions on any of the 10 locations, or we could have incorporated the subroutines for use in any other program.)

Algorithm for converting strings to floating-point numbers (subroutine in last month's program).
[Read in the string]
[Work out the sign for the floating-point number]
[Put ASCII code for sign in second location of floating-point number]
[Work out exponent for floating-point number]
[Put exponent in first location of floating-point number]
[Put mantissa in locations 3-10 of floating-point number] where:
[Work out sign for floating-point number] equals
[if leftmost character of string is " + " or " - "
then $[$ sign $=$ ASClI equivalent of leftmost character of sting. Drop leftmost character else $[$ sign $=43$ (positive)] ]
[Work out exponent of floating-point number] equals
[ [ if string contains $\mathrm{a}^{\text {".." }}$
then [exponent $=$ (character position
of "." within the string) -1 .
Remove "." from string]
else [exponent $=$ length of string] ]
loopwhile leftmost character of string
$=0$
do [subtract 1 from exponent. Drop left-
most character of string ] ]
[Put mantissa in locations 3-10 of floating-point number] equals
[ loop for $i=3$ to 10
do [put (ASCII equivalent of leftmost character of string) - 48 in location $i$ of floating-point number. Drop leftmost character*] ]

* Note that dropping the leftmost character of an empty string is considered to still leave an empty string.

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| 2SB648A | $40 p$ | BF960 | $99 p$ |
| BF256 | 38p | BF961 | 70p |
| 2SK55 | 28p | BF963 | $99 p$ |

[^3]| 0.85 | 74LS IOSN | 0.25 |
| :---: | :---: | :---: |
| 0.85 | 74LS 112N | 0.25 |
| 0.85 | 74SLI13N | 0.25 |
| 1.85 | 74LS114N | 0.25 |
| 1.85 | 74LS122N | 0.40 |
| 1.85 | 74LS123N | 0.55 |
| N | 74LS124N | 1.80 |
| N | 74LS 125 N | 0.29 |
| 0.11 | 74LS126N | 0.29 |
| 0.11 | 74LS 132N | 0.45 |
| 0.12 | 74LS133N | 0.30 |
| 0.12 | 74LS 136N | 0.25 |
| 0.14 | 74LS138N | 0.34 |
| 0.14 | 74LSI39N | 0.36 |
| 0.14 | 74LS 145N | 1.20 |
| 9N 0.14 | 74LS151N | 0.35 |
| ON 0.13 | 74LS 153N | 0.35 |
| 1N 0.14 | 74LS154N | 0.99 |
| 2N 0.15 | 74LS 155N | 0.38 |
| 0.28 | 74LS155N | 0.38 |
| 0.46 | 74LS 157 N | 0.33 |
| 0.14 | 74LS158N | 0.33 |
| 0.13 | 74LS 160N | 0.40 |
| IN 0.15 | 74LS 161N | 0.40 |
| 2N 0.15 | 74LS162N | 0.40 |
| 0.18 | $74 \mathrm{LS163N}$ | 0.40 |
| 7 N 0.14 | 74LS 164N | 0.46 |
| 2N 0.19 | 74 LS 165 N | 1.20 |
| ON 0.13 | 74LS166N | 0.80 |
| 0.14 | $74 \mathrm{LS168N}$ | 0.85 |
| 3 N 0.16 | 74LS16N | 0.85 |
| 0.15 | 74 LS 170 N | 1.40 |
| 0.16 | 74LS173N | 0.70 |
| ON 0.13 | 74LS174N | 0.55 |
| 0.33 | 74 LS 175 N | 0.55 |
| 7N 0.39 | 74LS181N | 1.20 |
| 8 N 0.65 | 74LS183N | 1.75 |
| 9N 0.59 | 74LS189N | 1.28 |
| 1 N 0.14 | 74 LS S 90 N | 0.55 |
| 4 N 0.15 | 74LSi91N | 0.55 |
| 5 N 0.15 | 74LS 192 N | 0.56 |
| 3 N 0.21 | 74LS 193N | 0.59 |
| 4N 0.18 | 74 LS 194 N | 0.39 |
| 75 N 0.28 | $74 \mathrm{LS195N}$ | 0.39 |
| 6 N 0.19 | 74LS 196 N | 0.55 |
| $8 \mathrm{~N} \quad 0.24$ | 74LS197N | 0.65 |
| $3 \mathrm{~N} \quad 0.50$ | $74 \mathrm{LS200N}$ | 3.45 |
| 5 N 0.70 | 74LS202N | 3.45 |
| $6 \mathrm{~N} \quad 0.18$ | 74LS221N | 0.60 |
| ON 0.32 | 74LS240N | 0.99 |
| 1 N 0.70 | 74LS241N | 0.99 |
| $2 \mathrm{~N} \quad 0.34$ | 74 LS 242 N | 1.65 |
| 3 N 0.34 | 74LS243N | 1.65 |
| 5 N 0.44 | 74LS244N | 0.83 |
| 6 N 1.20 | 74LS245N | 1.50 |
| 107N 0.25 | 74.S247N | 1.35 |


| 74LS248N | 1.35 |
| :---: | :---: |
| 74LS249N | 1.35 |
| 74LS251N | 0.46 |
| 74LS253N | 0.46 |
| 74LS257N | 0.55 |
| 74LS258N | 0.39 |
| 74LS259N | 0.39 |
| 74LS260N | 0.70 |
| 74LS266N | 0.24 |
| 74LS273N | 0.90 |
| 74LS275N | 3.20 |
| 74LS279N | 0.35 |
| 74LS280N | 2.05 |
| 74LS283N | 0.44 |
| 74LS290N | 0.58 |
| 74LS293N | 1.30 |
| 74LS295N | 1.50 |
| 74LS298N | 1.50 |
| 74LS365N | 0.35 |
| 74LS366N | 0.35 |
| 74 LS 367 N | 0.35 |
| 74LS368N | 0.35 |
| 74LS373N | 0.78 |
| 74LS374N | 0.78 |
| 74LS375N | 1.15 |
| 74LS377N | 1.99 |
| 74LS378N | 1.40 |
| 74LS379N | 2.15 |
| 74LS384N | 2.50 |
| 74LS385N | 4.20 |
| 74LS386N | 0.29 |
| 74LS390N | 0.68 |
| 74LS393N | 0.61 |
| 74LS395N | 2.10 |
| 74LS396N | 1.99 |
| 74LS398N | 2.75 |
| 74LS399N | 2.30 |
| 74LS445N | 1.40 |
| 74LS447N | 1.95 |
| 74LS490N | 1.10 |
| 74LS668N | 1.05 |
| 74LS669N | 1.05 |
| 74LS670N | 1.70 |
| RAM |  |
| 2102 | 1.70 |
| 2112 | - 3.40 |
| 2114/2 | 3.49 |
| 4027 | 5.78 |
| 4116/2 | 1.59 |
| 4116/3 | 1.49 |
| 4864 P | 12.50 |
| 6116P-3 | 12.50 |
| 6116P.4 | 11.25 |
| 8264 | 12.50 |


| 74CXX |  |  |  |
| :---: | :---: | :---: | :---: |
| $74 \mathrm{C00}$ | 0.20 | 8080 series |  |
| 74 CO 2 | 0.20 |  |  |
| $74 \mathrm{CO4}$ | 0.20 | 8080AFC/2 | 3.11 |
| $74 \mathrm{C08}$ | 0.20 | 8212 | 1.70 |
| 74C10 | 0.20 | 8214 | 3.50 |
| 74 C 14 | 0.55 | 8216 | 1.41 |
| 74 C 20 | 0.20 | 8224 | 1.85 |
| 74C30 | 0.20 | 8251 | 4.26 |
| 74 C 32 | 0.20 | 8255 | 3.97 |
| $74 \mathrm{C42}$ | 0.80 |  |  |
| $74 \mathrm{C48}$ | 1.03 | 6800/6809 |  |
| 74.73 | 0.50 | 88000 |  |
| 74474 | 0.50 | 6800 P 68400 | 3.75 4.25 |
| 74476 | 0.48 | 68400 68800 | 4.25 |
| $74 \mathrm{CB3}$ | 0.98 | 68800 6802 | 4.75 5.55 |
| 744855 74 C 86 | 0.96 | 68809 | 15.00 |
| $74 \mathrm{C89}$ | 2.68 | 6810 | 1.75 |
| $74 \mathrm{C90}$ | 0.80 | 68810 | 1.85 |
| 74.93 | 0.80 | 68810 | 2.04 |
| 74C95 | 0.94 | 6820 | 1.95 |
| 74C107 | 0.48 | 6821 | 1.75 |
| 74C151 | 1.52 | 68121 | 2.10 2 |
| 74C154 | 2.26 | 68821 | 2.34 4.25 |
| 74 C 157 | 1.52 | 6840 | 4.25 4.55 |
| 74C160 | 0.80 | 68440 | 4.58 |
| 74C161 | 0.80 | 68840 | 4.75 1.75 |
| 74 Cl 162 | 0.80 | 68850 | 1.75 2.17 |
| 74 Cl 163 | 0.80 | 6852 | 2.47 |
| 74C164 | 0.80 | 69452 | 2.75 |
| 74C165 | 0.84 | 68852 | 2.75 2.95 |
| 74C173 | 0.72 | 684888 | 5.25 |
| 74 Cl 175 | 0.72 |  |  |
| 74C192 | 0.80 | 280 seri | es |
| 74C193 | 0.80 |  |  |
| 74C195 | 0.80 | 280A | 4.99 |
| 74C200 | 4.52 | Z80ADRT | 7.50 |
| 74C221 | 1.06 | Z80APIO | 4.10 |
| 74.801 | 0.38 | Z80ASIO/1 | 14.00 |
| 74C902 | 0.38 | 280ASIO/2 | 14.00 |
| 74C903 | 0.38 | 280ASIO/9 | 14.00 |
| 74C904 | 0.38 | z80CTC | 4.00 |
| 74C905 | 5.64 | Z80ACTC | 4.50 |
| 74C906 | 0.38 | Z8001 | 65.00 |
| $74 \mathrm{C907}$ | 0.38 |  |  |
| 74C908 | 0.84 |  |  |
| 74C909 | 1.52 | PROM |  |
| 74C910 | 3.62 | 2708 | 2.00 |
| $74 \mathrm{C914}$ | 0.86 | 2716 | 3.55 |
| 74C918 | 0.98 | 2532 | 8.50 |
| 74C925 | 4.32 | 2732 | 8.50 |
| 74C926 | 4.32 |  |  |
| $74 \mathrm{C927}$ | 4.32 |  |  |

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# COMPONENTTESTER 

# Check out your semiconductors with this cunning but simple project. It's brilliant, even if we do say so ourselves (and we do). Design by Rory Holmes. Development by Tony Alston. 

When you've completed your latest design, a brilliant project which not only solves the world energy crisis but proves that Einstein made a small mathematical error as well, it can be very frustrating if you rush to your junk box and discover that you can't breadboard the circuit because the markings have rubbed off your transistors. To help with this problem we've come up with our latest design, a brilliant project which tells you which lead is which, whether the transistor is OK, what polarity it is and its approximate gain. Diodes and LEDs may also be tested, and for good measure we've thrown in an op-amp checker. The world energy crisis you'll have to figure out for yourself.

## Construction

Assembly is straightforward if the recommended PCB is used. Make sure to orientate IC1, IC2, D1 and D2 correctly, and use sockets for the ICs to avoid damage by soldering them. Remember to put the three wire links on the PCB!

Although there are quite a few offboard connecting wires, these should not be a problem if the circuit diagram, overlay and internal photos are studied carefully. Only one transistor test socket is shown on the circuit diagram but several types can be wired in parallel (as we did) to accommodate various types of transistor. The TO-5 and TO-18 types were epoxied to the front panel, as was the eight-pin DIL socket for the op-amp tester. Three insulated test terminals were also included for testing.other types of transistors, diodes and LEDs.

TX1 and TX2 are crystal mike inserts, Eagle type MC25 or similar. Warning! - most inserts have one terminal connected to their case and as we've used a metal front panel for this project, TX2 should be insulated from this panel. Otherwise, TX1 and TX2 will
be common linked and as the circuit diagram shows that TX1 is connected to $0 \mathrm{~V}, \mathrm{TX} 2$ 's connection to IC1, IC2 and C2 will be incorrectly taken to 0 V . We got round the problem when we glued a circular fibre washer to one insert before fixing it to the front panel.

## Testing Times

Transistors are plugged into the appropriate socket, and any type may be tested; NPN, PNP, small signal or power. No selection of NPN or PNP is necessary as this is done automatically by the tester. When the push-totest button is pressed, an intermittent tone is produced. The frequency of the tone is proportional to the gain of the transistor, giving a rough guide. The LEDs also flash alternately in time with the pulsing tone; the LED that is on at the same time as the tone indicates the polarity of the transistor. If the transistor has no gain or is open circuit there will be no tone, although the LEDs will still flash. If the transistor has a large leakage current or is shorted, there will be a 'two-tone' sound. If the

transistor has been inserted the wrong way there will be either no tone or a very high-pitched tone.

Diodes and LEDs may be tested across the ' $C$ ' and ' $E$ ' terminals. If it is OK, the LED under test will flash, accompanied by an intermittent highpitched tone and flashing indicators. Ordinary diodes require a series resistor (any old value) and should then produce an intermittent tone and flashing LEDs as before; the coincidence of flashing LED and tone indicates the anode.

Op-amps are plugged into the IC socket and no push-switch is required; power is only applied when the IC is inserted, and a good IC produces a continuous tone from the second insert.

## BUYLINES

No problems with anything used in this project; all components are standard items and are obtainable from the major mail order suppliers advertising in this issue. If you don't want to make your own PCB, you can obtain one from our PCB Service (see page. 94).




Fig. 2 Principle of the CCO.

Fig. 1 Circuit diagram of the Component Tester.

## HOW IT WORKS

The op-amp tester and transistor tester are completely separate circuits; we shall deal with the transistor tester first. IC1a, a Schmitt trigger inverter, forms a low frequency square wave oscillator with a period (determined by R1 and C1) of about 1 second. This square wave is used to switch the polarity of the 'power rails' (labelled 1 and 2 in the diagram) of the test transistor and its associated oscillator circuitry.

IC1b is used to buffer the square wave, and its output (on pin 6) is used to drive 'power rail 2 '. This switching signal from IC1b is also fed to the input of IC1c, which inverts it and drives 'power rail 1 '. Thus for half a second in each cycle rail 1 will be positive (high) and rail 2 (low); for the other half second rail 1 goes negative and rail 2 positive. Each power rail drives an LED (LEDs 1 and 2) via inverter gates IC1d and IC2d, such that an LED will be illuminated if its associated power rail is at 0 V . These LEDs will therefore flash alternately when the circuit is operating; providing an indication of the state of the power rails.

The oscillator circuit that is connected across these power rails is essentially the simple current-controlled oscillator shown in Fig. 2, but with some adaptations to enable it to work with either supply polarity. The oscillator of Fig. 2 works as follows. Assume $C$ is initially discharged, so that the input to the Schmitt inverter is low; the output is thus high and the diode, being reverse biased, is effectively out of circuit. Capacitor $C$ will now begin charging up from the current source and the input voltage to the Schmitt will be increasing. When the input passes the Schmitt threshold the inverter output will switch low; the diode is now forward biased and will rapidly discharge the capacitor. The process then repeats, producing a square wave output from the inverter with a frequency that is proportional to $C$ and the current from the source. The bigger the current from the source, the faster $C$ will charge and the higher the frequency will be.

The current source in our actual circuit is provided by the transistor under test. R2 supplies a small base current to the transistor, and the current flowing from the emitter will be proportional to the gain of the transistor. If the transistor is PNP it will only supply current to the CCO (currentcontrolled oscillator) when power rail 1 is negative with respect to power rail 2.

Similarly, power rail 1 must be positive for the oscillator to function if the transistor is NPN. Thus the CCO will produce an intermittent frequency for either transistor polarity (assuming the transistor is a good one) with a frequency roughly proportional to the gain. If the frequency is audible when LED1 is on, the transistor is PNP, and if LED2 and the tone coincide then it is NPN.

Going back to the oscillator of Fig. 2, we see that if the oscillator is to work when the supply connections are reversed, then the diode polarity must also be reversed. In our circuit this is achieved by using two diodes, D1 and D2. When power rail 1 is at 0 V , the NAND gate IC2b will be disabled and its output (pin 11) will be high. This output is inverted by IC 2c, thus reverse biasing D2 which is now effectively out of circuit. At the same time power rail 2 will be high, enabling NAND gate IC2a whose output (pin 4) will follow the logic level on the output of the Schmitt trigger IC1e via IC1f. Thus when IC 1e goes low during an oscillation cycle, the cathode of D1 will also go low, forward biasing the diode and discharging C 2 for the next cycle.

When the voltage on the two power rails is reversed a similar action occurs, but with D1 switched out of circuit and D2 providing the discharge path. The intermittent square wave produced at the output of IC1f is fed to crystal transducer TX2 which gives an audible note.

If an LED or diode is connected between the $C$ and $E$ terminals of the test socket, it appears to be a large-value current source in one direction only. Hence the circuit reacts as if a high-gain transistor were in circuit, and polarity is indicated as before.

The op-amp under test is configured as a simple RC relaxation oscillator. When the op-amp is plugged in, assume that its output (pin 6) is high (positive saturation). Then C3 will begin charging up to +9 V through R3 with a time constant C3.R3. When the voltage on C 3 reaches one-third of the positive supply (this fraction is set by R4 and R5), the op-amp output will switch low, with R4 and R5 providing positive feedback for Schmitt trigger action. C3 will then discharge towards -9 V , until the op-amp switches back to positive saturation. This cycle repeats indefinitely, producing a square wave at the op-amp output which is fed to transducer TX1. This produces an audible note if the op-amp is good.

PARTS LIST

| Resistors (all $1 / 4 \mathrm{~W}, 5 \%$ ) |  |
| :--- | :--- |
| R1 | 470k |
| R2 | 1M0 |
| R3 | 10k |
| R4 | 47k |
| R5 | 22k |
|  |  |
| Capacitors |  |
| C1 | 1u5 25 V tantalum |
| C2 | 10ndisc ceramic |
| C3 | 330n polyester |

Semiconductors

| IC1 | 40106B |
| :--- | :--- |
| IC2 | 40118 |
| D1,2 | $1 N 4148$ |
| LED1 | $0.2^{\prime \prime}$ red LED |
| LED2 | $0.2^{\prime \prime}$ green LED |

## Miscellaneous

PB1 momentary push-button IX1,2 crystal mike inserts 2 off PP3 batteries and clips; transistor sockets; IC sockets; case to suit


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[^4]

# TECH TIPS 

# Adjustable Sensitivity Continuity Tester 

David Wolfe, Cambridge

Continuity testers operate by comparing the resistance between the test probes with a fixed reference resistance (if the probe resistance is less than the reference then the tester indicates this somehow). This is fine if the tester is to be used in only one type of application, but means that the tester is limited to this application. For example, when testing continuity on a circuit board one is generally testing for very low'hookup' resistances; when testing long cable runs, however, such as in house wiring, one tests for resistances often up to several kilohms.

This design overcomes this problem by having an adjustable reference. The

tested resistance is configured as half of the potentiometer which is adjusted to give the required sensitivity. Obviously by changing the component values, especially that of R2, the range over which the tester can operate can be altered, but it should be remembered that for the tester to discriminate very low resistances the potentiomenter must be able to output voltages very close to 0 V .

Continuity can be indicated in
several ways depending largely on user preference, but also on parameters such as current consumption and parts availability eg a mechanical 'buzzer', an astable driving a loudspeaker or an LED. These would all need a suitable driving circuit as the op-amp could not do this directly. In the prototype a piezo buzzer was used for low current consumption. A CA3140 IC was chosen in this circuit for its ability to operate with inputs near to the negative supply rail.

## Micro-power VOX

David Ian, Hampton Court

Previously published voice operated switches seem limited in application due to their disproportionately high current requirements relative to the subsequently switched circuitry, eg battery operated baby alarms, portable transmitters and so on.

Including a visible indicator this design has, at 9 V , a quiescent consumption of a meagre 800 uA , rising to a maximum of 1.6 mA when triggered, but is capable of cleanly switching at least 250
mA at up to 30 V .
The 741 is wired as a decoupled, high-gain preamp, with RV1 controlling the switching point over a wide range of audio levels - anything from a whisper to a shout. The resulting voltage level triggers (via Q1) the monostable formed by three gates of a 4011. When the output of the third, inverting, gate goes high the N-channel VMOS FET, Q2, is enabled, thus completing the power supply of an external device.

The 'on' resistance of a VMOS FET is less than $2 R$ ("off" is tens of megohms) and quite large currents may be safely
handled before a heatsink becomes necessary.

To aid setting to a given sound level the unusual, but current-saving, arrangement at the output of the remaining 4001 gate provides a single flash from LEDI whenever the monostable is triggered.

C and R were selected for the particular requirement of an 'on' time of 14 seconds; 1u0 and 1M0 gives approximately one second delay, depending on the individual gate's transition point. Any medium to high impedance microphone could be used; the electret type shown was to hand.



## Four Input Stereo Mixer

R.D. Pearson, Sheffield

The mixer circuit shown was designed to allow four or more inputs to be mixed down, producing a stereo output. Each input has stereo panning and a level control. The gain of the input stages can be boosted according to specific needs by adding RX, making it possible to use a direct input from guitars, microphones and so on. Note that to avoid poor frequency response, the gain of this stage should be kept below 50 (keep RX above 2 k 2 ). The input impedance is 100 k and should be high enough for most applications.

The two output stages have sufficient gain to compensate for the attenuation of the panning controls. If more than four inputs are used it will be necessary to increase the gain of the output stages by decreasing the value of $R Y$ to 6 k 8 for six inputs or 4 k 7 for eight inputs.

741 op-amps should prove suitable for most purposes, but if lower noise is desired then a low noise op-amp such as the TL071 may be substituted. The simple zener regulated power supply shown should be suitable for general purpose applications.

## Anti-Theft Device

## G.J. Phillips B.Sc, Durham

Many audio retailers employ antitheft devices whereby a loop, made up of lengths of cable joined with plugs and sockets, is passed through the handles of radios and cassette
players. If the loop is broken an alarm sounds.

The circuit diagram shows a design which has been built in the lab and functions very well. R1 sets the quiescent current in the loop. The loops could include vibration sensors or any other suitable normally closed contacts. When the loop is broken,

the logic 0 at R1 causes the astable multivibrator formed by IC2a,b to be enabled via gate IC1d, which acts as an OR gate for Os at its inputs. The astable frequency is set at approximately $1 / 4 \mathrm{~Hz}$ causing the buzzer to sound intermittently.

The logic 0 at R1 also triggers the monostable formed by $1 \mathrm{C} 1 \mathrm{a}, \mathrm{b}, \mathrm{c}$ and the output of this monostable also enables the astable via pin 12 of 1C1d. Thus, if a quick-witted thief quickly remakes the broken loop or the vibration sensor quickly breaks the loop, the monostable ensures that the alarm continues to sound for approximately 20 seconds. If the loop is left open then the alarm will sound all the time. Unused inputs of the CMOS chips should betied to $V_{C C}$ or 0 V whereupon the quiescent battery drain will be less than a microamp.

R1 can be replaced with an LDR (ORP12) and a 10 M resistor used to replace the loop. The alarm is then triggered by light. Place the device in your components drawer and you'll be able to nab the guy who's been pinching your ICs when no-one's looking.



| $A$ | $B$ | $C$ | $D$ | MODE |
| :--- | :--- | :--- | :--- | :--- |
| 0 | 0 | $\bar{D}$ | $\overline{\mathrm{C}}$ | BISTABLE |
| 0 | 1 | $\bar{D}$ | $\bar{C}$ | ASTABLE |
| 1 | 0 | $\bar{D}$ | $\bar{C}$ | ASTABLE |
| 1 | 1 | $D$ | C | BISTABLE |

Heads Or Tails<br>D. Indyk, London

An ultra-simple heads or tails indicator can be built using a single 4077 EXNOR IC. The circuit is normally in a latched bistable mode; when the switch is closed the circuit will oscillate, ie toss the coin. The astable frequency is approximately $5-10 \mathrm{MHz}$. If desired a small push-tomake switch can be connected in series with the battery as an on/off switch, such that the battery will be disconnected from the circuit unless the device is being held. The LEDs can be any colour.

## Active 'Stereo' Bass Guitar

## J. Smalley, Nottingham

The circuit was designed to increase the musical capability and performance of a single pickup, passive bass guitar. While having a performance advantage over many 'off the shelf' active basses, this system also allows the musician to have his favourite bass converted to active status.

For optimum noise and consumption of battery current ( 650 uA quiescent), the TLO64 BIFET quad op-amp was chosen. As a result, the circuit may be broken down as follows: IC1a is a voltage follower and provides a low impedance 0 V rail to bias the remaining amplifiers. IC1b is also a voltage follower and serves to isolate the two filters from the pickup. IC1c,d are the high and low filters respectively. The response of each filter exhibits a shelving curve which rolls at $6 \mathrm{~dB} / \mathrm{cctave}$. In rough musical terms, the slope break points are arranged so that bass notes are handled by the low filter and the higher notes by the high filter.

C3 or C8 may be adjusted for a different slope position, and the ratio R4:R5 (high) and R13:R14 (low) for an alternative differential gain ratio. R17 and R18 may also be adjusted for pickups with different output levels. SW2 and SW3 allow the filters to be 'in' or 'out', and SW1a-d allows the original tone circuits to be connected to the output jack, and totally disconnects the electronics. Battery on/off is via a pair of insulated switching contacts on the stereo jack socket. In the instrument modification, the original jack socket is removed and a stereo version fitted in its place.

Musical use is very much a matter of experiment, but best results were obtained when using a stereo lead with a twin channel amplifier.

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AIcohol may be wonderful stuff (well, we think so), but it tends to have unfortunate side effects; too much of it will cloud your judgement, slow your reactions, affect your balance and, worst of all, make you certain that the exact opposite is true. Such a state of mind is dangerous if you intend using that modern offensive weapon, the motor car. The ETI Alcohometer is a crystal-locked reaction timer that is simplicity itself to use (always an advantage when you've had a few), and will prove to over-confident imbibers exactly how much effect the odd lunchtime pint or five has really had. Lots of fun at parties, too although once you have trouble holding the button down, it's probably time to leave. In a taxi.

## Button Up

When the Alcohometer is switched on the display is blank, except for the decimal point. To play, you hold down the push-button and wait. After a random time period lasting about one and a half to eight seconds, the display lights and starts counting up from zero. Releasing the push-button stops the count and displays your reaction time in seconds. To conserve power the display blanks automatically after a further eight seconds; if you're in a hurry to play again, pressing the pushbutton blanks the display and starts a new cycle. If you don't react within one second, the display latches at 000 so you can't claim a reaction time of 3 milliseconds just because the counter clocked round once before you noticed.

Brave ETI volunteers found that even one small drink could have a noticeable effect on reaction time, but don't take our word for it - start building one now and be ready for Christmas!

## Construction

The construction is elegantly simple since all the parts are mounted on two PCBs; nevertheless it's quite intricate, due to the high component packing density. Solder the components into the main board first, not forgetting the orientation of the ICs, capacitors and diodes (see overlay). Use IC sockets, it's always a good precaution. Veropins should be soldered into the holes for the pushbutton switch, and the switch soldered in turn to these, so that the height from the top of the button to the board is 27 mm . Remember to put in all the board links, including the two underneath the board, but do not solder in the vertical links to the display at this stage.

After building up the prototype we discovered an unusual problem. It appears that some manufacturers produce longer plastic DIL packs than others, and if your 4017s are too long they won't fit the board. The Motorola chips we used (MC14017BCP) are OK, but if yours are too long you can alwavs grind off the ends (carefully!)

If the crystal is a plug mounting type, don't worry; its pins can be cut shorter, and wire links soldered from the pins through the PCB holes. This same procedure will be necessary for the on/off switch (SW1) which is mounted sideways on the board (see photos). A small piece of plain PCB acts as a spacer between the switch and board, to align the switch with the moulded case cutout. It's a good idea to place the board in position on the case to check the exact switch placing. before securing it with superglue.

The display board is mounted above the main board and is held in


With the top off, you can see how everything fits into the case - just!


Fig. 1 Circuit diagram of the main control and oscillator section.
place by the vertical wire links (see photograph). Solder in all the components as per the overlay diagram, including the positive rail link, and noting the polarity of C9 and LED1. Sockets should be used for the seven-segment displays, both for protection and to give the required height. Lengths of tinned copper wire (about $2^{\prime \prime}$ ) should now be soldered at all the vertical lead out holes - 23 in total. After completing this carefully check the track side of the board for bad joints, solder bridges and other faults. It will be very difficult to correct mistakes after the boards are soldered together. Also check that all the ICs are plugged into the main board.

Now comes the tricky bit; all the 23 wires coming down from the display board must be inserted into the corresponding hole, vertically beneath, on the main board. It helps to trim the wires to different lengths, starting at about $1^{\prime \prime}$ at one end, and increasing to $2^{\prime \prime}$ at the other. They, can then be inserted one at a time, as the boards are lowered together. When the boards are together the separation (between both parallel component surfaces) should be adjusted to about 13 mm . The wires can be bent under the board to hold this position, and then soldered and cropped.

## Vero Intelesting

The case used is a two part moulded Verocase. This case has a built in battery compartment, and ready-made cutouts for the display and on/off switch. There is a small moulded stand-off in the centre of the bottom case half - this should be filed or cut down in size, until it's shallower than the three PCB stand-offs. The board assembly can now be fitted into the
bottom case half. The PCB edges may need filing for the good fit against the bottom of the case (be careful not to file away the copper tracks at either edge). An appropriate hole must be drilled in the case front for the pushbutton; a good method is to put a small blob of ink on the button head and then bring the case halves together in the correct position. The ink will leave a drilling mark. The board can be secured in the bottom of the case using



Fig. 2 Circuit diagram of the counter/display section (above) and the supply decoupling capacitors.


## BUYLINES

The case used is one of the Vero range, order code 202-22275G. The two PCBs available from our PCB Service on page 94, and none of the other components should cause any problems (although you should note the point made in the text about the 4017s).
ordinary adhesive pads, if they are used double thickness. The displays should just come in line under the window when the two case halves are together. A suitably sized piece of red filter plastic or polarising sheet should be cut and glued underneath the display cutout. The battery can be held more securely by sticking a piece of plastic foam into the battery compartment for a compress fit.

## Time To Test

You are now ready to test the finished article. With a PP3 (9 V) battery connected to the clip lead, and the power switch in the 'on' position, only the discrete decimal point LED should be illuminated. If you have access to a frequency meter you can use the trimmer capacitor to set the frequency of the crystal oscillator to exactly 1 MHz . Otherwise, leave it at about mid position. Now press down the push-button and keep it pressed; nothing should happen. . . Suddenly, the display will illuminate, and start counting at 1,000 counts per second. Having reached this stage, the idea is to let go of the button pretty smartly. The figure will freeze instantly, and continue to display your reaction time in seconds. After about eight seconds the display will disappear, and you may try again; alternatively, pressing the push-button will reset the display immediately for another attempt.

Note that it is impossible to cheat, except by precognition

## HOW IT WORKS

At some random time after push-button PB1 is pressed, the display comes on and commences counting. Releasing PB1 stops the count, freezing the display, which then shows the time elapsed between the end of the random period and the release of SW1. After a short time the display automatically tums off and resets, ready for the next reaction test. The random time period, which will be between $11 / 2$ to 8 seconds, is set up by IC2, a four bit binary counter wired to count down repetitively from $\mathbf{1 5}$ to $\mathbf{0}$. It is clocked at around 2 Hz by the slow oscillator built around IC1a and b, so that a complete count cycle takes about 8 seconds. With PB1 open, the output of IC1d will be high; this holds the output of IC3b in a low state and sets the $Q$ output of IC4a, a NOR latch, to high. Meanwhile IC2 is continuously counting down from 15 to 0 . One or other of $C$ and D, the two most significant digit outputs, will be high when the count is above 3, ie for about $61 / 2$ seconds of the 8 second period. Thus the output of NOR gate IC3a is taken low, enabling IC3b, whenever the count is within that range.

Now, operating PB1 takes IC1d output low, putting a low on the second input (pin 12) of IC3b and the set input of IC4a. With both inputs low, IC3b output will go high; a positive nulse is applied through C4 to the reset input of the latch, allowing the $\mathbf{Q}$ output to go low and sending a CLEAR pulse to reset the counter section. If the IC2 count is less than 4 however, IC3a's output will be high, holding IC3b low so that the latch, with its reset input pulled low by R6, cannot change state. Thus the random time period, which ends when IC2 clocks down to zero some time after PB1 is pushed, cannot be less than a 3 count, ie around $1 \frac{1}{2}$ seconds.

When IC2 reaches 0 , the carry out line (pin 7) goes momentarily low, taking one IC3c input low; the other input (on pin1) is already held low by the $\mathbf{Q}$ output of IC4a. Thus IC3c output goes high and the positive edge through C 5 will reset latch IC4c, so switching the GATE line from the $Q$ output to its active low state. The positive pulse through C5 also triggers the set input of latch IC4b, taking the Q output high which turns on
the display. This is the end of the random period and the start of the reaction timing.

The GATE signal starts the counter-timing section simultaneously with the display illumina tion. The user must now release PB1. As de scribed above the set input of latch IC 4a will then go high, taking the Q output high. This holds the set input of latch IC4c high and so takes the GATE line high again, stopping the count and er ding the reaction timing. The pin 6 input of IC1c is now held high by the Q output of IC4a. Thus, when the IC2 CO line goes low again after a further 8 seconds, IC1c will go high and the positive pulse through C2 and D2 will reset the latch IC4b, taking a $\mathbf{Q}$ output low and turning off the display to conserve power. The latch IC4b can also be reset via D 1 , which steers a positive pulse derived from C3 and R4 to the reset input. C3 is connected to OVERFLOW, the final divideby- 10 output of the counter section, thereby blanking the display when the count reaches 1 second.

The display has three decimal digits following the decimal point, which are driven by IC8,9, and 10. These are integral decade counters and seven segment decoder drivers (4026) which drive common cathode LED displays. Since the readout is in seconds it follows that the clock fre quency for the least significant digit must be 1 kHz . IC 3 d forms a CMOS oscillator, with the frequency set at exactly 1 MHz by the crystal. This output frequency is then divided down to 1 kHz by IC5,6 and 7 (4017 decade counters) and supplies the CLOCK for IC8 (pin 2).

All the pin 15s on ICs 8,9 and 10 are wired together and form the RESET/CLEAR line (active high). Likewise all pin 3 s form the DISPLAY line, a low on these turns off the display. The GATE line goes to pin 2 (the enable) of IC8; when this is taken low the clock is enabled and will start counting. Pin 2 of IC8 and 9 are both wired to ground to permanently enable their clocks. The circuit is powered by one 9 V PP3 battery with an onloff switch SW1 in the negative rail. LED1 indicates when power is on and also marks the decimal point. Capacitors C8 through C12 provide supply decoupling for the ICs.

## PROJECT : Alcohometer

PARTS LIST



Fig. 3 Overlay for the display board. Arrows indicate vertical wire links to the other PCB.


Fig. 4 Overlay for the main PCB. Note the insulated wire links under the board - IC4 pin 5 to IC2 pin 9, and IC4 pin 2 to IC1 pin 6.

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# DESIGNER'S NOTEBOOK 

## The XR2206 is a high-quality function generator chip, capable of producing sine, square, triangle, ramp and pulse waveforms. Ray Marston shows how to use the device in this month's edition of Notebook.



Fig. 1 Block diagram and pin notations of the XR2206 function generator IC.

generator ( 10 Hz to 100 kHz ). See Table 1 for values of C3.

| C3 | FREQUENCY RANGE |
| :--- | :--- |
| 140 | 10 Hz TO 100 Hz |
| 100 n | 100 Hz TO 1 kHz |
| 10 n | 1 kHz TO 10 kHz |
| 1 nO | 10 kHz TO 100 kHz |

Table 1. Values of C3 for different frequency ranges.

The XR2206 integrated circuit is undoubtedly the most useful function generator or waveform generator chip available. It can generate sine, square, triangle, ramp and pulse waveforms at frequencies ranging from a fraction of a hertz to several hundred kilohertz, using a minimum of external circuitry. The frequency can be swept over a 2000:1 range using a single control voltage or resistance, and sine wave distortion can typically be as low as $0.5 \%$. The chip incorporates special built-in modulation facilities that enable the generated waveforms to be subjected to AM or FM control, or to phaseshift or frequency-shift keying.

The XR2206 chip is housed in a standard 16 -pin DIL package and can be powered from either single or split supplies in the range 10 to 26 V . The sine wave output of the device has maximum amplitude of about $2 \mathrm{~V}_{\text {RMS }}$ and output impedance of 600 R . The frequency stability of the $I C$ is excellent, being about 20 $\mathrm{ppm} /{ }^{\circ} \mathrm{C}$ for thermal changes and $.01 \% N$ for supply voltage changes.


Fig. 3 High-performance split-supply sine wave generator. See Table 1 for values of C3.

## Basic Waveform Generators

The XR2206 is a reasonably easy IC to use in basic waveform-generator applications. Figure 2 shows how to connect it for use as a simple widerange sine wave generator that is powered from a single supply source in the range 12 to 18 V . The main timing resistance comprises R3-RV1; it is connected between pins 7 and 12 (ground) and is automatically selected by leaving pin 9 (FSK input) open circuit. The operating frequency can be varied over a decade range (using RV1) with any given
value of C3, as indicated in the diagram. The circuit generates a sine wave output at pin 2 , since a 220 R resistor is wired between pins 13 and 14 of the IC; typically, the sine wave distortion is less than $2.5 \%$ with this simple connection.

In Fig. 2, the voltage to pin 3 is biased at half-supply volts by decoupled divider R1-R2, so the pin 2 sine wave is also biased near half-supply volts. PR1 enables the pin 2 sine wave magnitude to be preset to a value at which distortion (due to clipping) is minimal. To set PR1, first disconnect R4 (so that a triangle output is obtained), then adjust PR1 so that no triangle clipping is visible. Now re-connéct R4 and check that a decent sine wave is available. Sine wave distortion can be reduced below the typical $2.5 \%$ value, if desired, by replacing R4 with a 470R preset and adjusting it for minimum distortion. The final sine wave output of the Fig. 2 circuit can be fully varied by RV2.

The Fig. 2 sine wave generator can be modified for splitsupply operation by replacing all ground connections with negativerail ones and by taking level control PR1 to the common supply (ground) line as shown in Fig. 3. This circuit also shows how the total harmonic distortion (THD) of the sine wave can be reduced to a typical value of $0.5 \%$ with the use of presets PR2 and PR3; these controls must be adjusted alternately to give the best possible sine wave output, after first setting


Fig. 4 Add-on modification for applying limited DC offset or nulling to the output of the Fig. 3 circuit.


Fig. 5 Variable-frequency split-supply triangle wave generator. See Table 1 for values of C3.

PR1 to give a non-clipped triangle waveform as already described.

When using the low-distortion sine wave facility illustrated in Fig. 3, note that the signal appearing on pin 3 of the IC is similar to that of pin 2 but has lower distortion and higher output impedance; also, the pin 3 signal is closely centred on the common or ground line, but the pin 2 signal is offset by a few hundred millivolts. If desired, slight DC offset can be applied to pin 3 , to bring output pin 2 to precisely zero offset value, by using the add-on modification shown in Fig. 4.

The XR2206 can be made to generate linear triangle waveforms by using the basic circuits of Figs. 2 and 3 without the sine-shaping resistors. Figure 5 shows the circuit of a variable-frequency split-supply triangle waveform generator. When used with a $\pm 9 \mathrm{~V}$ supply, the circuit can typically produce ramp signals with maximum peak-to-peak amplitudes of 12 V before clipping occurs.


Fig. 6 Simple fixed-amplitude variable-frequency square wave generator. See Table 1 for values of C3.


Fig. 7 Add-on variableamplitude circuit for use with the square wave generator of Fig. 6.


Fig. 8 Simple split-supply sine/triangle/square wave generator. See Table 1 for values of C3.

The XR2206 can be made to produce fixed-amplitude square wave signals at pin 11, either independently or simultaneously with sine or triangle waveforms, by wiring 4 k 7 load resistor between pins 11 and 4 , as shown in the split-supply circuit of Fig. 6. The rise and fall times of the square wave output signals are typically 250 ns and 50 ns respectively when pin 11 is loaded by 10pF. Figure 7 shows how a simple CMOS inverter stage can be used as a buffer between pin 11 and the final square wave output, to give a variable amplitude with improved rise and fall times.

Naturally, the basic sine, triangle and square wave generator circuits of Figs. 2 to 6 can be combined in a variety of ways to make multi-function waveform generators. Figure 8 for example, shows how various circuits can be combined to make a simple split-supply sine/triangle/square generator. Here, the fixed-amplitude square wave is taken directly from pin 11 of the IC and is produced simultaneously with the variable-amplitude sine or triangle waveforms, which are selected by SW1.

## Pulse And Ramp Generation

All of the circuits that we've looked at so far produce symmetrical output waveforms. The XR2206 can be made to produce non-symmetrical waveforms, such as ramp, sawtooth and pulse waveforms, by shorting the pin 9 FSK terminal to the pin 11 terminal, as shown in Fig. 9. Thus the circuit uses R1-RV1 to time one half of the waveform, and R2-RV2 to time the remaining half of the waveform.

The Fig. 9 circuit produces a variable-amplitude variableslope ramp output waveform from the slider of RV3, and a simultaneous fixed-amplitude pulse or variable mark/space ratio rectangle waveform from pin 11. The rise and fall (or on and off) periods of the waveforms can be independently controlled by RV1 and RV2 and can each be varied over a 100:1 range, giving a total mark/space ratio range of 100:1 to 1:100.


Fig. 9 Variable pulse and ramp generator circuit. See Table 1 for values of C3.

## AM Generation

The amplitude of the pin 2 output signal of the XR2206 can be modulated by applying a DC bias and a modulating signal to pin 1 as shown in Fig. 10. The amplitude of the pin 2 signal varies linearly with the applied voltage on pin 1 when this voltage is within 4 V of the half-supply value of the circuit; in split-supply circuits, of course, the half-supply value equals 0 V . When the pin 1 voltage is reduced below the half-supply value the pin 2


Fig. 10 Add-on AM facility for a split-supply circuit.
signal again rises in direct proportion, but the phase of the output signal is reversed. This last-mentioned phenomenon can be used for phase-shift keyed (PSK) and suppressed carrier AM generation.

The pin 1 terminal of the IC can also be used to facilitate gatekeying or pulsing of the pin 2 output signal. This can be achieved by biasing pin 1 to near half-supply volts to give zero output at pin 2 , and then imposing the gateor pulse signai on pin 1 to raise the pin 2 signal to the desired turn-on amplitude. The total dynamic range of amplitude modulation is 55 dB .

## FM And Frequency-Sweeping

The frequency of oscillation of the XR2206 is proportional to the total timing current $\left(l_{T}\right)$ drawn from pin 7 or 8 and is given by

$$
\mathrm{f}=\frac{320 \times I_{I}}{\mathrm{C}} \mathrm{~Hz}
$$

where $I_{T}$ is in milliamps and $C$ is in microfarads.
The timing terminals (pin 7 and 8) are low-impedance points and are internally biased at 3 V with respect to pin 12. The frequency varies linearly with $l_{1}$ over the current range 1 uA to 3 mA . Consequently, the frequency can be voltage-controlled by applying a voltage in the range 0 to $+3 \vee$ between pin 12 and the timing terminal via a suitable resistor, so that the timing current is determined by the resistor value and the difference between the internal ( +3 V ) and external ( 0 to 3 V ) voltages. This simple technique can be used to either frequency-sweep the generated signals using an externally applied sawtooth waveform, or to frequency-modulate the waveforms with an external signal.


Fig. 11 Frequency-sweep circuit giving a 6:1 frequency range.
Figure 11 shows the basic connections of a simple frequency-sweep circuit with a 6:1 range of frequency coverage. The external sawtooth has a peak amplitude of 2 V 5 : when the amplitude is zero, 3 V is developed across R and the frequency is $1 / R C$, as in the case of a normal resistancecontrolled XR2206 circuit. When the sawtooth is at its peak amplitude of 2 V 5 , only 0 V 5 is developed across $R$ and the frequency falls to $1 / 6 R C$. The frequency is thus determined by the instantaneous value of the sawtooth voltage. The frequency can, in theory, be varied over 2000:1 range by using this simple frequency-sweep technique.

Finally, Fig. 12 shows the basic method of applying FM to the standard XR2206 circuit. Here, the external modulation signal is applied to the junction of Ri-RV1 via blocking capacitor C1.


Fig. 12 Simple FM facility for the XR2206.



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Up until now PCBs were always the hardest component to obtain for a project. Of course you could make your own, but why bother anymore?
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In addition to the PCBs for this month's projects, we are making a vailable some of the more popular designs from our recent past. See the list below for details. Please note that NO OTHER BOARDS ARE AVAILABLE. If it's not listed, we don't have it!

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## PCB Foil Patterns

## PCB FOIL PATTERNS

The foil patterns for the two computer expansion boards are not included because they're too big, copyright, and not many of our readers can make plated through holes! The PCBs are available from Watford Electronics.



Above: Component Tester PCB.

Above: Foil pattern for the Bodywork Checker. Below: The board for the TV Sound Tuner.


The two foil patterns for the Alcohometer.


## DOSSING DOWN?

This feature could be better described as 'one man's fight against the system', or even, 'how not to knuckle under when your DOS dies!' As you may have already guessed this is the story, with the software to prove it, of one individual's desperate fight to replace his old and dying DOS. The system is NASCOM, the routines are universal - you can re-write them into whatever machine code you wish - and the result is superb. So, if your discs are down in the mouth as a result of an unusable DOS, cheer them up with our next issue.

## TRIED AND TRUSTED

Many of the original breed of personal computers have been slowly upgraded or replaced over the years. Not so the Exidy Sorcerer - despite a rather bleak period it's still with us. Continuing our series of re-reviews of popular machines we take a long look at this grand old system through the eyes of a family of dedicated users:

## TECHNOLOGY TAKES OVER

Over the next 12 months you are going to hear an awful lot about Information Technology, what IT is, what IT does and how IT is going to affect your lives. Information Technology is already here and working. In this issue we've spoken about the Teletext system, and next month we'll be going over the inner workings of the Prestel system, Britain's leading example of IT. Prepare yourself for the next year - order next month's issue today.

## AND THE REST

As if the above were not enough to tempt you, the next issue will also contain a full digital storage 'scope simulator for the classroom, routines to explain how computers crunch numbers, a simple statistics calculator, programs to pack your data tapes more thoroughly and all the usual features that you expect to see each month. A bumper bundle and all for less than the cost of a couple of pints!


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## We've got lots of projects to interest musicians next month:

## Drum Synthesiser

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This project was designed to match the HE Electronic Organ (see HE May to August 1981). It's a 13 -note, free-standing, foot-operated pedalboard (phew - what a mouthful), which can be plugged into the same amplifier as your organ, or it can be used with its own internal amplifier.

Now, although it's primarily intended to complement our organ, you can, of course, use it to accompany yourself while you play any other instrument. Thus you can have bass accompaniment to say, a guitar, flute, piano, or even the HE Drum Synthesiser.

## Car Electronics

There's no doubt that, although car manufacturers, overall, tend to be slow to change their ideas about the equipment that goes into their cars, they are at last waking up to the fact that electronics has a large part to play.

Guest writer Bill Mitchell tells you about the possibilities and probabilities of in-car electronics.


## Guitar Graphic Equaliser

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|  | $7 \times 15$ | ${ }^{22}+2{ }^{2}$ | 682 |  |  |
|  | $7 \times 15$ | 75 | 600 |  |  |
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|  | $7 \times 033$ |  | 300 |  |  |
|  | $7 \times 028$ | 115 | 272 |  |  |
|  | 70208 | 220 | 136 135 |  |  |
| $\begin{aligned} & 500 \mathrm{Vh} \\ & 300 \times 50 \mathrm{~mm} \\ & 6 \mathrm{~kg} \\ & \text { regulation } \end{aligned}$ | $8 \times 016$ | $25+25$ | 1000 | 1818 | $\underline{1553}$ |
|  | $8 \times 017$ | $30+30$ | 833 | +E20s | - 52 Ck |
|  | $8 \times 018$ | $35+35$ | 714 | P/P | P/P |
|  | $8 \times 026$ | $10+40$ | 675 |  |  |
|  | 8×023 | ${ }^{4} 5+45$ | 555 |  |  |
|  | ${ }^{1} \times 13$ | $5 \mathrm{~S}+50$ | $5 \infty$ |  |  |
|  | $8 \times 042$ | $55+55$ | 454 |  |  |
|  | ax028 | 110 | 154 |  |  |
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Star Trek Classic computer ame, rid ho universe cans, galactic mand long-range scans, galactic map, phasers. Program 5 K , graphice 2 K Pour Row $T$, graphics $2 K$. Four Row Take turns in plecing to get a line of four wins. Prooram 5 K , graphics 6 K COLOUR
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| Mocel No. | Oulpul pover Watls rms | $\begin{aligned} & \text { UISTO } \\ & \text { T.H.D } \\ & \text { TyD } \\ & \text { at } 1 \mathrm{kHz} \mathrm{~Hz} \end{aligned}$ | $\begin{gathered} \hline \text { ORTION } \\ \text { I.M.D. } \\ 50 \mathrm{~Hz} / 7 \mathrm{xH} \mathrm{~Hz} \\ 4.1 \end{gathered}$ |  | Size mm | $\begin{gathered} W \\ g \pi s \end{gathered}$ | $\begin{aligned} & \text { Price } \\ & \text { inc. VAT } \end{aligned}$ | $\begin{gathered} \text { Price } \\ \text { ex VAT } \end{gathered}$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| HY 30 | 15w/4.8! | 0.015\% | <0.006\% | $\pm 18 \pm 20$ | $76 \times 68 \times 40$ | 240 | £8 28 | £7.29 |
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| HY 200 | 120w/4-89 | 0 01\% | <0.006\% | $\pm 45 \pm 50$ | $120 \times 78 \times 50$ | 515 | £24.39 | £21.21 |
| HY 400 | 240w/4ת | $001 \%$ | <0.006\% | $\pm 45 \pm 50$ | $120 \times 78 \times 100$ | 1025 | ¢36.60 | £31.83 |

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| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | HY 200 F ( $120 \mathrm{~W} / 488 \quad 0.01 \%<0.006 \% \quad \pm 45 \pm 50 \quad 120 \times 26 \times 40$ 215 | HY 400 P | $240 \mathrm{w} / 452$ | $0.01 \%$ | $<0.006 \%$ | $\pm 45 \div 50$ | $120 \times 26 \times 70$ | 375 | $£ 32.58$ | $£ 28.33$ |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |

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| Mocel No. | Outpur power Watts m m | DISTORTION |  | $\begin{array}{\|c\|} \hline \\ \text { Supply } \\ \text { votage } \\ \text { Typ/Max } \end{array}$ | Size mm | $\begin{gathered} \mathrm{m} \\ \mathrm{gms} \end{gathered}$ | $\begin{aligned} & \text { Prose } \\ & \text { inc. VAT } \end{aligned}$ | $\begin{aligned} & \text { Price } \\ & \text { ex. VAT } \end{aligned}$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | $\begin{aligned} & \text { T.H.D } \\ & \text { Typ } \\ & \text { at } 1 \mathrm{kHz} \end{aligned}$ | $\begin{gathered} \text { 1.M. D. } \\ 50 \mathrm{~Hz} / 7 \mathrm{kHz} \\ 4.1 \end{gathered}$ |  |  |  |  |  |
| HD 120 | 60w/4-8\% | 0.01\% | <0.006\% | $\pm 35 \pm 40$ | $120 \times 78 \times 50$ | 515 | $\underline{25.85}$ | £22.48 |
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    $120 \mathrm{E}=140: \mathrm{F}=60: \mathrm{N}=16: X=0: Y=10$
    129 REM "•DRAWHEADANDBODY
    130 FOR I = 1 TO N
    $\left.140 \times 1=10^{\circ} \operatorname{COS}\left(46.283^{\circ} \|\right) / \mathrm{N}\right)$
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    160 HPLOT $X+E, Y+F$ TO X1 $+E, Y 1+F$
    $170 X=X 1: Y=Y 1$
    180 NEXT I
    $190 X=0 ; Y=10$
    200 HPLOT E, F + Y TO E, F $+Y+40$
    $210 G=F+Y+10: H=F+Y+40$
    219 REM *'ORAW ARMSAND LEGS
    $220 \mathrm{Y}=0$
    230 FOR K $=1$ TO 2
    $240 \mathrm{~W}=2^{*}(\mathrm{~K}-1.5)$
    250 HPLOT E, G TO E $\div 20^{*} \mathrm{~W}$, G
    260 HPLOTE, H TO E $+20^{\circ} \mathrm{W}, \mathrm{H}+20$
    270 HPLOT $\mathrm{E}+20^{\circ} \mathrm{W}, \mathrm{H}+20$ TO $\mathrm{E}+20^{\circ} \mathrm{W}+7, \mathrm{H}+20-7^{\circ} \mathrm{W}$
    280 NEXTK
    $290 Y=3-Y$
    300 HCOLOR $=Y$
    310 FORK $=1$ TO 2
    $320 \mathrm{~W}=2^{*}(\mathrm{~K}-1.5)$
    330 HPLOT E, G TO E $+20^{*} \mathrm{~W}, \mathrm{G}+20$
    340 HPLOT E, H TOE $+10^{*}$ W, $\mathrm{H}+25$
    350 HPLOT $\mathrm{E}+10^{\circ} \mathrm{W}, \mathrm{H}+25$ TO $\mathrm{E}+10^{*} \mathrm{~W} \div 10, \mathrm{H} \div 25$
    360 NEXT K
    370 FOR M = 1 TO 100:NEXT M
    380 GOTO 230
    390 END
    Program 1. An Applesoft cartoon.

    100 CLS
    110 DIM A(59), B(59)
    $120 \mathrm{FORI}=1$ TO 59
    130 READ AIII
    140 NEXT
    150 FOR I = 1 TO 59
    160 READ BIII
    170 NEXT I
    180 FOR I = 1 TO 19
    190 SET (A(I), B(1))
    200 NEXT I
    210 FOR I = 20 TO 39
    $220 \operatorname{SET}$ (A(1), B(1))
    230 RESET (All +20$), \mathrm{B}(I+20)$ )
    240 NEXT |
    250 FOR M $=1$ TO 50:NEXTM
    260 FOR I = 20 TO 39
    270. RESET (AI), B(11)

    280 SET $(A(1+20), B(1+20))$
    290 NEXTI
    300 FOR M $=1$ TO 50:NEXT M
    310 GOTO 180
    320 DATA $64,68,70,72,70,68,64,60,57$
    330 DATA $56,57,60,64,64,64,64,64,64$
    340 DATA $64,66,69,75,81,62,59,53,47$
    350 DATA $67,71,75,80,81,82,61,57,53$
    360 DATA 48,49,50,66,68,70,72,62,60
    370 DATA $58,56,66,68,70,72,74,76,62$
    380 DATA $60,58,56,58,60$
    390 DATA $19,18,17,15,13,12,11,12,13$
    400 DATA $15,17,18,21,25,25,27,29,31$
    410 DATA $33,22,22,22,22,22,22,22,22$
    420 DATA $33,34,35,37,36,35,33,34,35$
    430 DATA $37,38,39,24,26,28,30,24,26$
    440 DATA $28,30,34,36,38,40,40,40,34$
    450 DATA $36,38,40,40,40$
    460 END
    Program 2. A similar program to Program 1, but written for the TRS80IVideo Genie.

[^3]:    Prices shown exclude VAT. Postage
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