Quartus/Altera FPGA Design Simulation

80 Candles for the Pentode

Satnav with Propeller

5532 Power Amplifier

20 AC/DC clamp meters on the test bench

S-FSK Powerline Communication
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The reader/advertiser promise

If you are not in the electronics industry, or without a formal qualification in the field, it may be good to realise that electronics as a pastime would be non-existent without the wide diversity of manufacturers, distributors and suppliers of equipment, tools, components and CAD software — the friendly ones of course.

Although some readers have expressed a desire to see a version of Elektor without any advertising pages, short discussions on the phone or by email are usually sufficient to agree that their wish is unrealistic. For one thing, our advertisers make up for a good part of our income, the other main pillars being subscribers, newsstand sales and product sales. On the other hand, from a reader viewpoint, advertisers are the gateway to services and products (including components and tools) that are essential to enjoy electronics as a pastime, or pursue it at any level in education.

Sure, old hands with a fully loaded electronics workshop may object to seeing basically the same advertisers for years on end. But then, when I was learning about electronics at a young age, I believe I got as much information and fun out of browsing adverts, comparing component prices and seeing the big names in industry, than from actually reading articles, trying to understand it all and building circuits in front of me.

I am happy to say that Elektor is attracting an increasing number of companies, big and small, advertiser or not, wishing to run projects with us. Recently, ‘projects’ has taken on a wider meaning in also covering reader offers and contests. Together with our American colleagues at Circuit Cellar, we’re officially launching the NXP mbed Design Challenge at Embedded Systems Conference in Boston, USA on September 20–23. Invariably, reader offers and contests take a lot of time and effort to arrange exclusively for you — or should I say “for your pleasure”. More is in the pipeline.

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Elektor International Media provides a multimedia and interactive platform for everyone interested in electronics. From professionals passionate about their work to enthusiasts with professional ambitions. From beginner to diehard, from student to lecturer. Information, education, inspiration and entertainment. Analogue and digital; practical and theoretical; software and hardware.
The Elektor Personal Organizer 2011 makes planning your appointments a real pleasure, and you always have ready access to have handy information that everyone who works with electronics needs to know.

The Organizer 2011 at a glance:
- 2011 calendar (two pages per week)
- Appointments calendar (with corner perforations) in six languages
- 60 pages of technical information on electronics
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**Enhanced range of USB 2.0 to serial UART converter cables**

Future Technology Devices International Limited (FTDI) have announced the availability of their TTL-232RG family of USB to TTL serial UART converter cables. The TTL-232RG cables build upon the existing FTDI family of USB to TTL cables, by offering new versions to support an extended variety of voltage I/O levels. The cables feature a USB to serial converter PCB encapsulated within a standard type ‘A’ USB connector with a wire-ended asynchronous UART output. The cables are aimed at providing USB connectivity within applications with serial UART ports. The cables provide a fast and simple method for enabling USB connectivity in such applications, with minimal changes to existing user software.

A range of cables are available to for a variety of voltage I/O levels including +1.8 V, +3.3 V, +5.0 V or at user specified levels, to provide UART interfacing to devices such as MCUs, CPUs or FPGAs at a range of voltage levels. The cables derive power from the USB interface and have integrated voltage regulators removing the need for designers to provide external power or have voltage level shifters on their boards. Further, the cables can be used to provide an optional power output ranging between +1.8 V and +5.0 V for powering external circuitry. The TTL-232RG cables feature the FTDI FT232R USB 2.0 to UART converter IC with associated circuitry integrated within the cable USB connector. The FT232R manages the complete USB protocol within the device — meaning that no user knowledge of USB is required. The UART interface supports data transfers at up to 3 Mbps. Using the FTDI’s Virtual COM Port (VCP) drivers, users can easily access the UART interface as a (virtual) COM port with existing software applications, removing the need for any redesign. The FTDI D2XX drivers are also available to support application development using high-level software languages. FTDI’s royalty free drivers include Microsoft WHQL certified drivers for Window based operating systems, as well as drivers for Linux and Mac OS operating systems. All drivers are freely available for download from www.ftdichip.com/FTDrivers.htm. All cables are FCC/CE compliant. The range of USB to UART cables includes:

- TTL-232RG-VREG3V3-WE: USB cable with wire-ended UART connections at +3.3V voltage levels and +3.3 V / 250mA rated power output. Available now. Pricing $24.64 for single quantity.
- TTL-232RG-VREG1V8-WE: USB cable with wire-ended UART connections at +1.8 V voltage levels and +1.8 V / 100 mA rated power output. Available now. Pricing $24.64 for single quantity.
- TTL-232RG-VSW5V5-WE: USB cable with wire-ended UART connections at +5 V voltage levels and +5 V / 450 mA rated power output. Quantities available in August.

**Silabs: wireless remote control on a chip**

Silicon Laboratories Inc. have introduced an EZRadio® wireless IC solution designed to reduce the cost and complexity of one-way wireless links used in a wide range of consumer, industrial and automotive systems. The new Si4010 system-on-chip (SoC) RF transmitter enables developers to optimize remote keyless entry (RKE), garage door opener, remote control, building automation and security device designs for the lowest system cost and highest performance while ensuring one-way link integrity. The Si4010 RF transmitter is the industry’s first single-chip remote control IC requiring only one external bypass capacitor, a printed circuit board, battery and external case with pushbuttons to create a complete wireless remote control. Based on a patented crystal-less architecture, the Si4010 achieves ±150 ppm carrier frequency accuracy over the commercial temperature range and ±250 ppm over the industrial temperature range — twice the accuracy of traditional surface acoustical wave (SAW)-based transmitters — without using an external crystal.

The Si4010 transmitter SoC is best paired with Silicon Labs’ new Si431x RF receivers to enable a transmitter/receiver solution that substantially reduces the total bill of materials (BOM) and saves valuable board space for one-way link systems operating in the sub-GHz range (from 27 to 960 MHz). The Si431x receivers’ state-of-the-art integration requires only two external antenna-matching components, a crystal and a bypass capacitor while eliminating the need for costly RF SAW and IF ceramic filters.

The Si4010 is the first SoC transmitter with automatic antenna tuning, featuring a patented tuning circuit that automatically fine tunes the antenna for optimum transmit efficiency and constant output power. Variations in transmit frequency due to PCB loop antenna manufacturing tolerances and environmental variations can lead to significant antenna inefficiencies and wasted power. The Si4010’s antenna tuning circuit continuously maximizes antenna efficiency by adjusting an on-chip variable capacitor to resonate with the antenna’s inductance. The Si4010 supports programmable edge rate control for on-off keying (OOK) mode to reduce harmonic emissions and comply with governmental RF regulations. It also outperforms competing discrete solutions, offering ±10 dBm output power, exceptional range and robust links.

The Si4010 contains an embedded 8051-compatible MCU core with 4 kB of RAM, 8 kB of one-time programmable (OTP) non-volatile memory, a 128-bit EEPROM and 12 kB of ROM for library functions. These ROM-based functions enable developers to easily implement complex
features such as security encryption into their remote controls by using proven code to reduce risk and development time. The MCU's on-chip digital peripherals include wake-on-touch general-purpose I/Os (GPIOs), a patented 20-bit EEPROM counter providing one million cycles of read/write endurance, an LED driver, sleep timers, a debugger and a high-speed 128-bit Advanced Encryption Standard (AES) accelerator for secure one-way links. To accelerate the development of one-way link products, Silicon Labs offers a rich set of hardware and software tools including the 4010-DASKF_434 evaluation kit to demonstrate RKE fob functionality, the 4010-DAAKF_434 evaluation kit to demonstrate AES encryption functionality in RKE fobs, and the 4010-DKRF_434 development kit providing a comprehensive Si4010 software development environment.

Well kitted amplifier

Piccolo is a new 50 watt amplifier kitset from Italian kitset specialist APPSI using the famous LM3886 amplifier chip to create a great sounding, easy to build amplifier. Features that make the piccolo great sounding and easy to build include low negative feedback; most of the existing ‘gainclone’ kitsets “borrow” from the design of the almost mythical Japanese ‘47 Lab’ amplifiers which used the LM3875. While using roughly the same circuit, the Piccolo uses less negative feedback which makes it unconditionally stable. This contributes to its sound having a great sense of ‘ease’. Yet retaining low distortion (0.03%) and generous bandwidth. The Piccolo’s power supply is integrated in each 90x90mm (approx. 4” sq) mono-block module. As the power supply of any amplifier is actually part of the circuit, integrating it can have large benefits in keeping the circuit short and noise to a minimum. Small power supply capacitors are paralleled to give better performance than single large ones. While these modules can be built in to the dedicated ezPower™ chassis to form a stereo amplifier, they are ideal for active loudspeakers and their small size is great for quality multi channel systems. The amplifier design cuts down on the wiring and possibility for mistakes. Piccolo manages to provide excellent performance with remarkably few components. The only extra electrical component you need is a transformer. The build is made even easier as the PCB slots straight into the 50 mm high ezPower™ heatsink chassis from New Zealand company Design Build Listen who also distribute the kits, meaning no metal working is needed.

Xbee SIP adapter

Parallax’s new Xbee SIP Adapter (#32402; $24.99) is a fully-assembled, small-footprint solution for interfacing the most commonly-used Xbee module features with your 3.3 V or 5 V microcontrollers. With many Xbee adapters on the market, Parallax gave this design very careful consideration to the specification to customize the product for educational and hobby uses. The 2 x 5 dual SIP header makes a sturdy connection to your breadboard or through-hole board, and brings the basic connections to your prototyping area without taking up a lot of space. The more advanced Xbee features are still accessible, through additional headers on the module. Features:

• Onboard 3.3 V regulator
• 5 V to 3.3 V logic translator buffers common I/O pins
• Six status indicator LEDs for Power, Tx, Rx, RSSI, Associate and mode (Sleep/ON)
• Small footprint dual SIP header provides support and allows easy interfacing to DOUT (TX), DIN (RX), RTS, 5 V supply and ground
• 5-pin female header connections provides interfacing to other Xbee pins such as sleep, reset and associate
• A row of 10 plated through-holes with 0.1” spacing allows the option of soldering jumper wires or a header (not included) for access to the remaining Xbee pins in advanced applications
• An additional plated through-hole gives access to 3.3 V output for ADC reference (VREF) when required
• Adapter board is pre-assembled — no soldering required for using most common Xbee features
• Compatible with all Parallax microcontrollers, including the 5 V BASIC Stamp modules and 3.3 V Propeller P8X32A

www.parallax.com
(100639-1)
New function image supports NXDNB design

CML Microcircuits has announced the release of a new Function Image for the CMX7131 and CMX7141 ICs supporting the development of NXDN compatible digital voice radio equipment. Called NXDN, its common air interface (CAI) is a result of a joint development between Kenwood Corporation and ICOM Incorporated. NXDN digital radio equipment supports commercial Private Land Mobile Radio (PLMR/LMR) to serve light commercial, more demanding business and industry, and public safety applications, even on 6.25 kHz channels. The technology satisfies the US Federal Communications Commission (FCC) Part 90 'narrowbanding' requirements; as of December 2009, 70,000 RF channel licenses were approved for NXDN operation. CML is committed to the promotion, support and development of this exciting new radio technology.

The CMX7131 and CMX7141 are digital PMR processors built on CML's proprietary FirmASIC technology and are suitable for use in a number of digital radio systems now including NXDN. FirmASIC component technology enables on-chip sub-systems to be configured by a Function Image, a data file that is uploaded during device initialisation that defines the device's function and feature set. This technology provides the flexibility for the CMX7131 and CMX7141 to support dPMR, DCR and NXDN radios as well as analogue two-way radios.

The new Function Image developed by CML provides a number of features very important to NXDN design, including: a high performance 4FSK data pump supporting soft-decision coding mode (which improves the reliability of the data link) and integrated RRC filters; a high performance, autonomous AFSD that greatly simplifies host microcontroller development and risk, differentiating between frame sync cases, which can also reduce an NXDN receiver's average Rx power consumption; a voice codec (ADC and DAC) for microphone/speaker paths to complement an external AMBE-3000 vocoder device and integrated auxiliary functions that reduce total BOM cost and size.

As an aside, the Chairman of the NXDN Forum recently announced that the NXDN Forum and the dPMR MoU have agreed to collaborate on issues of common interest in the promotion of 6.25 kHz FDMA technology. CML has made available, on it's website, a downloadable brochure describing ETSI's role in Digital Private Mobile Radio (dPMR) available in English, French, Italian and Spanish.

www.cmlmicro.com www.nxdn-forum.com (100639-IV)

Mobile lighting controller

The AS3676 from austriamicrosystems is a highly Integrated Lighting Management Unit (LMU) with Ambient Light Sensing (ALS) and Dynamic Luminance Scaling (DLS). The chip integrates 13 current sinks, a high-efficiency step-up DC-DC converter and high-power charge-pump, an analog-to-digital converter (ADC) and a programmable low drop-out regulator (LDO), all incorporated with advanced algorithms for high-efficiency display and keypad backlight, fulfilight, smartlight and other advanced portable illumination and indication effects.

The integration of the main illumination features facilitates controlling all mobile lighting with a single chip. This simplifies hardware and software development time and shortens time to market. The features and flexibility of the AS3676 make it well suited for a number of portable consumer electronic products, including mobile phones, mobile TV's, mobile DVD players, mobile GPS devices, and MP3 players.

With both ALS and DLS supported by the AS3676, power conservation through automatic adjustment of backlighting and other LED functions is greatly simplified. In addition, built-in features like automatic dimming, pattern generation and audio sync function enable eye catching visual effects with minimum load on the baseband processor. The AS3676 is available in a CS-WLP30, 3x2.5 mm package. It operates over a wide 3.0 to 5.5 V supply range, and over a temperature range of −30 to +85 °C.

www.austriamicrosystems.com/MLU/AS3676 (100639-III)
Motor Drivers/Controllers

Here are just a few of our controller and driver modules for AC, DC, Unipolar/Bipolar stepper motors and servo motors. See website for full range and details.

Computer Controlled / Standalone Unipolar Stepper Motor Driver

Drives any 5-35Vdc 5, 6 or 8-lead unipolar stepper motor rated up to 6 Amps. Provides speed and direction control. Operates in stand-alone or PC-controlled mode for CNC use. Connect up to six 3179 driver boards to a single parallel port. Board supply: 9Vdc; PCB: 80x50mm. Kit Order Code: 3179KT - £15.95
Assembled Order Code: AS3179 - £22.95

Computer Controlled Bi-Polar Stepper Motor Driver

Drive any 5-50Vdc, 5 Amp bi-polar stepper motor using externally supplied 5V levels for STEP and DI-RECTION control. Opto-isolated inputs make it ideal for CNC applications using a PC running suitable software. Board supply: 8-30Vdc; PCB: 75x85mm. Kit Order Code: 3158KT - £23.95
Assembled Order Code: AS3158 - £33.95

Bi-Directional DC Motor Controller (v2)

Controls the speed of most common DC motors (rated up to 32Vdc, 10A) in both the forward and reverse direction. The range of control is from fully OFF to fully ON in both directions. and the speed are controlled using a single potentiometer. Screw terminal block for connections. Kit Order Code: 3166v2KT - £22.95
Assembled Order Code: AS3166v2 - £32.95

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Drivers are available in kit form (KT suffix) or assembled and ready for use (AS prefix).

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Here are just a few of the controller and data acquisition and control units we have. See website for full details. Suitable for all units: Order Code PSU445 £7.95

8-Ch Serial Isolated I/O Relay Module

Computer controlled 8-channel relay board, 5A mains rated relay outputs, 4 isolated digital inputs. Useful in a variety of control and sensing applications. Controlled via serial port for programming (using our new Windows interface, terminal emulator or batch files). Includes plastic case 130x100x30mm Power Supply: 12Vdc/500mA.

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Assembled Order Code: AS3108 - £84.95

Computer Temperature Data Logger

4-channel temperature logger for serial port. °C or °F. Continuously logs up to 4 separate sensors located 200m+ from board. Wide range of free software applications for storing/using data. PCB just 45x45mm. Powered by PC. Includes one DS1820 sensor.

Kit Order Code: 3145KT - £19.95
Assembled Order Code: AS3145 - £26.95
Additional DS1820 Sensors - £3.95 each

Rolling Code 4-Channel UHF Remote

State-of-the-Art. High security 4 channels. Momentary or latching relay output. Range up to 40m. Up to 15 Tx's can be learnt by one Rx (kit includes one Tx but more available separately). 4 indicator LED’s. Rx: PCB 77x85mm, 12Vdc/8mA (standby). Two and Ten channel versions also available.

Kit Order Code: 3180KT - £49.95
Assembled Order Code: AS3180 - £59.95

DTMF Telephone Relay Switcher

Call your phone number using a DTMF phone from anywhere in the world and remotely turn on/off any of the 4 relays as desired. User settable Security Password, Anti-Tamper, Rings to Answer, Auto Hang-up and Lockout. Includes plastic case. Not BT approved. 130x110x30mm. Power: 12Vdc.

Kit Order Code: 3140KT - £74.95
Assembled Order Code: AS3140 - £89.95

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Individually control 12 on-board relays with included infrared remote control unit. Toggle or momentary. 15m+ range. 112x122mm. Supply: 12Vdc/0.5A

Kit Order Code: 3142KT - £59.95
Assembled Order Code: AS3142 - £69.95

New! 4-Channel Serial Port Temperature Monitor & Controller Relay Board

4 channel computer serial port temperature monitor and relay controller with four inputs for Dallas DS18S20 or DS18B20 digital thermometer sensors (£3.95 each). Four 5A rated relay channels provide output control. Relays are independent of sensor channels, allowing flexibility to setup the linkage in any way you choose. Commands for reading temperature and relay control sent via the RS232 interface using simple text strings. Control using a simple terminal / comms program (Windows HyperTerminal) or our free Windows application software.

Kit Order Code: 3190KT - £69.95

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We have a wide range of low cost PIC and ATMEG Programmers. Complete range and documentation available from our web site.

Programmer Accessories: 40-pin Wide ZIF socket (ZIF40W) £14.95 18Vdc Power supply (PSU120) £19.95 Leads: Serial (LDC441) £3.95 / USB (LDC642) £2.95

USB & Serial Port PIC Programmer

USB/Serial connection. Header cable for ICSP. Free Windows XP software, Wide range of supported PICs - see website for complete listing. ZIF Socket/USB lead not included. Supply: 16-18Vdc.

Kit Order Code: 3149EKT - £49.95
Assembled Order Code: AS3149E - £59.95

USB ‘All-Flash’ PIC Programmer

USB PIC programmer for all ‘Flash’ devices. No external power supply making it truly portable. Supplied with box and Windows Software, ZIF Socket and USB lead not included.

Assembled Order Code: AS3128 - £49.95

See website for full range of PIC & ATMEL Programmers and development tools.
**Handheld spectrum analyzer with frequency coverage up to 43 GHz**

Anritsu Company introduces the MS272xC Spectrum Master series that provides the broadest frequency range ever available in a handheld spectrum analyzer. Providing frequency coverage up to 43 GHz in an instrument that weighs less than 8 lbs., the MS272xC series is also designed with an assortment of applications to test the RF physical layer, making it easier than ever for field technicians, monitoring agencies and engineers to monitor over-the-air signals, locate interferers, and detect hidden transmitters.

The MS272xC Spectrum Master is a vastly superior product compared to existing handheld and benchtop spectrum analyzers. To further lighten the load, the MS272xC Spectrum Master is integrated with a spectrum analyzer, and can be ordered with a channel scanner and interference analyzer to conduct all common field measurements, eliminating the need for multiple instruments.

A number of 3G/4G options can be easily incorporated into the handheld spectrum analyzers to measure LTE, HSPA+, W-CDMA, CDMA/EV-DO, GSM/EDGE, TD-SCDMA/HSDPA, and WiMAX signals.

Users can monitor and measure the spectrum quickly, as the MS2726C Spectrum Master has a fast sweep time of 27 seconds for a 43 GHz span with a 30 kHz RBW. Accuracy is not sacrificed for speed, however, as the MS2726C delivers excellent phase noise of -100 dBc/Hz at 10 kHz offset at 1 GHz and dynamic range of 104 dB. The MS272xC series utilizes the field-proven Spectrum Master design. A rugged housing can withstand the day-to-day operations associated with field use and its light weight makes it easy to carry the MS272xC analyzer up towers. It has a field-replaceable long-life battery and wide operating temperature of -10 to +55 degrees celsius. A large daylight viewable display makes it easy for users to see results in any environment. Display modes include a red night vision mode a black and white mode as well as two full color modes.

www.anritsu.com (100693-V)
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- Computerised LED Christmas light
- Programmable LED lamp

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The most popular dual opamp in the world of audio is the (NE)5532. An interesting power amplifier can be made by connecting enough 5532s in parallel, how about 32 for a start? This may sound like a radical course of action, but it actually works very well, making it possible to build a very simple amplifier that retains not only the excellent linearity but also the power-supply rejection and the inbuilt overload protection of the 5532, which reduces the external circuitry required to a minimum.

While not exactly a brand-new design, the type (NE)5532 dual operational amplifier (opamp) is a very capable device giving low distortion with good load-driving capabilities, and a remarkably good noise performance. It is only quite recently that better opamps for audio work have become available. While these can give truly outstanding results, the cheapest of them costs ten times more than the 5532, which is available at a remarkably low price — in fact it is one of the cheapest opamps, because it is so widely used in audio applications.

It should be mentioned at once that the obvious limitation with using opamps to drive loudspeakers is that the output voltage swing is limited compared with a conventional power amplifier, and using a single-ended array of 5532s will give about 15 $W_{\text{rms}}$ into 8 Ω. This output can be greatly extended by using two such amplifiers in bridge mode; one amplifier is driven with an inverted input signal so the voltage difference between the two amplifier outputs will be doubled, and the power output is quadrupled to about 60 $W_{\text{rms}}$ into 8 Ω. This should be enough for most domestic hi-fi situations.

The other unalterable limit set by the opamps is the maximum output current, set by the internal overload protection. A single 5532 section (one half of the dual package)
will drive 500 Ω to the full voltage output, though it is advisable to keep the loading lighter than this to maintain low distortion at high levels. If 4 Ω operation is required, twice as many opamps must be used to supply the doubled current demand. This also applies to bridged operation into 8 Ω. The system is designed so that either single-ended or bridged operation can be used; the basic design described here gives a working stereo amplifier with just three PCBs. The amplifier cards can be paralleled without problems, and facilities are provided to connect more PCBs in parallel for driving low-impedance speakers.

Overload protection is inherent in the opamps, but output relays are used for on/off muting and to protect loudspeakers against a DC fault.

A tour of the design
The schematic in Figure 1 shows one channel of the complete amplifier, which consists of unbalanced and balanced line inputs, and the power amplifier itself, which is divided into a +22.7 dB gain stage and an array of paralleled output opamps configured as voltage-followers, giving the maximum amount of negative feedback around them to minimise distortion. Let’s have a look at the various sections of the circuit.

The unbalanced input
This consists simply of RF filter R1, C1 and DC-drain R2, which are directly connected to the gain stage when JP1 is in the ‘unbalanced’ position.

The balanced input
This amplifier is an innovative design that gives very low noise. The conventional balanced input stage built with four 10 kΩ resistors and a 5532 opamp has a far worse noise performance than a simple unbalanced input, and is also much noisier than most power amplifiers; output noise is approximately -104 dBu. This balanced amplifier here solves this problem partly by the use of a dual balanced stage (IC5A, IC5B) amplifier that partially cancels the uncorrelated noise from each amplifier, giving a 3 dB noise reduction, and in a similar way improves the CMRR; it also uses much lower resistor values than usual (820 Ω instead of 10 kΩ) which produces less Johnson noise in the first place. This is only possible because it is driven by unity-gain buffers IC4A, IC4B, which also allow the input impedances to be much higher than usual, preventing loading of external equipment and further improving the CMRR. The noise output is less than -112 dBu, an 8 dB improvement over conventional technology.

The gain stage
The main input amplifier is another innovative design that achieves very low distortion by spreading the gain required over three stages. +22.7 dB could easily be obtained with one opamp but 5532s are not

---

**Specification — per channel, 8 ohm load**

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Specification</th>
</tr>
</thead>
<tbody>
<tr>
<td>Supply voltage</td>
<td>±18.3 V</td>
</tr>
<tr>
<td>Input sensitivity</td>
<td></td>
</tr>
<tr>
<td>- unbalanced</td>
<td>840 mV (16 W, 1 % THD)</td>
</tr>
<tr>
<td>- balanced</td>
<td>833 mV (16 W, 1 % THD)</td>
</tr>
<tr>
<td>Input impedance</td>
<td></td>
</tr>
<tr>
<td>- unbalanced</td>
<td>38.8 kΩ</td>
</tr>
<tr>
<td>- balanced</td>
<td>93.6 kΩ</td>
</tr>
<tr>
<td>Output power, sinewave</td>
<td></td>
</tr>
<tr>
<td>- 0.1 % THD</td>
<td>16 W</td>
</tr>
<tr>
<td>- 1 % THD</td>
<td>16.8 W</td>
</tr>
<tr>
<td>Output power bandwidth</td>
<td>1.5 Hz – 275 kHz</td>
</tr>
<tr>
<td>Slew rate</td>
<td>5 V/μs</td>
</tr>
<tr>
<td>Rise time</td>
<td>4 μs</td>
</tr>
<tr>
<td>Signal/noise ratio</td>
<td>(1 W ref.) 110 dBA</td>
</tr>
<tr>
<td>Harmonic distortion + noise</td>
<td></td>
</tr>
<tr>
<td>- 0.0005% (B = 22 kHz, 1 kHz, 1 W)</td>
<td></td>
</tr>
<tr>
<td>- 0.0009% (B = 80 kHz, 1 kHz, 1 W)</td>
<td></td>
</tr>
<tr>
<td>- 0.0004% (B = 22 kHz, 1 kHz, 8 W)</td>
<td></td>
</tr>
<tr>
<td>- 0.0005% (B = 80 kHz, 1 kHz, 8 W)</td>
<td></td>
</tr>
<tr>
<td>- 0.003 % (B = 80 kHz, 20 kHz, 8 W)</td>
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<tr>
<td>Intermodulation distortion</td>
<td>-0.0012% (1 W)</td>
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<tr>
<td>- (50 Hz : 7 kHz – 4 : 1) 0.0015% (8 W)</td>
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<tr>
<td>Dynamic IM distortion</td>
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<tr>
<td>- (3.15 kHz square wave + 15 kHz sine wave) 0.0035% (8 W)</td>
<td></td>
</tr>
<tr>
<td>Damping factor</td>
<td>-194 (1 kHz)</td>
</tr>
<tr>
<td>- 111 (20 kHz)</td>
<td></td>
</tr>
<tr>
<td>DC-protection</td>
<td>±1.5 V</td>
</tr>
<tr>
<td>Quiescent current</td>
<td>300 mA</td>
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</tbody>
</table>
Figure 1. Power is indeed in numbers: circuit diagram of the basic NE5532 audio amplifier (one channel shown).
completely distortion-free, and the THD would be significant. The first stage (IC1A, IC1B) gives +10.7 dB of gain; the two outputs are combined by R8, R9 to give a 3 dB noise advantage, as in the balanced amplifier. The second stage IC2A gives +6 dB of gain. The gain is less to maximise negative feedback because the signal level is now higher. IC2B is a unity-gain buffer which prevents the 1 kΩ input impedance of final gain stage IC3B from loading the output of IC2A and causing distortion. IC3B is less vulnerable to loading because it has maximal negative-feedback. IC3B gives the final +6 dB of gain; it is used in shunt-feedback mode to avoid the common-mode distortion which would otherwise result from the high signal levels here. It has a 'zero-impedance' output, with HF feedback via C8 but LF feedback via R13, so crosstalk is kept to a minimum while maintaining stability with load capacitance. The output at K3 is phase-inverted and can be used for bridging. IC3A is a unity-gain inverting stage which corrects the signal phase. The output is also of the 'zero-impedance' type.

The power amplifier
The power amplifier consists of thirty-two 5532 dual opamps (i.e. 64 opamp sections) working as voltage-followers, with their outputs joined by 1 Ω current-sharing resistors. These combining resistors are outside the 5532 negative-feedback loops, and you might wonder what effect they will have on the output impedance of the amplifier. A low output impedance is always a good thing, but not because of the so-called 'damping factor' which is largely meaningless as the speaker coil resistance always dominates the circuit resistance. 'Damping factor' is defined as load impedance divided by output impedance: we have 64 times 1 Ω resistors in parallel, giving an overall output impedance of 0.0156 Ω. This gives a theoretical damping factor of 8 / 0.0156 = 512, very good by any standards. The wiring to the loudspeaker sockets will have more resistance than this!

The output opamps may be directly soldered into the board to save cost and give better conduction of heat from the opamp package to the copper tracks. However, on the prototype built in the Elektor labs, high quality sockets were used. Having a lot of opamps in parallel could make fault-finding difficult — if there is one bad opamp out of 32 then you are likely to have to do a lot of unsoldering (or IC unplugging) to find it. The opamp array is therefore split up into four sections of eight opamps, which are joined together by jumpers K5–K12, so on average you would only need to unsolder (or pull out) four opamps to find a defective one. In my many years of experience with the 5532 has proven a very reliable opamp, and I think such failures will be very rare indeed. There is an output choke L1 for stability into capacitive loads, and catching diodes D1–D2 to prevent damage from voltage transients when current-limiting into reactive loads.

The output relay and its control
The output mute relay RE1 protects the loudspeakers against a DC offset fault and gives a slow-on, fast-off action so no transients are passed to the loudspeakers at power-up or power-down. The relay is controlled from the power supply board. With reference to Figure 2, at power-up R17 charges C24 slowly to give a turn-on delay. In operation C21 is charged and
T3 is on; when the AC power is removed
C21 discharges rapidly, T3 turns off, and
D8 turns on T4–T5, which discharge C24
and cause the output relay contacts to
be opened immediately. Even a brief AC power
interruption gives the full turn-on delay.
Normally T4 and T5 are off and D15 non-
conducting, but if a DC offset fault applies
either a positive or negative voltage via R13
or R14, T4–T5 turn on and the relays are
opened at once to protect the loudspeakers.

Power supply
Again referring to the circuit diagram of the
power supply unit in Figure 2, the ±18 V
symmetrical supply is regulated by two type
LT1083 TO3-P positive regulators. When a
5532 sees one supply rail disappear, this
opamp can get into an abnormal state in
which it draws excessive current. This could
obviously be catastrophic with this design,
so the PSU incorporates a mutual-shutdown
facility which shuts off each supply rail if the
other has collapsed due to short-circuiting or
any other cause. If the positive rail collapses,
T2 turns on and disables the negative supply.
If the negative rail collapses, T1 turns on and
disables the positive supply.

Cost
This project uses quite a lot of 5532s; 37 in
each channel, but that does not mean the
cost is excessive. In Great Britain, 5532s
can be had from Rapid [1] for 24 p each at
100-off (Rapid are prepared to deal with
anyone who has a credit card) This means
that the cost of all the opamps would be
about £18.00.

To be continued
Next month’s closing instalment will cover
approaches to constructing the amplifier
on circuit boards, some performance
figures obtained from our high-end test
equipment, and an outline of challenges
to those of you wishing to modify the
amplifier for higher output powers and/or
lower output impedance. Meanwhile this
month’s E-Labs Inside section has a page
or so on issues with electrolytic capacitors
encountered while the first prototype of the
amplifier was tested.

Internet Link
1. www.rapidonline.com
LabVIEW Embedded for ARM μCs

Clemens Valens (Elektor France)

If you think LabVIEW can only be used for producing attractive man/machine interfaces for esoteric applications, then this article is for you. If you are interested in easy programming for powerful microcontrollers, then you should read on too. And even if you haven’t the least idea what LabVIEW is, don’t just turn the page, for it’s well worth taking a look at this tool.

LabVIEW is flagship product from National Instruments. Originally designed as a tool for driving measuring benches and displaying data, LabVIEW has evolved a great deal in twenty-odd years and has become a powerful, unique graphic programming language. It’s the only tool on the market that makes it possible to develop an application on a computer, FPGA, or microcontroller using the same language and the same programming methods. It’s what is known as a multiplatform language.

A program ‘written’ in LabVIEW Embedded for ARM looks just like any other application written in LabVIEW, with a user interface and a diagram. In the case of an embedded application, the front panel may represent the board’s peripherals, like a switch or an LED, but also debugging elements like variables. In LabVIEW Embedded, the user interface is only used for simulating and debugging the application.

To give you an idea just what a LabVIEW for ARM program looks like, take a look at the screen snapshots illustrating this article. It’s a very simple application that compares twice the voltage (coming from the potentiometer on the board) with a threshold value. If the value is above this threshold, an LED lights up. The doubled voltage is also sent to the analogue output (don’t forget to remove the loudspeaker jumper).

The magenta blocks (Figure 1) are the elementary I/Os corresponding to one pin of the microcontroller. LabVIEW offers a number of predefined ones, like the LEDs, but you can add your own — for example, for a relay. The green LED in the diagram is an element that appears on the front panel (Figure 2). It mirrors LED1 on the board. On the front panel there are also the ‘threshold’ and ‘output’ boxes. These are program variables that are not accessible on the board, but which can be modified or displayed during debugging. The ‘2’ and ‘100’ boxes are constants, and the triangles are mathematical or logic operations.

The large grey rectangle represents a loop (note the arrow at the bottom right-hand corner). Everything within the rectangle is

What’s in the kit?

• DVD-ROM with LabVIEW and the LabVIEW Embedded for ARM module: these are NI tools that let you program an ARM microcontroller board in a graphical fashion.
• Keil μVision RealView microcontroller development kit: this is a standard integrated development environment (IDE) for programming a microcontroller in C/C++, assembler, etc. The IDE is called μVision, the toolchain is RealView.
• ULINK2 USB-JTAG adaptor for programming the microcontroller and debugging the application.
• ARM microcontroller evaluation board. There are two models: the MCB2370U from Keil based on the LPC2378 (ARM7TDMI-S) from NXP, and the EK-LM3S8962 (Cortex-M3), a Texas Instruments thoroughbred.
• Instructions and two USB cables.

Price of the kit as shown: € 8,949
For more details: www.ni.com/arm
LabVIEW Embedded also exists for PSD and for 32-bit microprocessors.
LabVIEW, μVision & RTM

μVision RealView can be implemented without LabVIEW and makes it possible to develop applications for other boards or your own hardware using an ARM μC in the ‘traditional’ way. At the time of writing, RealView supported over 260 processors with ARM7, ARM9, or Cortex-M3 cores. The kit includes a time-unlimited licence with one year’s technical support from Keil (renewable).

LabVIEW Embedded for ARM, on the other hand, requires use of μVision (supplied with the kit), except in simulation mode. The link between LabVIEW for ARM and μVision is RTM from Keil, a real-time operating system for microcontrollers. The program in LabVIEW is translated into a program in C which relies heavily on RTM. This is the program that is compiled by RealView into an executable for the microcontroller. Without the RTM libraries (or compatible ones, all you have to do is write them...), it won’t work. Note that the source codes for your LabVIEW program translated into C are accessible for inspection.

Of course, LabVIEW Embedded’s real interest is for more complex applications. A large number of functions (PSD, TCP/IP, serial port, maths, etc.) are available and ready to use. If you’re short of a function, you can create it yourself or import it via a third-party DLL (your own, for example). LabVIEW Embedded can be adapted to your hardware; the evaluation board included in the kit is just one example. Once you’ve mastered LabVIEW Embedded, you have an extremely powerful tool at your disposal. However, do make sure you use a powerful computer, otherwise the time you’ve saved in programming will be wasted waiting for it to compile.

(repeat until the loop is ended. Here the loop never ends, since the stop condition (the red hexagon) is closed by the green box containing the value ‘False’, where a value of ‘True’ is needed to exit the loop. The loop is clocked by the little metronome, which operates in such a way that it is run every 100 ms.

LabVIEW is multitasking, which explains why the real-time operating system from Keil (see box) is needed to produce the executable for the microcontroller. To illustrate this multitasking aspect, a second loop has been added which continually interrogates the board’s push-button and which lights LED2 when the button is pressed. The two loops run independently of each other. Compiling this simple application takes an astonishingly long time — over a minute on the test computer (even though it’s a fairly recent laptop: Intel Pentium T4200 @ 2 GHz, 4 GB RAM, Windows XP Pro SP3). This is due to the C compiler, which has a large number of files to process. The executable produced is in ELF format. The ‘arm-elf-size’ tool (part of the GNU toolchain for ARM, not LabVIEW) gives a size of just over 34 KB. An application consisting of just an empty infinite loop produces a 14 KB executable and the compilation time is just as long.

Of course, LabVIEW Embedded’s real interest is for more complex applications. A large number of functions (PSD, TCP/IP, serial port, maths, etc.) are available and ready to use. If you’re short of a function, you can create it yourself or import it via a third-party DLL (your own, for example). LabVIEW Embedded can be adapted to your hardware; the evaluation board included in the kit is just one example. Once you’ve mastered LabVIEW Embedded, you have an extremely powerful tool at your disposal. However, do make sure you use a powerful computer, otherwise the time you’ve saved in programming will be wasted waiting for it to compile.

Figure 1. Diagram for the test application.

Figure 2. The test application user interface during debugging.

Remember the right-click!

The fact that LabVIEW is a graphical programming language doesn’t mean it is child’s play, even though NI has gone to great lengths to explain everything properly. So do make sure you read the short instructions supplied in the kit, and don’t forget that a right-click can be helpful at all times.

(100337)
By Harry Baggen (Elektor Netherlands Editorial)

Everyone who has anything to do with electronics has at least a multimeter at home and on the work table. But what about a clamp meter? These instruments are often just as versatile as multimeters, and they aren’t limited to currents of a few amperes. They also don’t need to cost much more than a multimeter. Here we examine the features of twenty clamp meters suitable for measuring AC and DC current, in price classes from a few dozen pounds to several hundred pounds.

If you ask an electronics enthusiast whether he or she has a clamp meter, the usual response is that they’re more suitable for electrical installers and service technicians. Nevertheless, in many cases a clamp meter can be just as useful as a multimeter — and often even more useful. The current measuring range of most multimeters is limited to 10 A, which means you need to be careful that you don’t exceed the maximum current rating and blow the fuse for the 10-A range or burn out the sense resistor. At the same time, we see more and more circuits that draw hefty currents, and this involves more than just measuring the input currents of household appliances or other devices connected to the AC grid. Modern clamp meters, which are also able to measure DC currents, open up a wealth of new possibilities. For example, you can measure the starter motor current in a car, measure how much current an electric motor in a model aeroplane draws from the battery, or measure how much current the CPU on the

CLAMP METERS

Measure That Current!

Twenty AC/DC clamp meters on the test bench

22
motherboard of your computer guzzles. A multimeter is not really designed to handle any of these tasks, but a clamp meter simply takes them in stride.

**Available products**

While looking through the catalogues of instrument suppliers, we quickly noticed that an enormous variety of clamp meters is available. Some manufacturers have more clamp meters than multimeters in their catalogues. This suggests that there is a large demand for these meters, the majority of which are most likely used in the electrical industry. However, as we already mentioned, modern clamp meters are so versatile that they are also very attractive for all sorts of electronics applications. For instance, many modern clamp meters can measure DC current in addition to AC current, and most of them can also make many other types of measurements. For example, the group of meters we examined includes models that can measure capacitance, frequency and temperature as well as voltage and resistance. This makes them full-fledged substitutes for a multimeter. The main advantage is that they allow you to measure much larger currents, ranging from 100 to 1000 A, so you don't have to be constantly afraid of blowing a fuse or burning out a resistor in your meter.

We did our best to select meters in various price classes from the enormous variety of available instruments. All of the selected meters can measure AC and DC current, and they are also suitable for measuring relatively small currents in the low ampere range. We also included a few clamp-on current probes (also with combined AC/DC capability), which do not have their own display but can be connected to an existing multimeter or to an oscilloscope.

**Operation**

The mechanical construction of a clamp meter is very similar to that of a power transformer. The jaws or yoke of the meter usually consist of a laminated metal core with a circular or elliptical shape, which is divided into two parts so the jaws can be opened and the conductor to be measured can be positioned inside the core. When a current flows through the conductor, it induces a magnetic field, and the laminated core conveys this field to a small measuring coil (with several turns) that is wound on the core (see Figure 1a). The strength of the current flowing through this secondary winding can be read from a meter connected to the measuring coil. The amount of current flowing through the primary conductor can be determined from the winding ratio of the two coils.

In most modern clamp meters, the measuring coil is replaced by a Hall sensor (a semiconductor device that is sensitive to magnetic fields). Depending on the design and the accuracy of the meter, one or two Hall sensors are placed in the air gaps between the two parts of the laminated core (Figure 1b). The voltage generated by the Hall sensor is directly proportional to the strength of the magnetic field produced by the current in the measured conductor. In this way, clamp meters provide a simple and safe means to measure relatively large currents. Furthermore, meters that use Hall sensors are able to measure both AC and DC currents, and the latter capability is especially interesting for electronics enthusiasts.

Figure 1. Operating principle of a clamp meter: a transformer with primary and secondary windings (a). In most modern meters, the secondary winding is replaced by one or two Hall sensors (b).

Figure 2. A pair of Hall sensors are clearly visible in the air gaps between the jaws of this Fluke clamp meter.

Figure 4. Clamp meters are complex instruments with many adjustment points. We counted around 15 trimpots in this model.
<table>
<thead>
<tr>
<th>Device</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Agilent U1213A</strong></td>
<td>A large, professional clamp meter with jaws big enough for 2-inch cables. Many extra features, including frequency and duty cycle measurement, as well as simultaneous read-out of AC and DC current or voltage on the dual display, which also has a bar scale. Many basic settings can be configured in a separate menu. Includes case and probes.</td>
</tr>
<tr>
<td><strong>Benning CM2</strong></td>
<td>Dark red, handy, robustly built small clamp meter with all the basic functions, but no extra functions other than voltage and resistance. The Peak/Hold button function must be selected before switch-on, which isn't very convenient. Clearly written multilingual user guide. Supplied with carrying case and probes.</td>
</tr>
<tr>
<td><strong>Extech EX613</strong></td>
<td>Hefty, bright orange meter with capped inputs. Many extra features, including capacitance measurement, frequency measurement up to 100 MHz, and separate inputs for two type K thermocouples (included). Built-in voltage detector for AC power voltage. Probes and carrying case included.</td>
</tr>
<tr>
<td><strong>Appa 30R</strong></td>
<td>Handy, robustly built small clamp meter with all the basic functions but no extras. Suitable for conductors up to around 20 mm. The Peak/Hold button function must be selected before switch-on, which isn't very convenient. The 'True RMS' designation on the device does not match the description in the user guide. Includes carrying case and probes.</td>
</tr>
<tr>
<td><strong>ELV ST-9700T</strong></td>
<td>Small clamp meter with relatively large jaws. With regard to the mechanical construction, we noticed that the display window can be pressed in quite easily. The only clamp meter in the group without autoranging or auto shut-off. Separate on/off switch. Supplied with a temperature sensor, carrying case and probes.</td>
</tr>
<tr>
<td><strong>Fluke 355</strong></td>
<td>This heavy-duty clamp meter, suitable for currents up to 2000 A, is the benchmark device for this survey. The large dual display can show the frequency of the measured signal at the same time as the measured value. Built-in low-pass filter to eliminate HF interference. Extensive calibration report, case and probes included.</td>
</tr>
</tbody>
</table>
HT9021

Hefty, robustly built clamp meter with large jaws, from the Italian make HT. Clear display with a supplementary bar scale for monitoring voltage trends. Many measuring options plus a built-in contactless voltage detector. Suitable for voltages up to 1000 V. Temperature sensor, carrying case and probes included.

Kyoritsu 2300R

Handy, small clamp meter designed exclusively for measuring currents up to 100 A. A special feature of this clamp meter is the open jaws, which allow it to be easily fitted over a conductor (up to 1 cm diameter). Built-in contactless voltage detector for locating live power grid wiring. With carrying case.

Kyoritsu 2046R

Sturdy professional clamp meter with large jaws and many functions, including voltage, resistance, capacitance, frequency and duty cycle. Large, clear display with a bar scale. Built-in contactless voltage detector for AC grid voltage. Supplied with carrying case and probes.

Mastech MS2102

Handy meter with fairly large jaws fitted to a well finished and sturdy housing (relative to the low price). The display is fairly small but easy to read, and it has a bar scale for monitoring voltage trends. Pity that it doesn’t have a Peak button. Carrying case and probes included.

Megger DCM340

Sturdy meter with large jaws, fits nicely in the hand. Clear display with digital indicator and analogue scale. Unlike most of the other meters, this one has separate buttons for the extra functions, which is particularly convenient. Very succinct user guide. Supplied with case and probes.

Metrix MX675

Hefty, robust professional meter with large jaws, suitable for thick conductors. Large dual display, which can show current and voltage, frequency and voltage, or current and frequency at the same time. Simultaneous temperature display in °C and °F. Temperature sensor, carrying case and probes included.
**Multimetrix CM 605**

Simple, handy meter with fairly small jaws (1.2 cm maximum), suitable for measuring relatively low currents up to 100 A. The lowest range is 10 A, so even low currents can be measured reasonably well. Analogue voltage output for displaying AC and DC current signals on an oscilloscope. Probes and carrying case included.

**Peaktech 1615**

Hefty clamp meter with large display and substantial jaws with enough room for relatively large conductors. Many extra functions, including frequency, duty cycle and capacitance measurement. Autoranging can be disabled. Unfortunately, there is no button for peak readings. Temperature sensor, carrying case and probes included.

**Peaktech 1645**

Handy, small clamp meter with reasonably large jaws for conductors up to approximately 1.5 cm. Operation somewhat clumsy with rotary knob and button. Supplementary bar scale on display for monitoring signal variations. Built-in pocket torch and voltage detector for AC power voltages. Supplied with probes and carrying case.

**Velleman DCM268N**

Hefty clamp meter with large display and substantial jaws suitable for conductors up to 30 mm. Various extra functions, including frequency and capacitance measurement. Clear display with a supplementary bar scale for monitoring voltage trends. Plastic case, temperature sensor and probes included.

**Velleman DCM270**

Small, simple clamp meter limited to measuring DC and AC currents up to 80 A; no autoranging. Cannot measure voltage or resistance. Built-in pocket torch and contactless AC power voltage detector. Supplied with carrying case.

**Voltcraft VC-521**

Small, handy instrument for hobby use; rotary switch does not feel especially sturdy. The least expensive clamp meter in this survey, with a large number of measuring functions including frequency, duty cycle and capacitance. Built-in voltage detector for AC power voltage. Carrying case, probes and temperature sensor included.
### Practical test and lab test

We tested all of the meters in a number of practical situations, such as measuring the current draw by an electric motor in a model aeroplane and the starter current of a car. These tests revealed that a peak function is especially handy (see tables). We also compared the clamp meters with each other in a test setup that generated reference currents of 10 A and 40 A. All clamp meters are suitable for use with these current levels.

The resolution of the meters at these current levels is 10 or 100 mA, whereby we must immediately mention that residual magnetism in the measuring circuit can easily cause errors of the same order of magnitude with DC currents. For this reason, it is important to zero the meter immediately before making each DC current measurement. Naturally, this problem does not arise with AC currents.

For both DC and AC, the accuracy of all of the clamp meters at the current levels used in the tests (or the maximum current if the measuring range was smaller) was within the tolerance specified by the manufacturer (1 to 3%). We expected nothing less from the more expensive meters, such as the Agilent U1213A, Fluke 355 and Metrix M675, and they were in fact well within their stated specs, but we were pleasantly surprised.
<table>
<thead>
<tr>
<th>Features</th>
<th>Mastech MS2102</th>
<th>Megger DCM340</th>
<th>Metrix MX675</th>
<th>Multimetrix CM605</th>
<th>PeakTech 1615</th>
<th>PeakTech 1645</th>
<th>Veilemann DCM268N</th>
<th>Veilemann DCM270</th>
<th>Voltech VC-521</th>
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<td>£250 + VAT</td>
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<td>£76</td>
<td>£105</td>
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<td>AC A</td>
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<td>600 A</td>
<td>1000 A</td>
<td>100 A</td>
<td>1000 A</td>
<td>200 A</td>
<td>1000 A</td>
<td>80 A</td>
<td>400 A</td>
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<tr>
<td>Basic accuracy</td>
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<td>±1.5% + 5dgt</td>
<td>±1.5% + 5dgt</td>
<td>±2.5% + 5dgt</td>
<td>±3% + 5dgt</td>
<td>±2.5% + 8dgt</td>
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<td>±2.8% + 8dgt</td>
<td>±2.5% + 10dgt</td>
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<tr>
<td>DC A</td>
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<td>1400 A</td>
<td>100 A</td>
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<td>200 A</td>
<td>1000 A</td>
<td>800 A</td>
<td>400 A</td>
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<tr>
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<td>±1.2% + 5dgt</td>
<td>±1.2% + 5dgt</td>
<td>±2.5% + 10dgt</td>
<td>±2.8% + 5dgt</td>
<td>±2% + 5dgt</td>
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<td>±2.8% + 8dgt</td>
<td>±2.5% + 10dgt</td>
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<td>600 V</td>
<td>1000 V</td>
<td>600 V</td>
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<tr>
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<tr>
<td>Basic accuracy</td>
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<td>±1% + 2dgt</td>
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<td>±1.5% + 3dgt</td>
<td>±1% + 2dgt</td>
<td>±0.8% + 2dgt</td>
<td>±1.5% + 5dgt</td>
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</tr>
<tr>
<td>Resistance range</td>
<td>400 Ω</td>
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<td>10 kΩ</td>
<td>40 kΩ</td>
<td>100 MΩ</td>
<td>40 Ω</td>
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<tr>
<td>Continuity tester</td>
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<td>&lt; 30 Ω</td>
<td>&lt; 35 Ω</td>
<td>&lt; 100 Ω</td>
<td>&lt; 100 Ω</td>
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<td>&lt; 100 Ω</td>
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<td>True RMS</td>
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<td>–</td>
<td>–</td>
<td>–</td>
<td>X</td>
<td>–</td>
<td>–</td>
<td>–</td>
</tr>
<tr>
<td>Autoranging</td>
<td>X (selectable)</td>
<td>X</td>
<td>X</td>
<td>X (selectable)</td>
<td>X</td>
<td>X</td>
<td>X (selectable)</td>
<td>X</td>
<td>X</td>
</tr>
<tr>
<td>Auto shut-off</td>
<td>30 min./X</td>
<td>30 min./X</td>
<td>10 min./X</td>
<td>30 min./X</td>
<td>35 min./-</td>
<td>10 min./-</td>
<td>20 min./-</td>
<td>7 min./-</td>
<td>30 min./-</td>
</tr>
<tr>
<td>Peak/Δ Zero</td>
<td>–/–/X</td>
<td>X/X/X</td>
<td>X/X</td>
<td>X</td>
<td>–/–/X</td>
<td>X/X</td>
<td>X/X</td>
<td>–/–/X</td>
<td>–/–/X</td>
</tr>
<tr>
<td>Min/Max/Hold</td>
<td>–/–/X</td>
<td>X/X/X</td>
<td>X/X</td>
<td>–/–/X</td>
<td>X/X/X</td>
<td>X/X/X</td>
<td>X/X/X</td>
<td>–/–/X</td>
<td>–/–/X</td>
</tr>
<tr>
<td>Freq. measurement</td>
<td>X (400 Hz)</td>
<td>X (10 kHz)</td>
<td>–</td>
<td>X</td>
<td>X (100 kHz)</td>
<td>–</td>
<td>X (4 kHz)</td>
<td>–</td>
<td>X (10 kHz)</td>
</tr>
<tr>
<td>Temp. measurement</td>
<td>–</td>
<td>–/X</td>
<td>X (in °C and °F)</td>
<td>–/X</td>
<td>–/X</td>
<td>–/X</td>
<td>–/X</td>
<td>–/X</td>
<td>–/X</td>
</tr>
<tr>
<td>Extras</td>
<td>Bar scale</td>
<td>Bar scale</td>
<td>Dual display</td>
<td>Analogue output for AC/DC A</td>
<td>Capacitance measurement Duty cycle</td>
<td>Bar scale</td>
<td>Capacitance measurement</td>
<td>Bar scale</td>
<td>Capacitance measurement Duty cycle</td>
</tr>
</tbody>
</table>

The choice is yours. A large variety of products are available: the prices of clamp meters are determined by the guaranteed accuracy of the instrument and the level of technical support provided. We sell a variety of products at all price levels. Choose as per your needs. A higher frequency, such as current sinusoidal waveform, will require a more expensive, higher frequency clamp meter. Accuracy decreases as frequency increases. The unit of AC current measurement up to more than 20 kHz. All clamp meters are suitable for AC measurements. Only new expensive types, such as Mastech and Voltcraft, can deliver more accurate readings.
formal calibration are of secondary importance for hobby use.

Among the selected meters, we noticed striking similarities between clamp meters of different makes, which were most likely purchased from the same manufacturer in Asia. This similarity is especially clear with the Appa 30R and the Benning CM2, which are simply the same clamp meter with different colours. The specifications are also identical.

With the clamp meters priced under 100 pounds, we noticed that the mechanical finishing sometimes leaves something to be desired, and in some cases we wondered how long the rotary or slide switches would last. If we assume that hobbyists won’t make especially intensive use of a clamp meter, the Voltcraft VC-521 and the Mastech MS2102 provide all the basic features at a low price, and the latter in particular has fairly good construction. Unfortunately, they both lack a peak function, which is a very handy feature with a clamp meter.

If you want a somewhat sturdier and more reliable clamp meter, you need to spend a bit more. The Extech EX613 is the most versatile instrument in the moderate price range, and it can do practically anything a normal multimeter can do. The HT 9021, Kyoritsu 2046R and Megger DCM340 are accurate, good and sound instruments, but somewhat less versatile. If you really want to have a properly calibrated instrument you can rely on, your only real choice is the Metrix MX675 or the Agilent U1213A, with the latter offering a lot of features for its price. We intentionally left the Fluke 355 out of the picture, since it is in a class by itself and serves here as a benchmark for all the other meters we examined.

(100429-I)

<table>
<thead>
<tr>
<th></th>
<th>ELV CA60</th>
<th>Fluke i30</th>
</tr>
</thead>
<tbody>
<tr>
<td>Price</td>
<td>£59</td>
<td>£280 VAT</td>
</tr>
<tr>
<td>AC/DC A</td>
<td>60 A</td>
<td>20 A</td>
</tr>
<tr>
<td>Basic accuracy</td>
<td>±1.5%±5mA (DC)</td>
<td>±1%±2mA</td>
</tr>
<tr>
<td></td>
<td>±2%±5mA (AC)</td>
<td></td>
</tr>
<tr>
<td>Load resistance</td>
<td>&gt; 10 kΩ</td>
<td>&gt; 10 kΩ</td>
</tr>
<tr>
<td>Max. conductor dia.</td>
<td>9 mm</td>
<td>19 mm</td>
</tr>
<tr>
<td>Output sensitivity</td>
<td>100 mV/A</td>
<td>100 mV/A</td>
</tr>
<tr>
<td>Special features</td>
<td>2 measuring ranges</td>
<td>Available with banana plugs or BNC connector (S version)</td>
</tr>
</tbody>
</table>

If you only occasionally need to make current measurements or observe current waveforms, a good alternative is a clamp-on current probe, which can be connected to a multimeter or an oscilloscope. The CA60 from ELV Germany is an affordable model, priced at about 60 pounds. It has two measuring ranges, so sufficient output voltage is available even with relatively low currents. The Fluke i30 is clearly intended for professional users. This device is also well suited to measuring relatively low currents, with a measuring range extending to 20 A. The i30 is supplied with a calibration report.

The following suppliers and/or manufacturers kindly made products available for this article:

Appa: www.appatech.com, Velleman: www.velleman.be
Benning: www.benning.de, Peakech: www.peakech.de
ELV: www.elv.de
Extech: www.extech.com, Voltcraft: www.conrad.nl
Fluke: www.fluke.com
HT: www.htitalia.it, Megger: www.megger.com
Mastech: www.p-mastech.com
Metrix, Multimetrix (www.chauvin-arnoux.com)
FPGA Design Simulation
With Quartus Web Edition and ModelSim Altera Edition

By Paul Goossens (The Netherlands)

Simulations are an important part of circuit design. Circuits built with 'ordinary' components can be simulated quite well with conventional simulation packages, but as soon as you add complex digital logic to give your circuit a bit of intelligence, most conventional simulators are no longer up to the task. In such cases, it's necessary to use an emulator to mimic the behaviour of the logic system.

Whenever you use an FPGA in a design, you're dealing with more than just hardware. An FPGA needs an internal design in order to do something useful. In a manner of speaking, you personally assemble the hardware inside the FPGA. You need special software to generate the FPGA design; this software is usually provided by the FPGA manufacturer. Special software is also available for simulating the internal design.

Many FPGA manufacturers offer a free version of their professional simulation program, which has certain minor limitations compared to the full version. For instance, there may be a limit on the maximum number of lines of code you can use, and in many cases the simulation is slower than usual. The simulation software discussed in this article has a built-in limit of 10,000 lines of code. However, a design with more than 10,000 lines of code is a rather large design. Even with this restriction, you can still use the software if you split your design into several smaller parts. You can then test these parts one at a time.

Software
For the examples described in this article, we used software that is available free of charge on the Altera website: Quartus Web Edition and ModelSim Altera Edition. First we'll look at what you can do with Quartus. In addition to generating designs, this package allows you to simulate designs on a small scale. Incidentally, there are also other FPGA manufacturers that offer free simulation packages for their devices. For the truly demanding user, there is a broad selection of VHDL, Verilog and mixed HDL simulators available.
Basic aspects
A basic consideration with FPGA design simulation is that you are limited to digital simulation. With digital simulation, you have to connect your design to test code that provides the test signals for the design. You can view the output signals from the design under test in a window that strongly resembles a software logic analyser, but first you need to generate suitable input signals. With the simulator included in the Quartus package, you can use a graphic interface to draw the waveforms of these signals. After all the signals have been defined, you can start the simulation, which causes the signals you just defined to be applied to the inputs of the design. You can choose from two options for running a simulation. The first option is a functional simulation, which ignores the propagation delays inside the FPGA. This yields clearly defined results with ideal signal behaviour. The major advantage of this type of simulation is that it only requires compilation of the design, so you can ignore the place and route, optimisation and timing closure functions. This saves a lot of time with relatively large designs. Figure 1 shows the results of a functional simulation, based on a design for a simple shift register with only one output. You can see that the signal level on the output and the state of the internal register both change on the rising edge of the clock signal. This contrasts with Figure 2, which shows the results of simulating the same circuit with the second option: timing simulation. This type of simulation additionally takes the internal propagation delays of the FPGA into account. You have to zoom in to a high magnification factor in order to see the difference. Then you can see that the delay between the rising edge of the clock signal and the time when the output signal changes state is 6.006 nanoseconds. Is this fast or slow? The answer depends on how fast your design needs to be.

Optimisation
One of the first pitfalls you encounter with a timing simulation is that sometimes one or more signals are not visible in the simulation, even though they are visible in the functional simulation. Why do these signals disappear? This usually results from design optimisation by the software. Remember that the design only needs to be compiled for a functional simulation, but several additional steps are performed with a timing simulation, with a large number of design optimisations being made in the process. One of the consequences may be that a particular signal is no longer present in the FPGA.

If you need this signal for your analysis, you can connect it to an additional output of your design. When you do this, the software will think that this signal must be physically present at an output pin, so the optimisation process will not eliminate the signal. However, this may significantly alter the timing of the end result. It's nice to know that the optimisations never change the functionality of the design, so the design will not suddenly exhibit errors due to optimisation. However, it will take up less space in the FPGA, and it will most likely run a good deal faster.

More oomph
As already mentioned, the built-in simulator of Quartus is very easy to use. The downside of this is that it has only limited capabilities. If you need something more powerful, you can use ModelSim. The

**Figure 1.** Results of a functional simulation of a simple shift register design with one output.

**Figure 2.** Results of a functional simulation of the same design as Figure 1.
error during synthesis. FPGAs do not have any components that can be used to implement this statement directly, but it does not cause any problems in a simulation. This process is repeated indefinitely because there is no sensitivity list at the start of the process code. As soon as the process has finished executing, it starts again from the beginning.

Interactive
The biggest advantage of a testbench file is that it allows one or more of the outputs of the design under test to influence the input signals. In the STIM process, which is responsible for generating the 'reset' and 'datain' signals, we use an internal signal together with an output signal from the design under test to modify the input signals. The line 'wait until (clk'event and clk='0');' causes process execution

```vhdl
library IEEE;
use IEEE.std_logic_1164.all;
use IEEE.std_logic_textio.all;
use STD.textio.all;

entity test_simu2_file is
end test_simu2_file;

architecture stimulus of test_simu2_file is

file RESULTS: TEXT open WRITE_MODE is "results.txt";

procedure WRITE_RESULTS(
  clk: std_logic;
  reset: std_logic;
  datain : std_logic;
  dataout : std_logic
) is
  variable l_out : line;
begin
  write(l_out, now, right, 15);
  write(l_out, clk, right, 2);
  write(l_out, reset, right, 2);
  write(l_out, datain, right, 2);
  write(l_out, dataout, right, 2);
  writeln(RESULTS, l_out);
end procedure;

component simu2
port (
  clk : in STD_LOGIC;
  reset : in STD_LOGIC;
  datain: in STD_LOGIC;
  dataout : out STD_LOGIC
);
end component;

signal clk : std_logic;
signal reset : std_logic;
signal datain : std_logic;
signal dataout : std_logic;

begin
  DUT: simu2 port map ( clk => clk,
                        reset => reset,
                        datain => datain,
                        dataout => dataout );

  CLOCK_PROC : process
  begin
    clk <= '1';
    wait for 10ns;
    clk <= '0';
    wait for 10ns;
  end process;

  STIM : process
  begin
    wait until (clk'event and clk='0');
    reset<= '0';
    datain <= '0';
    wait until (dataout='0');
    -- wait for falling edge of clk
    wait until (clk'event and clk='0');
    -- wait for falling edge of clk
    datain <= '1';
    wait;
  end process;

  WRITE_RESULTS (                     
    clk, reset, datain, dataout ) ;
end architecture;
```

free version of ModelSim can run simulations based on VHDL testbench files, which offer you the full range of VHDL functions. Here we can use the shift register design mentioned above as an example. The most important part of the testbench file is printed out in Listing 1. For the time being, we’ll ignore the lines shown in boldface.

Two processes are described here. The first process implements a clock signal generator. A major difference between a VHDL file intended to be used for synthesis and a testbench file is the option of using 'wait' statements. The line 'wait for 10ns' causes process execution to pause for 10 nanoseconds. This sort of statement in a design ultimately intended to be executed in an FPGA will cause an error during synthesis. FPGAs do not have any components that can be used to implement this statement directly, but it does not cause any problems in a simulation. This process is repeated indefinitely because there is no sensitivity list at the start of the process code. As soon as the process has finished executing, it starts again from the beginning.

Interactive
The biggest advantage of a testbench file is that it allows one or more of the outputs of the design under test to influence the input signals. In the STIM process, which is responsible for generating the 'reset' and 'datain' signals, we use an internal signal together with an output signal from the design under test to modify the input signals. The line 'wait until (clk'event and clk='0');' causes process execution
to pause until the ‘clk’ signal changes state and the new state is ‘0’.
In other words, it waits until the next falling edge of the clock sig-
nal. The ‘reset’ and ‘datain’ signals are set to ‘0’ only after this fall-
ing edge occurs. The initial state of all internal signals is ‘U’, which
stands for ‘unknown’.
In the next line, the process waits until the ‘dataout’ output of the
design under test goes to ‘0’. The ‘datain’ signal is set to ‘1’ after
the next falling edge.
The final statement of this process is ‘wait’ without any time speci-
fied. What is the purpose of this line? Without it, the process would
normally repeat itself immediately after completing execution,
since it does not have a sensitivity list. The ‘wait’ statement with-
out any time specified causes an infinite delay and thus prevents the
process from repeating.
Figure 3 shows the results of this simulation. All testbench signals
and all internal signals of the design under test (DUT) are visible
here. All of these signals start with the state ‘U’, since the simula-
tor cannot determine what the actual state should be. In reality, all
of these signals must assume either the ‘1’ state or the ‘0’ state.
From the simulation, you can see whether the design ensures that
this happens, or whether the state of a signal is arbitrary. The initial
state of a signal may change as a consequence of a different optimi-
sation, so you should check that all signals have defined states after
a certain point in time, such as after a reset.

Testbench format
A testbench file differs from a normal VHDL file in several respects.
Taking Listing 1 as an example, you can see that entities are declared
in the usual manner. However, a remarkable feature of these decla-
rations is that these entities do not have any input or output signals.
A design usually has at least one input and one output, so that it can
do something useful. After all, your design must be able to respond
interactively to its surroundings. From this, we can conclude that
a VHDL testbench file is not a design that can be programmed in
an FPGA.
The final example shows how to save all the signal changes in a text file. The lines added to the file contents in Listing 1 are shown here in boldface. Listing 2 shows the contents of the resulting text file. The functions you need for using files are located in the 'std_logic_textio' library, and you declare them in the usual manner. You also declare the file in the architecture section.

The WRITE_RESULTS procedure is responsible for writing the data to the file. This procedure is self-explanatory except for one detail. We use a variable with the name 'now' in this procedure. This variable has a special function in VHDL: it holds the current time (in the context of the simulation). The procedure is called at the end of the listing. It is called whenever a 'wait' statement is executed in one of the processes or whenever a process completes execution.

**Conclusion**

FPGA simulations are especially important with relatively complex designs. VHDL testbenches have many more features than we can possibly describe here, but the features we have described are sufficient for quickly running large simulations.

For students and novices, these simulation tools provide a good opportunity to experiment with digital electronics to your heart's content at minimum cost, with no need to touch even a single piece of hardware (other than your computer).

(100370-1)

The files for Quartus Web Edition and ModelSim Altera Edition used for the simulations described in this article are available on the Elektor website for downloading (Item no. 100370-11).

**Internet Links**


---

**Additional features**

You can also use files in a VHDL testbench. For example, you can automatically create a file during a simulation and use it to save all changes to signal states in plain text format. You can also read the contents of a file.

**Listing 2**

<table>
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<th>0</th>
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<td>1</td>
<td>0</td>
<td>0</td>
<td>U</td>
</tr>
</tbody>
</table>
Python Programming and GUIs

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This book is aimed at people who want to interface PCs with hardware projects using graphic user interfaces. Desktop and web based applications are covered. The programming language used is Python, an object-oriented scripting language; a higher level language than, say, C. Obviously having fewer lines of code will be quicker to write but also fewer lines of code means fewer opportunities to make mistakes. Code will be more readable, and easier to modify at a later date. You can concentrate on the overall operation of the system you are making. This abstraction also applies when writing graphic user-interfaces. Writing low level code for graphics and mouse clicks and the like is something that you do not have to do. In Python all this is wrapped up in relatively simple functions. The book guides you through starting with Linux by way of a free downloadable, live bootable distribution that can be ported around different computers without requiring hard drive installation. Practical demonstration circuits and downloadable, full software examples are presented that can be the basis for further projects.

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The CL-3 was a delight to design and build but very disappointing in the marketplace. I attribute this to my complete lack of marketing skills and not on the merits of the project itself. Phillip Torrone of Adafruit Industries encouraged me to have another go at it, this time as an open source project. So here we go, but not before mentioning that a story on the design history of CL-3 is available as a free download from the Elektor website at 1).

From the concept...
As shown in Figure 1 the minimal CL-3 implementation uses a rotary encoder as input, emulating a mechanical combination lock dial as used on vaults. The user gets feedback via a dual seven-segment LED display, indicating the current dial position. The final output is a logic level pin that represents the ‘locked’ or ‘unlocked’ state. This level controls a relay that governs the opening of the ‘vault’ door. There is also another pushbutton input that is used in conjunction with the dial to program the combination in the field. This pushbutton needs to be located in the secure region of the circuit, where the general public does not have access.

In the normal mode of operation, the rotary encoder is the primary input. A quadrature output indicates movement as well as direction. You could also determine the speed at which the dial is being turned, if you wanted to get fancy, by measuring the period between changes in the input. A state machine is used to keep track of the current quadrature input. Note that this is not the same as the virtual dial position. Once movement is detected, i.e., the input has changed, two of the possible remaining three states are checked to determine if the motion is clockwise or anticlockwise. The information is used to update the virtual dial position in the range of 00 to 99. Just like a mechanical dial, the value should roll over from 99 back to zero as well as go from 00 to 99 in the opposite direction.

To the schematic
In terms of user interface, the CL-3 circuit has two outputs, which are also apparent from the circuit diagram in Figure 2. The first is the LED display LD1-LD2 and the second is the lock relay RE1. Everything runs under the tight control of microcontroller IC1. The current dial position is displayed on the multiplexed LED display, which is a separate unit connected to the microcontroller via connector pair K2-K6. A lookup
Features

- 6 digit operation (three 2-digit numbers in CW-CCW-CW sequence)
- ATtiny2313 microcontroller
- Optical or mechanical rotary encoder
- Hidden/secure programming button
- Free C code

Table in the controller’s memory is copied to the lower registers and used to translate the decimal value to the appropriate segment values. Those segment values are written to the ATtiny’s output port, which is in turn connected to the LED segment leads via current limiting resistors R1-R9. Once a short period of time has elapsed, the tens digit is blanked and the same process is repeated for the units digit. Rapidly alternating between the two digits gives the visual impression that both digits are illuminated at the same time, due to the persistence of vision effect in the human brain. Your eye isn’t fooled but your brain is.

The original AT90S1200 had only one external interrupt and it was used for the programming pushbutton input. I moved one of the encoder inputs to INT0 (PD2, pin 6). In the interrupt routine, the other encoder input is examined to determine the direction of dial motion.

The original CL-3 kit used a Panasonic mechanical rotary encoder that has also been discontinued. There is no direct replacement available from Panasonic, but any quadrature encoder should work. Mechanical encoders are much less expensive than optical devices, and for these in particular two small capacitors C1-C2 are provided on each of the outputs to prevent contact noise.

The relay driver is just one npn transistor with a current limiter resistor in its base line. The power supply is dead conventional based around the 7805 voltage regulator with its usual flock of decoupling capacitors, solid (C5 and C7) and electrolytic (C4 and C6). Diode D2 affords protection against power supply reversal.

The CL-3 software

I have corrected what could possibly have been a security issue in the original CL-3 firmware. On power up, if the unit was unlocked, the display revealed the last number of the combination for a few seconds. The new firmware corrects this, only a decade later. That’s some kind of response time, eh?

The source code file written for the new CL-3 is called ‘CL-3.c’ and contains the C language version of the firmware. It can be downloaded free from [1]. For your amusement the same archive file also contains the AT90S1200 assembly code for the old CL-3.

Building it (them)

The circuit board designs for the new CL-3 are shown in Figure 3. The artwork for

![Figure 2. Schematic of the new CL-3. The project consist of a controller section and a display/encoder section.](image)

![Figure 3. Component mounting plans of controller board and display/encoder board. Either connect them via cables/wires or stack the display/encoder board on top of the controller board. Copper track layout files available free from our website.](image)
**Quadrature decoding**

A quadrature output consists of two alternating signals that are 90° out of phase. These two signals are traditionally referred to as A and B. The two signals don't have to be exactly 90° out of phase with each other, but it is important that they do not change state at the same time.

Imagine a compass that only has a 'north or south' output and an 'east or west' output. It will only tell you the approximate orientation of the compass. At any given moment, it can only report the ordinal (or intercardinal) direction (i.e., NE, NW, SE or SW). By periodically noting the reported direction, an external device can tell when the orientation changes by comparing the present reading with the previous reading. By analysing these changes over time, the absolute position, direction of motion and angular velocity can be determined.

An incremental shaft encoder uses the same technique to report changes in the position of a rotating shaft or instrumentation dial. An absolute position is derived from the succession of changes reported by the sensor. The direction and velocity of the shaft (or dial) can also be derived.

Expanding on the compass example, the two outputs A and B correspond to the “North/South” and “East/West” outputs, respectively. Arbitrarily assigning the North and East conditions as “high”, we get the following table:

<table>
<thead>
<tr>
<th>Ordinal direction</th>
<th>North/South</th>
<th>East/West</th>
<th>Phase A</th>
<th>Phase B</th>
<th>Binary value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Southwest</td>
<td>Low</td>
<td>Low</td>
<td>0</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>Southeast</td>
<td>Low</td>
<td>High</td>
<td>0</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>Northwest</td>
<td>High</td>
<td>Low</td>
<td>1</td>
<td>0</td>
<td>2</td>
</tr>
<tr>
<td>Northeast</td>
<td>High</td>
<td>High</td>
<td>1</td>
<td>1</td>
<td>3</td>
</tr>
</tbody>
</table>

There are four possible combinations for any given reading. It follows that there are four possible transitions between one reading and the next for each of the possible original combinations. For example, the four transitions from the ‘Southwest’ state are:

<table>
<thead>
<tr>
<th>New reading</th>
<th>Transition</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Southwest</td>
<td>None</td>
<td>0</td>
</tr>
<tr>
<td>Southeast</td>
<td>Anti-clockwise</td>
<td>-1</td>
</tr>
<tr>
<td>Northwest</td>
<td>Clockwise</td>
<td>+1</td>
</tr>
<tr>
<td>Northeast</td>
<td>Error</td>
<td>Unknown</td>
</tr>
</tbody>
</table>

Note that the unexpected transition from “Southwest” to “Northeast” implies either a faulty reading or a missing transition through one of the other, expected readings. Your response to this condition depends entirely on your application. For trivial or noncritical applications (e.g., a combination lock), these events can safely be ignored.

The ‘Value’ column, except for errors, can then be added to a running accumulation of similar values to provide a virtual absolute position.

The 3-number (6-digit) code that opens the lock is field-programmed using the physically-secured programming pushbutton, S1. All opening and programming commences when the encoder is turned clockwise (CW). To program a new combination, dial in the first number and then press the programming button. The display blanks once to confirm the new number is stored in EEPROM. Dial the second number in counter-clockwise (CCW) direction and press the button again. The display blinks twice. Repeat CW for the third number, with the predictable result. Only once all three numbers have been entered does the firmware write the new combination to the non-volatile EEPROM memory. I encourage users to write down the combination first, then enter it into the device, then confirm the combination works.

If you disallow subsequent identical numbers (e.g., 1-1-1 is not allowed), there are 100 x 99 x 99 = 980,100 possible combinations.

**Opening the lock**

This is done just like programming the lock, i.e. CW-CCW-CW but obviously without

---

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**Opening the lock**

This is done just like programming the lock, i.e. CW-CCW-CW but obviously without
actuating S1 and with no blinking of the display. The lock opens when the 5th and 6th digit also match the programmed code. The relay remains actuated until the encoder is turned again either CW or CCW. The state of the lock is also stored in EEPROM, meaning that the relay is immediately actuated when the lock was open and the supply voltage gets switched off and on again.

Using an Arduino

Attaching the user interface panel to an Arduino is very simple. The firmware is contained in Cl_3.pde, which is also found in the software archive for this project [1]. I used a polling technique instead of interrupts to make it simpler to understand. The same basic functionality is there. The logic lights up the built-in LED on the Arduino when the lock is unlocked. You could easily change the code to send a message via the serial port or take other appropriate action.

Unlock the potential

I really enjoyed working on this project, which allowed me to see years of progress in microcontroller technology and programming. If you can, take a moment and let me know what you think about it. I'd really like to know.

Now it's time to turn it loose on the world. While the copyrights of this article remain the property of the Publishers (because writers gotta eat!), the CL-3 software and PCB design are hereby placed in the public domain with no restrictions other than "commercial use prohibited". Go nuts!

(100026)

**Multiplexed 7-segment display logic**

A single numeric digit, based loosely on the Arabic numerals popular in Western languages, can be represented with as few as seven independent 'segments', traditionally labelled 'a' through 'g'. See below. LEDs prearranged in this pattern have been available for decades in various colours and sizes. To save pins, either the anodes or the cathodes are connected together and the other terminals brought out separately, thus the characterization 'common cathode' or 'common anode'.

To directly drive the LED segments from a microcontroller, connect seven output lines (via current limiting resistors) to the LED display module. Connect the LED common pin to either V+ for common anode displays or ground for common cathode displays.

If you have lots and lots of extra output pins available, you can connect a single output line to each LED segment for as many digits as your application requires. If not, you can multiplex the outputs. This reduces the number of dedicated output lines. Generally speaking, you then give up the ability to illuminate all of the LED segments simultaneously.

One way to multiplex the LED display is to arrange them in a row and column array, where the rows correspond to segments and the columns to digits. All of the 'a' segments would be connected together, as would the 'b' segments, etc. Instead of connecting the common line from each display to either V+ or ground, the common lines are individually selected (or strobed) by more dedicated output lines. These 'digit select lines' are almost always buffered using some sort of current-amplifying circuit, such as a common-emitter amplifier, consisting of a transistor and a resistor.

Each digit is displayed for a very brief period of time, then the next one, etc. If this happens fast enough, the display appears to be uniformiy illuminating all the digits at once.

**COMPONENT LIST**

**Resistors**

<table>
<thead>
<tr>
<th>R1, R2, R3, R5, R6, R8, R9</th>
<th>100Ω</th>
</tr>
</thead>
<tbody>
<tr>
<td>R4, R7</td>
<td>10kΩ</td>
</tr>
</tbody>
</table>

**Capacitors**

<table>
<thead>
<tr>
<th>C1, C2</th>
<th>10nF</th>
</tr>
</thead>
<tbody>
<tr>
<td>C3, C5, C7</td>
<td>100nF</td>
</tr>
<tr>
<td>C4, C6</td>
<td>100μF 25V radial</td>
</tr>
</tbody>
</table>

**Semiconductors**

<table>
<thead>
<tr>
<th>D1</th>
<th>1N4148</th>
</tr>
</thead>
<tbody>
<tr>
<td>D2</td>
<td>1N4001</td>
</tr>
<tr>
<td>LD1, LD2</td>
<td>7-segment LED display, common anode (CA), h=10mm, Avago type</td>
</tr>
<tr>
<td>HDSP-315L</td>
<td></td>
</tr>
<tr>
<td>T1</td>
<td>BC557</td>
</tr>
</tbody>
</table>

**ICs**

| IC1 | Atmel ATTINY2313-2PU, programmed, Elektor # 100026-41 |
| IC2 | 7805 |

**Miscellaneous**

<table>
<thead>
<tr>
<th>S1</th>
<th>Tactile switch, 6mm, Multicomp type</th>
</tr>
</thead>
<tbody>
<tr>
<td>MC32830</td>
<td></td>
</tr>
<tr>
<td>S2</td>
<td>rotary encoder, Alps type EC11E15204A4E</td>
</tr>
<tr>
<td>K1</td>
<td>2-pin pinheader, 0.1 inch lead pitch</td>
</tr>
<tr>
<td>K2, K6</td>
<td>10-pin (2x5) pinheader 0.1 inch lead pitch</td>
</tr>
<tr>
<td>K3, K4, K5, K7</td>
<td>3-pin pinheader, 0.1 inch lead pitch</td>
</tr>
<tr>
<td>RE1</td>
<td>PCB mount relay, DPCO, 5VDC, Multicomp type HR52H-5DC5V</td>
</tr>
<tr>
<td>PCB, see <a href="http://www.elektor.com/100026">www.elektor.com/100026</a></td>
<td></td>
</tr>
</tbody>
</table>


**byter 10-2010**
One-Button Data Entry
Drop-in BascomAVR routines

By Vladimir Mitrovic (Croatia)

In microcontroller based projects you often need to set a bunch of parameters or initial values. If the project already has an alphanumeric or some other kind of display, even complex multiple-digit values can be effectively set with just one pushbutton that can serve for other purposes as well.

Based on an ATtiny2313, the test circuit in Figure 1 is ‘general purpose’ and could be part of just about any project requiring a user interface. Pushbutton S1, together with an LC alphanumeric display, enables a form of interactive data entry. The trick is, of course, in the program, which is written in Bascom-AVR. The program will run on all similar circuits based on AVR microcontrollers as well as on 8051 micros if you do the necessary family-related adaptations.

The idea is to monitor not only if, but also how long, S1 is pressed. Two different cases can be distinguished:

- the button is pressed briefly (shorter than 1 s);
- the button is pressed for a long time (longer than 1 s).

When the button is pressed for longer than one second for the first time, the program will enter data-entry mode. In this mode, the initial parameter value is displayed and the cursor is placed under the first digit (see Figure 2). Every short press on the S1 will toggle the sign and/or increment the selected digit by 1. Pressing the button longer than one second again will move the cursor under the following digit, which can now be edited by short presses on the button. Pressing the button longer than one second while the cursor is under the last digit will terminate data-entry mode and the program will continue to execute code in the main loop.

Examples
In order to explain how data-entry routines can be implemented in the real program, two example programs were written: EE_1_button_HEX.bas and EE_1_button_DEC.bas. Both programs can be downloaded free of charge from the Elektor website [1]. They perform a simple main task: increment a counter (‘Cntr’ variable) approximately ten times per second and display every new value.

If you extract the counting/displaying part of example programs, the remaining code contains the structure and the routines that enable 1-button data entry, which should be easy to ‘port’ or ‘migrate’ to other similar projects.

Program concept
The heart of any program that alters its behaviour depending on duration of some event is the time-measuring routine. In our programs, in the ‘Check_1’ subroutine we check if, and how long, the

![Figure 1. One button is all you need for an elegant and effective data/parameter entry (test circuit shown).](image)

*Figure 1. One button is all you need for an elegant and effective data/parameter entry (test circuit shown).*

*Figure 2. Every short press on the S1 will toggle the sign and/or increment the selected digit by 1. Pressing the button longer than one second again will move the cursor under the following digit, which can now be edited by short presses on the button. Pressing the button longer than one second while the cursor is under the last digit will terminate data-entry mode and the program will continue to execute code in the main loop.*
button is pressed. Note that although the switch is labelled S1 in the

diagram, it’s invariably ‘T1’ in the software.

T1 Alias PinD.0 ‘T1 input pin

Check_T1:
  T1_time = 0
  Debounce T1, 0, T1_on
  Return

T1_on:
  T1_time = 1
  While T1 = 0
    Waitms 10
    Incre T1_time
    If T1_time > 100 Then
      Exit While
    End If
  Wend
  Return

The ‘Debounce’ statement checks if the pushbutton is pressed at all.
If the check fails, program control returns to the main loop with ‘T1_

time’ variable set to 0. If the ‘Debounce’ statement establishes that
the button is pressed (T1=0), the program branches to the ‘T1_on’
routine and waits in While-Wend loop until the button is released
or 1 second passes (whichever comes first) and then returns to the
main program. If the button is released before 1 s passes, the ‘T1_
time’ variable will have any value from 1 to 100 (for time periods
from approx. 10 ms to 1 s). If the 1-second period lapses, the pro-
gram will stop monitoring the button and will return to the main
loop with T1_time = 101.

To enter the data-entry mode, you should periodically check in the
main loop if the switch is pressed for longer than 1 s and if it is, call
the data-entry subroutine:

Do
  Gosub Check_T1 ‘check if T1 is pressed >1s
  If T1_time > 100 Then
    Gosub Edit_cntr ‘if yes,
      ‘enter editing mode
  End If
  ‘... ‘else, do whatever
Loop

The data-entry subroutine consists of three parts:

1. initialization, in which the initial value is converted to the appro-
priate format for display;

2. loop, in which S1 (T1) is periodically checked and the displayed
value is edited as explained before and

3. finishing, in which the edited value is saved to become the new
initial value.

It is obvious that the program will not be able to execute the com-
mands from the main loop while the data-entry subroutine is exec-
cuted. Interrupt routines, if there are any, will be executed normally.
It is the programmer’s responsibility to ensure that such a condition
does not affect the hardware controlled by the microcontroller in a
destructive manner.

We will not explain the data-entry subroutine in detail. However,
several notes might be helpful in order to enable you to adjust it to
your own needs.
EE_1_button_HEX description
The initial value is stored in the ‘Cntr’ variable (the name comes from its use in example programs and you may edit it as you please). The variable type can be Byte, Word, Integer or Long. To enable manipulation of individual digits, the variable is converted to the string ‘Edit_string’ which should be properly dimensioned to accept the ‘Cntr’ variable (2B for Byte, 4B for Word/Integer and 8B for Long).

```
Edit_cntr:
    Edit_string = Hex(cntr)
    Cls
    Lcd Edit_string
```

The editing loop that extracts hexadecimal digits from the string is limited by the string length:

```
For I = 1 To Len(edit_string)
    Edit_x = Mid(edit_string, I, 1)
    ...  
```

Since BascomAVR has no function to increment the hexadecimal digit in the ‘Edit_x’ variable, a simple assembler routine ‘Incr_hex’ is written and called when a short press on the button is detected. Every edited digit is merged into the initial string and displayed before the next digit is selected:

```
If T1_time > 0 Then
    Gosub Incr_hex
    Mid(edit_string, I, 1) = Edit_x
    Lcd Edit_string
End If
Next
```

Near the end, after the last digit has been edited, the new value is converted back from the string into the numeric value and saved in the ‘Cntr’ variable:

```
Cntr = Hexval(edit_string)
Return
```

EE_1_button_DEC description
The initial value is stored in the ‘Cntr’ variable (again, rename it as you please). The variable type can be Byte, Word, Integer (signed) or Long (signed). To enable manipulation of individual digits, the variable is converted to the string ‘Edit_string’ which should be properly dimensioned to accept the largest possible value in the ‘Cntr’ variable. Allow for an extra byte for signed variables.

```
Edit_cntr:
    Edit_string = Str(cntr)
    Edit_string = Format(edit_string, ”+00000“)
    Cls
    Lcd Edit_string
```

Note that ‘Edit_string’ is formatted with the Format() function. This is necessary if you want to display the + sign and leading zeros; only displayed digits can be edited! The + character at the beginning of the mask is necessary only for signed variables and should be omitted otherwise. The editing loop that extracts digits from the string is limited by the string length:

```
For I = 1 To Len(edit_string)
    Edit_x = Mid(edit_string, I, 1)
    Edit_9 = Val(edit_x)
    ... 
```

Every digit is extracted as a hexadecimal character (‘Edit_x’) and a decimal number (‘Edit_9’). This is necessary as you have to manipulate string (sign) and numeric (digits) values. What will happen if the short press on the button is detected depends on the character that is extracted. If it is a sign, it will toggle:

```
If T1_time > 0 Then
    If Edit_x = ”+“ Then
        Edit_x = ”-“
    ElseIf Edit_x = ”-“ Then
        Edit_x = ”+“
End If
```

This part of the program may be deleted if the initial value is unsigned (i.e. if it is stored in the Byte or Word variable). If the extracted character is a number, it will be increased:

```
Else
    Incr Edit_9
    If Edit_9 > 9 Then Edit_9 = 0
    Edit_x = Str(edit_9)
End If
```

Every edited digit is merged into the initial string and displayed before the next digit is selected:

```
Mid(edit_string, I, 1) = Edit_x
Lcd Edit_string
End If
Next
```

Finally, after the last digit has been edited, the new value is converted back from the string into the numeric value and saved in the ‘Cntr’ variable:

```
Cntr = Val(edit_string)
Return
```

The data-entry routines are explained in a somewhat simplified manner; check the example programs in the free download [1] for the complete routines!

```
(090636)
```

Internet Link
1. www.elektor.com/090636
Discover the STM32!

By Jens Nickel (Elektor Germany Editorial)

Microcontroller evaluation kits are a wonderful resource. Those supplied by chip manufacturers are usually reasonably priced as development costs can be recouped in chip sales. For the engineer/student it’s a good opportunity to work hands-on with the devices and the software tools. From the manufactures viewpoint the more people who become proficient with their devices the more chips they will sell.

STMicroelectronics has already had a positive response to their low-cost 8-bit STM8S Discovery Kit. At the last Embedded World exhibition in Nuremberg they handed out 5,000 of these 8-bit evaluation boards free to visitors. The company is also about to introduce a similar kit for their 32-bit family of microcontrollers (STM32F). The controller chosen for the STM32VL-Discovery kit is the STM32F100RBT6B, one of their low-cost “Value line”, based on an ARM Cortex-M3 architecture with 128 KB Flash memory and 8 KB RAM [1]. The kit is due to be shipped mid September 2010 and will be stocked by a number of suppliers. Farnell already have plenty on back-order, not surprising considering the retail price of the kit is just £6.80 or $9.90 in the US.

STMicroelectronics also supply a range of STM32xxx-EVAL evaluation platforms but the STM32-Discovery is a much more economical introduction to the Value Line family. All software is available for download from the Internet and a USB cable will be needed to connect the kit to a computer. Unlike many other manufacturers whose evaluation boards are designed in the form of a large USB stick and are quite densely populated the STM32 Value Line Discovery has more board space and uses a rectangular board. Single rows of pin headers are fitted along the long side and at the lower end through which the processor pins have direct access to the outside world. Many other evaluation boards provide just a few solder pads for connecting external circuitry but the Discovery board is more versatile and can be used as a processor board with peripherals wired in the prototyping area or linked to an additional circuit board.

The hardware is divided into two areas; the main part contains the target controller together with a reset button, a ‘user’ button and two LEDs. These allow you to run and interact with simple demo programs without the need for any additional hardware. The somewhat smaller upper area of the PCB is fitted with a USB socket. Power for the board is derived from the external computer via the USB cable. Underneath there is a second (more powerful) STM32 controller which takes care of programming and debugging the target processor. The functions of this debugger are compatible with the ST-LINK programmer/debugger tool. This means that free ST-LINK software such as the ‘ST-LINK utility’ [2] also runs on the board to allow the STM32F100 memory contents to be read. A nice feature also is that the programmer/debugger functions are not restricted to the on-board target controller; it can be hooked up to an external STM32 controller using the 4-pin SWD (Serial Wire Debug) connector.

There is a choice of three tool chains available from IAR, Keil or Atollic to deal with the job of writing and debugging firmware for the STM32. We chose the ‘TrueSTUDIO for STM32’ from Atollic AB as the development environment. The Lite version can be downloaded for free [3] and is neither time nor code size limited but has fewer functions than the professional version. This powerful IDE based on ECLIPSE is simple to install and includes a good selection of project templates for different hardware platforms so for example it is not necessary to concern yourself with generating your own make-files (the first steps of creating a new project are described in the ‘quick start guide’ of the development environment). Selecting “STM32_Discovery” as the evaluation board will load a small demo project to control the two LEDs in response to user button presses. Although the newcomer may find the development environment a little daunting at first, they will soon get to know how the individual project files hang together, what information is shown in the windows and where the most important debugger control panel is. To run your own programs in the ‘Lite’ version it is necessary to activate the debugger and build.
generation of binaries is only possible in the professional version. In the 'Build Configurations’ menu (accessible from the main menu under ‘Project’) make sure that ‘Set Active’ has been selected in “Debug”. Otherwise new or modified programs will not be transferred to the controller for debugging.

Now there’s no reason why you can’t start thinking up your own applications for this powerful system. A good starting point for those new to the environment is the examples posted on the STM32-Discovery website 41. Running through these will quickly boost your confidence with the system. The downloadable data describes the board in detail (circuit diagram, pinouts and jumpers). STMicroelectronics have indicated that the website will be active by the 15th September at the latest.

1. www.st.com/mcu/inchtml-pages-stm32.html
2. www.st.com/mcu/familiesdocs-110.html (the last one in the list)
4. www.st.com/stm32-discovery

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### Electrolytic capacitors in audio circuits

By Ton Giesberts (Elektor Labs)

Here at Elektor, for more than 20 years we’ve held the opinion that electrolytic capacitors have the worst properties for audio signal paths, possibly with the exception of ceramic capacitors. Our dielectric of preference is plastic film, with polyester/terephthalate as a minimum. Even better are polypropylene and polystyrene versions. Of course, there are ‘ultimate’ capacitors like Teflon or silver-mica devices, but we’re talking about electrolytic caps here.

Electrolytic capacitors are commonly used as coupling devices in valve amplifiers, but we also frequently find them in solid-state amplifiers. The advantage of electrolytic capacitors is their large capacitance to volume ratio. In the original design of the ‘SS32 Power Amplifier’ (first part of this edition), two electrolytic capacitors were used, which we eliminated in the final design; a DC control is added instead to compensate for any offset. In this way any discussion as to whether electrolytic capacitors have an audible effect on the sound, in whatever way, can be avoided. Nevertheless we were curious what the effect of the electrolytic capacitors would have been if we had left them in the signal path. We will show the results of the THD measurements, tests with MLS and standard IMD signals did not result in any clear differences between input and output of the test configuration. The various test configurations are shown in Figure 1.

We started with the simplest measurement of a high-pass filter (Figure 1a). Figure 2 shows the FFT analysis of the generator and the output of the network. The generator generates a sine-wave at 20 Hz with an amplitude of $5 \text{ V}_{\text{rms}}$. The graph shows the signal from the generator (cyan) and the voltage across R1 (green). There is therefore an increase in distortion, but the harmonics are below –120 dB. In reality they are even lower, because they are only a little stronger than the harmonics from the generator, which are at –130 dB ($2^{nd}$ and $3^{rd}$ harmonics). Expressed as a percentage the distortion increased from about 0.00017% to 0.00023%. The measurement was repeated for other types of 220-μF electrolytic capacitors. Next was a non-polarised electrolytic. It came as a surprise that the comparison with the generator signal shows practically no difference. This was followed by the two different types of electrolytic cap from the first measurement, this time connected in series in a bipolar manner (Figure 1b). There still was practically no difference to be seen between input and output. To find out whether the polarity made a difference, we turned the second electrolytic around (Figure 1c); the measurement across R1 now showed two harmonics which were a few dB larger than the measure-
ment with one electrolytic. We can therefore conclude that a bipolar configuration, with respect to distortion, is preferable over a single electrolytic. We assume that the non-linearities are cancelling each other. We followed this with measurements using other electrolytics (different manufacturers and rated voltages). The results gave different pictures, but always an increase in distortion. In all these situations a bipolar connection had a positive effect.

After this we did tests with electrolytic capacitors that were 10 times smaller. The corner frequency is now at 7 Hz instead of 0.7 Hz with the 220 μF device. There is now a greater voltage drop across the capacitor. The result of a measurement using the configuration of Figure 1a at 20 Hz (5 Vₘₚ) can be seen in Figure 3. There is now considerably more distortion (about 0.005 %), and that is only due to the electrolytic! We also tried a bipolar connection, which gave a chart with a second harmonic that was about 17 dB lower, but the third was a little bigger. The rounded distortion figure remains the same.

We had one remaining question. Does the DC voltage across an electrolytic capacitor have an effect on the distortion? That is why we carried out a measurement according to Figure 1d. We don't have to show the result, because the two curves (with and without DC voltage) are practically on top of each other.

To get a feel for the quality of standard plastic film capacitors (Siemens/Philips 'MKT') we did two more tests with two versions of the high-pass filter with a corner frequency of 20 Hz. The first version was a large 4.7 μF/250 V capacitor and a 1.69-kΩ resistor. The second version was a cheap 2.2 μF/100 V capacitor (type without plastic case) and a 3.65-kΩ resistor. The results of the measurements are shown in Figure 4. Both versions gave pretty much the same result. Only the 2nd harmonic increased by 12 dB and the third remained practically identical.

From these tests we can conclude that when using electrolytic capacitors in the audio signal path it is desirable to locate the corner frequency well below the desired audio range. In cases where the impedances are low (kilo-ohms) there is no real alternative to electrolytics for blocking potential DC voltages. The disadvantage of the large capacitance is that a change in DC voltage will still be partially allowed to pass, because of the long time constants involved. Switch-off phenomena also last longer. A consideration can be to increase the impedance (results in more noise), so that it is still possible to use a polyester (MKT) device. The advantage is then that the polyester cap can be dimensioned for the desired corner frequency.

From this we can conclude that when using electrolytic capacitors as coupling capacitors there is practically no measurable effect on the audio signal provided that the capacitor is considerably over-dimensioned. With these 'too large' electrolytics it is also beneficial to use them in a bipolar connection. We have looked here at only one aspect (harmonic distortion). There are obviously many more characteristics of electrolytic capacitors that could have an 'audible' influence on the sound quality. Here is an overview of interesting publications if you would like more information about capacitors.

(100452-1)

Literature and Internet links
Capacitors in A.F. Circuits. Elektor February 1992
A real-time signal test for capacitor quality.
The Audio Amateur, April 1985
Picking Capacitors, Audio Magazine, February 1980
http://sound.westhost.com/articles/capacitors.htm
http://www.nationali.com/rap/Application/0,1570,28,00.html
A Pocket Projector

By Jens Nickel (Elektor Germany Editorial)

Advances in high power LED technology have made it possible to shrink the size and price of LCD projectors. Makers of so-called pico-projectors boast that their devices will fit into your pocket and are ideal for projecting stills and videos from flash memory video players, camcorders and portable DVD players onto a wall for everyone to see. Serious bit of kit or just a toy? With Conrad Electronic[1] offering the ‘Aiptek Pocket Cinema T15’ for 99 euros (some 80 pounds) we thought it was time for us to find out.

Our first impression was that the term ‘pocket projector’ is in no way an exaggeration. The device sits in the hand and looks well built. Included in the kit are a rechargeable battery, an AC power charger, a small tripod and an AV cable (3 x RCA sockets). The only video input on this unit is the AV connector for stereo sound and video signal. An S-Video signal provided by some laptops (or mobile phones) will also be suitable but you will need to buy an adapter cable. For our test we took the signal from an old DVD player.

In the first test we projected an A4-sized image (35 cm diagonal). Our initial subjective impression of the image was surprisingly favourable with colour saturation better than expected. The 640 x 360 resolution is less than a PAL signal, so the image is understandably a little soft and the picture shows some evidence of video processing. The biggest limitation however is poor image brightness (8 Lumen compared with 2000 to 15000 Lumen for a standard metal halide LCD projector). Attempts at viewing a bigger image (it can focus images with a diagonal of up to 127 cm) required an absolutely dark room.

At this point curiosity got the better of us; brandishing a set of screwdrivers we went in search of screws to undo. With the bottom half of the case removed we could see the main PC board and at the front, the projector lens extending from a black box which presumably contained the complete optical arrangement and the light source. To go any further we needed to lift the lid but it looked like it was glued to the middle section. Luckily lab assistant Jan Visser had come over to see what we were doing and he volunteered to lend a hand. He seems to have a sixth sense, able to identify the position of concealed screw heads, plastic clips and double-sided tape. A couple of minutes later the unit was dismantled. Inside we found the power LED fitted to a PCB with an aluminium plate to aid heat dissipation. A finned heat sink is also fixed to the plate. At the opposite side of this optical box we can see a thumbnail sized display screen. The operating principle is now fairly clear; light from the white LED (from Cree) passes through a series of lenses to produce a parallel beam of light. The beam passes through a half-silvered mirror set at 45° in the optical path and strikes the reflective display. The image on the display is reflected back and then out through the lens via the half-silvered mirror.

Identification marks on the LED PCB shows that the unit originated from the Hong Kong based company iView. Their website indicates it is possible to order the optical module type IPL-512DG which was specially developed for the Pico projector[2]. The site News pages report that this module has also been integrated into a mobile phone to become the first phone with this feature.

We also discovered that the mini display unit in the projector is a LCoS (Liquid Crystal on Silicon) unit. This type of display similar to an LCD is based on liquid crystal technology but works in a reflective rather than transmissive mode. The liquid crystals are applied to a mirrored substrate, as a liquid crystal pixel flips between on and off, light is either reflected from the mirror or blocked. This technology can produce high resolution images and is compact. We couldn’t find an English website with information on the HX7009 chip which is supplied by Himax in Taiwan[3]. The same company are also responsible for the HX8852 display driver chip which accepts digital video conforming to the ITU656 protocol[4]. Next to it is the TVP5150AM1 chip from TI[5], this converts PAL input signals into ITU656 digital signals. The largest chip on the board is an 8-bit microcontroller type HT46R23 from Holtek[6].

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[1] www.conrad.de (Part No 346313)
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A more versatile GPS receiver for in-car use and radio-control model applications was the spur to designing this project, with capturing motion speed and presenting this in both digital and analogue form a primary requirement. With eight CPUs onboard working in parallel, the Propeller microcontroller from Parallax manages these demanding graphic applications with ease.

For quite some time now there have been a number of approaches to capturing the speed of moving objects extremely accurately by means of GPS. Despite this, off-the-shelf gadgets produced for in-car route navigation generally do not have a built-in speed capture function. Maybe the manufacturers do not want complaints from customers when their tacho reading (which intentionally shows higher than actual speed) doesn't correspond with the GPS display. An additional hand-held satnav device doesn't make much sense in this situation, particularly as the speed display is very small. Squinting at tiny figures is not necessary with the GPS receiver described here, which shows the GPS speed with clarity in both analogue and digital form. Furthermore it can track your progress and speed changes during the last 90 minutes. The device can be used out on the road and equally well in model sports such as radio control (RC) cars.

Hardware
It was back in May 2006 when the Californians at Parallax launched their remarkable Propeller microcontroller. Truly exceptional was the provision of eight integrated CPUs that operated practically in parallel. Parallax calls these CPUs 'Cog'. The system speed is 80 MHz, producing a maximum performance of 160 MIPS.

The architecture of the Propeller makes it hard to compare with traditional microcontrollers. Because the eight Cogs can share tasks among themselves, interrupts are no longer required and the programmer has full control over the use of the Cogs. Reading in serial data, processing this data and output to the graphical LCD (GLCD for short) all take place in parallel.

The system speed of the Propeller is shown in the schematic (Figure 1), using a 5 MHz crystal that is multiplied internally by a factor of 16. The KS107/108-compatible GLCD equipped
with 128 x 64 pixels is controlled by an 8-bit wide data bus.
A 24LC256-I/P EEPROM (IC2) is used for storing an application-specific program (the firmware).
The MAX3232CPE (IC3) serves to adapt the levels between the Propeller and the GPS mouse used as GPS sensor. The latter can use either a 3.3 V or 5 V supply and must provide an RS-232 interface (4800 or 9600 baud). Serial GPS mouse units of this kind generally have a sub-D connection and it's worth checking this and the supply voltage before purchase.

**Propeller firmware**
Software for the Parallax Propeller is programmed with SPIN. On the Parallax homepage the necessary objects are already waiting for you:

- “FullDuplexSerial”
- “Propeller Eeprom”
- “Ks0108V1.6” (revised by the author)

With these objects we can create an EEPROM, a RS-232 interface for hooking up to a GPS mouse and the controls for a KS107/108-compatible GLCD.

![Diagram](Figure 1. The circuit comprises basically just a single Propeller chip (IC1) and an LCD for the graphics. K2 is for connecting a serial GPS mouse.)
The parallel processes of the Cogs make interrupts superfluous, which as already mentioned is a significant advantage of the Parallax Propeller.

In this GPS project you’ll have the pleasure of programming five Cogs:
1. The main program GPS_Start requires one Cog. From this main program the Cogs described next are called using the command COGNEW(entry...).
2. Cog for FullDuplexSerial.start. Reads in data from the GPS mouse over the serial interface.
5. Cog for Picture_Page_4_Cog. Graphical functions are programmed on this screen. As they require a relatively long processing time a dedicated Cog is provided.

Let’s now investigate the ReceiveDatas function of the fourth Cog in detail. With the library function FullDuplexSerial, spin a character from the serial interface compared with the write pointer ReceiveWritePointer. If the pointers are unequal, then the Variable ReceiveBuffer[ReceiveWritePointer] is read out and the read pointer is incremented by 1. This process is repeated until the write pointer is the same as the read pointer.

The GPS mouse delivers data using the NMEA-0183 protocol into ASCII format. Each set of data begins with the character ‘$’, followed by a two-character long transmit indicator and a three-character data label. After this come the data sets, separated by commas. Finally the set is closed with an optional checksum and CR/LF. A set can contain up to 82 characters in a format designated by the number of commas. The character determined by the read pointer is written into the array GPSBuffer. The start of a GPS data set is recognised by the ‘$’ character, after which the write pointer GPSPointer is set to zero. The character LineFeed (decimal 10) enables the end of a GPS string to be recognised. A GPS data set written to the GPSBuffer has the sample structure seen in Figure 2. In this example we are reading the GPS data sets $GPGGA, $GPVTG and $GPRMC containing the following GPS data:
- $GPGGA includes Latitude, Longitude, Quality of the signal received, number of satellites received and height above sea level.
- $GPVTG includes the speed.
- $GPRMC includes clock time, date and direction of movement (effectively compass direction).

Practical matters
To simplify construction a printed circuit board (Figure 3) has been designed, on which all components (apart from the GLCD) are mounted on the top side. The GLCD is attached to the other side. The power supply voltage is connected to the multipin connector K1; a range between about 8 V and 18 V DC is permissible. The GPS mouse is plugged in at multipin connector K2. Power for the mouse is applied either to pin 5 (5 V) or pin 4 (3.3 V) and Pin 3 (supply return). Pin 1 is commoned with the transmit line (TxD) of the GPS mouse and pin 2 with the receive line (RxD).
A jumper on K4 switches on the background lighting of the GLCD. Brightness is preset with P1 and contrast with P2. LED D2 indicates the state of the data received as follows:

- Steady light = Checksum and quality of received data in order
- Rapid flashing = Checksum error
- Slow flashing = Quality error

The Parallax Propeller chip is programmed in-circuit using the SPIN programming language, which can be downloaded free at the Parallax homepage and the ‘Prop Plug’ programming adapter from Parallax. Connection to the PC is by USB cable. On the GPS board the ‘Prop Plug’ is plugged into the multipin connector K3 (Figure 4).

Before operation you need to check whether the GPS mouse still needs to be configured with software that either came with it or else can be downloaded from the manufacturer’s website. The serial interface of the GPS Mouse should be configured as follows: 9600,N,8,1.

After powering up the GPS Propeller board the Receiver searches for the GPS mouse, first at 4800 baud. If a 9600 baud receiver is used, then the display will indicate only random characters initially. Afterwards the correct presentation of received data at 9600 baud will appear (Figure 5).

If after one second the data from a GPS mouse cannot be interpreted correctly the error message ‘no GPS signal’ is shown. A correct signal will produce an image with digital and analogue speed display (Figure 6).

At lower left the maximum speed achieved previously is shown.

Press button S2 can now switch between metric and imperial units (statute miles and feet). This setting is now valid for all subsequent displays and remains stored in the EEPROM even when power has been removed.

Operating S1 allows the following functional displays to be called up sequentially:

Direction of movement display (Figure 7) similar to a compass (digital and analogue). History display (Figure 8) with graphical

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### COMPONENT LIST

**Resistors**
- R1,R2,R3 = 10kΩ
- R4 = 1kΩ
- R5 = 5.6Ω
- P1 = 250kΩ trimpot
- P2 = 25kΩ trimpot

**Capacitors**
- C1-C9,C12-C15 = 100nF
- C9,C10,C11 = 10μF 16V

**Inductor**
- L1 = 10μH

**Semiconductors**
- D1 = 1N4007

**ICs**
- IC1 = P8X32A-D40 (Propeller chip; Parallax)
- IC2 = 24LC256-I/P
- IC3 = MAX3232CPE+
- IC4 = 7805
- IC5 = LM2937ET-3.3

**Miscellaneous**
- X1 = 5MHz quartz crystal
- LCD1 = graphic LCD 128 x 64, DEM128064
- K1,K4 = 2-pin pinheader
- K2 = 5-pin pinheader
- K3,K5 = 4-pin pinheader
- PCB # 090647-1 (www.elektor.com/090647)
Figure 5. Switching on the unit displays the GPS Search Image based on data received at 9600 baud.

Figure 6. If the data is correct a speed display appears in km/h. Pressing S2 will switch to miles per hour (mph) which is the norm around Rocklin, CA.

Figure 7. The compass-like display of motion direction.

Figure 8. History readout with graphical display of the speed progression.

progress readout through 90 minutes in 1 minute steps and additional display of current speed. At power-up the History function is first disabled. The word ‘OFF’ is displayed (on the right, next to ‘History’) indicates there is no data recorded. Operating switch S2 initiates recording and ‘ON’ appears. If switch S2 is held down for longer than 5 seconds in ‘OFF mode, the data is wiped. The legend ‘Erase Data’ alerts this. Another brief push of S2 from ‘ON’ to ‘OFF’ saves the current data into the EEPROM.

At lower left (at ‘#60’ on the display) a countdown mechanism is included. This decrements from 60 to 0, one second at a time, then the last second is shown. Next the countdown starts again from the beginning. This makes it easy to spot when the next display refresh is coming up. The countdown display is static when no GPS data is being received. Every minute a line is displayed showing the minimum and maximum speed limiting values.

Raw data display (Figure 9) with the data received from the GPS mouse. Press button pages through the data blocks $GPGGA, $GPGST and $GPRMC.

Group data display (Figure 10) with the display of all the data blocks: Speed in large characters, height above sea level, direction of movement, signal quality (0/1), total number of satellites, clock time and date (UTC time), position and maximum speed. The maximum speed achieved previously, which is also stored in the EEPROM, can be recalled by pressing S2. According to the module that was preselected for display on the analogue and digital speed display (Figure 6), the screen can show kilometres per hour (km/h) and metres (m) or miles per hour (mph) and feet (ft).

Internet Links
[1] www.parallax.com
[2] www.spinvent.co.uk/
(3) (Wikipedia, NMEA data format)
(detailed information on NMEA in German, use Google search engine or Google Chrome to translate)
(project page with software download and ordering details for PCB)

About the author
Following Communication Technology studies at University of Applied Sciences (UAS) in Bingen, Germany, Steffen Möritz has been employed for more than 20 years in the industrial automation field in the automotive industry. His spare time is occupied designing microcontroller circuits for automotive and model applications, primarily using Parallax Propeller and PIC microchips. Steffen can be reached at steffen@moeritz.com.
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ZigBee is a specification which defines a high level communication protocol using low power digital radios using the IEEE 802.15.4 standard. The idea behind ZigBee is that it offers an infrastructure for a Wireless Personal Area Network (WPAN) which is highly adaptive, simpler, less expensive and consumes less power than other WPANs.

There are essentially three flavours of ZigBee: ZigBee, ZigBee PRO and ZigBee RF4CE which is a similar technology that is used for RF control of consumer electronics.

The differences between the various technologies can be defined using a combination of the Network topologies, the security employed and the method of Network addressing. This can be shown in contrast in Table 1.

**Let's start networking...**
Each ZigBee network is essentially made up of a 'coordinator', 'router' and an 'end device'. They can all be fully functioning devices with an intrinsic function but will also perform ZigBee functionality. The coordinator forms and manages the network; the router can route messages as well as allow other devices to join the PAN router and the end device is just that. It can join a network but has no routing capability. These devices are defined in Figure 1. In essence the ZigBee network can be made up of two topologies: Tree/Star (Figure 2) or Mesh (Figure 3).

<table>
<thead>
<tr>
<th>Topology</th>
<th>ZigBee</th>
<th>ZigBee PRO</th>
<th>RF4CE</th>
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</thead>
<tbody>
<tr>
<td>Security</td>
<td>Normal</td>
<td>High</td>
<td>Normal</td>
</tr>
<tr>
<td>Network Addressing</td>
<td>Distributed</td>
<td>Stochastic</td>
<td>Pairing</td>
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One of the strengths of ZigBee PRO is the meshing network topology which means that the networks can be extended over considerable geographical areas, such as in a large building or even an industrial pipeline. It has a ‘self healing’ mechanism which means that if one of the components of a multihop route from one device to another leaves the network for any reason an alternative route using alternative adjacent devices will be identified and implemented.

As for the end products, in simple terms, a ZigBee device is constructed of a ZigBee compliant platform (a radio part and a ZigBee part) with the appropriate application (profile) sitting on top. The radio is based around the 802.15.4 standard (and comprises of a PHY and MAC layer) and the ZigBee ‘stack’ sits on top. The abstract layer models in Figures 4 and 5 illustrates this.

.. and then profiling

The ZigBee platform becomes a so called functioning ‘ZigBee device’ when it has a ‘profile’ placed on the top of it. A profile is a set of commands and attributes which are used to send specific messages over a ZigBee network. There are two types of profile: a Public Profile, which has been developed by the ZigBee Alliance members and has specific command attributes. Anybody employing a Public Profile must adhere to the rules of that particular profile. Presently there are several Public Profiles, which we will touch on later. There is also another type of profile, the Manufacturer Specific Profile (MSP). These are proprietary profiles which a manufacturer may develop because he wishes to use the ZigBee technology but not the Public Profiles.

Public ZigBee profiles

There are several profiles at present. These include

- ZSE, also known as Smart Energy;
- ZHA, also known as Home Automation;
- ZTS, also known as Telecoms Applications;
- ZHC, also known as Personal Healthcare;
- ZBA, also known as Construction and Building;
- ZRC, also known as Consumer Electronics.

Each is discussed briefly below.

Smart Energy Profile (ZSE) has been designed to be used in smart home area networks (HANs), to manage and improve the efficiency of energy consumption. There are many devices presently on the market from utility meters (gas, water and electric) to in-home displays, which can provide historical and real-time data indicating energy usage. Smart energy devices (Figure 6) are being enabled across the entire world from small households to entire governments.

Home Automation (ZHA) — was one of the first profiles to reach maturity and has developed to become a very robust and ubiquitous tool in the ZigBee box. It enables the user to simplify the house-

hold tasks and create a unique experience within the household. There are many products on the market which are very diverse in their applications; these include simple lighting control such as on/off and dimmer switches and lights. When used in connection with other types of technology the whole home automation system can be enhanced. For example, the light mood settings on one’s home can be set from a smartphone, or if arrival at home is going to be late the drapes could be closed remotely. The possibilities are end-
Telecoms Profile (ZTS) – is designed to enable information delivery to mobile-enabled devices. Imagine a visitor walking round a museum viewing the exhibits and when a particular exhibit is reached further information is required on this. Quite often it would mean reading the programme or typing in a number on a cumbersome audio handset supplied by the museum to play the specific audio. With ZigBee telecoms applications, the ZigBee enabled mobile device will automatically detect the location and send the appropriate information for that exhibit creating a more enhanced user experience.

Healthcare (ZHC) – is one of the newest profiles to be developed and is probably one of the most important profiles available. The world’s population is ageing, therefore greater demands are being placed on the healthcare infrastructure. ZigBee technology is being used to support the ageing population maintain their independence by providing assisted living support. There are devices which can be installed in an elderly user’s home which can monitor if they have a fall or can go further and monitor if a bath is overflowing or a gas cooker has been left on. In the instance that an ‘alert’ is issued a central monitoring station can pick up the message and provide assistance if needed.

In addition to this there is an increase in chronic medical conditions such as obesity, diabetes, chronic pulmonary disorders and high blood pressure. A ZigBee’d blood pressure meter is shown in Figure 7. As a result of this there are many organisations which have been formed with the objective of developing solutions and infrastructure to support the medical professions in meeting the demands of their patients needs. ZigBee technology is playing a very important role in healthcare and is increasingly being employed in medical devices or aids. This type of remote monitoring has attracted considerable interest over the last few years and some studies have suggested it could provide considerable savings for the worlds health services or enable doctors and health professionals to increase the number of patients they can attend to, in some cases doubling them!

Take for instance a diabetic who has to monitor their blood sugar 2-3 times per day. In the traditional system, they would take their reading on their handheld system and then usually record the results in a journal. They would take this to the doctor at their next appointment, which could be days or weeks away. The doctor would analyse the data and act upon it by which time the information recorded may have no bearing on how the patient is today. However, by using a ZigBee-enabled blood sugar monitor which is connected to a remote monitoring system (which could be a specialist centre staffed by qualified nurses or health professionals). The patient would use a device which would be connected to the remote monitoring station via a cradle. The patient would make their measurement which would be displayed on the meter, when the meter was replaced on the cradle the recorded data would be automatically sent to the remote monitor. This takes a few seconds compared to several days on the traditional system. Now the remote centre would assess the data and determine if any immediate action need be taken. A real life case in a trial of a similar system showed that a diabetic patient was not adhering to a strict diet and via the remote monitoring system a doctor was able to intervene before any serious condition occurred. So clearly the healthcare industry is one which can really benefit from ZigBee technology.
Construction and Building Automation (ZBA) — is designed to provide an adaptable system of management of complex buildings. It provides a means of economically centralising lighting, HVAC and security. This also allows remote monitoring of the efficiency of many of the systems. In addition to this, in new constructs where lighting and/or security systems need to be adaptable; this ZigBee profile provides an excellent way of achieving this adaptability.

RF4CE (ZRC) — This profile is built on the ZigBee RF4CE platform and will allow an advanced functionality and user experience in the control of home entertainment equipment and domestic products. Although at the time of writing this profile is in its infancy, the market requirement is constantly changing and the applications in which we will see the RF4CE profile are potentially endless.

Conclusion
Clearly ZigBee is incredibly versatile and in some respects it is only limited by the imagination of the designer to come up with innovative and fantastic new ideas for applications in which to employ the technology. Yes ZigBee does have its critics, and always will, which is embraced, as criticism is the fuel of innovation. So in the greater perspective ZigBee has a lot to offer and is clearly here to stay; as can be seen from the various examples above ZigBee can (and will) be used in many different product types and applications and who knows what’s next?

(090895)
Many electronics enthusiasts and professionals use the open-source operating system Linux because they appreciate the technological elegance of this proven and continually upgraded operating system. The Elektor ATM18 AVR board and the Minimod18 can also be used with Linux. Here we describe how to program these two boards in the popular Linux OS environment.

Windows users have it easy. If you have a suitable USB programmer, you can program the microcontroller module of the ATmega board directly in the AVR Studio development environment. Linux users, by contrast, are faced with an initial problem because AVR Studio is only available for Windows. Unfortunately, it does not run properly inside the Windows API wrapper Wine, which is able to simulate the Windows OS environment for many Windows programs. But where there's a will, there's a way. With the command-line program AVRdude, you can use the ATM18 AVR board under Linux. Of course, this also works with Minimod18, although 'direct' microcontroller programming is usually not necessary with this especially compact module because Minimod18 has a boot loader that can receive hex files via USB and store them in memory. If you 'overhaul' the Minimod18 with a programmer, the boot loader will be overwritten and one of the key features of the module will be lost. In this case, the boot loader must be downloaded into flash memory again to restore normal operation. The following instructions apply to a programmer compatible with AVRISP Mk II, such as the Elektor AVRprog USB programmer designed by Benedikt Sauter [3]. If you use a different type of programmer, see the inset for more information. The instructions are based on a Debian distribution of Linux.

Installation
Here we assume that you have a good basic understanding of how to install and use programs on a Linux system. In order to program the microcontroller of the Elektor ATM18 AVR board, you first need some software. As usual, you can use apt-get to meet this requirement fairly simply (assuming the computer has a connection to the Internet). However, you must first log in as the root user by typing su or sudo (depending on the distribution) and your password immediately ahead of the apt-get command.

Next, enter the following command after the prompt:

```
apt-get install avrdude avrpgcc-avr avr-libc gdb-avr avra make udev
```

Answer the question ‘Do you want to continue?’ with ‘Yes’.
The necessary packages will now be loaded and preconfigured. A few older packages may be de-installed and replaced by other packages or newer versions. If you see a message reporting that the kernel does not support udev, you must update the kernel before udev (or a new version of udev) can be loaded. Although it is possible to continue with the installation in this situation, you should not do so, since there is a risk of a system crash. A reliable kernel update is always the preferred solution. However, you can rest assured that Linux does not make it easy for foolhardy users to make mistakes of this sort. A particular file must be altered before Linux actually comes into danger.

Before you update the kernel, it's a good idea to quickly check whether the installation can actually run with the udev version already present. As the saying goes: nothing ventured, nothing gained!

After the installation process has completed, you can connect the USB programmer to the USB port and use the lsusb command to determine the device ID:

```
pole1:/home/wolf# lsusb
Bus 005 Device 003: ID 090c:6300 Feiya
   Technology Corp.
Bus 005 Device 001: ID 1d6b:0002 Linux
   Foundation 2.0 root hub
Bus 004 Device 002: ID 046d:c517 Logitech, Inc. 
   LX710 Cordless Desktop Laser
Bus 004 Device 001: ID 1d6b:0001 Linux
   Foundation 1.1 root hub
Bus 003 Device 001: ID 1d6b:0001 Linux
   Foundation 1.1 root hub
Bus 002 Device 001: ID 1d6b:0001 Linux
   Foundation 1.1 root hub
Bus 001 Device 004: ID 0a12:0001 Cambridge
   Silicon Radio, Ltd Bluetooth Dongle (HCI
   mode)
Bus 001 Device 001: ID 1d6b:0001 Linux
   Foundation 1.1 root hub
Bus 001 Device 005: ID 03eb:2104 Elektor
   wolf@pole1:-$
```

The ID of our programmer is 03eb:2104. If you can't read the ID with lsusb (sometimes new devices are not shown), you can use dmesg instead:

```
[ 5145.834284] usb 5-7.4.4: new full speed USB device using ehci_hcd and address 10
[ 5146.150088] usb 5-7.4.4: config 1 descriptor has 1 excess byte, ignoring
[ 5146.150088] usb 5-7.4.4: configuration #1 chosen from 1 choice
[ 5146.154083] usb 5-7.4.4: New USB device found, idVendor=03eb, idProduct=2104
```

The most recently recognised device appears at the end of the (long) output list. With the Elektor programmer, you will see idVendor=03eb, idProduct=2104. This gives you the assurance that the programmer has been recognised properly. If you don't like wading through the full dmesg output list, you can type dmesg | tail to display only the tail of the list. Incidentally, the number in square brackets at the start of each line in the list is not significant: it is simply the elapsed operating time of the computer, and it is not displayed by all versions of Linux.

If you use a different programmer, it will of course be reported with a different ID. If you can't find it in the device list, disconnect the programmer from the USB hub and enter the lsusb or dmesg command again. Now you should be able to see what has changed in the command output (which device is missing or has been added).

To avoid having to log in as the root user every time you use the programmer, you can edit the permissions.rules file, which is accessed via path /etc/udev/rules.d/020. Near the end of this file, just before LABEL="permissions_end", there is an entry containing the device ID you found above. You can use the vi editor (or any other suitable editor) to make the desired changes:

```
vi /etc/udev/rules.d/020_permissions.rules
```

```bash
# AVR ISP mkII Atmega Programmer
SUBSYSTEM=="usb_device",
  SYSEX{idVendor}="03eb",
  SYSEX{idProduct}"=2104", GROUP="plugdev",
```

Figure 1. Hey, it works under Linux too: microcontroller programming with the Elektor AVRprog USB programmer [3].
A matter of taste: command lines

Graphic front ends for AVRdude (which has a command line user interface) are available on the Web, although we haven't tested any of them. The fact that we restrict ourselves to the command line interface here should not be regarded as a value judgement; it simply reflects the notorious difference between Linux users and Windows users.

If your distaste for command line interfaces is sufficiently large, you can try alternatives such as a Java GUI [6] or a KDE program called Kontrollerlab [7], although it appears that the latter program is no longer being maintained. With the exception of the simulator, Kontrollerlab is certainly usable as a substitute for AVR Studio under Linux. Unlike AVR Studio, it supports AVRdude without plugins, so you can download program code to the ATmega directly from the IDE.

Other programmers

The AVRdude program has a large number of options. For example,

```
MODE="0660", SYMLINK="foo"
LABEL="permissions_end"
```

If you enter "plugdev" for GROUP=, users without root privileges will also be able to use the programmer.

If vi (the grandfather of all editors) isn't your cup of tea, you can make the changes with any other editor that has root privileges. A good choice here is Midnight Commander, which is something of a Swiss army knife for Linux.

The changes take effect only after the udev configuration has been reloaded as follows:

```
udevcontrol reload_rules
```

Now you can query the programmer with the following command:

```
avrdude -c avrispv2 -P usb -p m88
```

It replies:

```
apache: ATR device initialized and ready to accept instructions
Reading | *******************************************
| 100% 0.01s

avrdude: Device signature = 0x1e930a
avrdude: safemode: Fuses OK
avrdude done. Thank you.
```

All of the above applies equally well to the Minimod18. Its boot loader simulates a programmer (more specifically, a USBasp program) and can also communicate with AVRdude. In this case, you must enter the parameter -c USBasp instead of -c avrispv2 -P usb.

You also have to enter -p m328p for the microcontroller, as other-
wise AVRdude will complain that the signature is wrong.

**A hot topic: fuses**

This is not the place for playing around, since you can easily mess things up with the wrong fuse settings, or even make the microcontroller unusable. Whenever you need to change fuse settings, always engage your brain first and think things twice before you do anything.

Enter the following command to read the fuse settings:

```
avrdude -p m88 -c avrisp2v2 -P usb -F -v -B 10
-U hfuse:r:high.txt:i -U lfuse:r:low.txt:i
```

**AVRdude responds with:**

```
avrdude: Version 5.10, compiled on Jan 19 2010
at 18:01:15
Copyright (c) 2000-2005 Brian Dean,
http://www.bdmicro.com/
Copyright (c) 2007-2009 Joerg Wunsch

avrdude: AVR device initialized and ready to accept instructions

Reading | ################################### | 100% 0.22s
avrdude: Device signature = 0xff00ff
avrdude: Expected signature for ATMEGA88 is 1E 93 0A
avrdude: safemode: lfuse reads as 62
avrdude: safemode: hfuse reads as DF
avrdude: safemode: efuse reads as 7
avrdude: reading hfuse memory:

Reading | ################################### | 100% 0.00s
avrdude: writing output file "high.txt"
avrdude: reading lfuse memory:

Reading | ################################### | 100% 0.00s
avrdude: writing output file "low.txt"
avrdude done. Thank you.
polel:/home/wolf#
```

There’s not enough space here to print the full response (AVRdude

is a real blabbermouth). The two lines you’re interested in at this point are:

```
avrdude: safemode: lfuse reads as 62
avrdude: safemode: hfuse reads as DF
```

This gives you the fuse settings you need to know. At the same time, they are written to the files low.txt and high.txt as specified in the command. You can view the file contents with:

```
cat low.txt.orig
:01000000629D :00000001FF
```

```
cat high.txt.orig
:01000000DF20 :00000001FF
```

If it’s necessary to change the fuse settings, there is a handy tool available for calculating the hex values: Fusescalc [4]. Of course, you can also calculate them by hand with the aid of the data sheet. However, remember what we said: a single mistake could be fatal for the microcontroller.

You can use AVRdude to write the fuse settings to the microcontroller in the same way as you read them:

```
avrdude -c avrisp2v2 -P usb -p m88 -U
-lfuse:w:0x62:m -U hfuse:w:0xDF:m
```

Here we omit the response from AVRdude, which gets a bit boring after a while. The main thing to check is that there aren’t any error messages. If there are, first check the connecting cables and the supply voltage. If errors are indicated but you cannot find the cause, you can use the -f option to force AVRdude to try to write the settings to the microcontroller despite the error message(s).
Knuckle down, ATmega!
The next thing you need is a program for the ATmega. You can write your own with gcc or BASCOM (which runs nicely under Wine), or you can use any desired program you already have available, such as the LED running-light display program from the first article in the ATM18 AVR board series (which can be downloaded free of charge from the Elektor website[1]). In any case, you only need the hex file, which is designated main.hex here. Use the following command to download the hex file to the flash memory of the microcontroller:

```
avrdude -p m88c -c avrispv2 -P usb -F -v -B 10 -F -U flash:w:main.hex
```

The response from AVRdude (only the final part is printed here) is:

```
avrdude: Device signature = 0xl6930a
avrdude: safemode: lfuse reads as FF
avrdude: safemode: hfuse reads as DF
avrdude: safemode: efuse reads as 1
avrdude: NOTE: FLASH memory has been specified, an erase cycle will be performed
To disable this feature, specify the -D option.
```

```
avrdude: erasing chip
avrdude: reading input file "main.hex"
avrdude: input file main.hex auto detected as Intel Hex
avrdude: writing flash (146 bytes):
```

```
Writing | ############################################################### | 100% 0.01s
```

```
avrdude: 146 bytes of flash written
avrdude: verifying flash memory against main.hex:
avrdude: load data flash data from input file main.hex:
avrdude: input file main.hex auto detected as Intel Hex
avrdude: input file main.hex contains 146 bytes
avrdude: reading on-chip flash data:
```

```
Reading | ############################################################### | 100% 0.07s
```

```
avrdude: verifying ...
avrdude: safemode: lfuse reads as 62
avrdude: safemode: hfuse reads as DF
avrdude: safemode: efuse reads as 1
avrdude: safemode: Fuses OK
avrdude done. Thank you.
```

Now the microcontroller has the new program code in its flash memory, and you can use the program. Don’t forget to connect the appropriate I/O pins of the microcontroller to the inputs of the ULN2003 on the ATM18 AVR board [5], so you can see the running-light pattern on the LEDs.

You need a different program for the Minimod18, since it doesn’t have any LEDs on board (but it does have a display module). A program that outputs a simple message is adequate for testing.

As you can see, it’s not that difficult to use the ATM18 AVR board or Minimod18 module with a Debian Linux distribution. If you use a different distribution, you will need to modify these instructions appropriately because program installation is slightly different and there are a few other differences in how things are done.

We conclude our instructions in the friendly spirit of AVRdude:

Elektor/cc2 done. Thank you!

Now you can start having fun with the ATM18 AVR board or Minimod18 in your Linux environment!

(100177-1)

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Power Line Communication & the Chip Issue

Will S-FSK win the ‘format war’?

By Giacomo Cesari (Future Electronics, Italy)

If spread-spectrum frequency shift keying (S-FSK) becomes the dominant standard for smart meters, components implementing it will become commercially attractive to OEMs for any kind of PLC with a low data-rate requirement, for applications such as building automation and street lighting control. Here’s a guide to implementing S-FSK technology in new modem designs, describing how System-on-Chip (SoC) devices address the challenges of the application. Chip hunters — enjoy!

While technology for implementing Power Line Communication (PLC) has been in existence for decades, it has not yet succeeded in becoming a mainstream application. This is not least because of the technical difficulty of transmitting data reliably over a medium that also carries high alternating currents. Governments are responding to the threat of climate change by investing billions in the installation of smart energy meters, which promise to reduce power consumption and to improve the regulation of demand. The vision for smart meters, however, requires a means of communicating to and from a control centre, and the power line would appear to be a convenient medium for this. And so meter manufacturers and their utility company customers are now witnessing something like the 1980s’ Betamax vs. VHS ‘format war’, as competing semiconductor suppliers push rival technologies for implementing meter networks, not only via PLC but also short-range radio, cellular radio and so on.

What makes S-FSK so popular?

Frequency band allocations across power lines differ from region to region of the world. In Europe, EN 50065-1 (published by CENELEC) specifies the frequency allocations from 3 kHz to 148.5 kHz, see Table 1.

In North America, FCC Part 15 defines a wider and less rigid spectrum allocation, across a range greater than 500 kHz. In FSK modulation schemes, information is transferred to the Power Line in the form of two frequencies: \( f_s \) and \( f_m \) (where the index ‘s’ (space) means 0, and ‘m’ (mark) means 1). FSK modulation has been used for many years, and in the past, when it was implemented in the analogue domain, it was neither cost-effective nor easy to set these two frequencies more than around 2 kHz apart. Consequently these systems were very vulnerable to noise: any other device connected to the power line that generated noise at the frequency of the signal — this could be as simple as a vacuum cleaner — could cause the communication channel between nodes to be dropped. S-FSK is much less vulnerable to noise: this is because, in S-FSK, \( f_s \) and \( f_m \) are set typically 10 kHz apart. This development was possible because, in the digital domain, it is easy to generate these frequencies inside an IC. Because \( f_s \) and \( f_m \) are far apart, it is extremely unlikely that noise will swamp both frequencies simultaneously. The latest S-FSK ICs from ON Semiconductor and STMicroelectronics are also able to switch back to amplitude modulation to keep the channel open in case extremely high noise levels make frequency modulation impossible.

So noise at one of the frequencies might reduce the effective data rate, but the communication channel stays alive. This means that the S-FSK modulation scheme offers very high reliability — a vital characteristic for smart meter applications, which transmit mission-critical usage data.

The S-FSK technique is supported by industry collectively: IEC 61334 is a set of standards, aimed primarily at Smart Meter applications, which implements S-FSK. Large-scale deployments are now start-
ing to happen: France, for instance, has begun a very large smart meter deployment that uses S-FSK. But other countries are waiting for regulatory bodies to define other standards before they make a decision on which technology to implement. Indeed, while S-FSK provides a low data rate (up to 2400 bits/s), other modulation schemes promise higher rates. Some of these (for instance OFDM and DSSS), are being tested or even, in the case of the Differential Code Shift Keying (DCSK) technique developed by IC manufacturer Yitran, being deployed (in Spain). But for smart meters, S-FSK is definitely the front runner because of its proven reliability in the field. This means that integrated devices supporting the technique are becoming available in volume and at competitive prices. This in turn could spur a wider range of applications to adopt S-FSK.

What, then, are the main challenges facing the designer of a PLC modem that implements S-FSK? And to what extent do today's SoCs deliver a complete solution? Let's begin by understanding the principal functions of a power line modem.

### Basic architecture of a power line modem
A PLC modem's architecture typically consists of three blocks:
- Analogue Front End (AFE), comprising a coupling device, filters and a power stage
- Physical layer (PHY)
- Protocol

The physical layer incorporates a system for FSK modulation, filters and an amplifier. The protocol layer is the means by which communication between multiple nodes is managed. The protocol is responsible for safely transferring information between two or more nodes, managing the payload by encrypting it (if needed — this is not required in all cases), and generating the correct formatting for the frame to transmit properly. Figure 1 shows a standard PLC frame format, as defined by IEC 61334-5-1.

Whatever the frame format used, the protocol layer provides the following functions:
- Acknowledgement-based signalling, with the capability to retransmit lost packets
- CRC error detection to test the integrity of received data
- Carrier Sense Multiple Access (CSMA) capability using the BIU (Band In Use) detector to check if the line is in use — a means of collision-avoidance.
- Addressing
- Encrypting of data for transmission
- Full control over the following transmission parameters:
  - Acknowledged
  - Unacknowledged
  - Repeated transmit
  - Sequence numbering

**Extent of integration in today's modem ICs**
Early moves by semiconductor manufacturers to satisfy demand for S-FSK devices failed to provide completely integrated solutions, or left a significant amount of software implementation to the OEM designer. A popular reference design for S-FSK modems, DRM035, was released by the then Motorola Semiconductor in 2003, based on the 56F801 digital signal controller. Still marketed by Freescale Semiconductor today, the reference design can be used as a blueprint for the design engineer to develop their own solution. But this approach of implementing modulation and protocol layer functions in software left the design engineer to implement the AFE discretely — a tricky task. PLC systems connected to the AC grid must, at least in Europe, comply with the following standards:

- Powerline signalling (EN50065-1:2001, FCC Part 15)
- Powerline immunity (EN50065-2-1:2003, EN61000-3-2/3)
- Safety (EN60950)

**How to couple a PLC system to the grid**
Generally speaking, a coupling network (capacitor and transformer) is the means by which signals are injected (or received) onto (or from) the AC power line ('mains'). One side of the transformer is driven by the output stage of the PLC modem's amplifier.

The role of the capacitor is to reject the grid's 50 Hz (60 Hz) voltage; it also provides impedance to make the connection of the two voltage sources (AC grid and power line signalling) in parallel. The power amplifier driving the transformer can be implemented in two ways: with discrete transistors integrated inside an S-FSK IC (as in the ST7570); or in the form of a dedicated IC (such as the

![Figure 1. Standard power line data frame structure (IEC61334-5-1).](image-url)
AMIS-49587). An integrated power amplifier offers a very compact design, but it might in some instances restrict range, and be limited in its temperature tolerance. This could occur, for example, if the impedance at the power amplifier is very low.

For applications that require galvanic isolation, Avago Technologies has introduced the HCPL-800J coupling IC. As shown in Figure 2, the isolated output stage can directly drive a coupling network made of a capacitor and inductor.

Coupling to the power line is not the only problematic area of the AFE — the band pass filter is also important. Before designing a suitable filter, it is necessary to consider in which region of the world the modem is to be operated. In Europe, the EN50065 spurious emission limit curve governs the maximum level of all harmonics for all frequencies in the range from 10 kHz to 148 kHz. For example, if a modem transmits on a carrier frequency of 120 kHz, it is necessary to ensure that the second and third harmonics are at a level below 62 dBμV and 58 dBμV respectively.

The situation in the US is different: here, regulations do not specifically limit all harmonics, but rather limit only the harmonic levels at frequencies greater than 500 kHz. This means that if the same 120 kHz modem is to be used in the US, only out-of-band harmonics greater than the fourth order are of concern. Also there is a limit in the amplitude of the output signal of 122 dBμV.

**Today's integrated solutions**
The latest semiconductor product introductions for S-FSK modems are far more integrated than the Freescale reference design mentioned above — in fact, they are best described as S-FSK modem systems-on-chip. The AMIS-49587 from ON Semiconductor, released in its latest version in December 2009, implements the physical interface (PHY) in mixed-signal hardware blocks and the Media Access Control (MAC) and protocol layer on an embedded firmware ROM. The firmware runs on an embedded ARM7 core. The device can communicate with an applications processor via Serial Communications Interface (SCI).

By eliminating the need for the system designer to develop the protocol layer, ON Semiconductor greatly accelerates the end product development process — at an applications level, the system just needs to feed raw data to the AMIS-49587 over the SCI, and the SoC handles all the processing and transmission of the data.

The intention of STMicroelectronics' S-FSK SoC, the ST7570, is similar. It is a complete implementation of an S-FSK modem, including the whole AFE. Its architecture is slightly different from the AMIS-49587: the ST7570 performs modulation through turnkey firmware running on a DSP core; it uses an embedded 8051 controller core to implement the protocol layer's PHY and MAC.

These new devices – the ST7570 is about to come out of the ‘sampling’ phase — illustrate the level of integration that has just become available to designers of S-FSK modems. The main challenge in designing PLC networking gear has therefore now shifted from implementing the communications — this job is now done by SoCs — to value-added functions, such as the user interface, and appliance monitoring and control capabilities.

Perhaps this availability of turnkey IC designs, from multiple IC manufacturers that are big enough to support high-volume production, will do more than anything to drive the market towards the S-FSK as the modulation technique of choice for power line communications. The decisive blow in the PLC modulation 'format wars' might already have been landed.

---

**Table 1. Frequency band allocations under EN 50065-1.**

<table>
<thead>
<tr>
<th>Band name</th>
<th>Frequency range</th>
<th>Usage</th>
</tr>
</thead>
<tbody>
<tr>
<td>Band A</td>
<td>3 kHz – 95 kHz</td>
<td>For utility companies only</td>
</tr>
<tr>
<td>Band B</td>
<td>95 kHz – 125 kHz</td>
<td>Applications without an access protocol</td>
</tr>
<tr>
<td>Band C</td>
<td>125 kHz – 140 kHz</td>
<td>Applications with an access protocol</td>
</tr>
<tr>
<td>Band D</td>
<td>140 kHz – 148 kHz</td>
<td>Alarm and security applications with no access protocol</td>
</tr>
</tbody>
</table>
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Wheelie GT

Gear tooth sensing improves handling

By Günter Gerold and Uwe Hofmann (Germany)

It’s been over a year since we introduced the Elektor Wheelie (a self-balancing single axle vehicle). Now the GT modification described here senses the speed of each road wheel to give better stability and improved performance over uneven terrain.

Up until now the Elektor Wheelie control system used an accelerometer and a tilt sensor to sense movement of the vehicle’s footplate. The tilt angle and the rate of change of the tilt angle are used in a control system which drives two motors to keep the vehicle stable. Leaning forwards tilts the plate forwards and produces an acceleration response from the control system; leaning backwards has the opposite effect. The regulation is applied to both motors equally. A pot sensing left/right tilting of the control lever adjusts the current to the drive motors thereby allowing the Wheelie to turn corners.

The control system does not have any information on the rotational speed of the wheels so it cannot compensate for any load variation on individual wheels. When a wheel hits an obstacle the control system reacts by increasing drive to the free wheel, producing an unwanted turn. Off-road capabilities of the original Wheelie are therefore somewhat limited.

To solve this problem we have added rotation sensors to each of the two wheels. Now the control system can sense the wheel speed and adjust power to the individual motor, helping it to overcome obstacles.

The gear tooth sensor

A good way to measure the rotational speed of a gear wheel is to use a gear tooth sensor. This device (Figure 1) uses a magnet and a Hall-Effect sensor to measure magnetic field strength. When the device is placed close to the teeth of a rotating gear wheel the disturbance caused by each tooth moving through the field can be detected by the Hall-Effect sensor which outputs a sawtooth waveform. The ATS665 detector from Allegro includes a signal conditioning circuit with an amplifier and comparator (Figure 2); this generates a more useful square wave output signal. These sensors were originally developed for use in vehicular ABS systems and drive control.

The Elektor Wheelie uses a direct drive system with the final drive gear wheel connected directly to the road wheel. This configuration makes it simple to fit this type of sensor to the gearbox. This non-contact form of revolution sensing is very reliable, is not subject to wear and can be completely sealed from the environment. A small PCB (Figure 3) has been designed for mounting the sensor together with the other three components shown in the circuit in Figure 4. The board greatly simplifies the job of fitting the sensor to the gearbox. The newer ATS667 from Allegro can be substituted for the ATS665 shown in the circuit diagram.

Evaluating the data

The ATMega8 in Figure 5 evaluates the speed impulses received from the two sensors connected to K3 and K4. Connector K1 provides an I²C interface between the ATMega8 evaluation board and the existing ATMega32 (pins 22 and 23) on the Wheelie main board.
The sensors output 60 impulses for each revolution of the road wheel. A method of measuring speed by counting the number of impulses in a given time window does not give sufficient resolution especially when the Wheelie is travelling at low speeds. It is better to measure the time between impulses i.e. the time it takes for two gear teeth to pass the sensor. A free running timer is used to measure this period, the firmware keeps track of the timer overflows to calculate the time interval. The evaluation board configures itself as an PIC slave. When a request is received from the ATMega32 main board the evaluation board responds by returning the values.

The values sent are the reciprocal of the wheel speed. The ATMega32 first calculates the speed in km/h then the individual wheel speeds are summed and the value used to scale the control lever sensitivity.

The speed reading could also be used in the development of a speedometer for the Wheelie or as one of the operating parameters transferred over a telemetry link.

Fitting the components
The complete additional parts required to implement the GT modifications consists of a evaluation PCB to take the ATMega8 microcontroller and two small PCBs on which the sensors are mounted. The evaluation board (Figure 6) contains no SMDs and is not at all difficult to assemble. The sensor PCBs however have two SMD components (R1 and C1) which need to be soldered on the track side of the board. The Hall-effect

![Figure 2. The ATS665 sensor contains an integrated amplifier and comparator.](image)

**COMPONENT LIST**

**Sensor board**

Resistors

- R1 = 10kΩ

Capacitors

- C1 = 100nF

**Semiconductors**

- IC1 = ATS665 (or ATS667, see text)

**Miscellaneous**

- K1 = 3-pin pinheader
- PCB no. 100479-2*

*A kit of parts containing signal processing board (100479-1), two sensor boards (100479-2) and all components is available from the Elektor Shop, order code 100479-71.

![Figure 3. A small PCB simplifies the job of fitting the sensor to the gearbox.](image)

![Figure 4. Circuit diagram of the sensor PCB.](image)

![Figure 5. The evaluation board ATMega8 processes both gear tooth sensor signals.](image)
Microcontrollers

Figure 7. Mounting the sensor on the small PCB.

Figure 8. Release the four bolts and carefully remove the casing which holds the stub axle and the final drive gear wheel.

Figure 9. The correct hole position from the casing edge.

Table 1.
Connections between the ATMega8 PCB and the Wheelie main board

<table>
<thead>
<tr>
<th>ATMega8 signal processing board</th>
<th>Connection to the Wheelie main board</th>
</tr>
</thead>
<tbody>
<tr>
<td>K1/ Pin 1 (GND)</td>
<td>K3/ Pin 5 (GND)</td>
</tr>
<tr>
<td>K1/ Pin 2 (+5 V)</td>
<td>K2/ Pin 2 (+5 V)</td>
</tr>
<tr>
<td>K1/ Pin 3 (SDA)</td>
<td>IC7/ Pin 23 (SDA)</td>
</tr>
<tr>
<td>K1/ Pin 4 (SCL)</td>
<td>IC7/ Pin 22 (SCL)</td>
</tr>
</tbody>
</table>

Sensor

The sensor is mounted from the other side of the board and soldered on the track side. The legs are first bent flat along the body of the sensor (Figure 1) and then pushed through the board and soldered to the pads on the track side. Note that a small gap should be left between the sensor and PCB (Figure 7). This allows the sensor to be fixed to the PCB using epoxy adhesive. The lead ends can now be cropped close to the solder joints on the PCB. Finally a short length of heat shrink sleeving over the sensor will prevent any possibility of the leads shorting to the gearbox casing. Connections to the board can be made by soldering the three wires directly to the pads of K1. This is more robust and removes any reliability issues of a connector at K1.

Construction

The most time-consuming part of this modification will be fitting the rotation sensors into the two motor gearboxes. The greatest challenge is drilling one hole in the gearbox aluminium casing — and get it right first time round!

Firstly undo the four screws holding the gearbox cover in position then pull the cover off complete with the stub axle and large gearwheel (Figure 8). The housing will be filled with grease. The ball race housing contains a spring washer which will most likely adhere to the ball race as you pull it out of the housing. Keep this washer safe, you will need it later during reassembly.

The position of the hole to take the sensor can now be marked on the casing, it is 11.5 mm (0.45 inch) away from the edge of the casing (Figure 9 and Figure 10). Use a centre punch to mark the point; it will prevent the drill from wandering. Before drilling it will be necessary to clean the grease away from the area where the hole will be drilled. Lay some paper kitchen towels or clean rags on the bench to catch any swarf. Debris which does find its way into the casing can be removed with a pair of tweezers (the more meticulous types out there will have no doubt remove all the grease, flush out the housing after drilling and re-pack with grease).

Once the position has been marked and double checked make a pilot hole through the casing using a 3 mm (1/8 inch) drill. Check the position of the hole again before drilling through with a 9 mm (3/8 inch) drill. The sensor PCB (with its cables attached) can now be permanently fixed to the casing using two-pack epoxy glue (with the sensor chip positioned into the hole). Be sure to degrease and abrade this area of the case before gluing. Once the glue has set the hole (Figure 11) will be completely sealed by the PCB (Figure 12) and the sensor will be close enough to the gear teeth to generate impulses when the cog turns. Any debris remaining in the gearbox housing must be carefully removed before re-greasing the gear wheels and reassembling the housing (don't forget the spring washer).

Wiring

Three-core cables are used to connect the sensor PCBs to the ATMega8 PCB.
A four-way flat band cable connects to K1 of the ATMega8 PCB and to the points specified in Table 1 on the existing main board in the Wheelie.

**Software and Updates**

The firmware for the ATMega8 fitted to the evaluation board can be downloaded for free from the Elektor Website. The direct link is: www.elektor.com/100479.

A commented source file is included in the download folder for this article along with the Hexfile.

The Elektor shop stocks the complete kit to convert the standard Wheelie to the Wheelie GT including a pre-programmed ATMega8.

It will also be necessary to upgrade the existing ATMega32 main board firmware in the Wheelie so that it can handle data from the evaluation board. The new firmware (hex and source code) is available for free download from www.elektor.com/100479.

Some hardware modifications will also necessary to the main board for Wheelies using the GT sensors:

1. Replace the three 470 µF capacitors C1, C2 and C3 with better spec 1000 µF capacitors (use low ESR types e.g. the Panasonic FM series).
2. Change R14 to 47 kΩ and add a 100 nF capacitor between IC7/Pin34 (ADC6) and ground (solder it between the correct leg of R14 and ground)

The improvement in stability of the Wheelie GT version (especially in off-road situations) can be seen on one of the author’s YouTube videos; search for guenter1604.

Figure 10. The motor before...

Figure 11. ...and after fixing the sensor PCB
From Microphone to Line Input

By A.J. Ribbink (The Netherlands)

The requirements a microphone preamplifier is expected to meet are very different from those formulated for a line amplifier. For the first, a large gain and a low noise contribution are very important, while a line amplifier has to be able to handle a much larger signal without generating distortion. A microphone input has a sensitivity of a few millivolts; a line input must be able to cope with several volts.

In most cases a different type of circuit is used for each of these different types of input. With the circuit described here it is however possible to meet both requirements at the same time. Using only a single potentiometer, both the gain and the volume can be adjusted.

A dead common (audio) amplifier is shown in Figure 1. In this circuit the amplification (gain) $A$ is determined by the ratio of the feedback resistor $R_1$ to $R_1$ as follows:

$$A = (R+R_1)/R_1.$$  

[Diagram 1: Microphone input circuit]

Volume control $R_p$ is really nothing more than a voltage divider comprising $R_3$ and $R_2$, where $R_2 + R_3 = R_p$. Drawing it in a different way results in the schematic of Figure 2. The operation of the voltage divider can be expressed as:

$$D = R_2/(R_2+R_3).$$

Gain factor $A$ and division ratio $D$ together determine the amplification of the overall circuit as in

$$U_{out} = A 	imes D 	imes U_{in}.$$  

[Diagram 2: Volume control circuit]

The output voltage is therefore adjustable by changing $R_1$ and/or $R_2$.

Because of the opposite actions of $R_1$ and $R_2$ — a larger value of $R_1$ results in a smaller output voltage, while a larger value of $R_2$ increases the output voltage — these two resistors can be combined as illustrated in Figure 3. In this way, using only a single potentiometer, both gain and volume can be controlled.

[Diagram 3: Combined gain and volume control circuit]

$R_1$ is still present in Figure 3, where its function is to limit the maximum gain. The additional resistor $R_4$ has two purposes: It prevents a potential (undesired) short circuit of the amplifier output and forms the voltage divider for the volume control, together with the $R_3$ part of the potentiometer.

The circuit can be used with ICs as well as amplifiers built from discrete components. The value of $R_4$ can be changed to the desired output impedance and the volume control curve. If control all that way down to zero volts at $U_{out}$ is not required, then $U_{out}$ can also be taken directly from the output of the amplifier circuit.

A bad contact in the wiper of the potentiometer will not result in too much of a cracking noise, because $R_1+R_p+R_4$ are connected in parallel with feedback resistor $R$. The gain is reduced in the event of poor wiper contact, which means that cracking noise remains limited. In most circuits $R_1$ has a much smaller value than $R_2$. For a smooth control, $R_1$ should therefore change value much slower than $R_2$.

A (reverse) logarithmic potentiometer is therefore recommended. With a normal logarithmic potentiometer the volume will reduce when the potentiometer is turned clockwise! A reverse-log potentiometer or a logarithmic slide potentiometer mounted 'upside down' gives a normal, familiar control.

Suitable resistance values are, for example, 50 kΩ for $R_p$, 22 kΩ for $R_1$, 56 Ω for $R_1$ and 220 Ω for $R_4$. Using these values the maximum and minimum amplification are about 360 and 1.5, respectively.

At the lowest operating frequency the capacitors need to have a (much) smaller impedance than $R_1$ and $R_4$. The input impedance of the circuit is mostly determined by resistor $R_m$ and can be adjusted to suit whatever signal sources are likely to be connected. Noise suppression filters and/or frequency dependent networks can be connected in front of this. Frequency dependent feedback is not recommended for this circuit, because the the RC filter characteristic will change when the gain and volume are adjusted.

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By Andre Adrian, DL1ADR (Germany)

The first transistor radio was called Regency TR-1 and appeared on the market in 1954, at a time when radios with vacuum tubes had been around for decades. One of the most successful tubes is the pentode, a vacuum electron tube with five (Greek: pente) electrodes. A famous early pentode was the ‘RCA-34’ (UX-234), it reached commercial use in the early 1930s and was announced as a ‘Super-Control R-F Amplifier Pentode’. To celebrate the Pentode’s 80th birthday this article in the Retronics series describes the construction of a shortwave radio for the 49 m and 41 m broadcast bands, based on the ‘34′ pentode or later types. With only one tube and a 1.5 m (5 ft.) wire antenna, reasonable reception at headphone volume levels is within reach. Depending on the tube used, the anode voltage is a safe 6 to 45 volts supplied by batteries. The radio can be built in ‘classic’ fashion using a flat coil and variable capacitor, or in modern guise using a ferrite core and a variable capacitance diode. The successor to the pentode, the field effect transistor (FET), can also be used, although its higher gain will make tuning the radio a real achievement.

In the beginning: tungsten
The hot tungsten filament of an incandescent lamp emits electrons. This was observed by Thomas Alva Edison in 1883 as he installed an electrode next to a filament in the bulb. In vacuum, a current flows between filament and anode. In the course of the years more electrodes were ‘inserted’ between filament and anode. Beside the cathode and anode, the pentode has a control grid, a screen grid and a suppressor grid. The control grid can be said to correspond to the gate of a FET. The screen grid is usually connected to the positive supply rail and the suppressor grid is tied to the negative terminal of the anode battery.

The ‘34′ pentode has a barium oxide coated filament. The filament requires 2 V at about 60 mA. The resulting 120 milliwatts cause the filament to heat up to 800 °C and to glow nearly invisibly. The Russian subminiature pentode type 1H18B (1j18b) requires even less heating power: 1.2 V at 24 mA.

In Figure 1 you see pictured, left to right: type RES164 pentode (ca. 1928), type 34 pentode (ca. 1930), miniature pentode type EF97 (1957), and a subminiature pentode type 1H18B. The (spectacularly) low-voltage type EF97 can work with just 6 volts on its ‘plate’.

Basic operation of the Audion
An Audion consists of an LC resonant circuit for setting the reception frequency, and an amplifier. To enable very high gain to be achieved, part of the amplified RF signal is fed back to the input. Consequently the Audion operates as an amplifier on the verge of self-oscillation. The radio frequency is amplified and demodulated simultaneously. Amplitude demodulation is achieved by the different response of the control grid to negative and positive voltages. The rectifier in the tube between control grid and filament is conductive for positive voltages, but not for negative voltages. A ‘grid leak’ network of 1 MΩ in parallel with a capacitor of 68 pF at the control grid supports the grid rectifier.

A simplified Audion radio
Changes in the feedback cause changes in frequency. To keep this effect within limits, a radio ham called F.H. Schnell in the 1920s added an adjustable attenuator element in parallel with the feedback winding. In the simple circuit shown in Figure 2, all components determining the receiving frequency and feedback are connected to ground (earth) at a single point. The LC resonant circuit consists of variable capacitor C1 and a section of coil L1. The antenna and the control grid are connected to a tap on the resonant circuit.

Coils L1A and L1B work as an autotransformer and adapt the low AC resistance of the antenna and control grid to the high-impedance resonant circuit. The feedback is via C2 onto feedback winding L1C with its parallel connected attenuator in the form of potentiometer R1, which acts as the feedback control. L1C forms a second autotransformer with the rest of L1. With the centre of L1 grounded, a phase shift of 180 degrees is established between the feedback winding and the resonant circuit. The desired positive feedback is obtained in conjunction with the phase shift of the amplifier.

High-frequency choke L2 separates the RF part of the Audion from the AF part. Every inductor has stray parallel capacitance — invisible but always present! For the RF choke we have about 10 pF. The stray capacitance of the primary winding of output transformer Tr1 is much higher. Without L2, it would effectively short-circuit the high frequency signal.
Output transformer Tr1 matches the high AC resistance of the tube to the low impedance of the Walkman-style headphones. The two 32 Ω earpieces are connected in parallel. The ‘A’ battery supplies heater power to the tube, the ‘B’ battery, the anode (plate) voltage. An AA size NiMH battery is just fine as a filament power source. The anode battery consists of two series-connected 9 V batteries. Current consumption amounts to 24 mA from the A battery, and 2 mA from the B battery. Capacitor C3 short circuits any RF signal at the screen grid. The secondary of Tr1 is intentionally not grounded. The front panel should consist of a grounded metal plate, or at least have some kind of metal foil (grounded, of course) on the inside. The antenna connector should be located at the rear of the enclosure. All these measures reduce the sensitivity of the circuit against the dreaded ‘hand effect’ — if the radio operator’s hand is in the vicinity of the radio, the resonant circuit is detuned. The length of the antenna should be limited to 1.5 m (5 ft.). A longer antenna is possible and allows you to pick up more stations but only if connected via a small capacitor of 4.7 pF to 33 pF. A proper antenna earth connection is essential to good reception. The GND connection of the circuit should be connected to conductive heating pipe. Potentiometer R1 also adjusts the volume. If you turn it up too far you will hear a howling sound. Your Audion then works as a transmitter and may cause interference on other receivers. Remember, the Audion is a regenerative radio!

**Practicalities**
The parts list of the proposed receiver is given in the inset. The picture of the breadboarded prototype in Figure 3 shows the
Audion Parts List

C1 = 35pF tuning capacitor with reduction gear (vernier), or Philips 'beehive' style (Opperman Electronic, Germany)
C2 = 1nF ceramic multilayer capacitor
C3 = 10nF ceramic multilayer capacitor
L1 = spider web coil 10+5+5 turns 0.3 mm (AWG #28) enameled copper wire.
L2 = 1mH choke (Fastron SMCC)
R1 = 47kΩ potentiometer, linear
S1 = SPST on/off switch
Tr1 = 1:10 miniature AF transformer, colour code: blue (Reinhöfer Electronic, Germany)
V1 = 1J18b tube (NOS on Ebay)
3.5mm stereo jack socket
Battery holder for one 1.5V AA cell
Clip-on leads for 9 V 6LR22 batteries

The spider web coil support consists of 0.4 mm thick transparent PE plastic which is sold as sheets in the stationery trade (anyone remember late afternoon Powerpoint presentations using overhead projectors?). The template shown in Figure 4 is placed under the film and its outline is copied onto the sheet with a CD marker pen. The shape can be cut out using heavy duty paper scissors. To allow the slots to be cut easily 3 mm (1/8 in.) holes are drilled at the ends. For the complete winding you’ll need 3.2 m (approx. 11 ft.) of 0.3 mm (AWG #28) enameled copper wire, plus two pieces of about 10 cm (4 in.) for the taps. The wire should be wound tightly onto the former. After 10 turns, a wire loop is bent. Before you continue winding, solder the wire loop to the connection wire. The second tap is after another five turns.

For more on tube radios, visit the author’s website [1].

(091047)

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(July/August 2010)

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**InterSceptre**

(June 2010)

In our March issue, we introduced Sceptre, a fast prototyping system fitted with a 32-bit microcontroller. Even on its own, the board will let you produce some great results, but if we add an extension board to make it easier to access all its peripherals, the Sceptre platform becomes downright powerful. What's more, if you fit this extension board into a suitable case, you'll be able right from the start to develop a prototype that you can use 'properly' in a installation, with no trailing wires or bits of sticky tape holding everything together. Now that's what you call fast, convenient prototyping!

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**dsPIC Control Board**

(May 2010)

This control board has been designed for incorporation into typical industrial electronics applications like controlling motors or adjustment of static up- or down-converters. The objectives were to obtain a board with a large number of pulse-width modulation (PWM) generators, which enables us to control several motors and static converters at the same time. The cost of the control board needed to be as low as possible too. In addition, it must be possible to construct the board using a soldering iron, without requiring use of a reflow oven.

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**Reign with the Sceptre**

(March 2010)

This open-source & open-hardware project aims to be more than just a little board with a big microcontroller and a few useful peripherals — it seeks to be a fast prototyping system. To justify this title, in addition to a very useful little board, we also need user-friendly development tools and libraries that allow fast implementation of the board’s peripherals. Ambitious? Maybe, but nothing should deter you from becoming Master of Embedded Systems Universe with the help of the Elektor Sceptre.

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COMING ATTRACTIONS

Mobile O₂ Meter with Minimod18

Nicely complementing the CO₂ Meter and Mobile CO₂ Meter projects recently published in Elektor we now present an O₂ (oxygen) Meter.

For divers and cave explorers it is absolutely essential to instantly know the current oxygen concentration in their air supply (from the cylinder) or in the ambient air (deep underground). In this article we present suitable sensors for oxygen-level measurements, the way sensor signals are processed and, best of all, an oxygen meter based on the renowned Minimod18 board described in the January 2010 edition.

Energy Harvesting

Alternative energy sources are all the rage lately. In this article we present a number of ideas for powering circuits from solar energy, even under poor sunlight conditions. However, the less than constant and dramatically low voltages supplied by solar panels can be processed with the help of clever circuits, enabling voltages down to 200 mV to be used, and constant supply power to be guaranteed.

The 5532 OpAmplifier (2)

Have you ordered your NE5532’s yet? They’re cheaper by the hundred! In this the second and concluding instalment on what’s already known as “a remarkable audio amplifier” we’ll be discussing the OpAmplifier’s construction and performance, the latter to confirm (with graphs from our Audio Precision analyser) that a top-notch design is on the table. For purists and other advanced audio boffins we also have guidance on configuring the amp for bridged operation and higher output powers.

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