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$12 / 60 / 120 / 600 /$ $1,200 \mathrm{~V}$ (..c. $12 / 60 / 120 / 600 /$
$1,200 \mathrm{~V}$ a.c. $\quad 60 \mu \mathrm{~A} / 30 \mathrm{~mA} /$ $300 \mathrm{~mA} .{ }^{2 . \mathrm{c}} 2 \mathrm{~K} / 200 \mathrm{~K} / 2 \mathrm{meg}-$ ohm. -10 to +63 dB .



MODEL TE-12 20,000 O.P.V. $0 / 0 \cdot 6 / 6 / 30 / 120$ $600 / 1,200 / 3,000 / 6,000$ $0 / 6 / 30 / 120 / 600 / 1,200 \mathrm{~V}$ $0 / 60 \mu \mathrm{LA} / 6 / \mathrm{b0} / 600 \mathrm{~mA}$ $600 \mathrm{~K} / 6 \mathrm{Meg} . / 60 \mathrm{Meg} . \Omega 50 \mathrm{pF}$ 0.2 mFd . 5.97 . Post 17 p

MODEL TE-200 20,000 O.P.V
MIrror scale, overload protec Mirror scale, overioad protec-
tion. $0 / 5 / 25 / 125 / 1,000 \mathrm{~V}$ d.c.
$0 / 10 / 50 / 250 / 1,000 \mathrm{~V}$ $0 / 10 / 50 / 200 / 1,000 \mathrm{~V}$ a.c. $0 / 50 \mu \mathrm{~A}$ +62 dB . 82.85 . Post 15 p


MODEL $500 \quad 30,000$ O.P.V. with overload protection $100 / 250 / 500 / 1,000 \mathrm{~V}$ $0 / 2.5 / 10 / 25 / 100 / 250 / 500 /$ $1,000 \mathrm{~V}$ a.c. $0 / 50 \mu \mathrm{~A} / 5 / 50$ 500 mA . 12 amp . d.c 88.87. Pont paid.

HIOKI MODEL 750 X
43 ranges 0.0 .

## $0-3$ to $1,200 V_{\text {a }}$ $0.30 \mu A ~$ 0.300 m

$0.3 \mathrm{~K} / 30 \mathrm{meg}$ oh
+17 dB.
$\begin{aligned} & \text { \$8.97. Post 20p. }\end{aligned}$


HTIOOR4 MULTI-METER


## 10 amp .

$0 / 20 \mathrm{~K} / 200 \mathrm{~K} / 2 \mathrm{MES} / 20 \mathrm{ME}$ $-20+62.1 \mathrm{~B}$. \&12-50, Posi 25 p .

## 970 WTR MULTMETER $\begin{array}{lll}\text { Foatures } & \text { a.c. } & \text { current } \\ \text { rangen, } & 20.000 & \text { O.P.V. }\end{array}$ $0 / 0-5 / 25 / 10 / 50 / 250 / 500$ / 0/2-5/10/50/250/500/1,000V $0 / 50 \mu \mathrm{~A} / 1 / 10 / 100 \mathrm{ma} / 1 / 10$ $0 / 100 \mathrm{~mA} 1$

$0 / 5 \mathrm{~K} / 50 \mathrm{~K} / 500 \mathrm{~K} / 5 \mathrm{MEG} / 50 \mathrm{ME}$
$-20+62 \mathrm{~dB}$. 215. Post 25 p .


ROUND SCALE TYPE PENCLL TESTER MODFL TR. 88
 'ompletely portable, simple to use pocket sized teater.
Ranges $0 / 3 / 30 / 300 \mathrm{v}$
anc. Ranges
and th.c. at 2,000
O.P.V. Resistance $0 \cdot 20 \mathrm{~K}$
ONLY \&1.97, Post 13 p .


250A. $6 \mathrm{~K} / 6$ meg ohms. $-20 \mathrm{t} 0+22 \mathrm{~dB}$

## MODEL TH-12

20,000 O.P.F. Overload protection. Slide switch selector. d.e. $0 / 10 / 50 / 250 / 1,000 \mathrm{~V}$ $30 \mathrm{~K} / 300 \mathrm{~K} / 3 \mathrm{meg}$
+4.97. Post 150 p.

 $-20 t o+46 \mathrm{uB}$. 88

TMK MODEL TW-50K 46 ranges. mirror acale, $50 \mathrm{~K} / \mathrm{V}$
d.c. $5 \mathrm{~K} / \mathrm{V}$ a.c. D.c.: Volts $0.125,0 \cdot 25,1-25,2-5,5,10,25,50$ Volts: $1-\dot{J}, 3, \overline{5}, 10,2 \overline{5}, 50,12 \overline{0}$ $250,500,1,000 \mathrm{~V}$. D.c. Current
 $10 \mathrm{~K}, 100 \mathrm{~K}, 1 \mathrm{MEG}, 10 \mathrm{MEG} \Omega$ Decibela: -20
$\mathbf{2 8}-50$. Poat 17 p .

$50 / 250 / 500 / 1,000 / 2,500 \mathrm{~V}$ d.c. $0 / 15 / 50 / 1 \overline{5} 0$

2A d.c. $0 / 3 \mathrm{~K} / 300 \mathrm{~K} /$ आmeg. 28.85 . Post 20 p


HIOKI MODEL 700X protection. Mirror acale protection: Mirror acale
$0 \cdot 3 / 0-6 / 1-2 / 1 \cdot 5 / 3 / 6 / 12 / 30 / 60 /$ 120/300/600/1,200v d.c. 1-5/3/6/12/30/60/150/300/ $600 / 1,200 \mathrm{~V}$ a.c.
15/30 $\mu \mathrm{A} / 3 / \mathrm{a} / 30 / 60 / 150 / 300 \mathrm{~m}$. $6 / 12$ amp. d.c. $2 \mathrm{~K} / 200 \mathrm{~K} /$
$2 \mathrm{Meg} / 20 \mathrm{Meg}$ ohm -20 to
+6311 B . 418 -50. Post 20 p .


MODEL C-7080 EN 20,000 O.P
 $1,000 / 5,000 \mathrm{Y}$ d.c. $0 / 2 \cdot \mathrm{~J} / 10$
$150 / 250 / 1,000 / \mathrm{J}, 000 \mathrm{~V}$ a.c $0 / 50 \mu \mathrm{~A} / 1 / 10 / 100 / 500 \mathrm{~mA} /$ 10 amp. d.c. $0 / 2 \mathrm{~K} / 200 \mathrm{~K})$ 20 meg.
-20 to +50 dB.
218.95. Post 35.

U4812 MULTIMETER Extremely sturdy instrume tor general
$6670 . \mathrm{P} . \mathrm{V}$.
$0 / 0 \cdot 3 / 1 \cdot \mathrm{j} / 7 \cdot \mathrm{~F} / 30 / 60 / 150 / 300 /$ $600 / 900 \mathrm{~V}$ d.c. and 75 mV . $0 / 0 \cdot 3 / 1 \cdot 5 / 7 \cdot 5 / 30 / 60 / 150 / 300 /$ $600 / 900 \mathrm{~V}$ a.
$0 / 300 \mu \mathrm{~A} / 1 \cdot \overline{5} / 6 / 15 / 60 / 150$ 600MA/L $\overline{0} / 6 \mathrm{amp}$. d.c. $0 / 1 \cdot 5 / 6 / 15 / 60 / 150 / 600 \mathrm{M}$ Kol amp. a.c. $0 / 200 \Omega / 3 \mathrm{k} / 30 \mathrm{k} \Omega$. Accitracy With sturdy metal carrying case, leads and instructions. 29.50. Poat 25 p .


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Models-100TR MOLTMEETER/TRANBI8TOR TRSTER. mirror scale overload prote
 Ico. Complete with batteries. inatructions and lears.
ela-50. Pont 2 yp .


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CI-5 PULSE OSCILLO8COPE
For display of pulsed and periodic waveforms in
electronic circuits.VERT AMP. Bandwidth 10MHz. gensitivity at 100 kHz RMA/mm. 0.1-25;
HOR. AMP. Bandwidth
 100 kHz , $V$ RMS $/ \mathrm{RMm}$. $03-2 \overline{5}$; Pre-set triggered sweep $1 \cdot 3,000 \mu s e c .:$
free running $20 \cdot 200,000 \mathrm{~Hz}$ in nine ranges. free running $20-200000 \mathrm{~Hz}$ in nine range
Calibrator pipa. $220 \mathrm{~mm} \times \quad 360 \mathrm{~mm} \times$ 430 mm . $115 \cdot 230$, a.c. operation. e39. Carr paid.

TO-3 PORTABLE O8CILLOSCOPE


3intube, Y amp. Sensitivity
$0 \cdot 7 V_{\text {p-p/CM. Bandwidth }}$ $0 \cdot 7 \mathrm{~F}$ p-p/CM. Bandwidth
1.5cps.1.jMHz.

 Banduldth $1 \cdot \overline{\mathrm{c}} \mathrm{cps}-800 \mathrm{kHz}$.
Input imp. 2 meg $\Omega 20 \mathrm{pF}$. Time base. $\overline{3}$ ranges 10 cps . Time base. $\overline{3}$ ranges 10 cpg
300 kHz . $\begin{aligned} & \text { Synchronisation, } \\ & \text { Ilhuminated scale } 140 \mathrm{~mm}\end{aligned}$ internal/externa.
$\times 215 \mathrm{~mm} \times 330 \mathrm{~mm}$. Weight $\quad 15 \ddagger 1 \mathrm{~b}$. $2.20 /$ 240 V a.c. Supplied brand new with hamdbook. 840.00 . Carr. 50 p

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 2ll accessories
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ges $400 \mathrm{kz}-30 \mathrm{M} \mathrm{Hz}$. An ges $400 \mathrm{k} \mathrm{Hz-30MHz}$. An
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 metal case. Size $\overline{3}$ sin $\times \sin \times 2$ in. 480 . MODEL TE15 GRID DIP METER as Grid Dip, oscillater Absorption W'ave Meter and Oselllating Detcetor. Frequency range 440 k Hz
-280 MHz in 6 coil ? 80 MHz in 6 coils. $500 \mu \mathrm{~A}$ meter. 9V battery opers-
tion. Size $180 \mathrm{~mm} \times 80 \mathrm{~mm}$ tion. Size $180 \mathrm{~mm} \times R 0 \mathrm{~mm}$
$\times 40 \mathrm{~mm}$.
 $\times 40 \mathrm{~mm}$.
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BELCO AP-5A 8OLID STATE SLIE
SQOARE WAVE C.R. O8GLLATOR
Sine $18 \cdot 200,000 \mathrm{~Hz}$


MODEL MG-100 GIME 8QUARE GEIERATOR Range $19-2: 0,000$
Hz . Sine Wave
19 19
Square Wave. Out. put Sine or Square
$180 \mathrm{~nm} \times 90 \mathrm{~mm} \times$ mave $10 \mathrm{~N}^{\circ}$. P . to P . Hize 180 mn 90niml. Operation
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ATTREATOR Frequency $0-200 \mathrm{kHz}$.
Attenustor $0-111 \mathrm{~dB}$,
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a.c. and tic. Accuracy $0.5 \%$
(l.c. $10_{n}$ a.c. Scale lengh 16 c num.
$0 / 300 / 7 \mathrm{~J} 0 \mu \mathrm{~A} / 1 \mathrm{c} / 3 / \mathrm{T} \cdot \mathrm{J} / 15 / 30 /$ 7.5/150/300/750 $13 \mathrm{~A} / 1 \cdot 5 / 3 / 7.5$ p.d.c. $0 / 3 / 7 \cdot 5 / \mathrm{I} / 30 / 75 / 150 /$
$300 / 750 \mathrm{~mA} / 1: 5 / 3 / 7 \cdot 5$ anip. a.c. of $75 / 10 / 300 /$ $10 \mathrm{mV} / 15 / 3 / 7 \cdot 3 / 13 / 30 /$
$75 / 100 / 300 / 750 \mathrm{~d}$ d.c. $750 \mathrm{mV} / 1 \cdot 5 / 3 / 7 \cdot 5 / 15 / 30 /$
$75 / 150 / 300 / 750 V^{2}$
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sures $1 V$ d.c. but

can be used to mesure owile raing of and di.c. volt, current and ohus withe of a.c. plug in cards specification: Ampuran: $\pm 0 \cdot 2, \pm 1$ digit. Resolution: 1 mV . Number of digits: 3 plus fourth overrange algit. Overrange: $100 \%$ (up to 1.999 ). Input Impedance: 1000 Meg ohm. Measuring cycle: 1 per second. Adjugtment: Automatic zeroing, full gcale adjustment againat an internal reference voltage. Overload: to 100 V d.c. Input: Fuily foating ( 3 poles). Input power: $110-230 \mathrm{~V}$ a.c. $50 / 60$ cycles. Overall size: $5 \ln \times 2 \frac{1}{4} \operatorname{in} \times 8$ hing AVAILABLE BRAND NEW AND FULLY GUARANTEED.
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HEADPHONES Response
$10-1 \bar{\omega}, 000 \mathrm{~Hz}$. Impedance 4-b whms. Brand new Boxed and fully g'teed PRICE 28.50 . PRICE 26
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Postage 25 p per speaker 2 jin 8 or 64 obm P.P. 10 p Kll-iorm oabinete, tealy'

$$
\begin{aligned}
& \text { (17in } \times 10 \ln \times 6 \mathrm{in}) \\
& \text { with a } \times 13 \mathrm{in} \times 8 \mathrm{in} \text { or } 8 \mathrm{in} \text { cut out } \\
& (12 \mathrm{in} \times 12 \mathrm{in} \times 6 \mathrm{in}) \text { with a } 8 \mathrm{in} \times
\end{aligned}
$$8

CM70 PLANET atick metal,
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P. 2Jp)
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3W intereo module
E 275 (P. \& P. 10 p ).

$$
\begin{aligned}
& \text { With a } 13 \text { in } \times 8 \text { in or } 8 \text { in cuit out } \\
& (12 i n \times 12 i n \times 6 i n) \text { with a } 8 \text { in } \times
\end{aligned}
$$

${ }_{5} \mathrm{~W}$ atereo module ( 2 i in . matt/chennel)
45.60(P. \& P. 15p)

$$
\begin{aligned}
& \text { Sin or 8in cut out. } \\
& \text { (please specify cut o }
\end{aligned}
$$

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Transformer, $\mathbf{f 1} 30$. Loudspeaker, $\mathbf{6 1 \cdot 3 0}$. PCB ( $\sin \times 3 \frac{1}{2}$ in), 90 p.
ELECTRONIC PIANO (Sept. 72/Jan. 73)
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Panels drilled with locating holes only: C. 20 mm , 18 p ; D. $38 \mathrm{~mm}, 21 \mathrm{p} ; \mathrm{E} .60 .5 \mathrm{~mm}, 28 \mathrm{p}$; F. $64 \mathrm{~mm}, 31 \mathrm{p}$; G. Module locking rods complete, £l-20. SAVE MONEY by obtaining a complete kit of hardware which comprises I off each $A, C, D$, F, 5 off E, and 7 off B, G $£ 17.42$, Post Free. "SCOPE" built REGULATED POWER SUPPLY fully assembled and GUARANTEED. Voltage rails adjustable from 8-17V. Nom. $15-0-15 \mathrm{~V}$ at 500 mA per rail. Stab. ratio 250 : 1; Ripple and Noise $500 \mu \vee$ at full load. Load regulation $<1 \%$ zero to full load, line regulation $<\frac{1}{1} \%$ for $10 \%$ change in mains voltage. Size 4 in $\times 2$ tin $\times 7$ in fits easily into power supply subtrame. Terrific value at $\mathbf{f} \mathbf{1 9} 90$ Post Free. (Include ISp for insurance if required.)

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# THIS IS THE FIRST PAGE OF THE GREAT BI-PAK SECTION 

BRAND NEW FULLY GUARANTEED DEVICES

| $4 \mathrm{Cl07}$ | 0.20 | AD162 | 0.88 | BC148 | $0 \cdot 10$ | BD137 | 0.45 | BF188 | 0.40 | OC19 | 0.35 | 2G371 |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| AC113 | $0 \cdot 20$ | AD161 |  | BC149 | 0.12 | BD138 | 0.60 | BF194 | 0.12 | 0 C 20 | 0.68 | 29371 B | 0.12 | ${ }_{2} \mathbf{2 N} 2219$ | 0.20 0.22 | 2N3064 | 0.46 0.50 | 2N4059 | 0.10 0.12 |
| $\mathrm{AC115}$ | 0.88 | AD162 | (MP) | BC150 | 0.18 | BD139 | 0.55 | BF195 | 0.12 | $\bigcirc \mathrm{C} 22$ | 0.88 | ${ }_{20373}$ | 0.12 0.17 |  | 0.22 0.20 | 2N3055 | 0.50 0.14 | 2N4060 | 0.12 0.12 |
| AC117K | 0.20 |  | 0.55 | BC151 | 0.20 | BD140 | $0 \cdot 60$ | BF196 | $0 \cdot 14$ | 0 C 23 | 0.48 | 29374 | 0.17 | ${ }_{2} \mathrm{~N}^{2} 2222$ | 0.20 |  | 0.14 0.16 | ${ }_{2}{ }^{2 N} 4081$ | 0.12 0.12 |
| AC128 | 0.18 | ADT140 | 0.50 | BC152 | $0 \cdot 17$ | BD155 | $0 \cdot 80$ | BF197 | 0.14 | 0 C 24 | 0.50 | 2 G 377 | 0.80 | 2N2368 | 0.17 | ${ }_{2} \mathbf{2 N 3 3 9 1 A}$ | 0.16 0.14 | 2N4082 | 0.12 0.17 |
| $\mathrm{ACl}^{25}$ | 0.17 | AF114 | $0 \cdot 84$ | BC153 | $0 \cdot 28$ | ED175 | 0.60 | BF200 | 0.45 | OC25 | 0.88 | 29378 | 0.16 | 2N2369 | $0 \cdot 14$ | 2N3343 | 0.14 | 2N4284 | 0.17 0.17 |
| ${ }^{\text {ACl26 }}$ | $0 \cdot 17$ | AF115 | 0.84 | BC154 | 0.80 | BD176 | 0.60 | B F222 | 0.95 | OC26 | 0.25 | $2 \mathrm{G381}$ | $0 \cdot 16$ | 2N2369A | 0.14 | 2N3394 | 0.14 | 2N4285 | 0.17 0.17 |
| AC127 | 0.17 | AFl16 | $0 \cdot 84$ | BC157 | 0.18 | BD177 | $0 \cdot 65$ | BF257 | 0.45 | OC28 | 0.50 | 20382 | $0 \cdot 16$ | 2N2411 | 0.84 | 2N3395 | 0.17 | 2N4286 | 0.17 |
| ${ }^{\text {ACl34 }}$ | 0.14 | AF124 | 0.85 0.80 | ${ }_{\text {BC1 }}$ | 0.12 | BD179 | 0.70 | BF259 | 0.85 | $0 \mathrm{OC3}$ | 0.48 | $2 \mathrm{G414}$ | 0.80 | 2N2648 | 0.47 | 2N3403 | 0.21 | 2N4289 | 0.17 |
| AC137 | 0.14 | AF125 | 0.26 | BC16I | 0.50 | BD185 | 0.70 | ${ }_{\text {BF262 }}$ | 0.65 | ${ }_{0}^{0 \mathrm{OC} 36}$ | 0.50 | 26417 | 0.25 | $2 N 2711$ | $0 \cdot 21$ | 2N3404 | 0.88 | 2N4290 | 0.17 |
| $\mathrm{ACl}_{41}$ | 0.14 | AF126 | 0.28 | BC167 | $0 \cdot 18$ | BD186 | 0.65 | BF270 | 0.85 | 0 C 42 | 0.84 | 2N388A | 0.85 | 2N2712 | 0.81 | 2N3405 | 0.42 | 2 N 4291 | 0.17 |
| AC141K | $0 \cdot 17$ | AF127 | 0.28 | BC168 | 0.18 | BD187 | 0.70 | BF271 | $0 \cdot 80$ | $0 \mathrm{OC44}$ | 0.15 | 2N 404 | 0.55 .0 .20 | 2N2714 | $0 \cdot 21$ 0.17 | 2N3414 | 0.15 | 2 N 4292 | 0.17 0.17 |
| $\mathrm{ACl}^{2} 2$ | 0.14 | AF139 | 0.30 | BC1 69 | 0.12 | BD188 | 0.70 | BF272 | 0.80 | 0 C 45 | 0.12 | 2 N 404 A | . 0.28 | 2N2904A | 0.17 0.21 | 2N3415 | 0.15 0.28 | 2N4293 | 0.17 0.12 |
| AC142K | 0.17 | AF178 | 0.50 | BC170 | 0.18 | BD189 | $0 \cdot 75$ | BF273 | $0 \cdot 35$ | $0 \mathrm{C70}$ | 0.10 | 2N524 | 0.48 | 2N2905 | 0. 21 | 2N3416 | 0.28 0.28 | 2N5172 | 0.12 0.38 |
| ${ }_{4}{ }_{4} \mathrm{ClF} 5$ | 0.15 | AF179 | 0.50 | BC171 | $0 \cdot 14$ | BD190 | 0.75 | BF274 | 0.35 | $0 \mathrm{C71}$ | 0.10 | 2N527 | 0.49 | 2N2905A | 0.81 | 2N3525 | 0.75 | 2 N 5458 | 0.38 0.82 |
| ${ }^{4} \mathrm{ACl54}$ | 0.80 0.80 | AF180 | 0.50 0.45 | BC172 | $0 \cdot 14$ | BD195 | 0.85 | BFW 10 | $0 \cdot 60$ | OC72 | 0.14 | 2N598 | 0.48 | 2N2906 | 0.15 | 2N3646 | 0.09 | 2N5459 | 0.40 |
| AC158 | 0.20 | AF186 | 0.45 | BC 173 <br> BCl 74 | 0.14 0.14 | BD196 | 0.85 0.90 | BFX29 | 0.27 0.28 | OC74 | 0.14 | 2N599 | 0.45 | 2N2906A | 0.18 | 2N3702 | $0 \cdot 10$ | 28301 | - 50 |
| AC157 | 0.84 | AF239 | 0.87 | BC175 | 0.82 | BD198 | 0.60 | BFX85 | 0.22 0.80 | $0 \mathrm{OC76}$ | 5 | 2 N | 0. | 2N2907 | 0.20 | 2N3703 | 0.10 | 28302A | 0.48 |
| AC165 | 0.20 | AL102 | 0.65 | RC177 | 0.18 | BD199 | 0.95 | BFX86 | 0. 28 | OC77 | 0 | 2N698 | 0. | 2N2907A | 0.88 | 2N3704 | $0 \cdot 11$ | 28302 | 0.42 |
| AC166 | 0.20 | AL103 | 0.65 | BC178 | 0.18 | BD200 | 0.95 | BFX87 | 0.24 | 0 C 81 | 0.15 | 2N699 | 0. | 2N292, | 0.14 | 2N3705 | $0 \cdot 10$ | 2 S 303 | 0.55 |
| ACl67 | 0.20 | A8Y26 | 0.85 | BC179 | 0.19 | BD205 | 0.80 | BFX88 | $0 \cdot 22$ | OC81D | 0.15 | 2N706 |  | ${ }_{2} \mathrm{~N} 2925$ | 0.14 0.14 | 2N3706 | 0.09 | 28304 | $0 \cdot 70$ |
| ACl 68 | $0 \cdot 24$ | ABY27 | 0.80 | BC18n | 0.24 | BD206 | 0.80 | BFY50 | 0. 20 | 0 C 82 | 0.15 | 2N706A | 0.09 | 2 N 92 | 0.14 | ${ }_{2} \mathrm{~N} 3707$ | 0.11 | 28305 | 0.84 |
| ACl69 | 0.14 | ABY28 | 0.85 | BC181 | $0 \cdot 24$ | BD207 | 0.98 | BFY 51 | 0.20 | OC82D | 0.15 | 2N708 | 0.18 |  |  | 2N3708 | 0.07 | 28306 | 0.84 |
| AC176 | $0 \cdot 20$ | A8Y29 | 0.25 | BC182 | $0 \cdot 10$ | BD208 | 0.95 | BFY ${ }^{\text {P }}$ | 0.20 | 0 C 83 | $0 \cdot 20$ | 2N711 | 0.80 | ( |  | 2N3710 | 0.09 | 283307 | 0.84 0.54 |
| AC177 | 0.24 | AsYD0 | 0.25 | BC182L | $0 \cdot 10$ | BDY20 | 1.00 | BFY53 | 0.17 | OC84 | 0.20 | 2N717 | 0.35 |  | 0.11 | 2N3711 | 0.09 | 28321 | 0.58 0.48 |
| ${ }^{\text {AC178 }}$ | 0.28 | ASY51 | 0.85 | BC183 | $0 \cdot 10$ | BF115 | 0.24 | BPX25 | 0.85 | OC139 | 0.20 | 2ソ718 | 0.24 | 2N2926 |  | 2N3819 | 0.28 | 283222 A | 0.42 0.42 |
| ${ }^{\text {AC179 }}$ | 0.28 | A8Y52 | 0.26 | BC183L | 0.10 | BF117 | 0.45 | BEX19 | 0.15 | OC140 | 0.80 | 2N718A | 0.50 |  | 0.10 | 2N3820 | 0.50 | 28323 | 0.42 0.56 |
| AC180K | 0.17 0.20 | A8Y54 | 0.25 | BC184 | 0.12 | BF118 | 0.70 | BEX 20 | 0.15 | OC169 | 0.25 | 2N726 | 0.28 | 2N2926 (R) |  | 2N3821 | 0.35 | 28324 | 0.20 |
| AC181 | 0.17 | A8Y56 | 0.25 | BC184L | 0.12 | BF119 | 0.70 | B8Y26 | 0.15 | OC170 | 0.85 | 2N727 | 0.28 |  | 0.10 | 2N3823 | 0.88 | 28325 | $0 \cdot 70$ |
| AC181K | 0.20 | ASY57 | 0.25 | HC187 | 0.28 | BF123 | 0.45 | BSY26 | 0.15 | $0 \mathrm{Cl71}$ | 0.25 | 2N743 | 0.20 | 2N2928 (B) |  | 2N3903 | 0.28 | 28326 | 0.70 |
| AC187 | 0.22 | A8Y88 | 0.25 | BC207 | 0. 11 | BFi25 | 0.45 | B8Y27 | 0.15 | 0 O 200 | 0.25 | 2N744 | $0 \cdot 20$ |  | 0.10 | 2N3904 | 0.30 | 28327 | $0 \cdot 70$ |
| AC187K | $0 \cdot 20$ | A8221 | 0.40 | BC208 | 0.11 | BF127 | 0.50 | B8Y29 | 0.15 | $\mathrm{OC}^{0} 201$ | 0.28 | 2N914 | 0.14 | 2N3010 | 0.70 | 2N3905 | 0.28 | 28701 | 0.48 |
| 4 Cl 188 | 0.22 | BC107 | 0.09 | BC209 | 0.12 | BF152 | 0.55 | R8Y38 | 0.18 | OC202 | 0.28 | 2N918 | $0 \cdot 30$ | 2N3011 | 0.14 | 2N3906 | 0.87 | 40361 | 0.40 |
| AC188K | 0.80 | BC108 | 0.08 | BC212L | 0.11 | BF153 | 0.45 | B8Y 39 | 0.18 | 0 O 204 | 0.25 | 2N829 | $0 \cdot 21$ | 2N3053 | 0.17 | 2N4058 | $0 \cdot 12$ | 40362 | 0.45 |
| ACY17 | 0.25 | BCl09 | 0.10 | BC213L | $0 \cdot 11$ | BF154 | 0.45 | B8Y40 | 0.88 | ${ }_{0} \mathrm{C} 205$ | 0.85 | 2 N 930 | 0.21 |  |  |  |  |  |  |
| ACY19 | $0 \cdot 20$ | BC113 | 0.10 | RC214L | 0.14 | BF15s | 0.70 | BSY41 | 0.28 | $0 \mathrm{C309}$ | 0.40 | 2N1132 | 0.20 |  |  |  |  |  |  |
| 4CY19 | $0 \cdot 20$ | BC114 | 0.15 | BC225 | 0.25 | BF156 | 0.48 | B8Y95 | 0.12 | P346A | 0.80 | 2 N 1302 | 0.14 |  | DIO | ES AND | ECT | IERS |  |
| ACY20 | $0 \cdot 20$ | BC115 | 0.15 | BC226 | $0 \cdot 85$ | BF107 | 0.65 | B8Y95A | 0.12 | P397 | 0.42 | 2 N 1303 | 0.14 |  |  |  |  |  |  |
| ACY21 | 0.20 | BC116 | $0 \cdot 15$ | BCY30 | 0.84 | BF158 | 0.86 | Bul05 | 2.00 | $0 \mathrm{CP71}$ | 0.48 | 2N1304 | 0.17 | AA120 | 0.08 | 164 | 0.21 | A1 | 0.35 |
| ACY22 | 0.18 | BC117 | $0 \cdot 15$ | BCY31 | 0.28 | BF159 | 0.60 | Cll1E | 0.50 | ORP12 | 0.48 | 2N1305 | 0.17 | AA129 | 0.08 | BY164 | 0.60 | OA47 | . 07 |
| ACY27 | 0.18 | BCl18 | 0.10 | BCY 32 | 0.80 | BF180 | 0.40 | $\mathrm{C400}$ | $0 \cdot 30$ | ORP60 | 0-40 | 2N1306 | 0.21 | AAY 30 | 0.09 | BY $\times 38 / 30$ |  | - | 0.07 |
| ACY28 | 0.19 | BC119 | 0.80 | BCY 33 | 0.28 | BF162 | 0.40 | C407 | 0.25 | ORP61 | 0.40 | 2 N 1307 | 0.21 | AA7.13 |  |  | 0.25 | OA79 | 0.07 |
| ACY29 | 0.85 | BC120 | 0.80 | BCY34 | 0.25 | BF183 | 0.40 | C424 | $0 \cdot 20$ | gT140 | 0.18 | 2N1308 | 0.88 | BA100 | 0.10 | BY210 | - 30 | 0 O81 | 0.07 |
| ACY 30 | 0.28 | BC125 | $0 \cdot 12$ | BCY70 | 0.14 | BF164 | 0.40 | C425 | 0.50 | 8T141 | $0 \cdot 17$ | 2N1309 | 0.28 | BA116 |  | BYZ11 | $0 \cdot 30$ | OA85 | 0.09 |
| ACY31 | 0.28 | BC126 | 0.18 | BCY71 | 0.18 | BFlis | 0.40 | C 426 | 0.85 | Tis43 | 0.30 | 2N1613 | $0 \cdot 20$ | BA126 | 0.22 | BYZ12 | $0 \cdot 30$ | OA90 | 0.06 |
| ACY34 | 0.21 | BC132 | 0.12 | BCY72 | 0.14 | BF167 | 0.28 | C428 | 0.20 | UT46 | 0.27 | 2N1711 | 0.20 | BA148 | 0.14 | BYZ18 | 0.25 | OA91 | 0.08 |
| ACY 35 | 0.21 | BC134 | $0 \cdot 18$ | BCZ 10 | 0.20 | BF173 | 0-28 | C441 | 0.80 | 2G301 | 0.09 | 2N1889 | 0.82 | BA154 | 0.12 | BYZ17 |  | OA9b | 0.07 |
| ACY 36 | 0.88 | BC135 | $0 \cdot 12$ | BC211 | 0.25 | BF176 | 0.85 | C442 | 0.30 | $2 \mathrm{C302}$ | 0.19 | 2N 1890 | 0.45 | BA155 | 0.14 | BYZ18 | 0.86 | OA200 | 0.08 |
| ACY40 | 0.17 | BC136 | 0.15 | BCZ12 | 0.25 | BF177 | 0.85 | C444 | 0.85 | 2G303 | 0.19 | 2N1893 | 0.37 | BA156 | 0.18 | BYZ19 | 0.88 | 8D20 | 0.07 |
| tCY41 | $0 \cdot 18$ | BC137 | 0.15 | BD121 | $0 \cdot 60$ | BF178 | 0.80 | C450 | 0.82 | 2G304 | 0.24 | 2N2147 | $0 \cdot 72$ | BY100 | 0.15 | CG62 |  | 8D19 | 0.05 0.05 |
| ACY44 | 0.85 | BC139 | 0.40 | BD123 | 0.85 | BF179 | 0.80 | MAT100 | 0.18 | $2 \mathrm{C3} 06$ | 0.40 | 2N2146 | 0.57 | BY101 | 0.12 | (Eg) OA91 |  | IN34 | 0.05 |
| AD130 | 0.88 | HC140 | 0.80 | BD124 | 0.60 | BF180 | 0.80 | Matiol | 0. 20 | 2(1308 | 0.85 | 2N2160 | $0 \cdot 60$ | HY 105 | $0 \cdot 17$ |  | 0.05 | IN34A |  |
| ADI40 | 0.48 | BCl4l | 0.30 | ED131 | 0.50 | BF181 | 0-30 | MAT120 | 0.19 | 2G309 | 0.35 | 2N2192 | 0.35 | BY114 | 0-12 |  |  |  |  |
| AD142 | 0.48 | BC142 | 0.80 | BD132 | 0.60 | BF182 | 0.40 | MAT121 | 0.80 | 2G339 | $0 \cdot 20$ | 2N2193 | 0.35 | BY126 | 0.14 |  |  | 1N914 | 0.08 0.06 |
| AD143 | $0 \cdot 38$ | BC143 | 0.30 | BD133 | 0.65 | BF183 | $0-40$ | MPF102 | $0 \cdot 42$ | $2 \mathrm{G339}$ A | 0.10 | 2N2194 | 0.35 | BY127 | 0.15 | OA79 |  | 1 N 916 | 0.06 0.08 |
| AD149 | $0 \cdot 50$ | BC145 | 0.45 | BD135 | 0.40 | BF184 | 0.25 | MPF104 | 0.87 | 20344 | 0.18 | 2N2217 | 0.22 | BY128 | 0.16 | OAJ9 | 0.08 | ${ }_{19021}$ | 0.08 0.10 |
| AD161 | 0.88 | BC147 | $0 \cdot 10$ | BD136 | $0 \cdot 40$ | BF185 | $0 \cdot 30$ | MPF 105 | 0.87 | 20345 | 0.16 | 2N2218 | 0.80 | BY130 | $0 \cdot 16$ | OA58L | 0.21 | 15951 | 0.10 0.08 |

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SIL. RECTS. TESTED

PIV 300 mA 750 mA 1 A 1.5A 3 A 10 A 30A $\begin{array}{rlllllll}50 & 0.04 & 0.05 & 0.05 & 0.07 & 0.14 & 0.21 & 0.60 \\ 100 & 0.04 & 0.06 & 0.05 & 0.13 & 0.16 & 0.23 & 0.75\end{array}$ | 100 | 0.04 | 0.06 | 0.05 | 0.13 | 0.18 | 0.23 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| 0.75 |  |  |  |  |  |  |
| 200 | 0.05 | 0.09 | 0.06 | 0.14 | 0.20 | 0.24 | $\begin{array}{lllllll}200 & 0.05 & 0.09 & 0.06 & 0.14 & 0.20 & 0.24 \\ 400 & 0.06 & 0.13 & 0.07 & 0.20 & 0.27 & 0.37 \\ 1.25 \\ & 000 & 0.07 & 0.16 & 0.10 & 0.2 . & 0.34 \\ 0.40 & 1.86\end{array}$ $\begin{array}{lllllll}600 & 0.07 & 0.16 & 0.10 & 0.20 & 0.34 & 0.37 \\ 800 & 0.10 & 0.17 & 0.11 & 0.25 & 0.37 & 0.55 \\ 8 & 2.00\end{array}$ $\left.\begin{array}{rllllll}800 & 0.10 & 0.17 & 0.11 & 0.25 & 0.37 & 0.56 \\ 2\end{array}\right) \cdot 00$ $1200-\quad \begin{array}{lllllll} & 0.33 & - & 0.38 & 0.57 & 0.75\end{array}$

| , |  | ACS |  |
| :---: | :---: | :---: | :---: |
| VBO | M 2 A | 6A | 10A |
|  | TO-1 | 0.6 | T0-88 |
|  | \%p | 40 | ED |
| 100 | 80 | 50 | 78 |
| 200 | 50 | 80 | 80 |
| 400 | 70 | 78 | 1-10 |
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| TRAF8. CODE SIL. DUAL |
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TO-3 case. G.P. Switching
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| U | 60 Mixea Germanium Transietors AF/RF |  |
| U 3 | 75 Germenium Gold Bonded Sub-Min. like OA5, OA47 | 0 |
| U | 40 Germanium Transistora like OC81, AC128 | 0.80 |
| U |  | 0.50 |
| U | 30 8it. Planar Trans. NPN like BSY95A. 2N7 | 0 |
| U | 16 Bil. Rectiflers TOP-HAT 750 mA VLTG. RANG | 0.50 |
| 178 | 50 Sll. Planar Dloder DO-7 Glass 250 mA lite OA200/202 |  |
| U 9 | 20 Mlxed Voltages, 1 Watt Zener Dlores | 0 |
| U10 | 20 BAY50 charge storage Diodes DO.7 Glass |  |
|  | 25 PNP 8il. Planat Trans. TO-5 like 2N11s2, 2N2904 |  |
| U12 | 12 silicon Rectiners Epoxy 600 mA up to 800 PIV |  |
| U13 | 30 PNP-NPN Sil. Transletors OC200 \& 28104 | 0 |
| U14 15 | 150 Mixed silicon and Germantum Dlodes | 0 |
| U15 | 25 NPN Bil. Planar Trans. TO-5 like BFY51. 2 N 697 | 0.50 |
| U18 | 103 Amp Sllicon Rectifers Stud Type up to 1000PIV |  |
| U17 | 30 Germanium PNP AF Transistors To. 5 like ACY 17-22 | 0.50 |
| U18 | 86 Amp Billcon Rectiflera BYZ13 Type up to 600 PIV | 0 |
| U19 | 25 silicon NPN Tranaintora like BC108 |  |
| U20 | 121.5 Amp Bilicon Rectifiers Top Fit up to |  |
| 21 | 30 AF. Germanium Alloy Translators 20300 series \& OC71 | 0.50 |
| 23 | 30 MADT's like MHz Series PNP Translators | 0.50 |
| U24 | 20 Germanium 1 Amp Rectifers GJM Series up to 300 TIV | 0.50 |
| U25 | 25300 MHz NPN Bilicon Transiators 2N708, B8Y27 |  |
| 28 | 30 Fast Bwitching Billcon Dloden like 1N914 Micro-Min |  |
| U27 | 12 N P'N Germanium AF Tranalatora TO-1 Hke ACl27 | 0 |
| V29 | 101 Amo 8CR's TO-8 can, up to 600 PIV CRS1/25-600 | 2.00 |
| 30 | 15 Plastic silicon Planar Trans. NPN 2N292A |  |
| U31 | 20 Silicon Planar Plastle NPN Trans. Low Noise Amip 2 |  |
| U32 | 25 Zener Dlodes 400 mW DO-7 case $3-18$ volta mixed |  |
| U33 | 15 Plastlc Case 1 Amp Silicon Rectifers IN 4000 Series |  |
| U34 | 30 Silicon PNP Alloy Trans. TO-6 BCY26 28302/4 | 0 |
| U35 | 25 Silicon Planar Tranaibtors P'NP TO-18 2N 2906 | 0 |
| U38 | 25 Sillicon Planar NPN Transiators TO-5 BFY50/51/52 | 50 |
| U37 | 30 Sillicon Alloy Transistors s0-2 PNP OC200, 2832 | 0 |
|  | 20 Fart Switching gilicon Trans. NPN 400 MHz 2 N 3011 | 0.50 |
| U39 | 30 RF. Germ. PNP Transistors 2N 1303/5 TO.5 | 0.50 |
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BPECLFICATION
Frequency Response Harmonic Distortion 1. Tape Head 3. Magnetic P.U

$$
\begin{aligned}
& 35 \mathrm{mV} \text { into } 50 \mathrm{~K} \Omega \\
& 1 \cdot 5 \mathrm{mV} \text { into } 50 \mathrm{~K} \Omega
\end{aligned}
$$

$$
\begin{aligned}
& \text { All Input voltages are for an output of } 250 \mathrm{mV} \text {. Tape and P.U. Inputs } \\
& \text { equalised to RIAA curve within } \pm 1 \mathrm{~dB} \text {. from } 20 \mathrm{~Hz} \text { to } 20 \mathrm{KHz} \text {. }
\end{aligned}
$$

Bass Control $\pm 15 \mathrm{~dB}$ at $20 \mathrm{~Hz}_{2}$
Fiters: Rumble (High Pass) $\quad \mathbf{1 0} 15 \mathrm{~dB}$ at 20 KHz Scratch (Low Pass)
Signal/Nolse Ratio
input overload Bupply
Dimension

## 8 KHz

better than - 65 dB $+26 d \mathrm{~B}$
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|  |  | ght |  |
| :---: | :---: | :---: | :---: |
|  | Otis) | lb oz |  |
| 07 | 20 | 111 | $70 \times 6.0 \times 6$ |
| 100 | 60 |  | $8.9 \times 8.0 \times 7.7$ |
| 61 | 100 | 512 | $10.2 \times 8.9 \times 8.3$ |
| 30 | 200 |  | $12.0 \times 10.3 \times 10.0$ |
| 62 | 250 | 12 | $9.5 \times 12.7 \times 11.4$ |
| 55 | 350 | 15 | $14.0 \times 10.8 \times 12.4$ |
| 63 | 500 | 27 | $17.1 \times 11.4 \times 15.9$ |
| 92 | 1000 | 40 | $17.8 \times 17.1 \times 21.6$ |
| 128 | 2000 | 630 | $24.1 \times 21.6 \times 15.2$ |

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No. (Wotts) 16 .
$\begin{array}{ccccccc}113 & 20 & 11 & 7.3 \times 4.3 \times 4.40-115-210-240 \\ 64 & 75 & 1 & 14 & 7.0 \times 6.4 \times 8.00 .115-210.240 \\ 4 & 150 & 3 & 0 & 8.9 \times 6.4 \times 7.60-115-200-220-2\end{array}$
$\begin{array}{cccccc}66 & 150 & 3 & 0 & 8.9 \times 6.4 \times 7.6 \\ 67 & 300 & 6 & 0 & 10.2 \times 10.2 \times 9.5 \\ 600 & 12 & 8 & 14.0 \times 10.2 \times 11 & \cdots\end{array}$
$P \& P$
0.8522
0.85
$\begin{array}{ll}0.85 & 22 \\ 1.66 & 30\end{array}$ $\begin{array}{ll}2.68 \\ 3.89 & 52 \\ 5.78 & 67\end{array}$
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1.84
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5.78
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30 VOLT RANGE
$\begin{array}{cr}\text { Amps. } & \text { Weigh } \\ 0.5 & 16 \\ 02 & 4 \\ 1.0 & 2 \\ 0 & 0 \\ 2.0 & 3 \\ 3.0 & 2 \\ 4.0 & 6 \\ 6 \\ 5.0 & 0 \\ 0.0 & 8 \\ 6.0 & 7 \\ 8.0 & 10 \\ 10.0 & 0 \\ 10 & 2\end{array}$
Size cm.
$8.3 \times 3.7 \times 4.9$
$7.0 \times 6.4 \times 6.0$
$8.9 \times 7.0 \times 7.6$
$10.2 \times 8.9 \times 8.6$
$10.2 \times 10.0 \times 8.6$
$10.2 \times 10.0 \times 8.6$
$12.1 \times 10.0 \times 8.6$
$12.1 \times 10.0 \times 10.2$
$12.1 \times 10.0 \times 10.2$
$14.0 \times 11.7 \times 10.0$
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SINGLE CHANNEL SOUND／LIGHT CONVERTER
This compact and reliable unit operates from amplifiers with outputs from 5－loow．Does not impose a heavy load on the amplifier，or，if connected in the wrong polarity，cause any damage，as with some units．

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The SAXON 100． 848.50 ，carr．free． With an R．M．S．output of 120 W speech and music． loow continuous power，four individwally consrolled F．E．T．input stages and wide range bass and treble conerols，this amplifier has established itself as a unit offering quality and reliability at low cost．

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[^4]

## OUR TRANSISTOR

This seems a good time to reveal our own secret suspicions concerning the origin of the transistor. We admit that for long we have had an irresistible feeling that this device was in fact conceived with the private constructor expressly in mind. Yes indeed, and plenty of evidence points this wayother important users and impressive applications notwithstanding.

Forget, for a moment or two, those epic moon journeys monitored on colour television. Forget (if you can) those ubiquitous transistor radios. Forget those prodigious com-puters-and those in-vogue status setting personal appurtenances like pocket calculators and solid state wristwatches. Instead, reflect upon what the transistor has meant to us as private constructors and experimenters. For we as much as any other body of individuals have cause to be jubilant and grateful in respect of that revolutionary technological breakthrough a quarter century ago.

We suggest that if the trio of research physicists at Bell Labs had been working to a specification drawn up with the private constructor's interests foremost in mind, they could hardly have done better. Those readers able to cast their minds back to the pre-transistor days when the valve ruled the roost will certainly be appreciative of the transformation set in train by the solid state amplifying device. It ushered in an era of expansive amateur activities, by bringing within the private individual's reach, literally to his table top, previously undreamed of electronic techniques.

By comparison with the thermionic devices it was going to replace, the transistor when it made its debut seemed so ridiculously simple to handle, so small and undistinguished in appearance, so undemanding in power, as not to be true. Not that there were no headaches for the average constructor and experimenter initially, when trying to get to grips with strange new concepts involving solid state physics, with a little chemistry and metallography thrown in. It was rather a traumatic experience trying to adjust to the transistor's idiosyncrasies. (An h.t. line of minus 6 volts-what nonsense was this? Though we got our potentials right once again when the npn version arrived.) First attempts to make practical use of the strange component were accompanied with trepidation, for burnt-out transistors weren't uncommon in those pioneering days.

Seen now in retrospect, these were but little teething troubles. The advantages and rewards arising from the solid state device were soon made apparent. Without the transistor and its descendants amateur activity would never have reached the advanced technical stage, nor have been able to embrace such wide and diverse applications as it does today. Undoubtedly the basic simplicity of construction is the great single factor which has stirred up popular interest in solid state electronics. It is now an ideal sparetime hobby which can be pursued without difficulty in the home.

Yes, constructors and enthusiasts all, there is cause to be exuberant. And don't forget, without the transistor there could hardly be Practical Electronics. So we all owe a vote of thanks to Messrs. Bardeen, Brattain, and Shockley. And surely no true enthusiast will dare deny that our suspicions are well founded.-F.E.B.

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# 1) 

THERE must be many readers who possess a tape recorder whose quality does not match that of the rest of their hi-fi system and who would like to improve it to save the expense of buying a completely new deck.

This article describes how to convert a reasonable quality deck to hi-fi by renewing the electronics and the heads whilst retaining the original tape transport system. All that is required is a tape deck of reasonable quality, the author using a Collaro Studio. Full constructional details relating to the deck will be given.

Since it is assumed that the constructor will already possess a high quality amplifier, this will not be described. Microphone facilities are not included because in the author's opinion these are best provided by a comprehensive mixer.

## OVERALL SYSTEM

A block diagram of the overall system is shown in Fig. 1. A six-pole three-way switch is used to select either upper track, lower track, or stereo operation (S3). A, relay (RLA) is used to control record and playback modes. The relay itself is controlled by an external switch normally fitted to the tape deck. This switch is also used to control the supply to the bias and erase oscillator, which is on in the record position and off in the playback.

A ganged potentiometer (VR6, VR106) controls the gain of the record amplifier which is fed from the input signal via a•DIN socket. The output of
the record amplifier is fed via a resistor and the relay contacts to the record/replay heads. It is also fed to the meter circuit which can also be used to read the bias voltage.

The resistor between the record amplifier and the head is used to achieve constant current drive.

In the record mode the relay contacts connect the heads to the record amplifier. The bias oscillator is on and feeds the erase heads. Two 50 kilohm potentiometers (VR7 and VR107) are used to control the bias. When only single track operation is selected then the unused output of the bias oscillator is grounded via a 10 kilohm resistor ( R 42 ).

In the playback mode the output of the heads is fed directly to the playback amplifier and then out via another DIN socket.

## HEAD REQUIREMENTS

A tape recorder is only as good as its heads and to achieve the required performance heads of very high calibre are essential.

The characteristics required for the record/replay head are

1. Low eddy current loss at high frequency so that a high bias frequency can be used both to avoid interference (this being particularly important when recording stereo radio transmissions with its attendant sub-carrier output problems), and to enable the minimum pre-emphasis to be employed.
```
System
    Quarter track stereo tape link intended for use in
    conjunction with a high fidelity system. Features
    sinclude two level indicators, adjustable metered
        bias, and single track mono operation
    *)
    Overall Frequency Response
    7\frac{1}{2}}\mathrm{ i.p.s. }30\textrm{Hz}-17\textrm{kHz}\pm1\textrm{dB
    3妾i.p.s. }40\textrm{Hz}-11.5\textrm{kHz}+1,-1.5\textrm{dB
    1% i.p.s. }40\textrm{Hz}-6.5\textrm{kHz}\pm2\textrm{dB
Replay Characteristic
CCIR
    7\frac{1}{2}
    3\frac{3}{4}\mathrm{ i.p.s. 140 }\mu\textrm{S}
    1\frac{7}{8} i.p.s. 280\muS
```


## Distortion

$1 \%$ at 0 VU

## Signal to noise ratio

60 dB weighted according to the CCIF characteristic and using low noise tape

## Crosstalk

45dB

## Input

55 mV r.m.s. to infinity at 10 kilohm

## Output

1 volt r.m.s. from near zero source impedance. Protected against accidental short circuit
2. A well-engineered gap which is as close as possible to the mechanical gap width. This results in minimal gap losses at short wavelengths.
3. The magnetic core should have a low coercivity to prevent magnetic saturation occurring which would reduce the signal-to-noise ratio and cause second harmonic distortion. This is particularly important at low tape speeds or when using low noise tapes.

On the playback side the important characteristics are:
4. Low source impedance consistent with a high output, which is required to enhance the signal-tonoise ratio.
5. A narrow gap width and a small gap depth.
6. A ripple free response at low frequencies.

The erase head should be very efficient, requiring a low input power and so causing minimal damping of the oscillator tuned circuit.

The heads specified for the recorder fulfil these requirements and are manufactured by Wolfgang Bogen of West Berlin. They are obtainable in the U.K. from Cole Electronics Ltd., the sole agents (see Components List for the address).

## RECORD AMPLIFIER REQUIREMENTS

A recording amplifier raises the level of the input signal to an amplitude suitable for driving the head. It must incorporate pre-emphasis to compensate for the high frequency fall-off inherent in the head to tape characteristic, and provide a constant current output to the head.
$U=$ UPPER
$S=S T E R E O$
$L=L O W E R$



Fig. 2a. Constant current drive using the high output impedance of a transistor collector


Fig. 2b. Constant current drive using the head in the feedback loop


Fig. 2c. Actual method used which uses a series resis̀tor to provide the constant current

Furthermore, even allowing for the full preemphasis required, there should be no overload, and the general distortion figure should be low.

## CONSTANT CURRENT DRIVE

Because the record/replay head is an inductor its impedance rises with frequency. Therefore to induce a constant magnetic flux in the tape head the head -should be driven from a constant current source. (This is the opposite of the constant voltage drive which requires a low output impedance from the amplifier.)

A high impedance in comparison with that of the head is thus required and there are many ways of achieving this in practice.

Use can be made of the intrinsically high collector impedance of a transistor, or the head may be included in the feedback loop of an amplifier as shown in Figs. 2a and 2b. Both these|approaches give an excellent approximation to the ideal current drive requirements.

However, both systems have the disadvantage of bias rejection problems and the consequent need for tuned circuits in series with the head. As this is considered to be a major drawback, the author has adopted a different method of obtaining this constant current.

## METHOD USED

The basic recording amplifier has a very low output impedance and the record signal is fed to the head via a resistor of a value which is large in comparison with that of the head over the frequency range under consideration.

The bias signal is now attenuated by the resistor and the output impedance of the amplifier (see Fig. 2c).

The penalties to be paid for this solution are: firstly, the departure from accurate current drive (the source impedance is now no longer negligible in comparison with the head impedance); and secondly, the need for a high rail voltage if a reasonable value of series resistor is to be used.

The impedance of the specified Bogen head varies from 170 ohms to a maximum of 10 kilohms. At even the highest tape speed under consideration, the output from the head begins to drop at three kilohertz. Departure from constant current above this frequency can thus be compensated for in the recording pre-emphasis.

A value of 33 kilohms was chosen for the series resistor. Between 20 Hz and 3 kHz the variation in
the record current is truly negligible, and even up to 18 kHz the drop in current is only 3.7 per cent.

## RECORD AMPLIFIER CIRCUIT

The output required from the amplifier is $10 \cdot 8$ volts peak-to-peak for full modulation of the tape. Allowing for the full pre-emphasis to be utilised and for the saturation losses in the amplifier transistors, a rail voltage of at least 48 volts is required. In fact 55 volts are available thus allowing for a small overload.

Referring to the circuit diagram (Fig. 3), it will be seen that the circuit is similar in conception to that commonly employed in audio power amplifier ${ }^{-}$ design.

Transistor TR1 forms a low noise input stage and has a gain of 4.5 times. TR2 is the workhorse of the amplifier. It has a boot-strapped collector load for high gain with low distortion due to the limited modulation index of the collector current, and forms the driver for the output stage.

Transistors TR3. and TR4 comprise the output stage which operates in class $\mathbf{A}$, a requirement for low distortion when a large output is required.

The amplifier has good linearity even before feedback, and this, coupled with the high gain and hence large amount of allowable feedback, serves to reduce distortion to a very low level.

The overall loop gain at d.c. is urity giving exceptional stability of the mid-rail voltage. Components R5, R6, and C9 provide a.c. feedback, and the frequency response for the different speeds is shaped by Ll in conjunction with $\mathrm{C} 5, \mathrm{C} 6$, or C 7 , the frequency of maximum treble boost being switched according to the speed in use, and the amount being preset by the series resistors VR1 to VR3.

The nominal gain is approximately 70 and this gives an input sensitivity in the region of 55 mV . C11 is a phase shifting capacitor required to give stability by preventing high frequency oscillation.

## METERING CIRCUIT

It is the author's opinion that average reading meters of the volume unit (VU) variety allow short duration overloads to occur without any indication. The recording meters in this recorder are therefore of the peak reading kind having a fast attack, and a slow decay time of about 2.5 seconds.

This reduces the effect of meter movement inertia, for the pointer generally has less far to travel to

| Resistors |  |  |  |
| :---: | :---: | :---: | :---: |
| R1, R101 | 390ks | R26, R126 | $1 \mathrm{k} \Omega$ |
| R2, R102 | $180 \mathrm{k} \Omega$ | R27 | $5.6 \mathrm{k} \Omega$ |
| R3, R103 | 100ks | R28 | $1.2 \mathrm{k} \Omega$ |
| R4, R104 | $6.8 \mathrm{k} \Omega$ | R29, R129 | $1 \mathrm{M} \Omega$ |
| R5, R105 | $100 \mathrm{k} \Omega$ | R30, R130 | $15 \mathrm{k} \Omega$ |
| R6, R106 | $10 \mathrm{k} \Omega$ | R31, R131 | $10 \mathrm{k} \Omega$ |
| R7, R107 | $150 \Omega$ | R32, R132 | $2.2 \mathrm{k} \Omega$ |
| R8, R108 | $10 \mathrm{k} \Omega$ | R33, R133 | $56 \Omega$ |
| R9, R109 | 39k $\Omega$ | R34, R134 | $8.2 \mathrm{k} \Omega$ |
| R10, R110 | $6.8 \mathrm{k} \Omega$ | R35, R135 | $10 \mathrm{M} \Omega$ |
| R11, R111 | $220 \Omega$ | R36 | $270 \Omega$ |
| R12, R112 | $220 \mathrm{k} \Omega$ | R37, R137 | $39 \mathrm{k} \Omega$ |
| R13, R113 | $220 \Omega$ | R38, R138 | $82 \mathrm{k} \Omega$ |
| R14, R114 | 33 k S | R39, R139 | $150 \mathrm{k} \Omega$ |
| R15, R115 | $180 \mathrm{k} \Omega$ | R40, R140 | 10M $\Omega$ |
| R16, R116 | $100 \mathrm{k} \Omega$ | R41, R141 | $10 \mathrm{k} \Omega$ |
| R17, R117 | $33 \mathrm{k} \Omega$ | R42 | $10 \mathrm{k} \Omega$ |
| R18, R118 | $39 \mathrm{k} \Omega$ | R43, R143 | 15k $\Omega \frac{1}{2} \mathrm{~W} 5 \%$ |
| R19, R119 | $3.3 \mathrm{k} \Omega$ | R44 | 10S $\frac{1}{2} \mathrm{~W} 2 \%$ |
| R20, R120 | $3.3 \mathrm{k} \Omega$ | R45 | $3 \cdot 3 \mathrm{k} \Omega$ |
| R21 | 220 S | R46 | $3 \cdot 3 \mathrm{k} \Omega$ |
| R22, R122 | $560 \mathrm{k} \Omega$ | R47 | 10ת $\frac{1}{2}$ W 2\% |
| R23, R123 | $22 \mathrm{k} \Omega$ | R48 | $22 \Omega 5 \mathrm{~W} 5 \%$ wirewound |
| R24, R124 | $18 \mathrm{k} \Omega$ | R49 | $47 \Omega \frac{1}{2} W 10 \%$ |
| R25, R125 | $4.7 \mathrm{k} \Omega$ |  |  |
| All $\frac{1}{4}$ W $5 \%$ carbon film histab unless otherwise stated, 2 off of each except R21, R27, R28, R42 and R44-R49 |  |  |  |
| Capacitors |  |  |  |
| C1, C101 | 560pF | lystyrene |  |
| C2, C102 | $2 \cdot 2 \mu \mathrm{~F}$ | $V$ Tantalum |  |
| C3, C103 | $10 \mu \mathrm{~F}$ | Tantalum |  |
| C4, C104 | $100 \mu \mathrm{~F}$ | $\checkmark$ elect. |  |
| C5, C105 | $0.15 \mu \mathrm{~F}$ | \% polyester |  |
| C6, C106 | $0.068 \mu \mathrm{~F}$ | 5\% polyest |  |
| C7, C107 | $\begin{aligned} & 0.027 \mu \mathrm{~F} \\ & \text { (use } 0 . \end{aligned}$ | 5\% polyest $22 \mu \mathrm{~F}$ and 0. | $005 \mu \mathrm{~F}$ in parallel) |
| C8, C108 | $50 \mu \mathrm{~F} 2$ | elect. |  |
| C9, C109 | $2.2 \mu \mathrm{~F}$ | $\checkmark$ Tantalum |  |
| C10, C110 | $16 \mu \mathrm{~F}$ | elect. |  |
| C11, C111 | 47pF 1 | polystyre |  |
| C12, C112 | $0.47 \mu \mathrm{~F}$ | olyester |  |
| C13, C113 | $0.33 \mu \mathrm{~F}$ | olyester |  |
| C14, C114 | $10 \mu \mathrm{~F}$ | Tantalum |  |
| C15, C115 | $5 \mu \mathrm{~F} 10$ | Tantalum |  |
| C16, C116 | $50 \mu \mathrm{~F} 1$ | Tantalum |  |
| C17, C117 | $2 \cdot 2 \mu \mathrm{~F}$ | V Tantalum |  |
| C18, C118 | $0.1 \mu \mathrm{~F}$ | lyester |  |
| C19, C119 | 1,800pF | \% polystyr |  |
| C20, C120 | $50 \mu \mathrm{~F} 2$ | elect. |  |
| C21 | $50 \mu \mathrm{~F}$ | elect. |  |
| C22, C122 | $1 \mu \mathrm{~F}$ pol | ester |  |
| C23, C24 | 2,500 F | 40 V elect. |  |
| C25, C125 | 330 pF | ramic |  |
| C26 | $0.022 \mu$ | 10\% polyes |  |
| C27 | $0.22 \mu \mathrm{~F}$ | 0\% polyest |  |
| C28 | $0.022 \mu$ | 10\% polyes |  |
| 2 off each except C21 and C26-C29 |  |  |  |
|  |  |  |  |
| Potentiometers |  |  |  |
| VR1, VR101) |  |  |  |
| VR2, VR10 | $100 \Omega$ preset (R.S. Mouldtrim) (6 off) |  |  |
| VR3, VR10 | $1 \mathrm{k} \Omega$ submin skeleton preset (2 off) |  |  |
| VR4, VR10 |  |  |  |
| VR5, VR10 | $4.7 \mathrm{k} \Omega$ submin skeleton preset (2 off) |  |  |
| VR6, VR10 | $10 \mathrm{k} \Omega+10 \mathrm{k} \Omega \mathrm{log}$ ganged pot |  |  |
| VR7, VR10 | $750 \mathrm{k} \Omega+50 \mathrm{k} \Omega$ lin ganged pot |  |  |


| Inductors |  |
| :--- | :--- |
| $\left.\begin{array}{ll}\text { T1 } & \text { 20V-0-20V } \frac{1}{2} A \text { (R.S. 20V Rectifier) } \\ \text { T2 } & \text { Bias transformer } \\ \text { L1, L101 } & 4 \mathrm{mH} \text { (2 off) }\end{array}\right\}$(see next month for <br> details) |  |

## Heads

EH1 Bogen erase head type UL296
RPH1 Bogen universal head type UK207E
(These are available from Cole Electronics Ltd., 7-15 Lansdowne Road, Croydon CR9 2HB and Brenell Engineering Co. Ltd., 231/5 Liverpool Road, London, N. 1 who also market a tape deck incorporating these heads).

| Transistors |  |
| :---: | :---: |
| TR1, TR101 | BC159 |
| TR2, TR102 | BC147 |
| TRR, TR103 | BC157 |
| TR4, TR104 | BC147 |
| TR5, TR105 | BC148 |
| TR6, TR106 | BC157 |
| TR7, TR107 | BC149 |
| TR8 | BC158 |
| TR9, TR109 | 2N3823 |
| TR10, TR110 | BC159 |
| TR11, TR12 | BFY50 |
| All 2 off except TR8 |  |

## Diodes

| D1, D101 | OA47 (2 off) |
| :--- | :--- |
| D2 | BZY88C $27 V 400 \mathrm{~mW}$ Zener |
| D3 | BZY88C 20V 400 mW Zener |
| D4, D5 | BA145 (2 off) |
| D6 | 12V 1.5 W Zener |
| D7-D10 | BA148 or 1N4002 (4 off) |

## Integrated Circuit

IC1, IC101 SN72741P or 741OPA (2 off)

## Relay

RLA 12V $185 \Omega 4$ changeover contacts (ITT)

## Meters

ME1, ME101 VU meters. Sensitivity 1.228 V a.c. for $0 \mathrm{~dB}(300 \mu \mathrm{~A})$ (2 off)

## Switches

S1 4 pole 3 way (R.S. miniature Maka-switch)
S2 2 pole changeover miniature slide
S3 6 pole 3 way (R.S. miniature Maka-switch)
S4 see text

## Miscellaneous

SK1 5 pin $180^{\circ}$ DIN socket

SK2, SK3 B9A valveholder (2 off)
SK4 5 pin $180^{\circ}$ DIN socket
PL2, PL3 B9A plug (2 off)
PL4 Mains plug Bulgin type P429
LP1 $\quad 12 \mathrm{~V} 0.75 \mathrm{~W}$ lamp and holder
LP2 Mains neon with integral resistor
$9 \frac{1}{2}$ in $\times 5$ in 0.1 in matrix plain Veroboard
16 s.w.g. aluminium for chassis (see Pt. 2 for dimensions), TO5 clip-on heatsinks (2 off), plastic cable clips ( 2 off), capacitor clips ( 2 off), 21,30 and $34 \mathrm{~s} . \mathrm{w} . g$. enamelled copper wire



Fig. 3. Full circuit diagram of the stereo tape link. Only the upper channel has been shown; the lower channel electronics being identical to the upper channel. Components numbers of the lower channel are upper channel numbers plus 100
indicate a transient, and is easier for the eye to follow.

Referring now to the circuit diagram, transistors TR5 and TR6 comprise a unity gain voltage follower having a very low output impedarice.

The metering signal is rectified by diode D1 and stored in C15. Because of the low impedance of the voltage follower the major limitation on the charging time of C15 is the forward resistance of the diode which is a gold-bonded germanium type.

The discharge time is governed by the combination of the reverse resistance of the diode, R22 and the input impedance of TR7.

In the absence of an input signal the base of TR7 is at 6.7 volts. Preset VR4 is set so that TR7 is just cut off and hence no current flows through the meter. As the signal input increases the charge on C15 increases positively hence forward biasing TR7. Resistor R23 provides the meter with overload protection.

## BIAS AND ERASE OSCILLATOR

A very pure bias waveform is needed if the full dynamic range of the tape is to be realised. Also, due to the dependence of frequency response, output level, distortion and modulation noise on the bias amplitude, the oscillator amplitude should be drift free.

Push-pull operation is used to minimise even harmonic distortion and the rail voltage is regulated to ensure amplitude stability of the waveform.
So that adequate filtering of the oscillator harmonic output is assured, the tuned circuit must have a high working $Q$. This is achieved by the employment of a low impedance tuned circuit wound with thick wire on a low loss high permeability pot core. The tuning capacitor should have low dielectric losses as well.

The transistors are protected against reverse breakdown of their base emitter junctions by diodes D4 and D5. Although this breakdown does not interfere with the operation of the oscillator, it can, after a period of time, cause reduction in the current gain of the transistors.

Zener diode D6 regulates the voltage supplied to 12 volts. The bias is arranged to decay slowly by discharging C29 when switching off from record. This is essential if magnetisation of the heads is to be avoided.

In practice, the oscillator works well. The output rises by 0.25 dB over the first minute of operation and is thereafter stable. The waveform is good, as indicated by the inaudible change in the tape noise after virgin tape has been passed by the erase head. It is important when performing this test to pull the tape away from the record head as this will record modulation noise. Modulation noise is inherent in all biased recorders and can be reduced only by the use of low noise tapes and careful design of the heads.

## REPLAY AMPLIFIER

The output of the record/replay head when playing a tape rises at the rate of 6 dB per octave (i.e. it doubles for every doubling of frequency) up to the point at which high frequency fall-off occurs.
The replay amplifier compensates for this characteristic, and provides a high level output signal suitable for feeding long screened cables.

When considering the connection of the head to the amplifier it is important that no appreciable direct current flows through it, as this would result in magnetisation of the head, and a consequent increase in the background hiss of the tape. It is thus inadvisable to couple the head via a capacitor as the charging current for it will pass through the head.

## F.E.T. INPUT STAGE

The use of a field effect transistor as an input device allows direct connection of the head. Because of the very high input impedance of the f.e.t. it requires only a miniscule bias current (about 2 nA ) which is insufficient to affect the head. Compared to a bipolar transistor the f.e.t. also possesses a generally lower noise and distortion level.
The f.e.t. and TR 10 comprise a complementary feedback pair operating at a gain of 50 from an input signal of 2.7 mV at 1 kHz and at $7 \frac{1}{2}$ inches per second. This stage has a flat frequency response. The f.e.t. has a considerable spread in the gate to source bias voltage for a given drain current, being anywhere from 0.5 to 7.5 volts. It is not easy to design away this variation, and a preset resistor (VR5) has therefore been provided for setting-up.

## SUPPLY RIPPLE REDUCTION

An interesting feature of the input stage is the method of supply ripple reduction. Transistor TR8 is biased as a current source. Zener diode D2 maintains the base-emitter potential constant in the presence of ripple on the 55 volt line. reducing modulation of the collector current.
The high source impedance of the current combined with the low slope resistance of the Zener diode D3 attenuates the ripple to a negligible level.

## EQUALISATION

The function of equalisation is left to the last stage, thus resulting in lower noise levels, and greater accuracy of equalisation which, if carried out earlier could cause variations in the input impedance.
The general purpose operational amplifier type 741 is the equaliser and output stage and is eminently suitable for this application. Because of its high gain and low output impedance it is capable of producing very accurate equalisation. It has its own internal frequency compensation for stability and its output is short circuit protected.
The d.c. source impedances of the inverting and non-inverting inputs are similar and the d.c. gain of the stage is set to unity to minimise drift of the quiescent output voltage with temperature.
The equalisation components R37-39 and C19 are selected according to the speed in use and set to the CCIR standards of 70,140 and 280 microseconds.

## POWER SUPPLY

The power supply is quite simple, providing two outputs of 55 volts and 27 volts. Full wave rectification and large value reservoir capacitors are employed to produce good supply regulation and low output ripple.

In the second part of this article, next month, con-
struction of the Hi-Fi Tape Link will be described

## ARE WE ALONE?

From time to time the question of other living entities, whether they be elementary forms or whether equivalent to homo sapiens or better, is revived. Over the past year or so the climate of acceptability has improved in these matters and there are less sceptics each year.

While all science fiction has been treated as having license in this direction, examination tends to show that in our galaxy, at least, the chances of bug-eyed monsters or intelligent vegetations being our companions is losing ground. In general all the findings so far point to a cycle of emergence to the present level of homo sapiens as the most likely, though minor variations could occur.

At the present state of knowledge the evidence is in favour of similar forms of life to that which exists now. This being the case the field is narrowed quite considerably and statistically it is known that there is a high possibility of life existing elsewhere, but the possibility of communication presents a major problem.

From several sources recently there have come new suggestions for contacting other intelligences. Some are based on the concept that if there are fundamental laws governing the matter, they would be the same for all. The use of mathematical constants and the geometric forms regarded as fundamental. is a natural step.

## ECHOES FROM SPACE?

There have been two recent suggestions, one which relates to a cyclic condition of the movements of heavenly bodies and one which is based on the problem of long time echoes of known broadcasts.

The first of these suggestions involves the possibility of near approaches to the solar system by stars which might have a planetary system. Statistics show that there is a quite frequent approach to within 50 light years of the solar system.

A second line put forward is based on the phenomenon of echoes. first noted by Störmer and Van der Pol in 1928. While they appeared to be curious and puzzling at that time this is not really the case now.

However, renewed interest has been aroused by D. Lunan, who has studied available data over a long period and has offered certain suggestions as to the reason for the echoes.

One of the suggestions is that the echoes behave as though they were reflected from a point at or near the orbit of the moon. He has constructed maps which are very similar to the star maps of the northern hemisphere and suggests that the star Epsilon Boötes might

be the source from which a probe has been dispatched.

The probe may be in orbit and attempting to make earth beings aware of its existence by re-transmitting broadcasts. It is also suggested that the probe may have arrived in the solar system about 11,000 vears ago.

Perhaps all the evidence has not been released but the matter was given considerable discussion at a meeting of the Interplanetary Society.

The echoes are both long-time, that is years, and short-time, that is 3 to 15 seconds and it is the latter that Lunan has investigated.

It is easier to offer an alternative for this suggestion because the periods are short enough to avoid the complication of dissipation of power. A short time of three seconds could be a direct reflection from the earth's other moon that has so frequently been postulated or even from the moon itself over a long path.

However, for both of the extremes of timing there is a simple answer which involves the model of the ionosphere that was considered satisfactory for a long time. This was a layer type model like the successive skins of an onion.

The ionosphere in fact is not a bit like that and not only are there now many doubts about propagation theories that have been held for so long, but the layer theory disappeared with the advent of Sputnik 1. In this connection there is much evidence that indicates that there is no homogenity about these layers at all.

Moreover, there is considerable variation in the density of the northern and southern hemispheres. This variation of density in the layers has a considerable effect on
the length of reflection paths. It is, therefore, possible for a signal to go round the earth several times before emerging at a particular angle that could be recorded as an echo.

It would be possible to expand on the Lunan theory, but suffice to say that the answer to the appearance of regular patterns can only come from a specific attempt to make a practical test of Lunan's work. It is in fact an experiment that could be undertaken by amateurs and perhaps here is an opportunity for UFO supporters to produce some practical answers to their claims.

## USSR RESEARCH

As a result of finding amino acids in the Murchison meteorite the Russians have been carrying out further experiments on meteorites that have fallen in the past. They concluded - so state academicians Vinogradov and Vdovykin - that there is evidence for the chemical evolution of life.

They also state that it is not necessary for there to be special fortuitous circumstances for this to come about: in short, life could evolve anywhere where there is a dust accretion in the galaxy. This has been deduced from an examination of the Mighei meteorite which fell in the Ukraine in 1889.

The Murchison meteorite revealed amino acids and the Mighei meteorite shows an acid which is similar to $D N A$ but has a spiral which is symmetrical. From this fact they conclude that it is not of biological derivation.

## CRAB NEBULA AGAIN

The crab nebula has been much in the news of late. The original noting of this supanova by the Chinese astronomer Yang Wei-Tek was on July 4, 1054. Little other record of this occurrence is found in literature though it must have been an event of considerable note.

However. in 1955 W. C. Miller discovered evidence that the North American Indians had recorded the event. Pictures were found showing the crescent moon with the nova nearby. This was thought to be a unique record and the date was confirmed as July 5, 1054.

Recently another discovery was made at Fern Cave in California. This cave was occupied at the time of the event in 1054 and so could be depicting an event during the formation of the settlement. The timing has been checked by four investigators and though no absolute claim for identification is made it seems unlikely that it records any other event.
The fact that the nova was more brilliant than Venus must have made it an outstanding sight.

Gerry Brown


## SENSE OF SMELL

Apropos of our discussion some months back, relating to "sniffing", it seems that someone else has latched on to the idea of recruiting bugs to serve as extensions to our probosci. This time it's some form (goodness knows what) of marine bacteria which come as a packagedeal for detecting the smells associated with just about, anything from bombs to pineapples.

These wonderful little "critters" can, I gather, be trained (presumably in the good old Pavlovian way) to glow whenever they sense or come in contact with a material of one's choosing. It is then simply a matter of detecting this light with a photocell (photomultiplier?) to identify the type of smell. Unlike previous attempts to make bugs useful in the role of an olfactory sense, this way would appear to be far more flexible.

The particular application to which this artificial nose might show some real worth ultimately is tele-olfaction.

Using this new way to sense smell at a distance ought not to be too difficult, but may be complicated by the fact that odours do not seem to be represented to us in the same way that, say, colour is; to wit, it is not simply a question of discovering primary odours then combining them to produce "hues" (or phews!).
No doubt, the limitation would be on the number of trained bacteria packages one could incorporate in a transducer, since each package would only respond to a particular smell. Assuming a compromise could be accepted at around one hundred or so packages, that is one package for each of the more common smells, then by fanning air over the device, sequential interrogation of the bacterias' photocells would classify both type and amplitude of odour.
This, though, is only part way to the ultimate in communication. There still needs to be a method for reproducing the "pong" from the transducer output waveform. This should be a simpler job because, following receipt of this signal, it might then be decoded within the receiver and the resultant employed to address a corresponding set of control valves, so releasing the correct aromatic. As an alternative, heaters could be arranged to vapourise the material.

Whether there should be a delay between different signals is a moot point; naturally, it would not be the best of schemes to follow one smell by another when the chance existed that they might clash or produce some offensive "mixture".

Honestly, imagine one of these things going wrong. There is little doubt that TV servicemen would suddenly find new uses for the humble pair of bull-nosed pliers!


## SHOCK THE VILLAINS

Mugging, and the crop of pensioner-bashing cases we have witnessed lately must be distasteful to anyone with a sense of respect for their fellows. For a while now I have considered a number of possibilities for beating this, very real, public menace, but it is not something to be easily reckoned with.
Anyhow, having contacted the Chief Constable for my area and

discovered that, 'no, you were not permitted to use portable highvoltage equipment with a view to shocking the villians into submission", the only effective alternative seemed to be a notion based on the word "shock". The best deterrent thus appeared to be one that could attract a lot of attention to one's predicament by others in the vicinity of the incident.
An electronic device to meet our requirements generally needs to have quite bulky proportions, so probably the best "noise-maker" would be a small klaxon employing a miniature electric motor, this driving a coarse-pitch gear pinion bearing directly against a piece of bent metal (certainly noisier than any loudspeaker of twice the size). The metal "noise-maker" could be carved out of the lower part of a handheld lamp case the upper portion being utilised for photo-flash attention lamps.

This kind of alternative, at least, should come within the limits of the law and Fig. 1 gives the general picture of how such a set-up would function.
Two switches control the device, The "arm" switch (preferably concealed) connects a supply battery preparatory to possible operation. Simultaneously, photo-flash lamp drive is inhibited while a "set" pulse is fed to a six-stage ring counter whose outputs all go to logical " 0 '".

Release of the spring-return 'trigger' switch starts the klaxon and also causes the clock generator to run. In turn, a (deliberately over-run) "keep-alive" lamp rapidly flashes while the divider counts down the signal from the clock. Thus, every 13 or so seconds, the ring counter will be advanced one step and so fire (one at a time) the photo-flash lamps.

Naturally, the device ought not to be too inelegant physically, but should be of sufficient size to prevent any feeble-minded body-vandal shoving it in a convenient Gladstone bag!

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Arrcuw are the first to offer the new double w Jund ring core chokes designed for RF inze ferente suppression in Bur.Polar SCR and TRIAC circuits. The chokes are supplied as a kit inclusing a Celta Capacitor. For details see the new catalogue.




MOSt watches and small clocks being manufactured at the present time still employ a mainspring as the source of power and a balance wheel plus hair spring as the timing mechanism. The basic design of such timepieces has remained essentially unaltered since Huygens developed the balance wheel mechanism in the 17th Century.

## RECENT PROGRESS

The first battery powered wrist watch was marketed in 1952, but recent developments in micro-miniature integrated circuits have enabled high quality electronic watches to be produced which are much more accurate than the more common types of watch. Most of the quartz crystal controlled watches show an accuracy of about one minute per year.

Electronic watches do not have a spring which requires winding periodically, but their miniature battery must be replaced about once per year. Smaller movements suitable for ladies' watches are becoming available, but considerable problems arise in designing watches which can be powered by miniature mercury cells which must have the minimum acceptable life of one year.

## TYPES

Some of the main types of electronic watches can be classified in the following way. Many variations of these main types are available.

First generation watches employ a conventional balance wheel and hair spring as the timing mechanism, but they are powered by a battery instead of
a main spring. They have the advantage that they do not have to be wound, but their accuracy is not generally so much greater than that of a conventional watch.

Second generation watches employ a tuning fork mechanism operating at about $300-500 \mathrm{~Hz}$ as the timing device; a transistor circuit drives the tuning fork. Such watches have an accuracy of about one minute per month, this being considerably better than that of an ordinary watch. Tuning fork watches do not tick, but emit a slight hum.

Third generation watches employ a quartz crystal oscillator which normally operates at a frequency of between about 8 kHz and 33 kHz ; most types have an accuracy of about one minute per year. The pulses from the quartz crystal are used to control the speed of a motor which drives the hands of the watch. Various techniques are employed to accomplish this.

Fourth generation watches do not employ hands to indicate the time, but incorporate a digital display which shows the time in actual figures. These watches have no moving parts and are completely electronic. They employ a quartz crystal oscillator and have an accuracy of about one minute per year. They are only just becoming available.

## PRICES

Unfortunately the price of electronic watches is still quite high. The price of many watches is increased because a gold case is employed.
In the case of stainless steel cased watches, one finds tuning fork models ranging from just under


Fig. 1. The circuit diagram of the first electric watch
£40 upwards, with the smaller ladies' watches rather more expensive. The quartz crystal controlled watches are generally priced from about $£ 100$ upwards, but some of the watches providing a digital display are not so much more expensive than other quartz crystal controlled watches.
It seems fairly certain that the prices of electronic watches will fall during the next few years. The components of all electronic digital watches can be mass produced and assembled by relatively unskilled personnel. This will effect a great saving in labour costs at a time when the general trend is for labour costs to rise rapidly and integrated circuit production costs to fall-especially if large numbers are produced. Thus it seems we are experiencing a revolution in watch design.

Although one has the annual cost of the power cell replacement (typically $£ 1$ ), most electronic watches do not require servicing so frequently as ordinary watches. The forces in the transmission gearing are usually less than in conventional watches and therefore satisfactory functioning is almost independent of the increase of viscosity of the oil with time. In general the regular cleaning and preventative maintenance recommended for balance wheel watches are unnecessary with most electronic types; one only has the watch attended to if it stops or does not keep time with its normal accuracy.

## CLOCKS

Electronic clocks are also available with either a digital display or with the analogue display provided by ordinary hands. Many electronic clocks are quartz crystal controlled and provide an accuracy of about one minute per year. Both battery powered and mains types are available.

Some of the main types of electronic timepieces will now be considered in reasonable detail. The development of electronic clocks has followed a rather similar pattern to that of watches (although the problems of size are not nearly so acute) and therefore clocks and watches will be discussed together

## THE FIRST ELECTRIC WRIST WATCH

The first battery driven wrist watch was developed by the French Company LIP in collaboration with the Elgin Company of the U.S.A. This watch was introduced into the LIP range on 15th March 1952, but the same type of movement is still being produced by this Company. The Hamilton Company introduced a similar type of movement very shortly after the LIP watch became available.

Although this type of watch is electric rather than electronic, it will nevertheless be discussed, since it led to the development of some electronic types and has been described as the greatest advance in watch design for some centuries. The same basic principle has also been used in clocks manufactured by the Hamilton, Vedette, Bayard and Odo Companies.

## PRINCIPLE OF OPERATION

The principle of operation of the LIP type R. 148 movement is shown in Fig. 1. The R. 184 is similar, but it also indicates the date.

As the balance wheel rotates in an anticlockwise direction, the jewelled cam on the balance wheel pivot pushes the contact wire onto the contact. A current then flows from the power cell through the electro-magnet and the contacts and back to the cell. The energising of the electro-magnet at the instant the pole of the balance wheel is approaching the pole of the magnet causes the two poles to be attracted together so that the balance wheel is given an impulse.

The balance wheel continues to rotate after the contact has been made and the wire returns to its normal position. When the balance wheel swings in the opposite direction, the cam merely pushes the contact wire away from the contact and the magnet is not energised. The electro-magnet passes a current which increases to about 1 mA over a period of 3 milliseconds and then the current falls rapidly to zero. The current pulses occur every 0.4 s . For simplicity a magnetic shunt employed in this type of watch is not shown in Fig. 1.

## SPARK SUPPRESSION DIODE

A diode is connected in parallel with the electromagnet. When the contacts are joined little current flows through this diode, since the potential across the electro-magnet reverse biases the diode. When the contacts break, however, the current ceases to flow through the electro-magnet and a back e.m.f. is developed across the latter, owing to its inductance. The diode shorts out this back e.m.f. and thus prevents damage to the delicate contacts by sparking.
The cam illustrated in Fig. 1 consists of a jewel. The amplitude of the balance wheel oscillations can be controlled by adjusting the contact regulating lever; the latter rotates the contact arm on its axis.

The amplitude of the oscillations of the balance wheel remains fairly constant and this results in the watch keeping more accurate time than many watches which are powered by a main spring. There is some negative feedback, since if the amplitude of oscillation decreases, the contact time is increased and this provides more power which helps to stabilise the amplitude of oscillation.

The mean current taken from the 1.55 V cell is normally $5 \mu \mathrm{~A}$, but does not exceed $7 \mu \mathrm{~A}$. A 100 mA hour battery therefore has a life of some 15 to 18 months.


Fig. 2 (left). A circuit which can be used to drive the balance wheel of a watch

Fig. 3 (right). A circuit used in some self-starting watches and clocks for driving the balance wheel

## ELECTRONIC WATCHES WITH A BALANCE WHEEL

The main problem with the type of electric watch just described is that the contacts must be very carefully designed and positioned if they are to have a reasonable life. In any case, the contact life tends to be limited.

The work of Marius Lovet of France has led to the development of electronic watches with balance wheels in which the switching is carried out by a transistor.

The balance wheel of such electronic timepieces carries small permanent magnets which move near to two coils-a feedback coil and a power coil. The currents induced in the feedback coil by the movement of the balance wheel magnets drive the transistor into conduction so that the power coil receives a pulse. The magnetic field developed by this coil acts on the magnets to give the balance wheel a driving pulse.

## CIRCUIT

The type of circuit shown in Fig. 2 may be used in timepieces which employ an electronically driven balance wheel. If the latter is stationary, the base and emitter are at the same potential and therefore the collector current is extremely small.

When a magnet of the balance wheel moves past the feedback coil, however, the voltage induced in this coil causes the transistor to conduct. A current therefore flows in the power coil and an impulse is

The Swissonic 10 electronically driven balance wheel movement. The magnets can be seen fixed to the balance wheel and the power coil under the balance wheel. The first three letters of the days of the week are shown in French
produced which powers the balance wheel. The pulse duration is typically 10 milliseconds. Spurious oscillations of the circuit can be suppressed by connecting a capacitor between the base and collector of the transistor. The resistance of the coils may be a few hundred to a few thousand ohms.

One of the disadvantages of this type of circuit is that the balance wheel does not commence to oscillate automatically. The watch must be rotated to start it. This difficulty can be avoided by connecting a resistor of about 100 to 200 kilohms in the base circuit (as shown in Fig. 3) so that a very small current flows in the base and collector circuit even when the balance wheel is stationary.

The current flowing through the power coil interacts with the magnets of the balance wheel and causes the latter to move. The minute oscillations are built up by transistor action until the normal amplitude of vibration is reached.

Although a current flows through the circuit continuously, this current can be eliminated by connecting an electrolytic capacitor in series with the feedback coil (as shown in Fig. 3).
Contactless circuits of this general type are widely employed in some of the more economical types of electronic watch. For example, the LIP movement type RE. 50 (introduced in 1970) was the first electronic watch for ladies available anywhere in the world. Much of the internal volume is occupied by the $1.55 \mathrm{~V}, 60 \mathrm{~mA}$-hour cell which is 7.9 mm in diameter. One finds similar circuits in the 9154 "Dynotron" movement produced by Ebauches S.A.


- EbAUCHES SA


Fig. 4. The miniature tuning fork used in the Bulova 214 series of "Accutron" models
(a large group of Swiss watch manufacturers), in the Junghans 600.12 self starting wrist watch produced in Germany, etc.

Electronic clocks employing this type of movement are produced by Junghans (type ATO-MAT-WERK 726); the mean current consumption is less than $160 \mu \mathrm{~A}$ at 1.4 V or a total of less than 1.5 A -hour per year. Similar types of clock are produced by the Jaz Company.

## ELECTRONIC ALARMS

The first wrist watches with electronic alarm systems were introduced by the Nepro Company of Switzerland in 1972 after five years of development work. Although watches which have an alarm powered by a spring are available, the stored energy is rather small and this results in the intensity of sound being low and the duration limited. In addition, the alarm spring must be wound each time the alarm is used.

The Nepro watches employ a 1.5 V battery of 40 mA -hour capacity to power their wrist watch alarms. The current is about 8 to 12 mA and the battery has a life of about a year with normal use. The alarms of these watches sound for five minutes unless cancelled by a push button.

## ALARM MODELS

One type of wrist watch with an alarm produced by Nepro is the "Memotron". A vibrating membrane generates the sound in the MB 500 alarm system contained in this watch. This is the smallest electronic alarm in production, having a volume of only 0.8 cubic centimetres.

Watches using this type of alarm can be used to remind a person of appointments throughout the day and can therefore be used for many purposes for which normal alarm clocks are unsuitable. The Nepro Company also produce a range of very small alarm clocks with battery powered alarms.

## TUNING FORK WATCHES

The first tuning fork movement was announced by the Bulova Company on 25th October 1960. This watch, which was the result of six years of research by Dr Hezel of Bulova, represented a real revolution


Fig. 5. The circuit of the Bulova 214 series of "Accutron" watches


Fig. 6. The pawl mechanism used in the Bulova "Accutron" watches for converting the tuning fork vibrations into a rotary motion
in watch design. It was the first watch to employ an electronic mechanism and the first to use a timing mechanism not based on the balance wheel. In addition, it was the first watch to be delivered to the purchaser with a written guarantee of its time keeping accuracy; this is $\pm 1$ minute per month.

The frequency of operation of the tuning fork watches made by Bulova for men is 360 Hz . The mechanical loss of energy has been reduced to a very low level and this enables the low power consumption of about $8 \mu \mathrm{~W}$ to be obtained. The tuning fork mechanism is relatively insensitive to shock.

The tuning fork employed in the Bulova 214 series is shown in Fig. 4. It is made of a special alloy which is little affected by changes of temperature. A strong conical magnet and a magnetic cup are fitted to each prong of the tuning fork.

A coil is fitted over each magnet. The coils must be fixed in position so that they are very close to the vibrating magnets but never touch them. The coils have a total of about 16,000 turns, comprising about 200 metres of insulated copper wire of diameter 0.015 mm . About one quarter of the number of turns on the one coil is used for feedback purposes.

## CIRCUIT

The circuit of the 214 series of Bulova watches is shown in Fig. 5. It consists of a simple transistor oscillator with a feedback coil (or "phase sensing


Fig. 7. The inside of a Bulova 214 series "Accutron" watch
coil") and two power coils. Indeed, it is basically very similar to the circuits of Figs. 2 and 3 which drive balance wheel watches.
The alternating voltages induced in the phase sensing coil are applied (via the battery and capacitor) to the base-emitter junction of the transistor. This diode junction rectifies these voltages and the capacitor is charged with such a polarity that the voltage across it opposes the power cell voltage.

The resistor in parallel with the capacitor allows the charge of the latter to leak away slowly so that a small pulse of current flows each time the voltage across the phase sensing coil is near to its maximum value and at a time when the polarity of the voltage induced in the power coils opposes that of the power cell.

If the total voltage induced in the power coils at the instant the transistor conducts were exactly equal to the cell voltage, no current would flow through them. At the correct amplitude of vibration, the voltage induced across the power coils at a time when the transistor conducts is about 10 per cent less than the cell voltage. A 10 per cent increase in the amplitude of vibration would therefore result in the current pulses to the power coils becoming zero and the amplitude would then quickly fall to its correct value. Similarly, a 10 per cent decrease in the amplitude would raise the current through the power coils to double its normal value and return the amplitude quickly to normal.

## TRANSLATING THE VIBRATIONS

The vibrations of the tuning fork are converted into rotary motion by means of the rachet and pawl mechanism shown in Fig. 6. Attached to one arm of the tuning fork is a straight spring (the "index") tipped with a tiny jewel which engages ratchet teeth on an index wheel. The wheel advances one tooth for each complete vibration of the tuning fork. A
pawl holds the index wheel in position during the return stroke of the index jewel. Although the index wheel is only 2.4 mm in diameter and 0.04 mm in thickness, it has 300 teeth spaced at 0.025 mm intervals. The index wheel drives a gear train which turns the hands of the watch.
The rate of rotation of the index wheel is unaffected by any normal changes in the amplitude of vibration of the tuning fork. The index wheel will move one tooth per vibration of the tuning fork when the index jewel moves any distance between just over one and just under three times the distance between adjacent teeth. If the index wheel moves a distance just over the distance between successive teeth. the pawl jewel will push it back during the time the index jewel returns; the net movement is then one tooth.
continued on page 231

Four smatl Accutron watches for ladies, using a tuning fork movement



## YEAR OF THE HYBRID

This could be the year of the hybrid microcircuit. Thick film technology has made enormous strides through development of special inks for printing resistive networks, plenty of chip capacitors and active devices are available and potting techniques have also had their share of improvement. The result is a neat module of high reliability which has become extremely attractive to equipment manufacturers.

The manager of the very active hybrid microcircuit division of Welwyn Electric, Brian Attwood, tells me that although Welwyn started research on thin films some seven years ago and reached a high degree of perfection, the less expensive thick film technology has now reached the stage where it can do all the jobs of thin film. Welwyn are now fully concentrated in the thick film area with a plant capacity capable of handling $£ 1$ million worth of circuits a year, although he admits that actual production has not yet reached this figure.

Welwy is engaged in the high technology end of the business with as many as a couple of dozen components in a single package. At any one time some 60 different circuits are going through the plant and some 500 types of circuits, all custom built by Welwyn, are in field service.

Also bidding for a big share of the hybrid market but in the higher volume area is Newmarket Transistors Lid., a member of the Pye Group.

Two recently announced products are a low level audio amplifier, the performance of which can be adjusted as required by external components or links, and a selfcontained hearing aid amplifier.

The company states that thick film hybrids have now reached a
stage where costs are becoming appreciably lower than discrete assemblies. So confident is Newmarket that they are inviting users of circuits using discrete components to send details to the Newmarket design team who will then evaluate it in hybrid terms free of charge and submit a quotation for supplies.

Yet another firm spreading its wings with hybrids is Coutant Electronics, best known as a supplier of power supply units. Coutant started a thick film facility for inhouse use manufacturing mainly encapsulated voltage regulators and over voltage protection units. Now the company is offering a full design and manufacturing service to allcomers.

## POLLUTION MEASUREMENT

The big investment in design of pollution measurement equipment by Plessey looks as if it may be starting to pay off. The company's water quality monitoring equipment came through a four-month evaluation at the Water Pollution Research Laboratory, Stevenage, with flying colours and installations are now operating with the Sussex, Lancashire and Trent River Authorities. Orders have been taken from similar authorities in Sweden, Italy and France, the forerunners of a profitable export trade.

Another notable order, although valued only at $£ 20,000$ is for an environmental monitoring system at Lovisa, Finland, where that country's first nuclear power station is scheduled to come into operation in 1975. The Plessey installation measures meteorological, hydrographic and water quality data at the outfall of the station's water cooling system. A total of 38 separate parameters are automatically monitored and the resulting data should prove extremely valuable even before the station comes into operation.

## PROFESSIONAL COMPONENTS FOR THE AMATEUR

SDS Components at Portsmouth have opened a retail shop for the home constructor at their premises at Gunstore Road, Hilsea Trading Estate. This is not the first time a wholly professional supplier has turned to the amateur market for extra revenue and it will be interesting to see if the project fares better than some earlier attempts elsewhere which, in the end, proved uneconomic.

Many famous names are listed in the product list which can be obtained by sending 20p to SDS Components Ltd. Tim Curtis,
managing director of SDS, says that he intends to break down the bar* riers between the amateur fraternity and industrial manufacturers.

## NEW OUTLET FOR EVR

Electronic Video Recording EVR - has been slow in market development but good business could result from its use at sea. Most ships now have TV for the use of the crew when in port and in coastal waters. In fact Marconi Marine has supplied more than 4,000 multi-standard receivers to vessels of all types.

Ships' crews are just like their shore-based counterparts and like to have TV on tap all the time. Hence the interest in EVR which will allow the TV to keep entertaining from video telecartridges when out of normal reception range.

Marconi Marine has entered an agreement with Telmar Programme Service for the supply of recorded programmes on the EVR film cartridge system which is manufactured at Basildon, Essex. The cartridges each give two 30 -minute monochrome programmes or one 30 minute colour programme.

Many thousands of recorded programmes are available and ships will be able to exchange programmes at ports throughout the world. The playing units are manufactured by a number of companies under licence from the EVR partnership of ICI and CIBA. GEIGY.

## RACAL AND THE YOUNGER MAN

Chairman and managing director of Racal Electronics, Ernest Harrison, believes in youth. He likes to spot likely managers early in life and then give them the opportunity to show what they can do.

Some recent promotions prove the point. Keith Thrower who heads up a newly-formed Advanced Development Team has made big progress on the engineering side. He now has a top job at the age of 38. David Elsbury, 36, has climbed the ladder to become. managing director of Racal Mobilcal Lid., one of the most profitable of all Racal companies and the one that recently booked the largest ever contract for the company valued at $£ 1.8$ million.

Two. other youngsters who now have top jobs are Gerry Smith, heading up Racal's Singapore office, and Jim Diggins, who joined Racal at the age of 25 and is now managing director of Racal Communications Ltd.

Harrison told me that in general successful people need to be "well seen" before they are 30 years old. Very few good people, he savs, ever leave the company because promotion prospects are good.

## (P) AL_P. (Electronics) Ltd

## THE HY41



The HY41 supersedes the popular HY40 introduced by ILP last year. This highly improved module achieves true High Fidelity with a dramatic reduction in distortion (typically $005 \%$ at 1 KHz into 8 ohms!) and is electronically and mechanically compatible with the HY 40

With this important improvement the HY41 retains all of the quality characteristics found in the earlier version and P.C. board, Resistor, Capacitors, Hardware Mountings and comprehensive manual are included in the basic kit. No fur ther components are required to construct a compiete ower amplifier of extremely high performance sufficiently versatile to provide power not merely for Hi-Fi but also for public address systems and industry.

The free manual gives a full circuit diagram of the HY4t and its various applications including amplete stereo amplifier

Like its predecessor the HY41 is based on conventional and proven circuit techniques developed over recent years.
OUTPUT POWER: British Ratıng 40 WATTS PEAK, 20 watts
R.M.S. continuous.

LOAD IMPEDANCE: 4-16 ohms.
INPUT IMPEDANCE: 30 K ohms at 1 KHz .
VOLTAGE GAIN: 30 db at 1 KHz
TOTAL HARMONIC DISTORTION : less than $0.15 \%$ (typical $0.05 \%$ )
at 1 KHz .
FREQUENCY RESPONSE: $5 \mathrm{~Hz}-50 \mathrm{KHz}+1 \mathrm{db}$
SUPPLY VOLTAGE: +22 5volts D.C.
SUPPLY CURRENT: $\overline{0} .8$ amps maximum.

PRIICE: inc. comprehensive manual, P.C. board, five extra components and P. \& P.
MONO: £4.90
STEREO: $£ 9.80$

## UNIQUE HYBRID PRE-AMPLIFIER

The HY5 has rapidly established a position in the WORLD as the sole hybrid pre-amplifier to contain all feedback and equalization networks within an integrated preamplifier circuit.

Supplied with the HY5 are two stabilizing capacitors and by the addition of volume, treble and bass potentiometers it is ready for use.

Internally the HY5 provides equalization for almost every conceivable input, the desired function is achieved by use of a multi-way switch or by direct interconnection,

Two distinctive features of the HY 5 are its inbuilt stabilization circuit, allowing it to be run off any unregulated power supply from 16-25 Volts and a balance circuit which, when linked by a balance control to a second HY5, forms a complete stereo pre-amplifier
Specificaliy and critically designed to meet exacting Hi-Fi standards, the HY5 combines extremely low noise with a high overload capability. When used in conjunction with the HY41 and PSU45 forms a completely intergrated system.

## INPUTS

Magnetic Pick-up (within $\pm 1 \mathrm{db}$ R|AA curve) $2 \mathrm{mV} .47 \mathrm{~K} \Omega$
Tape Replay lexternal components to suit head. $4 \mathrm{mV} .47 \mathrm{~K} \Omega$
Microphone (flat) $10 \mathrm{mV} .47 \mathrm{~K} \Omega$
Ceramic Pick-up lequalized and compensatable) $20-2000 \mathrm{mV}$. variable
Tuner (flat) $250 \mathrm{mv} .100 \mathrm{~K} \Omega$
Auxiliary $1250 \mathrm{mV} .47 \mathrm{~K} \Omega$
Auxiliary 2 $2-20 \mathrm{mV}$. $100 \mathrm{~K} \Omega$

OUTPUTS
Main Pre-amp output 500 mV Direct tape output 120 mV .

ACTIVE TONE CONTROLS (Bexendall) Treble + 12db
Bass +12 db .
INTERNAL STABILIZATION
Enables the HY5 to share an unregulated
supply with the Power Amplifier.
SUPPLY VOLTAGE
16-25 volts
PRICE: MONO: $£ 3.60$


SUPPLY CURRENT
6 mA approx.
OVERLOAD CAPABILITY
better than 26 db on most sensitive input
infinite on tuner and auxl.
OUTPUT NOISE VOLTAGE: 0.5 mV

STEREO: $£ 7.20$

## POWER SUPPLY PSU45

The versatile P.S.U. 45 is designed to supply vour HY41's +HY5's in stereo or mono format.

## Specification

Input: 200-240 Volts.
Output: +22.5 Volts at 2 amps .
Overall Dimensions: L. $7^{\prime \prime}:$ D. 3.8'": H. 3.1"
PRICE: $£ 4.50$ inc. P. \& P.

WAITORD ELECTRONICS 35 CARDIFFROAD, WATFORD
C.W.O. Please. P. \& P. please add 10 p to order under
\&2.

## RESISTORS

High stability, low noise, carbon film resistors. Tubular miniature, high power resistors.

| Power Watts | Tolerance | Range | Values available | Price |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| $t$ | 5\% | 2.2-2.2M $\Omega$ | E/2 | Ip | 0.8 p |
| $t$ | $5 \%$ | 2.2-10M $\Omega$ | E12 | ip | 0.8p |
|  | 5\% | 2-2-10MS | E12 | 2 p | 1.5 p |
| 4 | 10\% | I-IOM $\Omega$ | E12 | 6 p |  |

Quantity prices available for any selection. Ignore fractions on total order.

## CAPACITORS

Electrolytics, general purpose, miniature, axial lead. Mullard O/5, O16, O17 series.

| 1 mf |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 1.5 mf | 63 V | ${ }_{6 p}^{6 p}$ | 22 mf | 63 V 63 V | ${ }^{6 p}$ |
| 2.2 mf | 63 V | 6 p | 100 mf | 40 V | 6 p |
| 3.3 mf | 63 V | $6 p$ | 150 mf | 25 V | 6 p |
| 4.7 mf | 63 V | 6 p | 220 mf | 25 V | 110 |
| 6.8 mf | 63 V | 6 p | 470 mf | 25 V | 13 p |
| 10 mf | 63 V | 6p | 680 mf | 25 V | 20p |
| 15 mf | 63 V | $6^{\mathbf{p}}$ | 100 mf | 25V | 25 p |

## POLYESTER FILM CAPACITORS

Mullard C296 series. 400 V d.c.
mf: $0.001,0.0015,0.0022,0.0033,0.0047,0.0068,0.01,0.015,0.022$, rice 21 p.
mf: $0.033,0.047,0.068,0.1$, price 4p.
mf: 0.15 , price 8 p .
mf: 0.22 , price 7 p .
mf: 0.33, price 10p.
mf: 0.47, price 13 p.
160 V d.c.
$\mathrm{mf}: 0.1,0.15$, price 4p; 0.22, price 5 p ; 0.33 , price 6 p ; 0.47 , price 7 p 0.68 , price 10 p ; 1.0 , price 13 p

Mullard 280 series P.C. mountings. 400 V d.c.
$\mathrm{mf}: 0.01,0.015,0.022$, price 3p; $0.033,0.047$, price 4 p
250 V d.c.
mf: $0.068,0.1,0.15$, price $4 p$.
$\mathrm{mf}: 0.22,0.33$, price 5 p .
$\mathrm{mf}: 0.47$, price $7 \mathrm{p} ; 0.68$, price 10 p ; 1.0 , price 11 p .
CERAMIC DISC CAPACITORS
Working voltage 50 V d.c., Plaquette body with 25 in leads Range: 22pf-10,000pf, price 2p.

## POTENTIOMETERS

Carbon track, $1 K-2 m$, log or linear, single gang, price 12 p ; $5 \mathrm{~K}-2 \mathrm{~m}$, log or linear, dual gang, price 37p; $5 K, 2 \mathrm{~m}$, log or linear, single gang with switch, 24p.
Knobs for above 10 p .
SLIDER POTENTIOMETERS
Single $10 \mathrm{~K}, 25 \mathrm{~K}, 50 \mathrm{~K}, 100 \mathrm{~K}$, log or linear, 31 p .
Dual gang, IOK plus 10K, etc., above values log or linear, 50p. Knobs for above @10p.

## PRE-SET POTENTIOMETERS SKELETON

Horizontal or vertical mountings, 0.25 W , miniature, price $5 \frac{1}{p}$ p


## DIN PLUGS AND SOCKETS

2 pin, ${ }^{3}$ pin, 5 pin ( $180^{\circ}$ or $\mathbf{2 4 0 ^ { \circ }}$ ). Plugs, price 12p. Sockets, price 8p.


# (1) 졀 <br> <br> 5ILVER <br> <br> 5ILVER THE FIRST TRAMSISTOR 

## THE TEAM

THE original transistor device was demonstrated in public for the first time on June 30. 1948, at Bell Telephone Laboratories New York premises. This was the pointcontact transistor, invented by two physicists, John Bardeen and Walter Brattain, who had been researching into solid state materials. They formed part of a Bell Laboratories team led by William Shockley.

A few months following the successful experiment that resulted in the first practical solid state amplifying device. Shockley set out to formulate a theory to explain transistor action. He proposed, in fact, the junction transistor structure. This theory was later developed in practice and the junction transistor became the very successful successor to the pointcontact transistor and the latter was obsolete by about 1956.


One of the first experimental pointcontact transistors.

A whole new field of semiconduction technology sprang from the exploitation of the $p n$ iunction. as originally proposed by Shockley. An amazing range of devices have been developed through the application of the pn junction principle. where conduction is performed through minority as well as majority current carriers-holes and electrons.

William Shockley was also responsible for proposing the four layer pnpn device, which led to the silicon controlled rectifier (thyristor); and also for a different class of unipolar devices, based upon the field effect. These inventions followed within a few years of the junction transistor.

For their outstanding contributions to transistor physics. John Bardeen. Walter Brattain. and William Shockley were awarded the 1956 Nobel Prize in Physics.


An early version of a junction transistor.


## DR WILLIAM SHOCKLEY

Dr W. Shockley was born in London of American parents on February 13, 1910. Reared in California, he received his B.Sc. degree at the California Institute of Technology in 1932 and his Ph.D at the Massachusetts Institute of Technology in 1936. He joined Bell Telephone Laboratories in 1936. During the war he served as Director of Research for the U.S. Navy. Afterwards he returned to Bell and became director of the solid state physics research programme which saw the development of early junction transistors.
In 1963 Dr Shockley was named the first Alexander M. Poniatoff Professor of Engineering Science at Stamford University. He has received numerous awards for scientific achievement and public service, and serves on many top level Advisory Committees in the United States. Dr Shockley has contributed over one hundred articles to scientific journals, and more than 70 U.S. Patents have been granted for his inventions.
\& Dr William Shockley is to lecture at the I. E.E. in London on February 14.

An early production version of the point-contact transistor.


## IERE and IEE

To celebrate this silver Jubilee, the Institute of Electrical Engineers, in conjunction with the Institution of Electronic and Radio Engineers, has arranged a number of events which will take place between the 12 and 16 February 1973.

The IERE is marking the 25 th anniversary of the invention of the transistor by devoting a specially enlarged issue of its Journal-The Radio and Electronic Engineer-to a unique collection of nearly 20 papers on semiconducting subjects contributed by leading British scientists and engineers who have worked in this field during the past quarter of a century.

This Transistor issue of the Radio and Electronic Engineer which is dated January-February 1973, may be obtained from the IERE Publications Sales Department, 9 Bedford Square, London WCiB 3RG, price $£ 2.00$ per copy, post free.
;

12th-16th February 1973
An exhibition at the Institution of Electrical Engineers demonstrating the evolution from the point-contact transistor to complex integrated circuits.

13th February 197310.00 a.m.
A colloquium "The 25th anniversary of the Transistor' at the Royal Society.

14th February 19735.30 p.m.
A lecture by Dr William Shockley "The invention of the transistor: an example of creative-failure methodology" at the IEE

15th February 197310.30 a.m.
A half-day discussion meeting "What next in semiconductors' at the IEE

### 3.00 p.m.

A lecture by Dr W. E. J. Farvis 'The influence of the transistor in our society and economy" at the IEE.


# FROM RECTIFYING CONTACTS TO PN لUNCTIONS 

AStartingly new and revolutionary technical device rarely appears simply out of the blue. The invention or discovery is generally a culminant achievement by a research worker who has followed in the train of a number of earlier researchers and experimenters.
Experience suggests that requisite conditions for a momentous breakthrough are (1) a sufficiently advanced state of theoretical knowledge, (2) a large demand or potential need for a particular kind of device and (3) a sufficiently sophisticated technology capable of undertaking development and then quantity production of some innovation.
Amplification of electric currents is an essential part of electronics. The idea of a solid state amplifier had been a vision in the minds of many scientists and experimenters for many years before the triumph of 25 years ago.
As beneficiaries of the electronic wonder of the century, it is appropriate for us to look back and try to pick out some of those stepping stones that formed a path, no matter how faint and discontinuous, which led to the ultimate success of Messrs. Bardeen, Bratain, and Shockley.

## EARLY INVESTIGATIONS

With some justification we can identify the commencement of the trail to the transistor with Michael Faraday, who in 1833 recorded the non-linear characteristics of certain conductors of electricity. He found that silver sulphide had a negative temperature coefficient.

In 1873 Willoughby Smith discovered that light affected the resistance of crystalline selenium.

In 1876 Adams and Day observed non-linear conduction phenomena on light-sensitive selenium cells.
(A fascinating light beam wireless communication system was created by the American experimenter and inventor Alexander Graham Bell (1847-1922). Speech was transmitted some distance in space by modulating a light beam and then received upon a selenium element, which produced an electrical output corresponding to the speech input.)

The earliest detailed investigation of the rectifying effect of dis-similar materials in contact, was carried out by the Austrian physicist Braun around 1874. Braun explored a variety of crystals, but mainly lead and ferrous sulphide, and used a base electrode and wire as a point-contact. He observed that the resistance was dependent upon the polarity of the applied voltage, as well as on the nature and condition of the contact surfaces. Braun likened this phenomena to the conduction between closely spaced electrodes in a gas.

Braun also appears to have been the first to observe the rectification effect produced by selenium.

Like many pioneers, Braun had his critics and his discoveries did not gain immediate recognition.

## POINT-CONTACT RECTIFIERS

Point-contact rectifiers were first put to practical use as signal detectors in the early days of wireless, around 1904. Natural lead sulphide was commonly used. During the mid-1920's the crystal detector was finally ousted from radio receivers by the thermionic valve.

## PLATE RECTIFIERS

Plate or "metal" rectifiers consist of thin semiconducting layers with metallic electrodes on either side. Cuprous oxide and selenium are the two most important materials that have been widely used.

The rectification effect of contacts between metals and cuprous oxide was already known to Braun. The first useful device appears to have been made by Grondahl in 1920.

The cuprous oxide rectifier was used on a large scale for power rectification and for use in measuring instruments, from the mid-1920's to the late 1930's. Then it began to be replaced by the selenium rectifier which offered many advantages.

The selenium photocell was widely used during the period 1930-40.

## NEW CONCEPTS

Investigation into the properties of semiconducting materials appears to have received a great impetus in the 1930's. One important new concept was the "hole", introduced by A. H. Wilson in 1931 when formulating the transport theory of semiconductors based on the band theory of solids.

But the mechanism by which the widely used metal-semiconductor rectifiers ("metal" rectifiers) operated was not clearly understood. One theory that received general acceptance was based on the Schottky effect (1938), a concept similar to the ideas put forward to explain thermionic emission. According to Schottky, cold emission or field emission is produced by the application of a few volts across a thin film of semiconducting material (the copperoxide coating); and this is due to stable space charges in the semiconductor alone, without the presence of a chemical layer.
Also during the same period N. F. Mott offered a theory for swept-out metal semiconductor contacts which is known as the Mott Barrier.
In 1938 Helsch and Pohl published an account of a solid-state amplifier. This consisted of a single crystal of potassium bromide with a single wire of platinum providing control of current through the crystal. No practical developments resulted.

## WAR-TIME ADVANCES

The invention of radar just before the second World War stimulated a new interest in the solid state rectifier as a detector of microwaves. And so the silicon point-contact detector was developed.

During the 1940 's the technique of crystal "doping" was introduced. Boron added to highly purified silicon was found to result in highly sensitive crystals. This arose from experiment and the theory behind this effect could not then be explained.
This was a very significant period for the future of solid state. For one thing, the requirement for improved detectors for operation at centimetric wavelengths led to an intensive search for new materials. The discovery of germanium was one direct result of this activity.

## POINT-CONTACT TRANSISTOR

Shortly after the war, Bardeen, Brattain, and Shockley were involved in research on copper oxide rectifiers. Bardeen showed that a Schottky barrier layer could exist at the free surface of a semiconductor. This layer makes the contact properties independent of thermionic work functions. Following upon this, Bardeen and Brattain initiated the series of experiments that led to the invention of the point-contact transistor.
They placed two point-contacts close together on the surface of a crystal. It was found that a current flowing through one could influence the current flowing through the second circuit. The first practical solid state amplifying device had arrived. This was December 1947.
In working out the basic mechanism involved in point-contact transistors, Bardeen discovered the carrier-injection phenomena. This injection of extra current carriers into the semiconductor in the region of contact when forward bias voltage is applied produces the "transistor" action.

Later, Shockley carried out a research experiment to diagnose the surface phenomena of the original transistor. In the process, he found he had devised a new type of transistor. This was patented as the junction transistor. An account of this important discovery was published in 1949. Within a few years the $p n$ junction device was to prove vastly superior to the point-contact device, both from manufacturing and application view points.

It is of interest to note that around the mid 1930's Shockley had instigated some research into copper oxide rectifiers in an attempt to invent a solid state amplifying device. The results were negative.

## DIFFERENT MECHANISMS

The rectifying properties of metal-semiconductor contacts are due to a different mechanism to that now understood to be responsible in the case of $p n$ junctions.

During the last decade, the metal-semiconductor type of contact has returned to favour, albeit with new materials, and it has assumed a new prominence in special devices such as field effect or metal oxide semiconductor transistors (m.o.s.t.), and Schottky barrier diodes used for microwave purposes. The m.o.s. technique is extensively used in integrated circuit manufacture.

Thus it seems the circle has been completed. Development of the $p n$ junction has stimulated further widespread research into solid state physics over these last 25 years, and a new interest has arisen in the rectifying phenomena which was the starting point of this "trail to the transistor". And so more devices seem likely to be developed in the future which make use of both mechanisms of current transport.

## ELECTRONIC TIMEPIECES



Regulation of the watch is effected by moving either of the small regulator plates which are attached to the ends of each side of the tuning fork. This makes an extremely minute change in the effective length of the fork. An adjustment of half a second per day can be made.

An interior view of the mechanism of the Bulova 214 series watches is shown in Fig. 7.

## MODELS

The Bulova tuning fork watches are known by the name "Accutron", a word derived from "accuracy" and "electronics". In 1970 this Company introduced the first Accutron watches for ladies (series 230 ). These employ a power cell of the same size as that used in Accutron watches for men, but the frequency of the electronically driven tuning fork is 480 Hz so that the movement can be made far smaller. The shape and construction of the tuning fork is different from that in the models for men, but the current consumption remains at $8-10 \mu \mathrm{~A}$.

A few other companies now offer tuning fork watches. Longines offer the Ebauches caliber 6312 "Mosaba" movement in their "Ultronic" watches. (Mosaba is a word derived from "Movement sans balance"). Baume and Mercier have given the name "Tronosonic" to their tuning fork watches, whilst Omega refers to such watches as " f 300 ". Bulova also produce clocks with tuning fork mechanisms.

## GRAVITATIONAL EFFECTS

The frequency of some types of tuning fork watches is dependent on their position owing to the pull of gravity on the prongs of the fork. When the prongs (or "tines") point downwards, the watch may run a few seconds per day faster than when the prongs are horizontal.
This disadvantage has been almost eliminated in the balanced Mosaba movement designed by Ebauches S.A. It operates at 300 Hz and is marketed as the Swissonic 100 range by Longines in their "Ultronic". The Omega " f 300 " range also employs a balanced tuning fork movement.
Next month the final article will look at, among other things, quartz, cybernetic and digital watches.

## P= 5ound Eynthafiser <br>  5ID:ULEET POWER SIPPL By G. D. SHAW <br> 

I F the modular concept outlined last month, is to be followed in its entirety, this implies that not only must each circuit be entirely self contained but also that each circuit, complete with its various controls, should be capable of operating as a separate entity either within, or external to, the framework of the synthesiser as a whole.

These factors, coupled with the experience gained in building the prototype, contributed towards a decision to redesign the instrument into a fully modular form based on one of the many commercially available racking systems.

This month modifications to a standard racking system will be described together with constructional details of the twin stabilised power unit that will supply the various synthesiser card modules.

## CHOICE OF RACKING SYSTEM

It was believed that, in the interests of economy, the Card Frame System 1 by Vero offered the greatest value. Utilisation of this particular system, however, means that the constructor will be obliged to manufacture his own modular inserts which may perhaps be a deterrent to those not having the facilities or experience to tackle sheet metal fabrication. In this case the modular racking system type 3E also by Vero has the advantage that it covers a range of components which may be easily assembled into modular inserts.

This latter system is extremely well designed and the component parts of the assemblies are precision made, these factors, naturally enough, being reflected in the price of the 3E System which is several times greater than that of System 1.

Fig. 2.1 shows a general view of the assembly of the card frame to be used as the mainframe of the synthesiser chassis. The aluminium end plates are already pre-drilled to suit the standard Vero components normally used in the assembly of a System 1 Card Frame. It will be necessary, however, to provide additional holes to allow for modifications to the standard assembly and to provide mounting facilities for a number of components. Drilling details are shown.

## MAINTAINING A TIGHT ASȘEMBLY

The slotted card support sections are made of plastic and are secured to the end plates by means of
self tapping screws. Repeated removal and replacement of these screws will result in wearing of the threads which will, in turn, make the assembly sloppy. It is suggested therefore that assembly of the mainframe is only attempted when all necessary drillings, etc. have been completed, and that, once assembled, it is left so. If the constructor wishes, for any reason, to have the facility of stripping the assembly down repeatedly he would be well advised to redrill the securing screw holes and tap them to take a suitably sized metallic thread insert.

## COMPONENTS

## RACKING SYSTEM

## Card Frame

Kit of parts for a System 1 Card Frame with a pair of guide mouldings to suit (Part No. CFMN/1)
SK7-SK13 8 pin McMurdo sockets (Part No. RS8) (7 off)
PL1-PL7 8 pin McMurdo plugs (Part No. RP8) (7 off)
Frame parts above available from Vero Electronics Ltd., Industrial Estate, Chandler's Ford, Eastleigh, Hants., SO5 3ZR

## MODULE FRONT PANELS

Fig. 2.2 shows the composite front panel layout with dimensions. It will be seen that the panel is divided vertically into eight separate sections. These sections represent the front panels of individual modular units and, with the exception of the strip on the extreme right of the panel, can be removed from the mainframe complete with their respective card supports and circuitry.

The right hand strip is permanently fixed to the mainframe by two small aluminium brackets.
The left-hand panel is slightly wider than the remainder mainly to act as "fill-in" on the full width of the front panel.


Fig. 2.1. Perspective view of the Card Frame System 1. The end slots on the upper mounting rail require to be elongated for future adjustment of McMurdo sockets


Fig. 2.2. Composite front panel. These sections represent the front panels of individual modular units as indicated

Fig. 2.3a. Drilling and bending details for circuit board support plate and McMurdo plug (b) details of retaining rod

## MODULE SUPPORT PLATE

Circuit boards are supported on a plate to one end of which is attached the module front panel and to the other end a plug which mates with a socket at the rear of the mainframe assembly. Fig. 2.3a shows details of this support plate. Care should, be taken in bending this item and the bends should be made as tightly as possible. Lack of attention to this point will mean that the overall depth of the plate is greater than specified with the result that the front panel, when attached, will not be flush with the card support moulding in the main frame.

Modules are retained in the main frame by means of a $\frac{3}{16}$ in $(4.5 \mathrm{~mm})$ rod, Fig. 2.3b, screwed 2B.A. at one end. which passes through the front panel and engages with a Rosan bush, or similar, in the power supply subframe. - This arrangement also provides a means whereby the module may be withdrawn from the main frame and, for this purpose, a locking collar is provided to. abut the lower lug on the circuit board support plate.

## P.S.U. SUB-FRAME ASṠEMBLY

Fig. 2.4 shows the bending and drilling details of the power supply sub-frame. As with the circuit board support plate care should be taken that the clamping and bending is carried out as shown and that the bends are made as tightly as possible. Details have not been provided about the positioning of holes for Rosan bushes or components within the sub-frame.

## POWER SUPPLY UNIT

In a project of this kind the predictability of circuit performance depends largely on the ability of the power supply to maintain its voltage rails within relatively close limits. This is particularly true where the oscillators and hold circuits are concerned since quite small variations in supply rail voltage can cause significant changes in the low frequency and "droop" characteristics respectively.

The power supply unit is based on one of the latest regulator i.c.s to appear from Fairchild, the $\mu \mathrm{A} 7815$ : This particular device is capable of passing up to 1.5 amps without the necessity of external series regulating transistors, thus it is operating well within its maximum capability. Output ripple and noise is around 500 microvolts or less while load

Dimensions shown thus ${ }^{\text {㭗 are for reference }}$
only and are to be marked from Components
Board
All dimns in mm.

regulation from zero to 400 mA is better than 1 per cent. (Total current requirement for the basic synthesiser is 400 mA per rail.)

The output voltage of the i.c. is specified as being - plus or minus 5 per cent of its nominal rated voltage thus the constructor may find up to 1.5 volts variation across the two power supply rails. This is not necessarily a disadvantage since all the voltage dividers in the synthesiser which require an accurately set voltage are fitted with presets.

## CIRCUIT

The circuit diagram of the power supply unit is shown in Fig. 2.5.
Construction is perfectly straightforward (see Fig. 2.6) the only recommendation being that the leads from the transformer to the bridge rectifiers be routed to avoid passing directly over the regulators and that they be twisted together in the interests of hum reduction.
Wires carrying a.c. to the power supply control strip on the front panel should pass through holes. drilled in the rear of the sub-frame and fitted with rubber grommets. These leads should be 10 in ( 254 mm ) in length measured from the rear face of the sub-frame.


Fig. 2.4. Bending and drilling details of the p.s.a. sub-frame


## COMPONENTS . . .

TWIN STABILISED P.S.U.

## Resistors

R1 $2 \cdot 7 \mathrm{k} \Omega$ R2-R5 $200 \Omega$ (2 off)
R6 $2 \cdot 7 \mathrm{k} \Omega$
All $2 \% \frac{1}{2}$ watt metal oxide

## Capacitors

C1-C2 $3,300 \mu$ F 63 V
High ripple elect. (2 off)
C3-C4 $100 \mu \mathrm{~F} 25 \mathrm{~V}$ elect. (2 off)

## Bridge Rectifiers

D1-D8 REc 41A (2 off)

## Integrated Circuits

IC1-IC2 $\mu \mathrm{A} 7815$ (Fairchild)
Macro Marketing Ltd., 396 Bath Rd., Slough, Bucks.

## Transformer

T1—Main transformer, primary 240 V ;
secondary $30-0-30 \mathrm{~V}$ at 1.5 A

## Miscelianeous

S1-Miniature double pole, single throw on/off switch
LP1-LP2 miniature 28 V filament lamps,
FS1- 500 mA fuse,
SK1-SK6 1 mm miniature sockets ( 6 off)
14 s.w.g. aluminium as required

Fig. 2.5. Circuit diagram of twin stabilised p.s.u.


Fig. 2.6. Mounting and wiring details of p.s.u. components

Smoothed d.c. supplying the indicator lamps and calibrating voltage points is taken from the d.c. busbars coupling the McMurdo Red-Range sockets and thus may pass over the top of the sub-frame.

When the power supply unit has been wired up, with the exception of the d.c. to the indicators and calibrating voltage sockets, the mainframe assembly may be commenced.

## ARRANGEMENT OF PANEL UNITS

Referring for a moment to Fig. 2.2, the constructor should decide at this stage the actual arrangement or order in which he wishes the panel units to be placed relative to one another. The arrangement shown need not be adhered to with the exception of the meter and reverberation unit and the power supply control strip, which have to be sited as shown at the extreme left- and right-hand sides of the front panel respectively.

This decision is necessary at this time in order that the support plates may be correctly placed between the slotted mouldings. Viewed from the front of
the assembly, the correct position of these plates is in the slot immediately to the right of the one occupied by a circuit-board support plate.

Since component placement on all front panels offers very little clearance at the left-hand side of the panel it is necessary to cut out the front face of the support plates as shown in Fig. 2.1. The 18 mm depih of the cut-out is adequate to clear potentiometers and sockets on all front panels except that of the output amplifiers which will be fitted with ganged "pan-pots".

The optimum position for the support plates is approximately one third of the distance in from the end plates, the exact point, of course, depending upon the module arrangement chosen by the constructor.

## MAIN FRAME ASSEMBLY

Assembly of the main frame should be started by loosely securing the bottom slotted moulding between the end plates. The support plates may then be dropped into their respective slots and the upper slotted moulding placed over the lugs on the top edge of the panels and loosely secured between the end plates. Ensure that the chamfered edge of the slots in the mouldings are towards the front face of the assembly and that the vertical panels are in the same respective slot in both mouldings.

This being so, the securing screws may be fully tightened and lugs on the vertical panels twisted through 45 degrees where they protrude through the upper and lower faces of the mouldings.

## SECURING THE P.S.U.

The power supply sub-frame may now be secured to the rear of the end plates. The lower socket support should be drilled to mate with the slotted holes in the power supply sub-frame (Fig. 2.4) and loosely secured in position. Similarly the upper socket support should be placed loosely in position.


Fig. 2.7. Socket arrangement and wiring on connector mounting rails

## POSITIONING THE SOCKET SUPPORTS

The final position of these latter supports is determined by inserting an assembled circuit support plate, complete with plug, into the slotted mouldings until the front panel is flush with the face of the mouldings.

A socket is now placed over the plug so that its securing lugs abut the supports at the rear face and the supports adjusted so that they align with the


Fig. 2.8. Wiring details for rear of p.s.u. control strip
socket lugs and are parallel with the front face of the mouldings. The socket supports can now be secured firmly at this point and the socket placed in position for the first of the modules to be incorporated into the assembly.

Ideally all the sockets should be placed in position at this time; but if this is not possible the first socket to be positioned should be immediately adjacent to the power supply control module so that its terminals can provide a convenient jumping off point for the leads supplying d.c. to this latter assembly.

The arrangement for supplying power to the individual sockets is illustrated in Fig. 2.7, while Fig. 2.8 illustrates the arrangement for wiring up the rear of the power supply control strip.

## TESTING THE P.S.U.

The power supply may be tested on completion of assembly and wiring up the main frame. The main purpose of testing is to establish whether the ripple and noise and output voltage levels of each regulator are within their rated specification. Load regulation may also be checked by coupling a 30 ohm 10 watt wirewound resistor across each power rail and observing the change in output voltage on the oscilloscope. If the offset on the scope is not sufficient to enable the trace to be observed at a sufficient degree of sensitivity, a high resistance voltmeter should be used.

The level of change of output voltage at the moment of connecting the resistor across the power rails is likely to be of the order of 150 millivolts or less. Note that the resistor will be dissipating about 7.5 watts and is likely to get uncomfortably hot after a few moments across the power rails. It is best therefore to incorporate a switch in series with the resistor.

The main frame assembly is designed to be accommodated in a standard 19 in case in the Vero range. A later article will include details of the types of cases which may be used and modifications necessary in order to fit a.c., d.c., and keyboard sockets to the rear of the case.

Next month: The operation and construction of the voltage controlled oscillators and voltage inverter will be given.


THis project uses a single, inexpensive, TTL integrated circuit, type SN7402, and a few discrete components

The SN 7402 i.c. is a quad, two-input, positive, NOR gate, intended for switching rather than linear applications. However, this device is available for less than the price of many single transistors.

Although when used as a gate its usefulness to the home constructor is rather limited, with a little ingenuity it can be adapted to perform other functions.

## THE SN7402 INTEGRATED CIRCUIT

Internal connections to the pins of the SN7402 are shown in Fig. 1.

It is housed in a dual-in-line 14 pin package. As will be seen from looking at Fig. 1, each gate has two inputs, and a single output. The output voltage of the gate will be high only if all the input voltages are low. The output potential will be low, if any of the inputs are at a high potential. Therefore, if one of the inputs is connected to earth, the input will be controlled by the remaining input alone.

The gate may now be biased into a linear operating condition, by connecting this input to earth through a suitable resistor. The value of this resistor must be such that the voltage at the output is approximately half the supply voltage.

## INJECTOR CIRCUIT

The function of the gate under the conditions described above is that of a Class $A$ amplifier. The input and the output are 180 degrees out of phase, and the stage thus has very similar characteristics to a single stage transistor amplifier.

By capacitively coupling two gates together, with each output connected to the input of the other gate.

Fig. 1. The internal circuit of the SN7402N quad two input positive NOR gate


Fig. 2. Circuit diagram of the complete signal injector and tracer unit
a circuit similar to that of a free-running, or astable multivibrator, is obtained. A circuit diagram showing two gates connected in this fashion is shown in the upper half of Fig. 2.

The circuit operates as follows. When the supply is connected, the voltages at the outputs will begin to rise, and due to slight variations in component tolerances in the circuit. one will start to rise more quickly than the other.

For example, assume that the output voltage of gate 1 rises most quickly. This rise in voltage will be coupled by C3 to the input of gate 2. This will cause the output voltage of gate 2 to fall, this fall in voltage being coupled by $C 2$ to the input of gate 1.

A further rise in the voltage at gate 1 output will occur, and this regenerative process will continue until this voltage is at its highest level, and gate 2 output is at its lowest level.

Capacitor C2 will now begin to charge through gate 1 , and the voltage at gate 1 input will begin to rise, causing the output voltage to fall. This fall in voltage is coupled to gate 2 , causing its output voltage to swing to a slightly higher level.

A regenerative action will again take place, until gate $i$ output is very low, and gate 2 output is very high. C3 will now begin to charge through gate 2 ,

## COMPONENTS . . .

## INJECTOR AND TRACER

Resistors

| R1, R2, R3 | $1.3 \mathrm{k} \Omega$ (3 off) |
| :---: | :---: |
| R4 | $1 \mathrm{k} \Omega$ |
| R5 | $4.3 \mathrm{k} \Omega$ |
| All $\pm 5 \%$ | W carbon |

## Potentiometer

VR1 $500 \mathrm{k} \Omega$ logarithmic
Capacitors
C1 $\quad 100 \mu \mathrm{~F} 6.4 \mathrm{~V}$ elect.
$\mathrm{C} 2, \mathrm{C} 3 \quad 0.22 \mu \mathrm{~F}$ (2 off)
C4 $\quad 0.1 \mu \mathrm{~F}$ disc ceramic
C5 $\quad 2 \cdot 2 \mu \mathrm{~F}$ non elect.
C6 $\quad 0.47 \mu \mathrm{~F}$
C7 $\quad 0.056 \mu \mathrm{~F}$
C8 $\quad 100 \mu \mathrm{~F} 6.4 \mathrm{~V}$ elect.
Integrated Circuit
IC1 SN7402N positive NOR gate

## Miscellaneous

| S1 | Double pole sing |
| :---: | :---: |
| JK1, JK2 | 3.5 mm jack sock |
| PL1, PL2 | 3.5 mm jack plugs |
| LS1 | 15 to $30 \Omega$, $2 \frac{1}{2}$ to $3 \frac{1}{\frac{1}{1} \mathrm{in}}$ speaker |
| $1.6 \mathrm{in} \times 2 \mathrm{i}$ | 0.1 in matrix Veroboard |
| Universal panels | Chassis members: 4 in $\times 2$ in side off); 5 in $\times 2$ in side panels (2 off); |
| $\mathrm{n} \times 4 \mathrm{in}$ pl CU52A type U7 | es (2 off) (Home Radio type CU51A, d CU156 respectively) $1 \frac{1}{2} V$ batteries off) and battery holder |

and the opposite will occur. The circuit will thus begin to oscillate continuously, the actual frequency of oscillation being determined by the value given to C3 and C2.

The output is taken through the isolating capacitor, C4, to the output socket. The output wave shape will be almost square, and harmonics of the fundamental frequency will thus be available up to frequencies of many megahertz.

## TRACER CIRCUIT

The signal tracer circuit consists of the remaining two gates suitably biased, and coupled by a capacitor, C6. The circuit of the tracer is shown in the lower half of Fig. 2.

There is enough output available to drive a 25 ohm impedance loudspeaker. This should preferably have a fairly high efficiency, as, the output is not very large, although perfectly adequate for this function.

Capacitor C 7 is used to limit the high frequency response of the amplifier, which is otherwise so good, that instability arises.

Resistors R4, and R5 bias the output stage, and as R5 is connected to the output, it also introduces a certain amount of negative feedback, to the amplifier.

The input to the amplifier is taken through C5, and the potentiometer which is the sensitivity control.

## RADIO FREQUENCY DETECTOR

If the tracer is to be used with r.f. signals, a detector must be added at the input of the amplifier. This could be made an integral part of the unit, but in the prototype a separate r.f. probe was used. The circuit of this probe is shown in Fig. 3.


Fig. 3. The circuit of a simple detector which can be built into a small metal tube to enable the tracer to be used with radio frequencies

COMPONENTS

## R.F. DETECTOR

Resistors R6 $56 \mathrm{k} \Omega$ R7 $2.2 \mathrm{k} \Omega$

## Capacitors

C9 $1,000 \mathrm{pF}$
Diode
D1 OA91

## Miscellaneous

Metal tube for case plus metal for prod


Photograph of the completed signal injector and tracer unit showing the layout of the front panel. The box is built using universal chassis members

The casing, and metal prod for the probe will both have to be home made. They can be constructed from whatever suitable materials are at hand. Pen cases, pen light torch cases, and 35 mm film containers are popular for housing this type of probe.
The metal prod can be a long bolt, or something similar. The hardware used for the prototype probe consists of a disused penlight torch case, some rubber grommets, and a steel knitting needle for the metal prod. The output should be taken through a screened lead to a 3.5 mm jack plug. The components can be mounted on a small piece of $0 \cdot 1$ in matrix Veroboard.
If the detector is to be an integral part of the unit, an extra jack socket will have to be mounted on the front panel. The break contact of this should be wired in parallel with that of the other input socket. The detector is wired between these two sockets.
same time. The internal wiring of the i.c. is such, that the power supply must be connected to all, or none of the gates.
The circuit has, therefore, to be arranged so that when the plug is inserted into the tracer input socket, the break contact of the jack disconnects one of the biasing resistors of the injector, thus disabling it. When the injector lead is plugged in, the result is the same, except that it is the tracer, output stage biasing which is removed.
The unit requires 6 V at about 20 to 25 mA , and this is supplied by four batteries wired in series. A supply of no more than 7 V may be used, or the i.c. will be destroyed.

## CONSTRUCTION

The circuit is built on a piece of $0 \cdot 1$ in matrix Veroboard, measuring $2 \mathrm{in} \times 1.6 \mathrm{in}$. A diagram giving the layout of this board is given in Fig. 4.
When completed this board is bolted to the rear right-hand side of the case and must be insulated from the case. This may be achieved by placing a piece of expanded polystyrene between the board and the case. Cl is not mounted on the board, but is mounted on switch SI.

## ALUMINIUM CASE

An aluminium case is constructed using Home Radio Universal Chassis Members, and measures $\operatorname{Sin} \times 4 \mathrm{in} \times 2 \mathrm{in}$.

The front, back, and side panels are bolted together using eight 2BA bolts, the holes for which are already drilled in the panels.
The base plate is fixed in place by four 6BA bolts.

## CIRCUIT ISOLATION

Due to breakthrough between the trace and injector circuits, these cannot both be run at the

Break copper strips at 9D, $10 \mathrm{~F}, 10 \mathrm{G}, 10 \mathrm{I}, 10 \mathrm{~J}$ and 10 K


Fig. 4. Layout of the components on the Veroboard panel and interwiring details


Two 4BA self-tapping screws are required to hold the lid in place. The holes for these should be drilled centrally on the flanges of the side panels, using a No. 31 twist drill.

A drill of the same size may be used to drill the holes for the 6BA bolts. A No. 24 twist drill is used to make the mounting holes in the lid. The speaker should be mounted on the lid at any convenient place.

Details of the front panel can be seen in the photograph. The exact size of the mounting holes will vary according to the type of switch, or jack socket used. but the positioning of the holes will of course remain the same.

The batteries are positioned to the rear, and at the left-hand side of the case, opposite the main component board. They should be mounted in a battery holder of the type specified in the components list. Battery clips of the type used with PP3, or PP6 batteries are used to make the connection to the holder.

## USING THE UNIT

Switch S1 is the main supply on-off switch.
As described earlier, the function of inject, or trace is obtained by merely inserting the appropriate test lead.

When using the injector, the first test would be to the speaker of the radio, or amplifier under test. Tests should then be made at the inputs of the earlier stages, working forwards towards the r.f. stages, or the amplifier input.

When a test is made, and no output comes from the speaker, the faulty stage has been located. It is the stage which lies between the last, and the penultimate one to be tested. Because the output of the injector is so rich in harmonics, it may be used to test the a.f., r.f., and i.f. stages.

## USING THE TRACER

To use the tracer, the same procedure is used, except in reverse, with the first test being made at the input of the amplifier, and then working towards the speaker.

A signal of some kind must be connected at the input and when this signal is not received by the tracer the faulty stage has been located. It is again the one that lies between where this last test, and the previous one were made.

When testing i.f., or r.f. stages of a receiver, the r.f. probe must be substituted for the ordinary test leads.

NEWS BRIEFS
Tital Monitoring System
THE TASK of monitoring tidal conditions and wave height along the coast of Spain presents enormous difficulties using conventional monitoring techniques. To overcome this problem Plessey Environmental Systems have supplied pressure sensors and a digital logging system to the Spanish maritime authorities to provide precise information at 25 of the harbours around Spain's coastline, which is one of the longest in Europe.
The pressure sensors are accurate to $\pm 0 \cdot 3$ per cent of full scale and this measured pressure is encoded into a frequency modulated analogue signal which is transmitted to a central data collection unit in Madrid where it is stored on magnetic tape for subsequent analysis by computer.

## Hew Radar Testing Equipment for Europe's MRCA

Europe's new multi-role combat aircraft (MRCA) uses a highly sophisticated radar system and efficient testing is vital for such a system. EMI Electronics' Systems \& Weapons Division have just announced that they are supplying radar testing equipment to assist in-flight testing.
Unlike conventional airborne radar testing methods which limit thorough evaluation of the radar to the period that the aircraft is in flight, the EMI systems will enable the radar's performance to be examined in detail on the ground.

When fitted to the aircraft, the equipment will continuously record the video signal from the radars onto magnetic video tape which can later be replayed on ground equipment.
The new technique is expected to yield far more information from each test than was previously possible.

## UK Tops Europe in Data Transmission

The number of terminals sending and receiving computer data in the Post Office's Datel services has grown by more than a quarter over the last year. Datel terminals in service reached a new peak of 22.214, a growth of 27 per cent. Although the UK has only a quarter of Europe's computers, there are more data-transmission terminals in Britain than in the rest of Europe combined.

## 

## I.C. LINEAR OHMMETER (January 1973)

This article was the work of Charles Griffiths B.Sc (Eng). We apologise for the fact that an incorrect initial was attached to our author's name.
BIOLOGICAL AMPLIFIER (January 1973)
In Fig. 3, page 34, the value of resistor R5 should be $5.6 \mathrm{k} \Omega$. In the last paragraph under the side heading Alphaphone on page 38 reference to C 10 should read C11.
Fig. 5, page 39, the pin numbers 9 and 10 should be transposed in both the circuit diagram and case interwiring. In the case interwiring pin 12 should be 11. These corrections bring Fig. 5 into agreement with the circuit diagram Fig. 3.


THe bulk of the Digi-Cal logic has now been described. Throughout the articles covering the logic boards from display to ADDER, readers will constantly have encountered a variety of signals. attributed to a mysterious PROGRAMME BOARD.

The far-reaching influences of this board thus having been established, the time has arrived to delve into the method of deriving the plethora of programme output signals, a most important aspect of the calculator.

## DIGI-CAL PROGRAMME

The requirements of Digi-Cal are humble, reprogramming not being required in the normal run of things and the tasks for which a programme is necessary, namely the four arithmetic functions of addition, subtraction, multiplication and division can be carried out with only a few programme steps and without recourse to any high level language facilities other than the press of an appropriate key.

Programming then is carried out at the basic level of logic gates and flip-flops, and the Digi-Cal philosophy is retained by making the programmes variable by wiring in diodes where required in a matrix arrangement.

The separate jobs to be controlled by the programme include shifts, transfers, register and counter clearing, routing and clock pulse initiation, all of which must be carried out in a strict sequence.

## PRINCIPLES OF PROGRAMME GENERATION

The programmes for Digi-Cal are formed in a diode matrix in the form of a Read Only Memory or R.O.M. "Memory" because a number of separate addressable locations are provided, and "Read Only" because the instruction data in each address location is fixed at the wiring-up level and is not altered by the operator.

In the present system the R.O.M. array is addressed sequentially by means of a counter. In this
way each location in the memory is addressed in turn starting each time at address " 1 " and continuing to the final address of each R.O.M. in an incremental fashion.

It is not possible to jump back or forward in the sequence and random addressing of a particular location is likewise impossible.

Pausing during a sequence for an indefinite length of time is possible, as is aborting the sequence by clearing the address counter and stopping the clock.

## BASIC CIRCUIT

The incremental address COUNTER/R.O.M. programme system is best understood initially by means of a simplified circuit without any trimmings (Fig. 9.1).


Fig. 9.1. Principles of Read Only Memory Programme generation

Here, a four stage binary counter with 16 possible states is driven continuously by a clock pulse train. The system states of the counter are individually decoded by gates which produce an active low output when inputs are in the appropriate states.

These gates are in the form of an SN74154 TTL M.S.I. decoder which has extra facilities to generate the complement versions of the four inputs required by the decoder gates and a common overriding enable, or strobe input which can be used to inhibit all outputs.
The pin connections of the SN74154 is shown in Fig. 9.2. Referring again to Fig. 9.1 with Fig. 9.2 in mind, it can be seen that, as the counter steps through the sequence, each output from the decoder is enabled in turn for one clock period, thus establishing the programme steps.

The number of operations to be controlled by the programme depends on the requirements of the machine controlled, and can be few or many as required only four being shown in the basic circuit.

All that remains to perform a particular operation in a particular programme step, is to connect a diode across the appropriate intersection where the step line crosses the operation line.

## DIODE MATRIX

Inverters are used at the output end of the operation lines to act as what could be described as "sense amplifiers" in traditional memory terms. Another way of looking at the operation lines and inverters is as multi-input Nor gates with the number of inputs to each gate being determined by the number of times that an operation is used.

Note that each operation can be used any number of times and also that any number of different operations can be activated simultaneously.

The basic circuit has no facilities for stopping or starting the sequence which therefore runs continuously. Also only one programme is catered for and, because of the ripple-through counter circuit propagation delays combine to give spurious pulses of a few nanoseconds width on some of the step lines.

All of these disadvantages have to be overcome in the Digi-Cal circuit, which is shown in Fig. 9.3.


Fig. 9.2. The pin connections of the SN74154 four line to 16 line decoder

## FULL CIRCUIT

The full circuit looks very much more complicated at first sight but the R.O.M. arrays can be easily identified, and the rest of the tangle breaks up into sections with specific jobs.

First out of the way are IC108, 109 and 110 which are simply there to divide the master (ungated) clock signal from board ce by a factor of a thousand, to allow the programme circuits to operate at slow speed.

This eases reflection and decoding problems while keeping the operation of the calculator logic tied to a master synchronising clock signal. The only special thing about these dividers is the fact that the three SN7490 are connected in the $\div 10$ mode which gives a 1:1 mark space output from the final stage.

This is arranged by connecting the A stage of the circuit after the B, C, D stage.

## STOPPING AND STARTING

An SN7493 (IC112) is used as the programme counter, and it is made to stop or start counting by means of a gate in its clock line. Gate G1 itself is controlled by a latch flip-flop which is SET by a pulse from the equals key monostable in the keyboard logic and reset by the programmes themselves.

Setting the latch is accomplished by using the clock input with a permanent " 1 " on the D input and clearing is achieved by using the programme stop programme operation to energise the clear input of the first flip-flop via a monostable formed from the other flip-flop and a couple of inverters.
This method of forming a monostable was described in the article covering the clock board, Fig. 7.5, a monostable being necessary in this case to prevent "race" conditions removing the RESET input before the latch was properly cleared.

## PROGRAMME SELECTION

Three separate programmes are required in DigiCal ; three rather than four because the sequences for addition and subtraction are identical, the distinction being drawn by the fact that the adder board operates either as a subtractor or an adder depending on the function code which is produced directly by keyboard depressions.

The adD/SUBTract programme is quite simple as might be expected and requires only a few steps. Seven steps are, however, provided to allow reprogramming if any "frills" such as round-off or true negative answer are considered possible later.

The multiply and divide programmes are separate and have a possible 15 steps each, to allow for the increased complexity of these operations. Some spare steps are also left in these operations, and can be employed as required.

The selection of the required programme is accomplished by utilising the $\mathrm{G} 1 / \mathrm{G} 2$ enable inputs on the SN74154 decoders providing the multiply and divide sequences, and by using the $D$ input for the same purpose on the SN7442 of the shorter adD/SUBTRACT sequence.

Using the D input as an enable is possible with the SN7442 because if the $D$ input is high, the output selected must be greater than seven and since only outputs one to seven are used, a high D input means all programme steps are disabled even though they may be addressed by the A, B and C counter outputs.

Fig. 9.3. The full circuit diagram of the PROGRAMME BOARD. Power supply connections to the SN74154 and SN74119 are shown in Figs. 9.2 and 9.6. All others have $V_{c c}$ to pin 14 and GND to pin 7 except the SN7490 and SN7493 which has $V_{C C}$ to pin 5 and GND to pin 10 , and SN7442 which has $V_{C e}$ to pin 16 and GND to pin 8

```
COMPONENTS
Capacitors
    C35, C36 10\muF 15V elect. (2 off)
    C37-C40 0.047\muF (4 off)
Diodes
    D104-D142 West Hyde type "red" (or any small
                        silicon diode) (39 off)
Integrated Circuits
    IC108-IC110 SN7490 (3 off)
    IC111 SN7474
    IC112 SN7493
    IC113 SN7400
    IC114, IC115 SN7442 (2 off)
    IC116, IC117 SN74154 (2 off)
    IC118 SN74119
    IC119, IC120 DTL9935 (2 off)
    IC121, IC122 SN7404 (2 off)
Printed Circuit Board
    0.1in Veroboard (8.2in \times 3.4in)
Edge Connector
    32 way 0.1in pitch edge connector (optional)
```


## STROBE SYSTEM

Enabling the appropriate programme decoder is not done in a d.c. manner, with a constant input throughout a particular sequence but in an a.c. manner by routing enabling or "strobe" pulses to the selected decoder which occur in the centre of each addressing period.

This scheme is used to kill two birds with one stone, since by enabling decoder outputs only in the middle of each address period the problem of spurious outputs or "glitches" is overcome.

By the time the enable, pulse arrives the address counter is resting in a particular state and propagation delay problems are overcome.


Fig. 9.4. Gate G2 is used to generate a strobe pulse by detecting simultaneous $C$ and $A$ outputs during the clock period as shown in the truth table. The decoder is used to select the appropriate part of the programme according to the FUNCTION CODE inputs

This selection system involves IC114, G2 and G3 and to make the principle clearer it is redrawn in an integrated way as Fig. 9.4. Two states of the final divider (IC110), count sequence are detected by gate G2 which gives a negative strobe pulse near the end of the run (see Truth Table).

The strobe pulse is applied to the c input of IC114 which is being under-used in this application as a two to four line decoder with strobe input.

One of the outputs zero to three is addressed by the two line function code generated by the keyBOARD in response to the arithmetic selection made by the operator.

The selected output will remain high however, until the strobe pulse takes the c input low. The result of all this is that a continuous stream of strobe pulses is delivered, via the logic described, to the strobe or enable input of the selected programme decoder. Gate G3 is used as a negative logic NOR gate to enable the single add/SubTract programme whether addition or subtraction is called.

## PROGRAMME OUTPUTS

Fifteen programme operations are possible on this board, and although more of these would be useful, board space is a limiting factor for the layout of the R.O.M. matrix.

Because of this limited number of available lines, some outputs do two or more jobs which seem to be unconnected. For example, line a is responsible for normalising the entry register, clearing the $z$ REGISTER, and clearing the EQUALS LatCH on the $m$ COUNTER boards.

## PROGRAMME OPERATION CODE ASSIGNATIONS

a Normalise Entry Register
Clear $Z$ Register
Clear M Counter Equals Latch
b Clear Carry Store
Clear M Counter (to 000000)
c Set latch, changing clock count to DOWN and disabling A inputs to ADDER
d Transfer the contents of the $E$ register to the $Z$ register
e Start Arithmetic Clock
$f$ Stop programme sequence
g Transfer the contents of the $A$ register to the $Z$ register
h Clear the Programme Counter
i Clear E register
j Clear A register
$k$ Preset NORM code from the thumbwheel into the clock counter

I Transfer contents of $M$ counter to $A$ register
m A register NORM in progress
n Preset M counter to 999999

- Clear latch array/select A register for display with point position determined by thumbwheel

These operations are not related and are grouped together because they do not interfere with each other if performed at the same time.

It must be remembered, however, that if a clear z signal is required by a programme, the other signals are also produced which may or may not be important if a "home-made" programme sequence is employed.
Programme outputs are arranged to be either "active high" or "active low" depending on the requirements of the logic they drive, and for this reason either one or two inverters are placed at the end of the operation lines.
The inverters connected directly to the R.O.M. lines are from the DTL family and are type 935 which do not have input diodes (Fig. 9.5). This enables the R.O.M. diodes to be used as an integral part of the circuit without reducing noise immunity.

## LATCH ARRAY

The latch array performs a variety of jobs which are related to the programme, one of the most obvious of which is to stretch programme operations over a number of steps.
"Stretching" is achieved by using the programme to SET a latch in a particular step, and to RESET it in another. The latch output is then used to control a particular operation which must be continued for longer than one step.

Latch L6 is used in this way to control the clock counter direction and adder enable during a register normalisation in the multiply sequence. Latch L5 is used to control routing logic during an $\mathrm{E}^{2}$ operation, being SET by pressing the $\mathrm{E}^{2}$ key and reset by the clear latch array programme operation at the end of each sequence.

Latches L3 and L4 are not used, but are available if required, and L1 and L2 operate together to control the display selection.

Latch Ll controls the decimal point selection (fixed or floating) and L2 controls the register selection ( A or E ).

The i.c. used in this position is the very versatile SN74119, which like the SN74154 decoders is housed in a 24 pin dual-in-line package. The logic of this device is simply that of six cross-coupled gate latches with a common clear line, and is shown in Fig. 9.6.


Fig. 9.5. This diagram shows how the R.O.M. diodes and the DTL inverters are used to form NOR gates


Fig. 9.6. Internal logic of the SN74119 hex SET/RESET LATCH

## CLOCK AND CLEAR

There are only a couple of items left on the full circuit to be mentioned, the first of these being G4 which is used to nor the clear e output from the programme and the clear e input from the ce key to give a combined signal.
Next, the e register normalising clock output is taken as a tapping from the divider-chain so that although much faster than the final programme clock, the normalising clock is slower than the arithmetic clock to prevent difficulties with the long line lengths used.

## PROGRAMMING

The programmes used in the prototype are shown in Fig. 9.7, which presents the sequences as flow diagrams.
The operations inside the shaded boxes in the multiplication and division programmes show the conditional branching operation carried out by the clock generating board, and are included in the flow diagram for completeness.
Each of the square boxes corresponds to the programme step with which it is numbered, several operations being possible at each step. The spare steps do not give rise to any outputs from the board, but may be used to advantage if the basic programme is expanded or re-arranged.
Re-programming does not require any particular skill other than commonsense and a knowledge of the way the circuits operate.

The writer has had very little time to consider just what improvements could be programmed into the calculator, but with a little ingenuity and perhaps modification, true negative answers (as opposed to complement versions as the machine stands) should be possible.


Fig. 9.7. This flow diagram shows the three programmes used on the PROGRAMME BOARD. The same programme is used for addition and subtraction as mentioned in the text


Fig. 9.8. The layout of the components on the Veroboard panel. Crosses show breaks in copper strips


Diode matrix construction

```
PROGRAMME BOARD WIRING
DESTINATIONS
aa not used
ab START PROG (fr kbd)
ac RECALL K (fr kbd)
ad UNGATED CLK IN (fr CB/43)
ae FUNC CODE A (fr kbd)
af DISPE (to disp)
ag E2 (fr kbd)
DISPE (fr kbd)
FUNC CODE B (fr kbd)
ai CNT DIR'N (to CB/28)
ak CLEAR E (to ED/2)
al CLEAR E (fr kbd)
am not used
GND
E E2 (to E2 logic)
ap +5V
aq CLEAR Z REG etc (to Z2/43, kbd (NORM) and
    M1/19)
ar CLEAR CARRY etc (to AD/23, M1/20, M2/20)
as DISPA (to DISP)
at START CLK (to CB/44)
au not used
av DEC PT SEL'N (to kbd)
aw TRANSFERETOZ (to Z1/24)
ax E REG NORM CLK (to kbd)
ay ADD ENABLE (to AD/37)
NORM A REG (to CB/37)
ba PRESET NORM CNT (to CB/27)
bb TRANSFER M TO A (to CB/39)
bc notused
bd PRESET M (to M1/21, M2/21)
be CLEAR A REG (to A1/44, A2/44)
bf TRANSFER A TO Z (to Z2/24)
```


## CONSTRUCTION

This circuit is built on a Veroboard panel (Fig. 9.8 ) and carries a good deal of wiring on both sides. The R.O.M. operation lines are formed by the printed tracks on the board, but the step lines are formed from bare tinned copper wire running at right-angles to the tracks.

These are spaced from the board by about $\frac{3}{8}$ in so that diodes may be soldered-in where required. Note that the diodes are inserted with their red ends uppermost, connected to the step lines.

Wiring is congested but not critical, thanks to the low programme speed, and both sides of the board carry wire interconnections.

An edge connector of 32 or more ways is recommended for use with this board, but it is not essential; connections can be soldered directly to the printed tracks if desired. If an edge connector is used it is necessary to clean the track ends thoroughly with fine emery paper and then coat them with a tarnish preventer and cleaner such as Electrolube.

## TESTING

Programme selection and step sequences can be easily checked after construction by using a large value capacitor on the clock board to give a very slow programme sequence. Steps can then be followed with a multimeter set to a low voltage range.
Operational testing can be carried out only with the add/SUBTRaCT programme until the M COUNTER boards and $\mathrm{E}^{2}$ logic are constructed, but when this is working, little trouble should be experienced with the other programmes.

Note: In Fig. 6.10 (Dec. 72) Z1/22, Z1/43, Z2/22, and $Z 2 / 43$ go to $Z 2 / 44$ not CB/13. $Z 1 / 21, Z 2 / 21$ go to $C B / 23$. $A 1 / 42, A 2 / 42$ go to $C B / 40$. A1/43, A2/43 go to CB/38. In Fig. 7.7 (Jan. 73) CB/40 should go to A1/42, A2/42.

Next month: M Counter Boards

## Designing with

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6

## Simple Memory Circuits

THis month we will show how simple memory circuits may be constructed from gates and how the master-slave bistable is developed. In the way that a transistor bistable can be made from two cross-coupled inverting stages, a bistable can also be made from two inverting gates. This is shown in Fig. 6.1 for NAND gates, although a similar NOR circuit is also possible.

If the clear and set inputs are at 1, and so have no effect, then for either gate a 1 at its input would produce a 0 at its output, and vice versa. Since the output of one gate is the input of the other, the two outputs must be in opposite states. If $Q$ is 1 then $\bar{Q}$ must be 0 , and vice versa.

If the clear input is 0 and the set input is 1 , then $\overline{\mathrm{Q}}$ becomes 1 and Q becomes 0 . If the set input is 0 and the clear input is 1 then the opposite state is set up with Q equal to 1 and $\overline{\mathrm{Q}}$ equal to 0 .

If a momentary 0 input is applied to either the set or clear input the corresponding output will become a 1 and will remain in this state until a 0 is applied to the other input. The circuit can therefore act as a memory and can be used to eliminate the effects that are obtained from contact bounce on a mechanical switch. This could be checked as an experiment (Fig. 6.2).

From the truth table it can be seen that, if 0 levels are applied to both inputs, then both the $Q$ and ' $Q$ outputs will be 1 . For a true bistable circuit this condition would be avoided.

## GATED MEMORY

When input gates are added to the basic memory in Fig. 6.1, it can be made to respond to input levels only during a specific clock time interval, as shown in Fig. 6.3. While the clock input is at 0 , gates 1 and 2 have a 1 at their outputs and the set and clear inputs are locked out of the memory. New information can only reach the memory when the clock input is at 1 .

A 1 at the set input with a 0 at the clear input will give a 0 input to gate 3 and a 1 input to gate 4 , then $Q$ will be a 1 . The other possible states are shown in the truth table.


Fig. 6.1. The basic bistable circuit

momentary 0 on an INPUT sets corresponding OUTPUT to 1
Fig. 6.2. The basic bistable circuit used to eliminate the effects of switch contact bounce

If both set and clear inputs are at 0 when the clock goes high the inputs to the memory remain at 1 and the output does not change from its previous state. In the truth table $T_{n}$ represents the time during the clock pulse while $\mathrm{T}_{\mathrm{n}}+{ }_{1}$ represents the time after the clock pulse. Similarly $\mathrm{Q}_{\mathrm{u}}$ represent the state of the memory at the time of the clock pulse. (These are common terms used in manufacturers' data on digital i.c.s.)
When the sampling interval is over and the clock returns to 0 , further changes in set and reset can have no effect until the next clock pulse. The memory is said to have "clocked", because the sampling point can be timed to occur when the required data is at the set and clear inputs, and data outside this time will be ignored. For this circuit the set and clear levels need not be pulses because input pulsing is provided by the clock.

## DATA LATCH

The previous circuit had three useful conditions, $\mathrm{Q}_{n}, 0$, and 1 . In some applications the data latch

## CLOSSAR

BISTABLE CIRCUIT A circuit in which the output has two stable states.
NAND GATE A logic circuit where all inputs must have 1 level signals to produce a 0 level output.

NOR GATE Any one input or more than one input having a 1 level signal will produce a 0 level output.
TRUTH TABLE A chart which tabulates all the combinations of possible states of the inputs and outputs of a circuit.


| INPUTS $T_{n}$ |  | $\begin{gathered} \text { OUTPUT } T_{n}+T \\ Q \end{gathered}$ |
| :---: | :---: | :---: |
| CLEAR | SET |  |
| 0 | 0 | $Q_{n}\left(Q\right.$ at $\left.T_{n}\right)$ |
| 0 | 1 | 1 |
| 1 | 0 | 0 |
| 1 | 1 | INDETERMINATE |

Fig. 6.3. The gated memory with SET and CLEAR inputs


Fig. 6.4. The gated memory with a single data input


EARTH....PIN 7
Fig. 6.5. The gated memory with a single data input and the minimum number of gates. It can be realised with an SN7400N

TRUTH TABLE

| INPUTS |  | OUTPUT $T_{\mathrm{n}}+1$ |
| :---: | :---: | :---: |
| J | K | Q |
| 0 | 0 | $Q$ at $T_{\mathrm{n}}$ |
| 0 | 1 | 0 |
| 1 | 0 | 1 |
| 1 | 1 | $Q$ at $T_{\mathrm{n}}$ |



1 Isolate slave from master
2 Enter information to master

Transfer information from master to slave
Fig. 6.6. The basic action of a master-slave bistable circuit

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is used for storing the output information in counters or computers until the readout has been achieved. This enables counting to recommence while the readout from a previous phase is still taking place.

If only the two middle entries in the truth table are required (as is usual) then a single data input can be used while the complement is obtained from an inverter, Fig. 6.4. The input data is up-dated during the clock pulse and retained when the clock goes low.

## MINIMISED DATA LATCH

The circuit in Fig. 6.4 required five gates to realise it because the $D$ was obtained by the direct inversion of D. However, D can be obtained from the output of gate 1 , because $D$ is only needed at the input to gate 2 when the clock is high; therefore, gate 1 will provide the required information.

The minimised circuit shown in Fig. 6.5 may be realised as an experiment using a single SN7400N. Data D will be entered into the memory while the clock is high and retained when the clock is low.

## LOGIC GATE BISTABLES

In the previous circuits the inputs are coupled to the bistable circuit all the time the clock pulse is high, so that these circuits can only be made to toggle (divide by two) if the inputs are a.c. coupled. However, it is essential in logic circuits to overcome this problem without giving up the advantages of d.c. coupling, so circuits were developed which prevent the output transitions from altering the input information which was present when the leading edge of the clock occurred.

These circuits are usually designed in the form of two gated memories, one holds the output state and one holds the input information read in at the beginning of the clock pulse. Since this information is later transferred to the output memory, such a circuit is called a "master-slave" bistable.

Several different versions are available but they all have the master-slave action shown in Fig. 6.6. Here the operation shown is the J-K function where information is transferred from master to slave on the falling edge of the clock pulse. From the truth table it can be seen that this J-K master-slave bistable will divide by two if $J$ and $K$ are taken to $I$, and an input applied to the clock.

## CONCLUSION

We have shown how the simple memory circuit develops into the master-slave bistable, with its many advantages.

## Part 7 will describe counters and displays



The International \& Allied Equipment Exhibition, Bloomsbury Centre Hotel, London, WC2 March 13 to 15

## BOAT SHOW' 73

THE Yachtsman's winter break, this year in the setting of a sunny Spanish harbour, the annual International Boat Show provides us with a shop window of marine electronics for small craft.

As in previous years there has been a steady development of the electronic gadgetry available, this year has seen the introduction of the l.e.d. (light emitting diode) to boat electronics, a new type of knotmeter and some advancement in the receiver, transmitter field.
One firm whose name must be to the forefront when we consider this year's innovations is EMI; in fact one could call it their year of innovation, with no less than four completely new instruments available. The most interesting new product is a magnetic log.
The new log operates by the transducer generating an a.c. magnetic field in the water surrounding it. As the boat moves through the water a small e.m.f., dependent on the boat speed, is produced and sensed by the transducer head. This induced signal, fed back through the system, is amplified to give a display of speed and distance. Housed in the normal EMI Electra range case the biggest advantage of this instrument is that there are no moving parts below the water and virtually no projection (only 3 mm ) below the hull surface.

The other innovations from EMI are an automatic direction finder, an f.m. radio telephone, and a gas detector and alarm. The direction finder is a three band receiver covering the range 175 kHz to $3,000 \mathrm{kHz}$. To use it, it is tuned to the required radio beacon and automatically gives an immediate and continuous bearing of the transmitter. The unit has its own internal supply or can be powered from a 12 V or 24 V ship's supply.

The Electra Gas Alarm uses a solid state imported detection unit and provides both visual and audible alarms of dangerous concentrations of gases.
It is interesting to note that the Electrascan MK11 radar has been ordered by the RNL1 for use in lifeboats. Whilst on the subject of radar both Decca and Electronic Laboratories have introduced MKII radars and the Decca Super 101 MK1I is now sold with a magnifier that increases picture size to 9 inches.

## TECHNICAL INNOVATION

As far as real technical innovation goes, this is the first time that we have found a l.e.d. used in a depth sounder. Produced by Marine Electronics and available for about $£ 39$, the sounder works on the same principle as a neon type and probably has a similar accuracy.

Instead of providing a point of light at the depth reading, the l.e.d. is turned on when the pulse is transmitted and off when the pulse is received, thus providing an arc of light showing the depth.
Brookes and Gatehouse-manufacturers of perhaps the most reliable and widely used instruments for the racing yachtsmen-have, we feel, done the sensible thing in offering most of their instruments for use on ship's supply. At last no more fiddling with batteries in a heavy seaway.

Baron Instruments, another contender for the "crack" racers' instrument panel, have redesigned their meter faces and introduced a new range-the Baron Sailboat Console-the meters are lit by l.e.d.'s and are $4 \frac{1}{2}$ inches in diameter. Provided the instruments can prove themselves at sea, with the new meters (which were in fact suggested by us last year) we feel that this range is the most likley to rival B. and G. this year,

Finally we think it is a pity to see Smiths Industries cashing in on the boat market with standard Radiomobile car radios and tape units. Without the necessary marinisation we feel that these units will not stand up to marine use--particularly in salt water-and for Smiths to push the standard product for this purpose is surely not good policy.

# PAIENTIR RETCETM 

## WIRIMG HARNESSES FOR CARS

The Ford Motor Company patent No. 1287074 is concerned mainly with wiring harnesses for cars but most electronic enthusiasts will see far wider applications.

In a vehicle all the various electrics, such as sidelights, headlights. stoplights and wipers are usually connected to a power source by separate power carrying cables. The electrics are controlled by switches (usually located inside the vehicle remote from the devices) in the oower cables from one of the supply terminals. The vehicle chassis acts as a common return to complete the circuit to the other supply terminal.

Providing heavy duty power lines for each separate remote electrical function is uneconomical. The Ford system is based on the house "ring main" idea and uses a flat strip power conductor capable of handling the total current load for all the vehicle electrics, see Fig. 1.

The strip conductor is flexible and is coated with flexible insulating material. On the surface of the insulating material a thin conducting layer is deposited and etched away to form a parallel series of thin low current control conductors. Each of these has connecting pads spaced out along its length and Ford suggest that in a practical system 20 to 30 control conductors can be used along with one main power conductor.

The vehicle power source is permanently connected to the power
conductor by a pin connector which bites through the insulating material and into the conductor. Similar power connectors can be used to connect the power conductor to any electrical device which requires a permanent supply of power.

Each remote electrical function that requires only a switchable power supply (such as headlights, etc) is connected to the power connector by a gated olug. The gated plugs each house a transistor of which the emitter is permanently connected to the power conductor.

A resistor of around 1,000 ohms is connected between the base and emitter of the transistor and the base of the transistor is also connected to the required control conductor of the wiring harness. The transistor collector is connected externally to whatever electrical function is to be powered, Fig. 2.

The gate is controlled by a trigger signal from the control conductor and will only pass current to its load when a control signal is present.

In Fig. 2, the electrical functions or loads are lamps LP1 and LP2. When manual switches S1 and S2 are "off" the base of transistors TR1 and TR2 are at the same potential as the emitters and neither transistor conducts; thus lamps LP1 and LP2 do not liaht. If switch $\mathrm{S1}$ is turned "on" the potential of control conductor CC1 falls, transistor TR1 is switched on and current flows throuah lamp LP1. Similarly if switch S2 is turned on, current flows through lamp LP2. The circuit takes into

## BP1 287074



Fig. 1


Fig. 2
account the high cold current surges involved in lighting most vehicle lamps.

Although SCR's may be used it is easier and cheaper to use switch resistors R1 and R2 (around 100 ohms each) which, together with the resistances connected between the emitters and bases of the transistors, form potential dividers which hold the base potentials constant at a level to which they fall on closing the switch. Current through the transistor can only increase until the voltage across the low emitter resistance (cable resistance, etc) raises the emitter potential to a similar value to the base potential.

## BIO-NOTES

Readers of Gerry Brown's fascinating column will have noticed his comments in the January 1973 issue concerning "Electrophonic Hearing' and his suggestion that the effect of feeding electric currents direct to the brain should be re-examined as a possible aid to the deaf.

Anyone wishing to follow this line of research will be interested by the content of two British patents Nos. 1284158 and 1286 316 respectively from the ZCM Corporation of USA and Hermann Mengeler of Germany. Both patents were published some while ago but each contains useful technical information on the subject.

The ZCM patent details a method of using an audio signal to pulse a microwave radio signal, the resultant pulses being then fed to electrodes which are capacitively coupled to the subject's skin. This is claimed to produce a brain sensation in some respects equivalent to hearing.

The Mengeler patent claims a comparable system for introducing video information to the brain. In brief, a miniature TV camera produces a scanning voltage which is capacitively coupled to the subject's temples. This supposedly stimulates the optic nerve and produces sensations which the subject may train himself to equate with sight sensations.

Reference to these two patents betore experjmentation could well save readers wasted time and work -it could also save their subjecfs unnecessary electric shocks.


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"

# mariet PLACE 

Items mentioned in this feature are usually available from electronic equipment and component retailers advertising in this magazine. However, where $a$ full address is given, enquiries and orders should then be made direct to the firm concerned.

## LOW COST DIGITAL MULTIMETER

A new $£ 49$ digital multimeter from Sinclair Radionies promises to be a strong contender in the professional quality meter market having as it does a claimed high accuracy, high input impedance, portability, and relative low cost. It is aimed at the replacement market as an economical substitute for analogue multimeters.
Measuring $190 \times 130 \times 58 \mathrm{~mm}$ and weighing $1 \frac{1}{2} \mathrm{lb}(0.6 \mathrm{~kg})$, the multimeter can easily be held in the hand when making measurements. The leads are fixed to the instrument and wrap around a recess in the lightweight polypropylene case. Simple yet effective finger plate selector switches form an integral part of the case.
The instrument has ranges extending from $1 \mu \mathrm{~A}$ up to 1 A d.c., 1 mV to $1,000 \mathrm{~V}$ d.c. and a.c., and resistance ranges from 1,000 ohms to $1 \mathrm{M} \Omega$ full scale. Display is by three Nixies with a carry of one in a single neon. This gives an effective $3 \frac{1}{2}$ digits without using a fourth tube.

The measuring technique is such as to reduce current consumption to a minimum, in fact, the manufacturer's specification promises 80 hours of switched-on operation with a current drain of 12 mA .

Input impedance is very high at $1,000 \mathrm{M} \Omega$ compared to analogue meters, as are the accuracy parameters of 0.4 per cent on volts d.c., and 1 per cent on a.c. with an f.s.d. error of $\pm 2$ digits.

An interesting aspect of the circuit is that it is all discrete, with a component count of around 300 (including 100 transistors) all mounted on three p.c.b.s.

In operation all inputs are brought within a 1 V range and the incoming signals are analogue-todigital converted to produce a pulse train the length of which is. proportional to the input level. The
pulse train is passed to a chain of three cascaded pump circuits. Component values have been so chosen that the incoming pulse is summed in units, tens, and hundreds.

A "ring-of-ten" counter strobes each Nixie numeral terminal in sequence and for each strobe "tops up" the pump circuits. This action is used to time the point in the strobe sequence when the supply is switched onto the relevant Nixie to illuminate the correct numeral.

This instrument is available, by mail order, direct from the makers, Sinclair Radionics Ltd., London Road, St. Ives, Huntingdonshire.

## CASES

Encouraged by the success of their Contil Mod-2 range of instrument cases, West Hyde Developments recently launched a new complementary range of cases designated Mod- 3 types.
The Mod- 3 cases have fixed sides. in scuff resistant p.v.c. coated steel, made of two parts rigidly locked together. A chassis is supplied with the case, this and the panels being made up first and then assembled afterwards in the case.

Interior drilled side flanges allow the chassis plate to be positioned in any convenient position. The side flanges also take the rear and front panel fixing screws.
Case dimensions vary from 3 in $\times 7$ in to 6 in $\times 11$ in and further details and price list can be obtained from West Hyde Developments Lid., Ryefield Crescent. Northwood Hills. Northwood, Middlesex.

## WAFER SWITCHES

The wafer switch is still one of the widest used components in electronics, and Ultra Electronics (Components) Ltd are now marketing a range of wafer switch kits.

One of the advantages of the switch kit is its versatility and ease with which the designer can make up prototype switching assemblies and change them on the spot.
The Centralab Switchkits are packaged as separate components: the Series PA 1000 and PA 2000 kit contains. for example. 1.000 clips. 800 eyelets, 925 contacts. stators, rotors, nuts and packing washers. plus 25 index assemblies. 25 adjustable stops, eyeletting pliers and assembly tools and instructions.

Further information and details of the switch kits can be obtained from Ultra Electronics (Components) Ltd.. Fassetts Road. Loudwater. Bucks.
Also available from Ultra Electronics is a free switch wallchart giving typical ratings and configurations.

## SPECIAL OFFERS

Here is a chance to stock up with solid state devices at bargain prices. We have been advised by $\mathbf{A}$. Marshall \& Son Ltd., that their advertisement next month will list a series of special offers, available only to readers of this magazine.

This well-known component supplier will be disposing of a large amount of stock at reduced prices. prior to moving into new premises.


Contil Mod-3 cases from West Hyde Developments


BELLLIG LEE IMSULATED TRRMINALE. Red or Black, Jamp max. 10 p pair, p.p. 3 p
BERCOSTAT WIREWOUTD REEOSTAT. 30 volt,

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P.p. on above jtems 3pp.

## MADNS MEONS

Red or Green. Size: $\ddagger$ in $\times 1 \nmid \mathrm{in}$. 16p, $p . p, 34 \mathrm{p}$
LEVER ACTION P.O. 1000 TYPE 8 WITCHES
Lock 4-pole changeover, 15p, p.p. 3pp. Ex equip
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Screened Phono Leads 46 in long. $15 p$.
3.5 mm JACK 3 . mmm JACK 71 th in long, 40 p .

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MDLLARD \& HALLORY ECBRW TERMIEAY CAPACITORS $4,000 \mu \mathrm{~F} 64 \mathrm{~V}, 7,100 \mu \mathrm{~F} 40 \mathrm{~V}, 60 \mathrm{each}$. $20,00030 \mathrm{~V}, 25,00025 \mathrm{~V}, 35,000$ 15V. 30 p each) p.p. 10p.

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PRLLTED CIRCUIT BOARD/19 ACY 19's 100 OA200 Dlodes: I reed relay: 1 AZ 229 zenner ass. capacitor/ resistors. Power supply $22 \mathrm{~V}, 250 \mathrm{~m} / \mathrm{A}$ d.c. Output 240 V a.c. 12, p.p. 20p. Ex equip.

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SOCKET, 21.50 , p.p. 6 p.

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8 ohm
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8 ohm $£ 8.00$
$£ 4.95$
Coiled Headphone
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\& 1.35
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Amplifier module AE1000 3 watt,
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# Project 60 Stereo FM Tuner 



## with phase lock-loop principle

Amongst the many advanced electronic features to be found in this remarkable stereo tuner, use of the phase lock loop principle ensures standards of audio quality better than from any other method of detection yet used. Varicap diode tuning, accurately formed printed circuit coils, an I.C. in the special stereo decoder section and switchable squelch circuit for silent tuning between stations contribute to the unsurpassed performance of this tuner, irrespective of price consideration. But the Project 60 FM Stereo Tuner is far from expensive - indeed, it offers fantastic value for money and will bring the thrill of stereo radio to many who previously may not have been able to afford it. The tuner may be used with any good system as well as Project 60, but if you use it with other Project 60 modules, you will find the matching front panels particularly impressive in appearance as wellas function.

SPECIFICIATIONS
Number of transistora: 16 plus 20 in I.C Tuning range : 87.5 to 108 MHz .
Sensitivity: $7 \mu \mathrm{~V}$ for lock-1n over full deviatıon.
Squelch level : typically $20 \mu \mathrm{~V}$.
Signal to noise ratio: $\pm 65 \mathrm{~dB}$.
Audio frequency response: $10 \mathrm{~Hz}-15 \mathrm{Khz}$ ( $\pm 1 \mathrm{~dB}$ ).
Total harmonic distortion: $0.15 \%$ for $30 \%$ modulation.
Stereo decoder operating level : $2 \mu \mathrm{~V}$
Cross talk: 40 dB .
Output voltage: $2 \times 150 \mathrm{mV}$ R.M.S. max. (typically $2 \times 50 \mathrm{mV}$. stereo)
Operating voltage: $25-30 \mathrm{~V}$ DC at 100 mA .
Indicators: Stereo on tuning.
Size: $93 \times 40 \times 207 \mathrm{~mm}$.


Integrated circuit
high fidelity amplifier

Having introduced Integrated Circuits to hi-fi constructors with the IC.10. the first time an IC had ever been made avallable for such purposes. we have followed it with an even more efficient version, the Super IC. 12, a most exciting advance over our original unit. This needs very few external resistors and capacitors to make an astonishingly good high fidelity amplifier for use astonishingly good high fidelity amplifier for use
with pick-up. F.M. radio or small P.A. set up. etc. with pick-up. F.M. radio or small P.A. set up. etc
The free 40 page manual supplied. details many other epplications which this remarkable IC. make possible. It is the equivalent of a 22 tran-
sistor circuit contained within a 16 lead DIL package. and the finned heat sink is sufficient for all requirements. The Super IC. 12 is compatible with Project 60 modules which would be used with the $Z .50$ and $Z .30$ amplifiers. Complete with free manual and printed circuit board.

## SPECIFICATIONS

Output power: 6 watts RMS continuous (12 watts peak). $6-8 \Omega$. Frequency Reaponse: 5 Hz to $100 \mathrm{KHz}=1 \mathrm{~dB}$. Total Harmonic Distortion: Less than $1 \%$. (Typical $0.1 \%$ ) at all output powers and frequencies in the audio band ( 28 V ) Losd Impedance: 3 to 15 ohms Input Impedance: 250 Kohms nominal. Powar Gain: 90dB (1.000.000.000 times) after feedback. Supply Voltage: 6 to 28 V . Quiescent current: 8 mA at 28 V . Size: $22 \times 45 \times 28 \mathrm{~mm}$ in. cluding pins and heat sink.
Manual avalable separately 150 post free.
With FREE printed circuit
board and 40 page manual
$\mathbf{£ 2 . 9 8}$ Post tiee

## Project 605

The easy way to buy and build
 Project 60

Project 605 is one pack containing: one PZ5. two 230's. one Stereo 60 and one Masterlink. This new module contains all the input sockets and output components needed together with all necessary leads cut to length and fitted with neat little clips to plug straight on to the modules. Thus all soldering and hunting for the odd part is eliminated. You will be able to add further Project 60 modules as they become available adapted to the Project 605 method of connecting.
Complete Project 605 pack with $£ 29.95$
comprehensive manual. posi free
col Everything you need to assemble a superb 30 wati high fidelity stereo amplifier without having to soider.

## the world's most advanced high fidelity modules

## Z.30 \& Z.50 power amplifiers

The $Z .30$ and $Z .50$ are of advanced design using silicon epitaxial planar transistors to provide unsurpassed standards of performance. Total harmonic distortion is an incredibly low $0.02 \%$ at $15 w(8 \Omega)$ and all lower outputs. Whether you use $Z .30$ or $Z .50$ amplifiers in vour Project 60 system will depend on personal preference, but they are the same size and are intended for use principally with other units in the Project 60 range. Their performance and design are such, however, that Z .50 s and Z .30 may be used in a far wider range of applications.
SPECIFICATIONS ( 2.50 units are interchengesble with 2.30 s in all applications), - Power Outputs: 2. 3015 watts R.M.S. into 8 ohms using 35 volts: 20 watts R.M.S. into 3 ohms using 30 volts
Z. 5040 watts R.M.S. into 3 ohms using 40 volts 30 watts R.M.S. Into 8 ohms using 50 volts

Frequency response: 30 to $300.000 \mathrm{~Hz} \pm 1 \mathrm{~dB}$. Distortion : $0.02 \%$ into 8 ohms . Signal to noise ratio: better than 70 dB unwerghted. In put sensitivity: 250 mV into 100 Kohms (for 15 w into $8 \Omega$ ). For speakers from 3 to 15 ohms impedance. Size: $14 \times 80 \times 57 \mathrm{~mm}$.


## Stereo 60 Pre-amp/control unit

Designed specifically for use on Project 60 systems. the Stereo 60 is equally suitable for use with any high quality power amplifier. Since silicon epitaxial planar transistors are used throughout. a really high signal-to-noise ratio and excellent tracking between channels is achieved. Input selection is by means of press buttons, with accurate equalisation on all input channels. The Stereo 60 is particularly easy to mount.
£9.98


PECFICATIONS-Input sensitivities: Radio - up to 3 mV . Mag. p.u. 3 mV : correct to R.I.A.A. curve $\pm 1 \mathrm{~dB} .20$ to 25.000 Hz . Ceramic $p u$. -up to 3 mV Aux - up to 3 mV . Output: 250 mV . Signal to noise ratio: better than 70dB. Channel matching: within 1dB. Tone controis: TREBLE +12 to -12 dB at 10 KHz : BASS +12 to -12 dB at 100 Hz Front panel: brushed aluminium with black knobs and controls. Size: $66 \times 40 \times 207 \mathrm{~mm}$.

## A.F.U. High \& Low Pass Filter Unit

Bult. rested and guaranteed.
For use between Stereo 60 unit and two $Z .30$ s or $Z .50$ s. The unit is very easily mounted and is unique in that the cut-off frequencies are contınuously variable. As attenuation in the rejected band is rapid ( 12 dB /octave). there is less loss of the wanted signal than has previously been possible. Amplitude and phase distortion are negligible. The A.F.U. is suitable for use with any other amplifier system. There are two filter sections - rumble (high pass) and scratch (low pass). H.F. cut-off ( -3 dB ) variable from 28 KHz to 5 KHz . L.F. cut-off (-3dB) variable from 25 Hz to 100 Hz . Distortion at 1 KHz ( 35 V . supply) $0.02 \%$ at rated output. Operating voltage from 15 to 35 V . Current 3 mA . Size: $66 \times 40 \times 90 \mathrm{~mm}$.


## Power Supply Units

Designed specifically for use with the Project 60 system of your choice. Use PZ.5 for normal Z.30 assemblies and PZ. 6 or PZ.8 where a stabilised supply is essential.

PZ. 530 volts unstabilised $\mathbf{f} 4.98$
PZ.6 35 volts stabilised $£ 7.98$ PZ. 845 volts stabilised (/ess mans transformer) $£ 7.98$ PZ.8mains transformer E5.98

Typical Project 60 applications

| System | The Units to use | together with | Units cost |
| :---: | :---: | :---: | :---: |
| Simple battery record player | Z. 30 | Crystal P.U., 12V battery volume control, etc. | £4.48 |
| Mains powered record player | Z.30, PZ.5 | Crystal or ceramic P.U. volume control. etc. | £9.45 |
| 12W. RMS continuous sine wave stereo amp. for average needs | $\begin{aligned} & 2 \times 2.30 \text { s, Stereo } \\ & 60 ; \text { PZ. } 5 \end{aligned}$ | Crystal. ceramıc or mag. P.U., F.M. Tuner, etc. | £23.90 |
| 25W. RMS continuous sine wave stereo amp. using low efficiency (high performance) speakers | $\begin{aligned} & 2 \times 2.30 s, \text { Stereo } \\ & 60 ; \text { PZ. } 6 \end{aligned}$ | High quality ceramic or magnetıc P.U.. F.M. Tuner, Tape Deck, etc. | £26.90 |
| 80W. (3 ohms) RMS continuous sine wave de luxe stereo amplifier. (60W. RMS into 8 ohms) | $2 \times 2.60 \mathrm{~s}$, Stereo 60; PZ.8. mains transformar | As above | ¢34.88 |
| Indoor P.A. | Z.50, PZ.8, mains transformer | Mic., guitar, speakers. etc., controls | £19.43 |
| F.M. Stereo Tuner (£25) \& A.F.U. (E5.98) may be added as required. |  |  |  |
|  |  |  |  |

## Guarantee

If. within 3 montha of purchasing any product direct from Sinclair Radionics Lid., you ere dissatisfied with it, your money will be refunded at once. Many Sinelair appointed Stockiste also offer this same guerantes in co-operation with Sinclar Radionice Lid.
Each Project 60 module is tested bafore laaving our factory and is quarenteed to work pertectly Should uny defect arise and is guaranteed to work pertectly. Should uny defect arise in norms use. We will service it at once and without any of purchsse. Outside this period of guwantee amall charge of purchase. Outside this period of gubrantee small charge postage by surfece mal. Air Mail is charged at cont.

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Powerful I amp. REVERSIBLE motor. Speed 3,750 r.p.m. complete with external gear train
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| 700 | $16-24$ |
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[^6]
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$31 \mathrm{p} .0 .1 \mu \mathrm{~F}, 4 \mathrm{p} .0 .15 \mu \mathrm{~F}, 0.22 \mu \mathrm{~F}, 5 \mathrm{p}, 0.33 \mu \mathrm{~F}, 6 \frac{1}{2} \mathrm{p} .0 .47 \mu \mathrm{~F}, 8 \frac{1}{2} \mathrm{p} .0 .68 \mu \mathrm{~F}, 11 \mathrm{p} .1 .0 \mu \mathrm{~F}, 13 \mathrm{p}$ $3 \frac{1}{2} \mathrm{p} .0 .1 \mu \mathrm{~F}, 4 \mathrm{p} .0 .15 \mu \mathrm{~F}, 0.22 \mu \mathrm{~F}, 5 \mathrm{p}, 0.33 \mu \mathrm{~F}, 6 \frac{1}{3} \mathrm{p} .0 .47 \mu \mathrm{~F}, 8 \frac{1}{2} \mathrm{p} .0 .68 \mu \mathrm{~F}, 11 \mathrm{p} .1 \cdot 0 \mu \mathrm{~F}, 13 \mathrm{p}$.
$1.5 \mu \mathrm{~F}, 20 \mathrm{p} .22 \mu \mathrm{~F}, 24 \mathrm{p}$. $1.5 \mu \mathrm{~F}, 20 \mathrm{p} .22 \mu \mathrm{~F}, 24 \mathrm{p}$.
$\begin{array}{llll}\text { MYLAR } & \text { CAPM } \\ 0.001 \mu \mathrm{~F}, & 0.002 \mu \mathrm{~F}, & 0.005 \mu \mathrm{~F}, 0.01 \mu \mathrm{~F}, & 0.02 \mu \mathrm{~F},\end{array}$ $\begin{array}{lll}0.001 \mu \mathrm{~F}, & 0.002 \mu \mathrm{~F}, & 0.005 \mu \mathrm{~F}, 0.01 \mu \mathrm{~F}, \\ 2.02 \mu \mathrm{~F} \\ \text { 21p. } 0.04 \mu \mathrm{~F}, & 0.05 \mu \mathrm{~F}, 0.068 \mu \mathrm{~F}, 0.1 \mu \mathrm{~F}, & 34 \mathrm{p} .\end{array}$

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| $0.22 \mu \mathrm{~F}$ | 35 V | $4.7 \mu \mathrm{~F}$ | 35 V | $33 \mu \mathrm{~F}$ |
| $0.47 \mu \mathrm{~F}$ | 35 V | $6.8 \mu \mathrm{~F}$ | 25 V | $47 \mu \mathrm{~F}$ |
| $1.0 \mu \mathrm{~F}$ | 65 V | $10 \mu \mathrm{~F}$ | 25 V | $100 \mu \mathrm{~F}$ |

ELECTROLYTIC CAPACITORS Miniature P.C. mounting 5p each.

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| VEROBOARD |  |  | JACK PLUGS AND SOCKETS |
| :--- | :--- | :--- | :--- | :--- |
|  | 0.1 | 0.15 | Andind screened $18 p \quad 2.5 \mathrm{~mm}$ in |



Standard screened
Standard insulased Standard insulased

Stereo screened $\begin{array}{lll}\text { Stereo screened } & 35 \mathrm{p} & 3.5 \mathrm{~mm} \text { inscreene } \\ \text { Standard socker } & 15 \mathrm{p} & 2.5 \mathrm{~mm} \text { solate }\end{array}$ | Stereo socket | 18 p | 2.5 mm socket |
| :--- | :--- | :--- |
| 3.5 mm socket |  |  |

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\begin{aligned} & \text { BD116 } \\ & 310121 \end{aligned}
$$ \& $$
\begin{aligned} & 78 \mathrm{p} \\ & 50 \mathrm{p} \end{aligned}
$$ \& OC44 ()C4J \& $$
\begin{aligned} & 13 p \\ & 13 \mathrm{p} \end{aligned}
$$ \& TIP33A TIP34A \& $$
\begin{array}{r} 95 p \\ \mathbf{4 1 . 8 0} \end{array}
$$ \& $$
\begin{aligned} & 3 N 3711 \\ & 40251 \end{aligned}
$$ \& $10 p$ 49 p <br> \hline dC10\% \& 15. \& ALI0: \& 58p \& BD130 \& 48 p \& OC71* \& 12 p \& 2N697 \& 18p \& $40 \times 36$ \& 55 p <br> \hline 4 Cl 26 \& 11 p \& AL103 \& 49 p \& BD131 \& 59D \& OC7-3 \& 12p \& ${ }^{2} \mathrm{~N} 1171$ \& 24p \& \& <br> \hline ACAT \& 110 \& A1103 \& 85p \& BF194 \& 15p \& OC81 \& 13p \& $\because \mathrm{N} 1304$ \& 25 p \& \& <br> \hline 1C1:8 \& 11p \& - 11111 \& 95 p \& $\mathrm{EFFYJO}^{\text {a }}$ \& 150 \& OC81D \& 13p \& $\cdots \mathrm{N} 1305$ \& 25 p \& \& <br> \hline C176 \& -25p \& $\mathrm{BCl}^{8}$ \& 8 p \& BF「 ${ }^{\text {B }} 1$ \& 12p \& $0 \mathrm{OC3}$ \& 20 p \& 2 N 2646 \& 475 \& \& <br> \hline ACJ41K \& 20p \& 13 Cl 08 \& 8 p \& Bsy ${ }^{\text {ajob }}$ \& 15p \& OC170 \& 249 \& $2 \times 2926$ \& 10p \& DIODES \& <br> \hline  \& 20 p \& $\mathrm{BCL}^{109}$ \& 8 p \& \$1 C040: \& 18 p \& OC:00 \& 25p \& ${ }^{2} 30.3$ \& 20 p \& IN4001 \& 4 p <br> \hline AD14 \& 40 p \& 13C1J4 \& 0 p \& M F0404 \& 145 \& OC201 \& 25p \& $2 \mathrm{~N} 30 . \mathrm{j}$ \& 49p \& IN 4002 \& 40 <br> \hline AD100 \& 44 p \& $13 C 168$ \& 0 D \& ME4401 \& 10p \& O< \& 25p \& 2N370? \& 12p \& $1 N 4003$ \& 5 p <br> \hline AD161 \& \& BC169 \& 4 P \& ME4102 \& 12p \& OCR \& 30 p \& 2 N 3703 \& 12p \& IN4004 \& 7 D <br> \hline AD16: $^{3}$ \& 55p \& BC18\% ${ }^{\text {ch }}$ \& \& YE600- \& 14 p \& OC39 \& 36 p \& ${ }^{2} \mathrm{~N} 3704$ \& 12 p \& 0490 \& 6 p <br> \hline \& 15p \& 13C1831. \& 8 p \& atibiol \& 14 p \& OC3J \& 25p \& $2 \times 370$ \& 12 p \& 0.491 \& 6 D <br> \hline +F113 \& 15p \& BCIR4. \& 8 p \& MEtio:? \& $15 p$ \& OC3\% \& 369 \& - N 3706 \& 10p \& OA: 00 \& 10 p <br> \hline AFIIf \& -15p \& $\mathrm{BC} \mathrm{C}_{1} 12 \mathrm{~L}$ \& 8p \& MP8111 \& 32p \& Tlp:29 \& 48p \& - N 3707 \& 10 p \& O. 202 \& 8p <br> \hline AP117 \& 15p \& JC.314L \& 8 p \& MP8511 \& 340 \& TIP30A \& 559 \& 2 N 3708 \& 9 p \& 1844 \& 10 p <br> \hline \multicolumn{4}{|l|}{\multirow[b]{2}{*}{CAPACITORS}} \& \multirow[t]{2}{*}{$$
\begin{aligned} & \text { n1p8513 } \\ & 0 \mathrm{C} 41 \end{aligned}
$$

 \& 45p \& T1P31A \& 58p \& $\because \mathrm{N} 3709$ \& 10p \& IN414. \& 4 D <br>\hline \& \& \& \& \& 13p \& TIP3.A \& 69p \& $2 \times 3710$ \& 10p \& WO: \& 32p <br>
\hline
\end{tabular} <br> MULLARD POLYESTER CAPACITORS C280 SERIES <br> $0 \cdot 10 \mu \mathrm{~F}$ MULLARD POLYESTER CAPACITORS C296 SERIES <br> $400 \mathrm{~V}: 0.001 \mu \mathrm{~F}, 0.0015 \mu \mathrm{~F}, 0.0023 \mu \mathrm{~F}, 0.0033 \mu \mathrm{~F}, 0.004 \pi \mu \mathrm{~F}, 81 \mathrm{p} .0 .0068 \mu \mathrm{~F}, 0.01 \mu \mathrm{~F}, 0.01 \mu \mu \mathrm{~F}$

 $160 \mathrm{v}: 0.01 \mu$ <br>  <br> ELECTROLYTIC CAPACITORS-MULLARD C426 SERIES <br>  $64 / 64,2 / 54,50 / 64,100 / 74,200 / 54,320 / 6 \cdot 4,4 / 10,16 / 10,32 / 10,64 / 10,105 / 10,200 / 10$ $4 / 40,8 / 40,15 / 40,32 / 40,50 / 40,0 \cdot 64 / 64 / 16.164,5 / 64,10 / 64,20 / 64,32 / 64$ <br> MULLARD C437 SERIES <br> $100 / 40,160 / 25,150 / 16,400 / 10.1540 / \hbar 4,400 / 4,1,000 / 25,9 \mathrm{p} .100 / 64,160 / 40,250 / 22,400 / 1 \mathrm{~h}$ $640 / 10,1,250 / 4,1,000 / 64,1,600 /-2,12 \mathrm{p}$. $160 / 64,250 / 40,400 / 2 \cdot 5,640 / 16,2,000 / 4,1,000 / 10$
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Miniature
MM6 6V, $500 \mathrm{~mA}+6 \mathrm{~V}, 500 \mathrm{~mA}$ MM12 $12 \mathrm{~V}, 250 \mathrm{~mA}+12 \mathrm{~V}, 250 \mathrm{~mA}$ MM $2020 \mathrm{~V}, 150 \mathrm{~mA}+20 \mathrm{~V}, 150 \mathrm{~mA}$ L. $\quad \mathbf{L} .29$ plus 13 P P. \& P .

LT1 $6.3 \mathrm{~V}, 1.5 \mathrm{~A}-75 \mathrm{p}$ plus 18 PD . \& P LT2 $6.3 \mathrm{~V}, 3 \mathrm{~A}-87 \mathrm{p}$ plus 26 p P. \& p .
 LT5 9-0-9V, 0.5A-75p pius ${ }^{2} 2$ p. LT6 $12-0-12 \mathrm{~V}, 1 \mathrm{~A}-95 \mathrm{p}$ plus 26 p . Multi-tapped
MT30/2 0-12-15-20-24-30V, 2A$\begin{array}{ll}\text { MT60/I } & \text { O-5 }-20-30-40-60 \text { p. p. \& A P. }\end{array}$ MT60/2 0-5-20-30-40-60 V . 2 A P.
Chargor
CT/10
IA- 61
05
plus 26p p. \& $p$

T/03 4A-f1.60 plus 30p p. ${ }^{\text {s }}$
Speaker Matching 3-8 $16 \Omega$
Example: 16 n speaker to 8 amplifier. 90p plus 20p p. \& $p$
SEMICONDUCTORS, etc. Zeners- 400 mW , 15p; 1.5 W , 22ip L.D.R.-ORP12, 56 p S.C.R.-400 P.I.V., $3.0 \mathrm{~A}, 57 \mathrm{p}$ Bridge rectifier- 40 P.I.V., I.5A, 50 p
Bridge
50 p rectifier-200 P.I.V., 2.0A $\stackrel{50 \mathrm{p}}{\mathrm{50}}$
Transistor sockers-7p
D. $1.1 .{ }^{\text {sen }}$ sockers-14 pin, 20p
 IN4002-100 P.I.V., 1.AA, 7p
in4003-200 P.I.V., $1.0 A, 80$



ALUMINIUM BOXES
with lids and screws
Type. Pricep. \& ${ }^{\text {W. }}$.


 $\begin{array}{lllll}\text { GB11 } & \text { 4in } & 2 \text { tin } & 2 i n & 38 \mathrm{p} \\ \text { GB1 } & 13 \mathrm{p} \\ \text { GB12 } & \text { 3in } & 2 \mathrm{in} & 1 \mathrm{in} & 33 \mathrm{p} \\ 13 \mathrm{p}\end{array}$

 GB16 10 in 2in $\begin{gathered}\text { 3in } 92 \mathrm{p} \text { 26p } \\ \text { These sizes fit } \\ \text { standard } \\ \text { veroboards }\end{gathered}$

EQUIPMENT CASES aluminium with sloping front panel.
 $\begin{array}{llllll}\text { SF } & 2 \mathrm{in} & 5 z i n & 2 \text { Iin } & 45 \mathrm{p} & 12 \mathrm{p} \\ \text { SF2 } & 2 \text { in } & 7 \text { 7in } & 3 \text { in } & 60 \mathrm{p} & 16 \mathrm{p}\end{array}$
 Stove-enamelled
silver-grey ham mer finished, 25p $\qquad$

## CONSOLE CASES

in piain aluminium, ideal for mixers,
Type W. A B C D Price p. p .
 $\begin{array}{ccccccc}\text { GB21 } & 10 & 9 & 3+2 & 3 & 61.58 & 30 p \\ \text { GB22 } & 12 & 9 & 3+2 & 3 & 61.72 & 300\end{array}$



VEROBOARD

|  | 0.1 | 0.15 |
| :---: | :---: | :---: |
| Siz* | metrix |  |
| 2 ¢in $\times 3$ ¢ in | 22p | 16p |
| $2 \mathrm{fin} \times 5 \mathrm{sin}$ | 24p | $25 p$ |
| 3xin $\times 3$ 3in | ${ }^{24}$ | 25 |
| 17in $\times 2$ tin | 75p | 57p |
| $17 \mathrm{in} \times 3$ Lin | $1{ }^{\circ}$ | 75 |
|  |  |  |

ELECTROLYTICS

| $1 \mu \mathrm{~F}$ | 450 V | 19p | 1,000 F | 25 V | 27 |
| :---: | :---: | :---: | :---: | :---: | :---: |
| $2 \mu \mathrm{~F}$ | 450 V | 20p | 1,000 F | 50 V | 12p |
| $4 \mu \mathrm{~F}$ | 350 V | 14p | 2,000 F | $25 V$ | 19p |
| $8 \mu \mathrm{~F}$ | 450 V | 17p | 2,000 F | 50 V | 53 p |
| $16 \mu \mathrm{~F}$ | 450 V | 18p | 2,500 F | $25 V$ | 45 p |
| $25 \mu \mathrm{~F}$ | 25 V | 7p | $2.500 \mu \mathrm{~F}$ | 50 V | 60 p |
| $25 \mu \mathrm{~F}$ | 50 V | 10p | 3,000 F | 25 V | 48p |
| $32 \mu \mathrm{~F}$ | 450 V | 17p | 5,000 $\mu \mathrm{F}$ | 25 V | 60 p |
| $50 \mu \mathrm{~F}$ | SOV | 10p | 5,000 $\mu \mathrm{F}$ | 50 V | $¢ 1.10$ |
| $100 \mu \mathrm{~F}$ | 25 V | 10p | 8-8 0 F | 450 V | 18p |
| $100 \mu \mathrm{~F}$ | 50V | IIp | $8-16 \mu \mathrm{~F}$ | 450 V | 20p |
| 250 $\mu \mathrm{F}$ | 25 V | 14p | $16-16 \mu \mathrm{~F}$ | 450 V | 27p |
| $250 \mu \mathrm{~F}$ | 50 V | $17 p$ | $16-32 \mu \mathrm{~F}$ | 450 V | $63 p$ |
| $500 \mu \mathrm{~F}$ | $2 S V$ | 18p | 32-32 $\mu \mathrm{F}$ | 450 V | 49p |
| $500 \mu \mathrm{~F}$ | 50 V | 25p | $50-50 \mu \mathrm{~F}$ | 350 V | 38 |

MINIATURE ELECTROLYTICS
1 FF 63V 6p $47 \mu \mathrm{~F}$ 10V 7p $47 \mu \mathrm{~F}$ 25V 6p $3.3 \mu \mathrm{~F} 63 \mathrm{~V} 6 \mathrm{p}$
$68 \mu \mathrm{~F}$ 16V 6p

| $100 \mu \mathrm{~F}$ | 10 V |
| :--- | :--- |
| $220 \mu \mathrm{~F}$ |  |
| 16 p |  |

$\begin{array}{lll}330 \mu \mathrm{~F} & 16 \mathrm{~V} & 11 \mathrm{p} \\ 470 \mu \mathrm{~F} & 10 \mathrm{~V} & 11 \mathrm{p}\end{array}$ $8 \mu \mathrm{~F}$
$10 \mu \mathrm{~F}$
$10 \mu \mathrm{~F}$
$16 \mu \mathrm{~F}$
$33 \mu \mathrm{~F}$

| $500 \mu \mathrm{~F}$ | 16 V | 19 p |
| :--- | :--- | :--- |
| 50 |  |  |

CASSETTE OWNERS!
For Philips and similar cassette recorders. PU12 Power unit for connection to
systems, giving $7 \frac{1}{f} V$, stabilised $\{3.25$ PU14 As above but switched for $\mathbf{1 5 . 1 0}$ pP75 Mains power supply, output $£ 1.95$ All units are complete with cable and plug.

## CASSETTES

Top quality British made, low noise, complete with transparent library cases-
C60-40p: C90-55p; C120-70p

BATTERY ELIMINATORS
suitable for transistor radios and similar tight current equipment Output 6 V d.c. pp9 Input 240 V a.c. Output 9 V d.c

## ILLUSTRATED CATALOGUE

Post Free
15p
CONTROLS, Log. or Lin.
Single, less switch, $15 p$
Single, D.P. switch, 24p
$5 \mathrm{k} \Omega, 10 \mathrm{k} \Omega, 25 \mathrm{k} \Omega, 50 \mathrm{k} \Omega$. $100 \mathrm{k} \Omega, 250 \mathrm{k} \Omega$ 500kn, $1 \mathrm{Mn}, 2 \mathrm{Mn}$

## RESISTORS

All $5 \%$, highastability, E12 values. it $W$, Ip W,Ifig: IW,4p; 2W, 6p
SW, 10p; 10W. 12p

## SWITCHES

roggle switches, standard size SW20-S.P.S.T. 18p; SW21-D.P.D.T. 23 p Push Button, miniature, SWI-I3p Wafer switches (rotary)-24p each.
SW4-I pole, 12 way. SW5- 2 pole, 6 way SWG-3 pole, 4 way, SW7-4 pole, 2 way SW8-4 pole, 3 way.

BONDED ACRYLIC FIBRE
B.A.F. wadding, I 8 in wide, lin thick. The deal lining for speaker enclosures. 30p per

TYGAN top quality loudspeaker covering material. Please send 6p for samples, sizes and material.

MAGNETIC COUNTERS
Brand new, neat, 48 vol

## PLUGS

Car aeria
Co-axial

D.IN. 3 pin D.I.N. 4 pin 180 D.I.N. 5 pin, 240 D.I.N. 6 pin $\qquad$
lack, $2 \frac{1}{2} \mathrm{~mm}$ screened
fack, $3 \frac{1}{2} \mathrm{~mm}$ unscreened
Jack, $3 \frac{1}{2} \mathrm{~mm}$ screened
Jack, tin unscreened
Jack, sin screened
Jack. stereo, unscreened
jack, stereo, screened
Phono, plastic top
Phono, plated metal
Wander, red or black
Banana 4 mm , red or black

## LINE SOCKETS

## Car aerial

O.I.N. 2 pin (speaker)
D.IN. 3 pin
D.I.N. 5 pin
D.I.N. 5 pin, 180
D.I.N. 5 pin, 240
O.I.N. 5 pin, $240^{\circ}$
Jack, $3 \frac{1}{2} \mathrm{~mm}$

Jack, tin screened
Jack, stereo, screened
Phono, plated meta!

## C

## CAPACITORS

 1

| CAPACITORS |  |  |  | $\begin{aligned} & 0.0027 \mu \mathrm{~F} \\ & 0.003 \mu \mathrm{~F} \end{aligned}$ | $\begin{aligned} & 500 \mathrm{~V} \\ & 500 \mathrm{~V} \end{aligned}$ | $\mathrm{S} / \mathrm{M}$ | $\begin{array}{r} \text { 15p } \\ \text { 50 } \end{array}$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 2.2pF | 500 V | 5/M | $71 p$ | $0.0033 \mu \mathrm{~F}$ | $125 \vee$ | P.S. | ${ }_{6 p}$ |
| 3.3 pF | s00V | 5/M | 7 lp | $0.0033 \mu \mathrm{~F}$ | 500V | Poly. | 6p |
| 5pF | soov | S/M | $71 p$ | $0.0031 \mu \mathrm{~F}$ | 1,000V | MDC | 6p |
| 10 pF | 125 V | P.S. | Sp | $0.0036 \mu \mathrm{~F}$ | 500 V | S/M | 15p |
| 10 pF | 500 V | S/M | 710 | $0.0047 \mu \mathrm{~F}$ | 125 V | P.S. | 9p |
| 15pF | 125V | P.S. | 5p | $0.0047 / 1 \mathrm{~F}$ | 500 V | Poly. | $6 p$ |
| 15 pF | 500 V | Cer. | $4 p$ | 0.004711 F | 500 V | S/M | 20p |
| 18 pF | 500 V | S/M | $71 p$ | $0.0047 \mu \mathrm{~F}$ | $1,000 \mathrm{~V}$ | MOC | 8p |
| 22pF | 125 V | P.S. | 5p | $0.005 \mu \mathrm{~F}$ | 100V | Mylar | 3p |
| 22pF | 500 V | 5/M | 7 p | $0.005 \mu \mathrm{~F}$ | soov | Cer. | 5p |
| 25pF | 500 V | S/M | 7 p | $0.0068 \mu \mathrm{~F}$ | 125 V | P.S. | 101 p |
| 27 pF | 500 V | Cer. | 4p | $0.0068 \mu \mathrm{~F}$ | 500 V | S/M | 30p |
| 33 pF | 125 V | P.S. | 5p | $0.0068 \mu \mathrm{~F}$ | 500 V | Poly. | 6p |
| 33 pF | 500 V | SIM | ${ }^{7} \mathrm{p}$ | $0.0082 \mu \mathrm{~F}$ | 125 V | P.S. | 101p |
| 39pF | 500 V | S/M | 71 p | $0.0082 \mu \mathrm{~F}$ | 500 V | S/M | 30 p |
| 47pF | 125 V | P.S. | 5p | $0.01 \mu \mathrm{~F}$ | 18 V | Dise | 4p |
| 47pF | 500 V | Cer. | 4p | $0.01 \mu \mathrm{~F}$ | 125 V | P.S. | $10 \frac{1}{\text { P }}$ |
| 50pF | 500 V | S/M | $71 p$ | $0.01 \mu \mathrm{~F}$ | 160 V | Poly. | 4 p |
| 56pF | 500 V | S/M | $7{ }^{19}$ | $0.01 \mu \mathrm{~F}$ | 250 V | M.F. | 3p |
| 68pF | 125 V | P.S. | 5p | $0.01 \mu \mathrm{~F}$ | 400 V | Poly. | 3p |
| 68 pF | 500 V | S/M | 71p | $0.01 \mu \mathrm{~F}$ | 500 V | Cer. | 5p |
| 75 pF | 500 V | S/M | 7 p | $0.01 \mu \mathrm{~F}$ | 500 V | 5/M | 30p |
| 82pF | 500 V | 5/M | 71 p | $0.01 \mu \mathrm{~F}$ | 600V | MDC | 7p |
| 100 pF | 125 V | P.S. | 5p | 0.01 1 F | 1,000V | MDC | 9p |
| 100 pF | 500 V | S/M | 71p | $0.015 \mu \mathrm{~F}$ | 160 V | Poly. | 3p |
| 100pF | 500 V | Cer | 5p | $0.015 \mu \mathrm{~F}$ | 400 V | Poly. | 3 p |
| 120 pF | 500 V | S/M | 71p | $0.02 \mu \mathrm{~F}$ | 100 V | Mylar | 3p |
| 150 pF | 125 V | P.S. | 5 p | $0.022 \mu \mathrm{~F}$ | 18 V | Disc | 5p |
| 150 pF | 500 V | S/M | $71 p$ | $0.022 \mu \mathrm{~F}$ | 250 V | M.F. | 3p |
| 150 pF | 500 V | Cer. | ${ }^{5 p}$ | $0.022 \mu \mathrm{~F}$ | 400 V | Poly. | 3 p |
| 180pF | 500 V | S/M | 7 7p | $0.022 \mu \mathrm{~F}$ | 600 V | MDC | 719 |
| 200pF | 500 V | S/M | 7 p | $0.022 \mu \mathrm{~F}$ | 1.000 V | MDC | 10p |
| 220pF | 125 V | P.S. | ${ }_{5 p}$ | $0.033 \mu \mathrm{~F}$ | 250 V | M.F. | $4 p$ |
| 220pF | 500 V | Cer. | 5 p | $0.033 \mu \mathrm{~F}$ | 400 V | Poly. | 4p |
| 250pF | 500V | 5/M | 8 p | $0.047 \mu \mathrm{~F}$ | 12 V | Disc | 6p |
| 270pF | 500 V | Cer. | 5 p | $0.047 \mu \mathrm{~F}$ | 160 V | Poly. | 3p |
| 300 pF | 500 V | S/M | ${ }^{8 p}$ | $0.047 \mu \mathrm{~F}$ | 250 V | M.F. | ${ }^{3 p}$ |
| 330 pF | 125 V | P.S. | 5p | $0.047 \mu \mathrm{~F}$ | 400 V | Poly. | 4p |
| 330pF | 500 V | S/M | 8 p | $0.047 \mu \mathrm{~F}$ | 600V | MDC | 8p |
| 390pF | 500 V | S/M | 8 p | $0.047 \mu \mathrm{~F}$ | 1.000 V | MDC | 10p |
| 470pF | 125 V | P.S. | 5p | 0.14 F | 30 V | Disc | $6 p$ |
| 470pF | 750 V | Dise | 5p | $0.1 \mu \mathrm{~F}$ | 250 V | M.F. | 4p |
| 500pF | 500 V | S/M | 8 p | $0.1 \mu \mathrm{~F}$ | 400 V | Poly. | 5 p |
| 560pF | 500 V | S/M | 8 p | $0.1 \mu \mathrm{~F}$ | 600 V | MDC | 10p |
| 680pF | 125 V | P. S. | 6 p | $0.1 \mu \mathrm{~F}$ | 1.000 V | MDC | 14p |
| 680pF | 500 V | S/M | $8 p$ | $0.15 \mu \mathrm{~F}$ | 250 V | M.F. | 5p |
| 820pF | 500 V | S/M | 8 p | $0.22 \mu \mathrm{~F}$ | 160 V | Poly. | $6 p$ |
| $0.001 \mu \mathrm{~F}$ | 100 V | Mylar | 3 p | $0.22 \mu \mathrm{~F}$ | 250 V | M.F. | ${ }^{5 p}$ |
| $0.001 \mu \mathrm{~F}$ | 125 V | P.S. | $6 p$ | $0.22 \mu \mathrm{~F}$ | 400 V | Foil | 10p |
| $0.001 \mu \mathrm{~F}$ | 400 V | Poly. | 3 P | $0.22 \mu \mathrm{~F}$ | 1.000 V | MDC | 15p |
| $0.001 \mu \mathrm{~F}$ | 500 V | S/M | 10p | $0.33 \mu \mathrm{~F}$ | 250 V | M.F. | 8 p |
| $0.001 \mu \mathrm{~F}$ | 500 V | Cer. | 5p | $0.47 \mu \mathrm{~F}$ | 250 V | M.F. | 8 p |
| $0.001 \mu \mathrm{~F}$ | 1.000 V | MDC | 6p | $0.47 \mu \mathrm{~F}$ | 400 V | Foil | $15 p$ |
| $0.0015 \mu \mathrm{~F}$ | 400 V | Poly | 3 P | $0.47 \mu \mathrm{~F}$ | 1.000 V | MDC | 25p |
| $0.0015 \mu \mathrm{~F}$ | 500 V | S/M | 10p | $1.0 \mu \mathrm{~F}$ | 250 V | M.F. | 15p |
| $0.0015 \mu \mathrm{~F}$ | 500 V | Cer. | 5 p |  |  |  |  |
| $0.0018 \mu \mathrm{~F}$ | 500 V | S/M | 10p | Note |  |  |  |
| $0.002 \mu \mathrm{~F}$ | 100V | Mylar | 3p | $S / M=s i$ | ver mica | $1 \%$ |  |
| $0.002 \mu \mathrm{~F}$ | 500 V | Cer. | 5p | P.S. $=$ poly | lystyren | e $2 \%$ | tol. |
| $0.0022 \mu \mathrm{~F}$ | 125 V | P.S. | 6p | MDC | .c. rati | $\mathrm{g}=300$ |  |
| $0.0022 \mu \mathrm{~F}$ | 500 V | S/M | 10p | M.F. $=$ M | uilard | in. foil |  |
| $0.0022 \mu \mathrm{~F}$ | $1,000 \mathrm{~V}$ | MDC | 6p | Cer. $=\mathrm{c}$ | eramic. |  |  |

## Bi.P <br> 

## COMPLETE TELEPHONES <br>  <br> EX. G.P.O. NORMAL HOUSEHOLD TYPE <br> ONLY 95p <br> post \& Packing 35p each

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| TESTED AND GUARANTEED PAKS |  |  |
| :---: | :---: | :---: |
|  | 4- Photo Cells, Sun Satteries. 0.3 to $0.5 \mathrm{~V}, 0.5$ to 2 mA | 50p |
| 879 |  | 50p |
| 881 | $10 \begin{aligned} & \text { Reed switches } \\ & \text { large and } 5 \text { mali }\end{aligned}$ | 50p |
|  | $200 \begin{aligned} & \text { Mixed Capacitors. Approx. } \\ & \text { quantity, counted by weight }\end{aligned}$ | 50p |
| 2 | 250 Mixed Resistors. Approx. quantity counted by weight | 50p |
| H7 | 40 Wirewound Resistors. Mixed ypes and values | p |
| н9 | 2 OCP7I Light Sensitive | p |
| H28 | 20 OC200/1 2/3 PNP Silico | 50p |
| H30 | $20{ }^{1}$ Watt Zener Diodes; | Op |
| $\mathrm{H}^{35}$ | 100 Mixed Diodes, Germ. Gold bonded etc. Marked and Unmarked | 50p |
| H38 | $30 \begin{aligned} & \text { Short lead Transistors, } \\ & \text { NPN Silicon Planar types }\end{aligned}$ | 50p |
| H39 | 10 integrated circuits 6 gates ${ }^{\text {ind }}$ | 50p |
| $\mathrm{H}^{40}$ | 20 BFY 50,2 2 2 N 696.2 N 1613 . unMarked untested packs | 50p |
| BI | 50 Germanium Transistors | 50p |
| 1 | $\overline{150}$ Ginmanium Dioles | 50p |
| 883 | 200 Trans. manufacturers' ${ }^{\text {jecte }}$. jects an types | 50p |
| 1 | $100 \begin{aligned} & \text { Sificon Diodes DO-7 glass } \\ & \text { equive to OA200, OA202 }\end{aligned}$ | 50p |
| 1 | $100 \begin{aligned} & \text { Sil. Diodes sub min. } \\ & \text { IN9I4 and IN9i6 eypes }\end{aligned}$ | 50p |
| 888 | 50 Sil. Trans. NPN. PNP equiv to 0 C200/1 equivia BSY95A, ect. | 50p |
| H6 | 40250 mW Zener Diodes 0.7 Min. Glass Type | 50p |
| ¢ा5 | 30 7op Hat Silicon Rectifiers | 50p |
| H16 | 15 Experimenters' Pak of supplied | 50p |
| H17 | $20 \begin{aligned} & 3 \text { amp. Silicon siud Recri- } \\ & \text { fiers, mixed volts. }\end{aligned}$ | 50p |
| H20 | 20 BY126/7 Type sificicon Rectivolts $\qquad$ | 50 |
|  |  Can | 50p |

make a rev counter FOR YOUR CAR
The 'TACHO BLOCK. This encapsulated block will turn any $0-1 \mathrm{~mA}$ meter into a counter for any car with C1 each


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Silicon Planar Plastic Transistors. unmarked, untested, factory clearance. A random sampling showed these to be of remarkably high quality.
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ALL TYPES
500 for $\in 3$ 1,000 for $\mathbf{E 5}$ 10,000 for $£ 40$
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OUR VERY POPULAR 3p TRANSISTORS
TYPE "A" PNP Silicon Alloy, TO-5 can.
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 at the most shateringly low pricesof all time. All are fully tested marked and guaranteed

```
40W NPN
40W NPN
40W PNP
```

90W PNP
PAKS complementary pairs
MP40 $40 \mathrm{~W}+40 \mathrm{~W}$ pairs
MP90 $90 \mathrm{~W}+90 \mathrm{~W}$ 60D
Please state

## A CROSS HATCH GENERATOR

 FOR $\mathbf{£ 3} \mathbf{5 0}$ : : 1YES, a complete kit of parts including Printed Circuit Board. A four position switch gives Integrated Circuit design for easy construction and reliability. This is a project in the September edition of Practical Television.
This complete kit of parts costs £3.50, post paid.
A MUST for Colour T.V. Alignment.
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Full of Short Lead Semiconductors \& Electronic Components, approx. 170. We guarantee at Transistors PNP \& NPN, and a host of Diodes \& Rectifiers mounted on 'Printed Circuit Panels. Identification Chart supplied to give some information on the Transistors.

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 ROAMER 10 WITH VHF INCLUDING AIRCRAFT
## 10 TRANSISTORS. 9 TUNABLE WAVEBANDS, MW1, MW2, LW, SW1, SW2, SW3

 TRAWLER BAND, VHF AND LOCAL STATIONS ALSO AIRCRAFT BANDBuilt-in ferrite rod aerial for MW/LW. Retractable, chrome plated 7 section telescopic aerial, can be angled and rotated for peak short wave and VHF listening. Push-pull output using 600 mW transistora. Car Aerial and tape record sockets. 10 transistors plus 3 diodes. Fine tone moving coil speaker. Ganged tuting condenser with vHF section. Separate coil for Aircraft Band. Volume/on/ont, wave change and tone controls Attractive case in black with silver blocking. Size 9 in $\times$ in $\times 4$ in
Easy to follow instructions and diagrams. Parts price list and easy build plans 30 p (l'REE th parts). Earpiece with plug and switched socket for private liatening 30 p extra $\qquad$


7 TUMABLE WAVEBANDS: MW1, MW2, LW, SW1, SW2, SW3 AND TRAWLER BAND. Built-in ferrite yod aerial formy and LW. Retractable chrone plated tele-
scopic aerial for short waves. Push-pull out put using 600 mW transistors. Car aterial and tape record sockets Selectivity qiwitch. 8 tiansistors plus 3 dioiles. Fine tone moving coilspeaker. Air spaced ganged toning condenser. Yolume/on/off, tuning. wave change and tone controls. Attractive case in rich chestnut shade with gold blocking. Size 9in $\times$ Fin $\times 4$ in approx. Easy to follow nstructions and diagranles. Parts price list and easy build plans 25p (FIREL with parts). Earpiece with plug TOTAL


## POCKET FIVE <br> 

8 TUNABLE WAVE-
BRADS: MW, LW,
WITH EXTENDED
MW BAND FOR EASIER TUNING OF LUXEMBOURG, ETC. 7 stages- 5 transistors and 2 diodes, supersensitive ferrite rod aerial, fine tone mowing coil
speaker. Attractive hlack and goll case. Size 51 in $x$ speaker. Attractire hlack and gold case. Size $5 \frac{1}{2} \mathrm{in} \times$
If in $\times 3$ in. Easy buidd plans and jarta price list 10p (FREE with parts).

TOTAL $P$ Q P.P. \& INS. 23p
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BUILD RADIOS, AMPLIFIERS, ETC., FROM EASY STAGE DIAGRAMS. FIVENNITS INCLUDing master unit to construct. Component include: Tuning Condenser: 2 Volume Controls: 2 Terminal Strip: Ferrite Rod Aerial: 3 Plugs and Terminal Strip: Ferrite Rod Aerial: 3 Plugg and Armature Unit: 10 Transistors: 4 Diodes: Resistors: Capacitors: Three in Knobs. Units once con Unit, enabling them to be stored for future use. Ideal for Schools, Educational Authorities and all those intice lied in radio construction, I'arts price liat anl easy build plans 25 p (FREE -jith parts).


CASE AND PLANS F5, 5?
P.P. \&iNS. 32p (OYERSEAS P. \& P. EI)

[^8]
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|  | 2 wa | 3 wa | 4 w | 5 way | 6 way | 8 way | 9 way | 10 way | ioway |
| 1 pole | 40p | 40p | 400 | 400 | 40p | 40p | 40p | 40p | Op |
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