## PRACTICAL

# 반 <br> MAY 1974 <br> ESp 

 DESIGN CHART$\rightarrow$


MODEL X25
220-240 Volts or 100-120 Volts. The leakage current of the NEW $\times 25$ is only a fow microamps and cannot harm the most delicate equipment even when soldered "live". Tested at $1500 v$. A.C. This 25 watt iron with its truly remarkable heat-capacity will easily "out-solder" any conventionally made 40 and 60 watt soldering irons, due to its unique construction advantages. Fitted long-lite iron-coated bit $1 / 8^{\prime \prime} .2$ other bits available $3 / 32^{\prime \prime}$ and 3/16" Totally enclosed element ceramic and steel shaft. Bits do not "freeze" and can easily be removed. MODEL G
18 watt 18 watt


You know the type . . . whenever you need him to do one thing he'll want to do the other. And nothing will make him change his mind.
It's the same with some people's soldering irons.
Hopelessly inefficient heat control can give an iron a will of its own and make soldering operations a nightmare. If this is what soldering means to you it's time you woke up to Antex. Just choose a new model from the comprehensive Antex range of advanced soldering instruments, with unique constructional advantages . . . Iow leakage characteristics, interchangeable bits to match solder-joint requirements, and really precise heat control.
Choose ANTEX-the warm hearted iron
miniature iron,
 fitted with long-life
iron-coated bit $3 / 32^{\prime \prime}$ Voltage 240,220 or 110 2 other spare bits available $1 / 8^{\prime \prime}$ and $3 / 16^{\circ}$
PRICE f2. 26 (rec. retail) P\& P10p.

- MODEL CCN 220 volts or 240 volts.
The 15 watt miniature model CCN also has negligible leakage. Test voltage 4000 v . A.C. d keep yourcool)
elosed element in ceramic shaf MODELC Miniature 15 watt soldering iron titted 3/32' iron-coated bit. Many other bits available from $3 / 64^{\prime \prime}$ to $3 / 16^{\prime \prime}$ Voltages $240,220,110$ 50 or 24. PRICE $£ 2.05$ (rec. retail) P\& P 100 .
other bits availab-coated bit $3 / 32^{\prime \prime}$. and $3 / 64^{\prime \prime}$ including Heat Shield. PRICE E2.48 (rec, retail) P \& P 10 p .

PRICE £2.05
(rec. retail) P \& P $10 p$. Suitable for production work and as a general purpose iron

MODEL SK. 1 KIT
Contains 15 watt miniature iron fitted with $3 / 16^{\prime \prime}$ bit, 2 spare bits $5 / 32^{\prime \prime}$ and $3 / 32^{\prime \prime}$, heat sink, solder, stand and "How to Solder" booklet

for connection to car batterv Packed in strong plastic wallet with booklet "How to Solder". PRICE £2.54 PRICE $£ 2.54$
(rec. retail) $\& P 12 p$.

MODEL MLX KIT
Battery operated 12 v 25 watt iron fitted with 15' lead and 2 heavy clips

MODEL SK. 2 KIT
Contains 15 watt miniature iron fitted with $3 / 16^{\prime \prime}$ bit, 2 spare bits $5 / 32^{\prime \prime}$ and 3/32" heat sink, solder. and "How to Solder" booklet. PRICE $£ 3.25$ (rec. retail) P \& P $10 p$.


ST3 Stand - This stand is made from high gradeinsulation material with a chromium plated strong steel spring. It is suitable for all models and replaces all previous stands. The two sponges at the side which are easily replaceable serve to keep the soldering bits clean. Spare bits can be accommodated

> as shown on the illustration.
 isto
 $230 / 24 \mathrm{CV}$
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MULLARD POLYESTER CAPA
400V: $0.001 \mu$ F, $0.00 \mid 5 \mu$ F, $0.0022 \mu$ F, $0.0033 \mu$ F, 21 p. $0.0047 \mu$ F, $3 p, 0.0068 \mu$ F, $0.01 \mu \mathrm{~F}$ $0.015 \mu \mathrm{~F}, 0.22 \mu \mathrm{~F}, 0.033 \mu \mathrm{~F}, 3$ ip $0.047 \mu \mathrm{~F}, 0068 \mu \mathrm{~F}, 01 \mu \mathrm{~F}, 4 \frac{1}{2} \mathrm{p}$. $0.15 \mu \mathrm{~F}, 6 \frac{1}{2} \mathrm{p} \cdot 0.22 \mu \mathrm{~F}$ $8 \frac{1}{2} \mathrm{p} \cdot 0.33 \mu \mathrm{~F}, 12 \mathrm{p} \cdot 0.47 \mu \mathrm{~F}, 14 \mathrm{p}$.


MINIATUAE CERAMIC PLATE CAPACITORS
$50 \mathrm{~V}:(\mathrm{DF}) 22,27,33,39,47,56,68,82,100,120,150,180,220,270,330,390,470$,
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$\mathbf{2}$ POEACh.
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(pF) $10,15,22,33,47,68,100,150,220,330,470,680,1000,1500,2200,3300$. 4700, 4p $10,000,4 \frac{1}{1 p}$.

| RESISTOAS CF-High Stab |  |  |  |  |  |  |  |
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| W. | Type | Range | $1-99$ | 100-499 | 500-999 | $1000+$ | Size mm |
| t | CF | 22-1 M | 1 | 0.75 | 0.60 | 0.55 | $2.4 \times 7.5$ |
| $\frac{1}{4}$ | CF | 22-2M2 | 1 | 0.75 | 0.60 | 0.55 | $3.9 \times 10.5$ |
| $\frac{1}{2}$ | CF | 22-1 M | 1 | 0.75 | 0.60 | 0.55 | $5.5 \times 16$ |
| $\frac{1}{4}$ | MF | 10-2M7 | 2 | 154 | 1.32 | 1.1 | $3 \times 7$ |
| $\frac{1}{2}$ | MF | $10-2 \mathrm{M} 2$ | 2 | 1.43 | 1.21 | 0.99 | $4.2 \times 10.8$ |
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| 2 | MF | 10-10M | 4.5 | 3.52 | 3.08 | 2.75 | $8 \times 17.5$ |

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SUB-MIN. 0.05 W Vertical. $100 \Omega$ to $220 \mathrm{~K} \Omega \mathrm{sp}$ each

Wavechange Switches $1 \mathrm{P}, 4 \mathrm{~W} ; 1 \mathrm{P}, 12 \mathrm{~W}, 2 \mathrm{P}$,
$2 \mathrm{~W}: 2 \mathrm{P}, 4 \mathrm{~W} .2 \mathrm{P}, ~$


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| Miniature Mullard El |  |  | VEROBOARD 0.10 .15 |  |  |  | POTENTIOMETERS |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 1.04F63V | 61p | 68, F $16 \mathrm{~V} 6 \frac{1}{1} \mathrm{p}$ | $2 \frac{1}{21}$. |  |  | $\begin{aligned} & 28 p \\ & 19 p \end{aligned}$ | 5 witch 26p |  |  | ors, 10K. 100 K .500 K .30 | 0mm, 3 |  |
| 1. $5 \mu \mathrm{~F} 63 \mathrm{~V}$ | ${ }^{61 p}$ | $68 \mu \mathrm{~F}$ \% $63 \vee 12 \mathrm{p}$ |  |  |  | 33p ${ }^{38 p}$ | DIO |  |  |  |  |  |
| $\frac{2.2 \mu \mathrm{~F}}{3} \mathbf{3} \mathrm{~F} 63 \mathrm{~V}$ |  | $100 \mu \mathrm{~F}$ <br> $100 \mu \mathrm{~F}$ <br> 25 V <br> 10 p <br> 1 p |  |  |  | ${ }^{7}$ |  |  | 2 Pin 12 p | ( $\mu$ F/V) : $1 / 25,2 / 25,4 / 25$ | 4.7110 | 25, 8/25, 10/10, 10/50. $16 / 25$, |
| $40 \mu \mathrm{~F} 40 \mathrm{~V}$ | $6{ }^{6}$ | $100 \mu \mathrm{~F} 63 \mathrm{~V} 14 \mathrm{p}$ | $2{ }^{2} \frac{5}{5}$ | Plain) |  | 14p | IN4002 7ip |  | ${ }^{3}$ Pin 13p | 22/63, 25/25. 25/50, 32/2 | 25, 50/25 | 100/10, 100/25, $6 \frac{1}{2} \mathrm{p} .50 / 50,8 \mathrm{p}$. |
| $4.7 \mu \mathrm{~F} 63 V$ | $6{ }^{19}$ | $150 \mu \mathrm{~F}$ 16V 6 ¢p | 21 ${ }^{\frac{1}{2}} \cdot 3 \frac{3}{3}$ | (Piain) |  | 12p | IN4003 9p |  | in $180^{\circ}$ 15p | 100/50, 200/25, 11p. 25 | 50/50, 18 | p. $500 / 10,11 \mathrm{p} .500 / 25,15 \mathrm{p}$. |
| $68 \mu \mathrm{~F} 53 \mathrm{~V}$ | ${ }_{61 p}$ | $150 \mu \mathrm{~F} 63 \mathrm{~V} 15 \mathrm{p}$ | 5 3z |  |  | ${ }_{59}^{22}$ | IN400491p |  | 5mm Jack $14{ }^{\text {dip }}$ | 500/50, 18p. 1000/10, 15 |  | 5, 22p. $1000 / 50,40$ p. 2000/10, |
| 8. $0 \mu \mathrm{FF} 20 \mathrm{~V}$ | $6{ }_{6} 6$ | $220 \mu \mathrm{~F} 64 \mathrm{~V} 6 \frac{1}{2} \mathrm{p}$ | Insertio |  | 59p | 54p | iN4005 12p |  | hono jack $5 \frac{1}{1} \mathrm{p}$ | $20 \mathrm{p} .1000 / 100.90 \mathrm{p} .200 \mathrm{p}$ | 80/25. 300 p | 2000/100, 95p. 2500/25. 38p. |
| $10 \mu \mathrm{~F}$ $10 \mu \mathrm{~F}$ 25 V | $61 p$ $6 . p$ |  | Pins, Pk |  |  | $\begin{aligned} & 44 p \\ & 10 p \end{aligned}$ | $\begin{aligned} & \text { IN4006 14p } \\ & \text { NNI } \end{aligned}$ |  |  | 251-VO, $62 \mathrm{p}-300 / 50$ H. $4450,14 \mathrm{p}$. | 80p. 813500 |  |
| $10 \mu \mathrm{~F} 63 \mathrm{~V}$ | $6{ }^{6}$ | $220 \mu \mathrm{~F} 63 \mathrm{~V} 21 \mathrm{p}$ |  |  |  |  | IN916 70 |  | 2 Pin | 16/450, 23p. 32/350, 33 | p. 50/25 | 20p. $100 / 500,88 p$. |
| $15 \mu \mathrm{~F} 16 \mathrm{~V}$ | $61 p$ | $330 \mu \mathrm{~F} 16 \mathrm{~V} 12 \mathrm{p}$ |  | STO |  |  |  |  | 3 Pin 10p | METALLISED PAPE | ER CAP | CITORS |
| $15 \mu \mathrm{~F}$ $16 \mu \mathrm{~F}$ 10 V |  | $\begin{aligned} & 330 \mu \mathrm{~F} \\ & 470 \mu \mathrm{~F} \\ & 64 \mathrm{~V} 9 \mathrm{P} \end{aligned}$ | ACI26 | 16 ${ }^{\text {p }}$ p |  |  | $\begin{array}{ll} O A 5 & 42 p \\ O A A 7 & 9 p \end{array}$ |  | Pin $180^{\circ} 12 \mathrm{p}$ | $250 \mathrm{~V}: 0.05{ }^{\prime} \mathrm{F}$. $0.1 \mu \mathrm{~F}, 6 \mathrm{p}$. | 0.25, 6p. | 0. $5 \mu \mathrm{~F}, 7{ }^{\frac{1}{2} \mathrm{p}}$. $1 \mu \mathrm{~F}$ |
| $22 \mu \mathrm{~F} 25 \mathrm{~V}$ | $6{ }^{6} \mathrm{p}$ | $470 \mu \mathrm{~F} 40 \mathrm{~V} 20 \mathrm{p}$ | AC128 | 22 p | BC213L | 12 p | OAB1 110 |  | d. Jack 14tp | $500 \mathrm{~V}: 0.025 .0 .05,6 \mathrm{p} .0 .1$ $1000 \mathrm{~V}: 0.01 \mathrm{p} .0 .022$ | 1, 6p. 0.25 |  |
| $22 \mu \mathrm{~F} 63 \mathrm{~V}$ | ${ }_{6} 6$ | $680 \mu \mathrm{~F} 16 \mathrm{~V} 15 \mathrm{p}$ | BC107 | $11 p$ | BCC214L | $17 p$ | OA200 8p |  | 5inmalack $5 \frac{1}{3}$ | $\begin{aligned} & 1000 \\ & 28 p . \end{aligned}$ |  |  |
| $32 \mu \mathrm{~F}$ $33 \mu \mathrm{~F}$ 16 V | ${ }_{6} 1$ |  | BC108 BC | ${ }_{12 \mathrm{p}}$ | OC41 | 18 p | Integrat |  | 5 cr |  |  |  |
| $33 \mu \mathrm{~F} 40 \mathrm{~V}$ | ${ }_{6}^{6+p}$ | $1000 \mu \mathrm{~F} 25 \mathrm{~V} 25 \mathrm{D}$ | BC148 | 12 p | OCal | 16p | Circuits |  | Twin Scree | Wire, Metre | 12 p | film 5\%. |
| $32 \mu \mathrm{~F}$ 63V | ${ }_{61 p}$ | $1500 \mu \mathrm{~F} 5.415 \mathrm{p}$ | ${ }^{8 C 1} 49$ | 12 p | $\mathrm{OCl}^{\text {Cl }}$ | ${ }^{23} \mathrm{p}$ | $\mu \mathrm{A} 709 \mathrm{C}$ | 50 p | Stereo Screen | ed Wire, Metre | 12 p | Ultra low noise new resistors. |
| $47 \mu \mathrm{~F}$ lov | ${ }^{61 p}$ | $1500 \mu \mathrm{~F} 16 \mathrm{~V} 25 \mathrm{D}$ | ${ }^{8 C 182 L}$ | 12 p | T1543 | ${ }^{311}$ | $\mu A 741 C$ | $55 p$ | Connecting | Wire, All colours. Metre |  | ith full |
| $47 \mu \mathrm{~F} 25 \mathrm{~V}$ | ${ }_{6}{ }^{1 / p}$ | $2200 \mu \mathrm{~F} 10 \vee 25 \mathrm{p}$ | ${ }^{\text {BCl }} 831$ | 12 p | 2 N 2926 | 11 D | $\mu \mathrm{A} 723 \mathrm{C}$ | 1 | Neon Bulb, | $\checkmark$ Wire Ended | 5 for 24 p | each E12 value. $10 \Omega$-IM. |
| $47,2 \mathrm{~F} 63 \mathrm{~V}$ | 610 | $3300 \mu \mathrm{~F} 6.426 \mathrm{p}$ | BC184L | $13 p$ | 3702 | p | 14 | 35p | N | 40 V . Red, Amber, Clear | $18 \frac{1}{2} \mathrm{P}$ | total $305 . \quad$ ¢2.75 |

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ette Recorder $\mathbf{6} 12.75$. ette Recorder 12.75.
AKAI GXC 40 Stereo cass- Phones.

## BUILD THIS RADIO

 22p), carr. etc. 32p

## FIBRE OPTICS

0.01 diam. Mono filament $\mathbf{6 3 . 0 0}$ per 50 metre reel 0.11 diam. 64 Fibres Sheathed $\$ 1.00$ per metre. SPRAYS 15 mm diam. (Mare's Tail Spray $\mathbb{E} 10 \cdot 50.7 \mathrm{~mm}$ diam. (5-00).

310 Radio

## 300 345 <br> 455 65

115
12
125
130
605
610
8
8
8
8

24
275
570
570
590 SWR meter 12
620 NI-CAD Charger
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| Parameter | Conditions | Performance |
| :---: | :---: | :---: |
| HARMONIC DISTORTIOS | Po $=3$ WATTS $\mathrm{f}=1 \mathrm{KHz}$ | 0.25\% |
| LOAI IMPEDANCE | - | 8-168 |
| InPUT IMPEDANCE | $\mathrm{t}=1 \mathrm{~K} \\|_{2}$ | $100 \mathrm{k} \Omega$ |
| Frequency response -3dB | $\mathrm{Po}=2$ WATTS | $50 \mathrm{Hz-25K} \mathrm{~Hz}$ |
| sensitivity for Rated o/p |  | 75 mV . RMs |
| DIMENSIONS | - | $3^{*} \times 22^{* *}=1^{\prime \prime}$ |

The above table relates to the AL10. AL20 and AL30 modules. The following table outlines the differences
in their working conditions.

| Parametes | Alio | AL20 | AL30 |
| :---: | :---: | :---: | :---: |
| Maxitnum Supply Voltage | 25 | 30 | 30 |
| Power out for $2 \%$ T.H.D. $(\mathrm{RL}=8 \Omega \mathrm{I}=1 \mathrm{KHz}$ ) | 3 watts RMS Min. | 5 watts RM8 Min. | 10 watts RMS Min. |

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AL 30. 10 watts
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PA12 PRE-AMPLIFIER SPECIFICATION

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Frequency Rearon


Frequency Response
Harmonic Disturtion
Inputs: I. Tape Heal
Radio. Tune
$20 \mathrm{~Hz}-20 \mathrm{KHz} \pm 1 \mathrm{~dB}$
3. Magnetic P.U.
1.25 mV into $50 \mathrm{~K} \Omega$

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| - 100 plua less $10 \%$ on the $25+$ colurnn |  |  | No's | $\begin{gathered} 1+ \\ \mathbf{~} \end{gathered}$ | $\begin{gathered} 25+ \\ f \\ 1.39 \end{gathered}$ | No' | $\begin{gathered} 1+ \\ \mathbf{f} \\ 3.35 \end{gathered}$ | $\begin{gathered} 25+ \\ \mathbf{e} \\ 3.28 \end{gathered}$ | Linear |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| No's | $1+$ | $25+$ | 7447 | 1.48 |  | 74123 |  |  |  |  |  |
|  | $\pm$ | f | 74.48 | 1.98 | 1.81 | 74141 | 1.10 | 0.89 |  |  |  |
| i400 | 0.20 | 0.18 | $74 \overline{0} 0$ | 0.20 | 0.16 | 74145 | 1.65 | 1.48 | ก | gra | ed |
| 7.101 | 0.20 | 0.18 | 7451 | 0.20 | 0.19 | 74100 | 3-30 | 2.75 |  |  |  |
| 7402 | 0.20 | 0.18 | -453 | 0-20 | 0.10 | 74151 | 1.21 | 1.15 |  |  |  |
| 7403 | 0.20 | 0.18 | 74.4 | 0.20 | 0.18 | 74153 | 1.32 | $1-15$ | CII | dit |  |
| 7404 | 0.21 | 0.18 | 74tio | 0.20 | 0.18 | -1154 | 2.20 | 1.87 |  |  | ¢ |
| 7405 | 0.21 | 0.19 | 7470 | 0.33 | 0.27 | 74155 | $1 \cdot 65$ | 1.21 | 301 | D1L | 0.50 |
| 7406 | 0.54 | 0.48 | 7472 | 0.33 | 0.30 | 74106 | 1.85 | 1.21 | 301 | T099 | 0.55 |
| 7407 | 0.54 | 0.48 | 7473 | 0.46 | 0.42 | 74157 | 1.54 | 1.43 | 301 | 8 PIN | D1L 0.48 |
| T 708 | 0.29 | 0.24 | 7474 | 0.44 | 0.40 | 71150 | 2.18 | 2.05 | 301 A | DIL | $0 \cdot 69$ |
| $\bigcirc 409$ | 0.29 | 0.24 | 7175 | 0.83 | 0.57 | 74161 | 2.18 | 2.05 | 301 A | T049 | 0.69 |
| 7410 | 0.20 | 0.18 | 7476 | $0 \cdot 55$ | 0.51 | 7+162 | 4.84 | 4.54 | 301 A | 8 PIN | DIL 0.66 |
| 7411 | 0.23 | 0.21 | 7480 | $1 \cdot 10$ | 0.89 | 74163 | 4.84 | 4.54 | 307 | DIL | 0.69 |
| 7412 | 0.40 | 0.33 | 7481 | 1.37 | 1.26 | $7+164$ | 2.73 | $2 \cdot 63$ | \$07 | T09! | 0.69 |
| 7413 | 0.31 | 0.28 | $748:$ | 1-10 | 0.89 | 7+165 | 2.73 | $2 \cdot 63$ | 307 | 8 P15 | D1L 0.68 |
| 741i | 0.49 | 0.47 | 7483 | 1.32 | 1.29 | 74166 | 4.28 | $3 \cdot 94$ | 308 | T099 | 6.45 |
| 7417 | 0.57 | 0.53 | 7484 | $1-32$ | 1.21 | 74174 | 2.78 | 2.66 | 308 A | T090 | 8.40 |
| 74:30 | 0.20 | 0.18 | 7485 | 2.75 | 2.84 | 74175 | 1.84 | 1.81 | 709c | DIL | 0.35 |
| 74:2 | 0.60 | 0.58 | 7489 | 0.49 | 0.41 | 74176 | 1.99 | 1.87 | 709 c | TO99 | 0.31 |
| 74.3 | 0.60 | 0.58 | 7489 | 4.95 | 4.29 | 74177 | 2.75 | $2 \cdot 64$ | 723 c | DIL | 0.98 |
| 7425 | 0.60 | 0.53 | 7490 | 0.82 | 0.71 | 74180 | 2.75 | 2.64 | 723 c | T099 | 0.95 |
| 74.5 | 0.35 | 0.31 | 7492 | 0.84 | 0.65 | 74181 | 6.49 | 6.05 | 741 c | 8 PIN | DIL 0.38 |
| 7427 | 0.55 | 0.51 | 7493 | 0.82 | 0.68 | 7418: | 2.16 | 2.02 | i41c | 14 1'1 | - DIL, 0.39 |
| 74.8 | 0.85 | 0.78 | 7494 | 1.04 | 0.83 | 74184 | 2.68 | 2.42 | itlc | T099 | 0.41 |
| 7430 | $0-20$ | 0.18 | 7495 | 1.14 | 1.03 | 74190 | 3.08 | 2-98 | $74 \% \mathrm{c}$ | D1L | 0.46 |
| 7.432 | 0.42 | 0.33 | 7496 | 1.25 | 1.15 | 7i191 | $2 \cdot 30$ | 2.24 | $7+88$ | D11. | 0.38 |
| 7433 | 0.97 | 0.91 | $7+100$ | 2.75 | 2.58 | 74192 | 2.53 | 2.30 | 748 c | TO49 | 0.41 |
| 7437 | 0.77 | 0.74 | 74104 | 1.18 | 1.14 | T+193 | $2-53$ | 2.30 | 2N+1 |  | 1.38 |
| 74.88 | 0.77 | 0.74 | 71105 | 1.18 | 1.14 | 71194 | 3.27 | 3.15 | MC13 | $10 \mathrm{P}^{2}$ | $3 \cdot 38$ |
| 7440 | 0.20 | 0.18 | 74107 | 0.48 | 0.48 | 74190 | 2.42 | 2.30 | - | 寿 |  |
| 7441 | 0.81 | 0.78 | 74110 | 0.67 | 0.65 | 74196 | 2.18 | 2.08 |  |  |  |
| 7442 | 0.81 | 0.78 | 74111 | 1.52 | $1-40$ | 74197 | $2 \cdot 18$ | 2.08 |  |  |  |
| 7443 | 1.40 | $1 \cdot 32$ | 74118 | 1.10 | 0.80 | 74198 | 6.85 | 8.05 |  |  |  |
| 7444 | 1.57 | 1.52 | 74119 | 1.84 | 1.54 | 74199 | 86.65 | 8.05 |  |  |  |
| 74.45 | 2.31 | 2.18 | 741:21 | 0.47 | 0.42 | 74200 | 26.40 | 21.12 |  |  |  |
| 7446 | 2.31 | $2 \cdot 18$ | 74122 | 1.70 | 1.58 |  |  |  |  |  |  |


| Resistors | VEROBOARD |  |  |
| :---: | :---: | :---: | :---: |
| f watt $5 \%$ carton ... ........ 10 |  | ${ }^{0.1}$ | ${ }^{0} 15$ |
| watt $5 \%$ carbon . . . . . . . ${ }^{1 / \mathrm{p}}$ | 14838 | 27p | ${ }_{23 \mathrm{p}}^{19 \mathrm{p}}$ |
| 1 watt 10\%\% carbon | 33 $\times 38$ | ${ }_{27 \mathrm{p}}$ | ${ }_{23 \mathrm{p}}$ |
|  | $31 \times 5$ | 31 p | 31 p |
| range 10 cutus to 1 megohms. | $17 \times 2 \frac{1}{17}$ | ${ }^{82 \mathrm{p}}$ | ${ }^{63 p}$ |
|  | $17 \times 3:$ | £1.10 | 87p |
| MIINITRON DIGITAL | Pin insertion tool | 57 p | ${ }_{570}$ |
| INDICATOR TYPE 3015F | Spot face cutter | $4{ }^{\text {d }}$ | 46 p |
| Read 0.9 and decimals. ONLY $£ 1.95$ | Y's 36 Pins | 20 D | 20 p |


| Electroiytic |  |  |  | $\sim 4$ |  | VOLUME CONTROLS <br> Potentiometers |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Capacitors |  |  |  |  |  | Cathon track $500 \Omega$ to $22 \mathrm{M} \Omega$ |
|  |  |  |  | Sirgle 13p. Dual gang (atereo) 44p. |
| ${ }_{4}^{47 \mu \mathrm{~F}}$ | 6. |  | 16 VOLT |  |  | 40 VOLT |  | Single type with D.P. switch extra. |
|  | ${ }_{61 p}$ | ${ }_{33}{ }^{4} \mathbf{4}$ | 63, D | $68 \mu \mathrm{~F}$ | 10 p |  |
| $100 \mu \mathrm{~F}$ <br> $\begin{array}{l}200 \mu \mathrm{~F} \\ 300 \mu \mathrm{~F}\end{array}$ | 83 p | 150 10 |  | $100 \mu \mathrm{~F}$ | ${ }^{9 p}$ |  |
|  |  | 150, |  | $220 \mu \mathrm{~F}$ | ${ }_{11 p}$ | CARBON SKELETON |
| $3 \times 0 \mu \mathrm{~F}$ $100 \mu \mathrm{~F}$ |  | ${ }^{220 \mu \mathrm{~F}}$ | 9 p | ${ }_{6}+0 \mu \mathrm{~F}$ | 19p | PR |
| $4700 \mu \mathrm{~F}$ | 28 p | $680 \mu \mathrm{~F}$ |  | $680 \mu \mathrm{~F}$ $1000 \mu \mathrm{~F}$ | ${ }_{25 p}^{25 p}$ | Strall high quality type dinear |
|  | 6.3 VOLT | $\begin{aligned} & 1000 \mu \mathrm{~F} \\ & 1500 \mu \mathrm{~F} \end{aligned}$ | 17p | $1000 \mu \mathrm{~F}$ $2200 \mu \mathrm{~F}$ | ${ }_{44 \mathrm{p}}^{25}$ | only). Ail yalves 100.5 meg ohris. |
| $33 \mu \mathrm{~F}$ | 6ip | $2000 \mu \mathrm{~F}$ | 43p |  |  | $\begin{array}{ll}-1 \text { watt } \\ -2.5 \text { watt } & 6 \frac{10}{2} \text { peach }\end{array}$ |
|  | ${ }^{61}{ }^{81} \mathrm{p}$ | 25 V |  |  |  |  |
| $150 \mu \mathrm{~F}$ <br> $470 \mu \mathrm{~F}$ | 11 p | $10 \mu \mathrm{~F}$ | ${ }_{6}^{6} \mathrm{p}$ |  |  |  |
| $680 \mu \mathrm{~F}$ $1500 \mu \mathrm{~F}$ | ${ }^{13 \mathrm{p}}$ |  | ${ }_{6} 61 \mathrm{p}$ | 63 V |  |  |
| $1500 \mu \mathrm{~F}$ $2200 \mu \mathrm{~F}$ |  | ${ }_{100 \mu \mathrm{~F}}^{+17 \mathrm{~F}}$ | ${ }_{80}^{81}$ | $1 \mu \mathrm{~F}$ | 61p | kit Available |
| $3300 \mu \mathrm{~F}$ | 28 p | 150 |  | $2.2 \mu \mathrm{~F}$ | ${ }_{81}^{61} \mathrm{P}$ |  |
| 10 VOLT |  | ${ }^{22} 470$ | 10 p | . ${ }^{4.7 \mu \mathrm{~F}}$ | ${ }^{6}+1$ | SLIDE SWITCH |
| $22 \mu \mathrm{~F}$ | 6\}p | $680 \mu \mathrm{~F}$ |  | $10 \mu \mathrm{~F}$ | ${ }_{6}^{6}+\mathrm{P}$ |  |
| $47 \mu \mathrm{~F}$ | 81 P | 1000 $/ \mathrm{F}$ | 22 D | 2i $\mu \mathrm{F}$ | ${ }^{6.1} \mathrm{D}$ | SPST llp each. D.P.D,T. 13 |
| $100 \mu \mathrm{~F}$ | $6{ }^{6} \mathrm{P}$ | ${ }^{2200 \mu \mathrm{~F}}$ | ${ }_{680}^{380}$ | ${ }^{68 \mu \mathrm{H}}{ }^{600 \mu \mathrm{~F}}$ | 110 |  |
| ${ }_{3}^{220 \mu \mathrm{~F}}$ | ${ }_{10 \mathrm{p}}$ |  |  | $150 \mu \mathrm{~F}$ | 18p | MINIATURE NEO |
| $470 \mu \mathrm{~F}$ | 10p | 40 V | LT | $220 \mu \mathrm{~F}$ | 19 p | LAMPS |
| $1000 \mu \mathrm{~F}$ | ${ }_{200}^{11 p}$ | ( ${ }^{6.8 \mu \mathrm{~F}}$ |  | ${ }^{330 \mu \mathrm{~F}}$ | ${ }_{28 \mathrm{p}}^{22 \mathrm{p}}$ | 240 V or 110 - 1-4 5 p. 5 plus 49 |
| $1500 \mu \mathrm{~F}$ $2200 \mu \mathrm{~F}$ | 24p | $\mathrm{P}=15 \mu \mathrm{~F}$ |  | ${ }^{1}$ | ${ }_{44}{ }^{28}$ |  |
| Mullard Polyester Capacitors |  |  |  |  |  |  |
| C280 SERIES |  |  |  |  |  |  |
|  |  |  |  |  |  |  |
| C296 SERIES |  |  |  |  |  |  |
|  |  |  |  |  |  |  |
| $160 \mathrm{~V}: 0.01 \mu \mathrm{~F}, 0.015,0022$, |  |  |  | 0.033, 0 |  | $1 \mu \mathrm{~F} 149 \mathrm{p} . \quad 15 \mu \mathrm{~F} 22 \mathrm{p} .22 \mu \mathrm{~F} 24 \mathrm{p}$. |

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## N 3055 ), tested/unnarke

 30 Plastic FET'S, unnarked/unteated Sumilar to 2 N 3819.$$
20 \text { TO5 transiatore }
$$

unterted/ummarked.
20 TOly transistors pup like BCles BC179, etc., untested/unmarked 30 Plastic 2 N 305 s , ummarked/un .
10 General purpose, fully tetsed FETs.
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250 bigh Stab. $1 \%, 2 \%, 5 \%$, re Rintors.
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## Transformers



## 50V transformers

Prim, 200-2,0-240, secondary
 $\begin{array}{lllll}13 T 606 & 100 \mathrm{~mA} & 6-0-6 \mathrm{y} & 0.98 & 15 \mathrm{p} \\ \text { BT409 } & 100 \mathrm{~mA} & 9-0-9 \mathrm{~V} & 0.88 & 15 \mathrm{p} \\ \text { BT12012 } & 100 \mathrm{~mA} & 12-0-12 \mathrm{~V} & 1.08 & 15 \mathrm{p}\end{array}$ $\begin{array}{lllll}\text { BT12012 } & 100 \mathrm{nA} & 12-0-12 \mathrm{~V} & 1.08 & 15 \mathrm{p} \\ \text { BT24024 } & 100 \mathrm{n} \mathrm{A} & 24-0-24 \mathrm{~V} & 1.85 & 15 \mathrm{p}\end{array}$
Se

| Semiendetrs. |  | OC3ti | 50p | Bridge | Rect. |
| :---: | :---: | :---: | :---: | :---: | :---: |
| AC12ti | 14p | OC4: | 18p | 1 amp |  |
| AC127 | 18p | OC45 | 10 p | 100 V | 22p |
| AC128 | 15p | OC71 | $11 p$ | 200 V | 24 p |
| ACl41K | 26p | 0 O81 | 12p | 600 ${ }^{+}$ | 27p |
| AC142K | 26p | $2 \times 706$ | $14 p$ | Thyrist |  |
| AClib | 18p | 2N1131 | 24 p | l amp |  |
| ACls 7 | 24p | 2N1132 | 28p | $50 \mathrm{~V}$ | 29p |
| AC188 | 24p | 2 N 2904 | 20p | 100 V | 32p |
| ACl87K | 28p | 2N2926 | 11p | 200 V | 34 p |
| ACl88K | 28p | 2N30J3 2N 3004 | 26p 55 p | $400{ }^{\text {d }}$ | 44p |
| AD149 | $48 p$ $38 p$ | 2N3004 | 55p 49 p | 3 amp |  |
| AD161 AD162 | 38 p 40 p | 2×300\% | 49p 14 p | 50 V | 39 p |
| AD162 | 40p | 2N3703 | 13p | 100 Y | 44 p |
| Ak110 | 20p | 2N3704 | 140 | 200 V 400 V | p |
| AF116 | 20p | 2N3705 | 13p | 400 V |  |
| AFll | 20p | 2N3706 | 12D | $5 \mathrm{Sax}^{\text {a }}$ | $46 p$ |
| BCa 07 | 13p | 2N3707 | 13 p | 100V | 570 |
| 13 C 108 | 13p | 2\$3708 | 11 D | 200 V | 68 p |
| BC109 | 13p | 2N3709 | 12p | 400 V | 77 p |
| BC147 | 18p | 2N3710 | 12 p |  |  |
| BC148 | 13p | 2N3711 | 12p | Trisce |  |
| HC149 | 13p | 2N3819 | 35 p | 2 amp |  |
| HC182 | 13p | 40301 | 55p | 100 V | 33p |
| BC183 | 11p | 40352 | 55 p | 200 V | 65p |
| 13C18: | 14p | 40536 | 88 p | $400 \%$ | 770 |
| 10212 | 13p | 1N914 | 8D | 6 amp |  |
| BC2 13 | 13p | IN916 | 8 p | 100 V | 68p |
| P( $)^{2} 14$ | 13p | 1) 4001 | 7 p | 200 V | 88 p |
| 160131 | 68p | IN4002 | 8 p | 400 V | 99p |
| 13D132 | 90p | [N4003 | 10p | 10 amp |  |
| BF194 | 10p | 1) N 4004 | 10p | 100V | 98p |
| BF 145 | 17p | $1 \times 4006$ | 15p | 200V | £1.32 |
| 13FL44 | 27p | IN4148 | 8p | 400 - | £1.43 |
| ISFY50 | 18p | IN5400 | 16p |  |  |
| BFY51 | 18p | IN5401 | 17 p | 400 m |  |
| B1Y54 | 17 p | 1 N 5402 | 19 p | Zener | Diodes |
| MP8111 | 38 p | IN5404 | 24 p | $3 \cdot 3$ to |  |

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SP 25 Mk. IV turntable, G800 cartridge. $P$ and $\mathbb{C}$
R.R.P 1112
Our Price $£ 84$ 20 (cart \&1-j0) Di Plice eo 20 (cari 104 week

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hearsink which is derm power modes co their logical conciusion by incorporating with it a heatsink, which is designed in special high conductivity alloy sufficient for normal audio use module comparable chassis sinking. All this without significantly increasing the size of the Consistent with modern thinking a triple rated ourpurgarcuis.
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INPUTS
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Input impedance $47 \mathrm{k} \Omega$ । kHz
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Tape 100 mV .
Main output. OdB ( 0.775 V ).
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 $p$
$c$
1.22
2.40
2.89
5.63
8.36
1519
21.99
28.70
39.17 1
22
30
36
52
67
82
8
$\vdots$ $\begin{array}{ll}73 & 3000 \quad 40 \text { O } 21.6 \times 13.4 \times 18.1 \quad \ddot{O} \quad \ddot{ } \\ \text { CASED AUTO TRANSFORMERS }\end{array}$

LOW VOLTAGE SERIES (ISOLATED)
PRIMARY $200-250$ VOLTS 12 ANDIOR 24 YOLT RANGE Ref. Amps. Weight Size cm. Secondory Windings $P$ \& $P$ $\begin{array}{lll}1110.5 & 0.25 \\ 16\end{array} 8^{0 z}$

108
72
116
1772
116
17

115| 226 | 60 | 30 | 32 | 0 |
| :--- | :--- | :--- | :--- | :--- | $17.2 \times 15.3 \times 140012 V$ at $30 A \times 2 \times 28$.Ref. Amps Weight Size cm. Secondory Taps$C^{P \&} P$

1.42
1.92
112
79
3 ..... $\times 5.8 \times 4.8$
$\times 6.7 \times 6.1$

$$
\begin{aligned}
& 7.0 \times 6.7 \times 6.1 \\
& 8.9 \times 7.7 \times 7.7 \\
& 9.9 \times 8.3 \times 8.6 \\
& 0.9 \times 8 \times 8.6
\end{aligned}
$$

$\begin{array}{r}21 \\ 51 \\ 117 \\ \hline 99\end{array}$
89
0.5
10
2.0
3.0
へ

$$
\begin{array}{r}
9.9 \times 8.3 \times 8.6 \\
9.9 \times 8.6 \times 8.6 \\
12.1 \times 8.6 \times 10.2
\end{array}
$$

Ref. Amps. Weight
No.
103
105
106
107
118
119RRef. Amps. Weight $5 i z e \mathrm{~cm}$. Secondory Taps
No.
124
126
127
$\square$

?${ }_{122}^{122}$
MINIATURE TRANSFORMERS WITH SCREENS
Ref.

$\begin{array}{llll}100,330 & 4 & 3.9 \times 2.6 \times 2.9 & 9.0 .9 \\ 330,330\end{array} \quad 4.8 \times 2.9 \times 3.5 \quad 0.9,0.9$
$\begin{array}{llll}500 \\ 1 A & 14.1 \times 5.4 \times 4.8 & 0.8-9.0-8-9 \\ 7.0 \times 6.4 \times 6.1 & 0-8.9 & 0.8-9\end{array}$$\begin{array}{lrll}1200.200 & 12 & 4.0 \times 66 \times 6.1 & 0-8.9,0.8 \\ 20.9 \times 2.9 \times 3.5 & 0.15,0-15\end{array}$$\begin{array}{lll}200,300 \\ 1 & 4 & 4.8 \times 2.9 \times 3.5 \\ 6.1 \times 5.8 \times 4.8 & 0-20,0.20\end{array}$206 IA.|A $212 \quad 8.3 \times 7.7 \times 7.0 \quad 0.15 .20,0-15-20$$203500.500218 .3 \times 7.0 \times 7.0$ 0.15-27.0.15-27204 IA,IA 3 - $8.9 \times 7.7 \times 7.7$$\begin{array}{cc}P & \& \\ 1 & p \\ 31 & 10 \\ 52 & 22 \\ 12 & 10 \\ 52 & 10 \\ 03 & 22 \\ .73 & 30 \\ .52 & 10 \\ .80 & 22 \\ .41 & 30 \\ .08 & 38 \\ .82 & 38 \\ .86 & 38\end{array}$
PLEASE ADD 10\% FOR V.A.T.
BABRIIE electronics3,THE MINORIES, LONDON EC3N IBJTELEPHONE: 01-488 3316/8

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BSR HI-FI AUTOCHANGER STEREO \& MONO
Plays $12^{-}, 10^{-}$or $7^{*}$ records. Auto or Manual. A high quality unit backed by BSR guarautee. AC $200 / 250 \mathrm{~V}$ Size $13!\times 114 \mathrm{jn}$.


Above motor board 3 in. Below motor board $2 \frac{1}{3}$ in with STEREO and MONO XTAL $\mathbf{1 6 . 7 5}$ Pont 25p.

PORTABLE PLAYER CABINET Modern deaign. Black rerine covered. Silver tront grille.
Padded Lid. Chrome fittings.
Size $17 \mathrm{in} \times 15 \mathrm{in} \times 7 \mathrm{zin}$. $\begin{array}{lr}\text { Padded lid. Chrome fittinge. } & \text { Size } 17 \mathrm{in} \times 15 \mathrm{in} \times 7 \mathrm{in} \\ \text { Few only ia red rexine. } \\ \text { Motor board cut for BSR deck } & \mathbf{4 4 . 5 0} \text { Post } 25 \mathrm{p}\end{array}$
Motor board cut for BSR dech

## 4 Transistor Mono Amplifier

Powerful 3 watt output, 15 ohm. AC mains operated with translormer. 3-Controls, volume, treble, basa and On/Off switch with knobs. Ready made on printed circait board. Fused inputs and outputs. Famous make. Size $\sin$ wide $\times 4$ in deep $\times 3$ in high.
Suitable 7 in $\times 4$ in
$\mathbf{6 5 . 9 5} \begin{gathered}\text { Pont } \\ 25 p\end{gathered}$

R.C.S. DISCO DECK SINGLE RECORD PLAYER

## Fitted with auto stop. A cos stereo/mono cartridge. Baseplate

 Size 11 in $\times 8$ ijn. Turnta ble. Size 7 in diameter. AiC main.200250 V motor has a separate winding 14 volt to power a 200250 V motor

## £5.50

METAL PLINTH AND PLASTIC COVER Cut out for moat Garrard or B.S.R. Most will play with cover in position. $12 \frac{1}{2} \times 14 \frac{1}{2} \times 7$ in.
 Covered in black leatherette.

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COMPACT PORTABLE STEREO HI-FI
Two full size loudspeakers $131 \times 10 \times 3$ inn. Plager unit clips to loudapeakers making it extremely compact, all records 33 r.p.m., 45 r.p.m. Separate volume and tone


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 SMITH'S CLOCKWORK 15 AMP TIME SWITCH 0 TO 60 MINUTESSingle pole two-way Surface mounting with fixing screwa. Will replace existing

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BLANE ALUMINIOM CHASSIS. 18 s.w.g. 2tin gides $6 \times 4$ in $45 p ; 8 \times 8$ in $53 \mathrm{p} ; 10 \times 7$ in $65 \mathrm{p} ; 12 \times 8 \mathrm{gin} 85 \mathrm{p}:$ $14 \times 9$ in $90 \mathrm{p} ; 16 \times 6 \mathrm{in} 90 \mathrm{p}: 12 \times 3 \mathrm{in} 50 \mathrm{p}: 16 \times 10 \mathrm{in} \mathrm{f1}$.


 $14 \times 3$ in $20 \mathrm{p}: 10 \times 7$ in $24 \mathrm{p} ; 12 \times 5 i n 25 p: 12 \times 8 \mathrm{in} 34 \mathrm{p}$
$18 \times 8 \mathrm{in} 34 \mathrm{p} ; 14 \times 9$ in $40 \mathrm{p} ; 12 \times 12 \mathrm{in} 47 \mathrm{p} ; 18 \times 10 \mathrm{in} 60 \mathrm{p}$ PAXOLIM PANEL $10 \times 8 \mathrm{in} 20 \mathrm{p}$.
1 P inch DIAMETER WAVECEANGE SWITCEES, 30p ea. 2 p. 2-way. or 2 p. 6-way. or 3 p. 4 -way.
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watt. 8 tate 3 or 8 or watt. 8tate 3 or 8 or
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The moving coil diaphragm gives a good radiation pattern to the highor frequenciea and a mooth ertenalon of total responpe $3 \pm \times$ Rin deep. Rating 10 W , 8 ohm. Cromover $41 \cdot \mathrm{e} 5 \quad E \mid \cdot 90$ Post 20p.

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 $3,5,8,10,13$ and $5-0-5 \mathrm{~V}$ \&1.30, Ditto 5 amp .81 .50 ;
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2 stage triode pentode valve. 3 watts output. Volume on/ofl and tone controls. Printed circuit
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## GROWTH RATE

wHILST economic growth may hold the key to the nation's problems, there is another area of growth which is more directly our concern and perhaps the cause of a little uneasiness among some constructors. We refer to the prevalent pattern of electronic design: a trend towards expansion in circuitry, though not necessarily increased complexity in strictly constructional terms since much of current circuitry is usually accounted for by i.c.'s.

We sympathise with those who view with some apprehension this continuing circuit growth rate. Yet it has to be said that designers must, within reason, be allowed their heads, and the truly exciting progress that has been made in new and ever widening fields during the last decade or so would not have been achieved without an uninhibited approach to devices and circuits.

It also has to be said that cost-effectiveness is a vital matter so far as the majority of constructors is concerned. So we try in our contents to achieve something like a fair balance between the big and the small in constructional projects. As an example, this can be demonstrated very effectively if we take a look into a field which has undergone intensive cultivation by musically orientated designers these last few years.

One of the original roots of the now strongly flourishing creative sound branch of electronics was the pop music world, for it has rapturously seized upon every novel distortion effect the electronic designers could devise, since the earliest solid state days. So we have experienced a rash of effects units some bearing such unashamedly honest descriptive titles as fuzz and wha wha, and others of a more conventional musical character such as tremolo and vibrato.

In the meanwhile, the whole impediment of the audio laboratory has been pressed into service for rather more serious musical purposes, and to the established form of signal generators have been added new breeds of circuits, some designed specifically for the creation of artistic sounds. The synthesiser is the ultimate result of the amalgamation of an assortment of signal generating and processing circuits. So from simple pop effects units to sophisticated synthesiser. A story of electronic expansion and growth that has its parallel in other fields, of course.

Yet entry into the field of creative sound does not depend upon the possession of an expensive and complex instrument, desirable though this might be. There are easier ways to make a start. For instance the Sound Bender featured on this month's free Design Chart provides an admirable opportunity for anyone to commence experimenting, at no great cost. Use of this uncomplicated unit in conjunction with other simple circuits and with standard items of audio equipment will "open another window" and encourage active exploration of the fascinating possibilities of sound creation and manipulation.
F.E.B.

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AMONG the many electronic effects featured with modern "sounds" are those obtained by modulation, such as a tremulous wavering, percussive shaping, certain rhythms, machine-like voices, and controlled fading. Modulation is the varying or shaping of one signal by another, as distinct from mixing which merely superimposes signals.

The intention with the Sound Bender described here is to offer a basic unit which can be adapted for use with electronic musical instruments and audio equipment to create a wide range of effects. The modulator will handle input signals in the range 20 to 500 millivolts r.m.s., is powered by a nine volt battery or mains unit, and has a $\pm 3 \mathrm{~dB}$ frequency response of 30 Hz to 20 kHz .

## MODULATOR ACTION

The basic unit is an audio modulator whose action is best understood by referring to the block diagram of Fig. 6. The modulator is shown as an envelope shaper, with a continuous sinewave tone on Input 1,
and a control pulse on Input 2. The control pulse defines the amplitude and duration of the tone output, and hence the envelope shape.

Depending on tone, frequency and waveform, and control pulse attack and decay rates, the resulting output signal can be made to sound like a guitar, double-bass, piano, tympani or bongo, to mention only a few.

With other input signal combinations, the characteristic sound output could be unique or imitative of other kinds of musical instrument.

It will be noticed in Fig. 6 that modulator output amplitude is zero when the voltage on Input 2 is zero. The audio modulator takes advantage of the principle that two signals of equal amplitude but opposite phase will cancel to give a null output when mixed.

## PRINCIPLE OF OPERATION

A simplified circuit of the modulator is shown in Fig. 1. The signal to be modulated (Input 1) is fed


Photograph of the completed audio modulator

ALL DIAGRAMS APPEAR ON THE SPECIAL DESIGN CHART INCLUDED WITH THIS ISSUE


## By D. BOLLEN


to a phase splitter stage which gives a pair of output signals (A and B) of similar amplitude and opposite phase.

Signal A is passed straight to the mixer after undergoing a fixed attenuation by $R_{A}$, while signal $B$ is given a similar attenuation by the potential divider formed by $R_{B}$ and the source-drain resistance of a field effect transistor TRA. A voltage on the gate of TRA will vary the source-drain resistance and hence the degree of attenuation given to signal B.

If the $A$ and $B$ signals are of equal amplitude at the mixer input when TRA has a zero gate voltage, then any subsequent change of gate voltage, positive or negative, will give a proportional increase of output from the mixer. It is also possible to adjust the modulator for, say, half maximum output when the voltage on TRA gate is zero, then the output amplitude will be increased and decreased by positive and negative gate voltages respectively.

## CIRCUIT DESCRIPTION

In the complete circuit of the modulator Fig. 2, transistors TR1 and TR2 function as a long-tailed pair phase splitter. An input signal applied to TR1 base via R1 and C1 will give rise to a pair of output signals of opposite phase at the collectors of TRI and TR2. R7 then attenuates the signal from TR2 collector while R8 and TR3 give a variable attenuation to the signal from TR1. Both signals are mixed and amplified by TR4.

With no voltage or signal on Input 2, TR3 gate is held at earth potential by R9, and the sourcedrain resistance of TR3 is set by a positive bias of about 2 volts applied by way of R11 and VR1 to the source of TR3. This arrangement allows direct coupling to Input 2 , and modulation by very slow waveforms; such as ramps and squarewaves of many seconds duration.

In the Fig. 2 circuit, C6 offers a low impedance path to a.c. signals across source resistor R12. Modulator input impedances are approximately 33 kilohms for Input 1 and 1 megohm for Input 2.

## MODULATOR CONSTRUCTION AND TESTING

Modulator components may be assembled on a piece of $0 \cdot 1$ in matrix Veroboard 36 holes long by 18 holes wide. Cut the Veroboard to size, break the copper strips with a spot face cutter at the locations shown in Fig. 3, and solder the six terminal pins in position.
When soldering components to the circuit board, use a small soldering iron and take care not to bridge adjacent copper strips with blobs of solder. A useful tip is to run a sharp knife along the gaps between strips to break any stray whisps of solder, and to remove surplus flux.

Check that electrolytic capacitor polarities are as shown in Fig. 3, and that transistor leads are correctly orientated.
The following checks can be carried out with a testmeter when the completed modulator is connected to a nine volt battery. Approximate voltages relative to battery negative: TR1 can 5 V ; TR3 source with VRI slider set mid-way 1.5 V ; and TR4 can 5 V . Current consumption should be in the region of 17 mA .

If there is a serious discrepancy in any of the above test voltages, check for incorrect component location, short circuits, or dry joints.

Now test the modulator with a music or tone signal of about 200 mV r.m.s. applied to Input 1, and the modulator output connected to the tape or tuner input of an audio amplifier. The signal should be heard loud and clear with VR1 set to midtrack.

Rotate VR1 slider clockwise and trim carefully for a null in sound output. In practice, the modulator will not completely suppress the audio signal at null point and a faint sound will still be heard. Having obtained a null, it should now be possible to bring up the sound output to full strength by applying approximately +0.5 V d.c. to Input 2 ; derived from a 1.5 V battery with a 2.2 megohm resistor in series with the positive battery terminal.


## TREMOLO UNIT

Tremolo is a smooth and regular variation of sound amplitude produced by a very low frequency modulation, of ten heard with individual musical instruments, but also capable of being applied overall to the total sound output of a group or orchestra. In addition, voices can be made to sound interestingly "nervous" by the judicous application of tremolo. The musical term "rate" is used to denote tremolo modulation frequency, and "depth" the level of modulation applied.

Block diagram Fig. 4 shows a typical tremolo setup, where a signal is modulated by the output from a very low frequency sinewave oscillator, before being fed to a power amplifier and loudspeaker. The signal source in Fig. 4 could be a guitar pick-up, a microphone pre-amplifier, or a tape recorder.

The design of a variable rate low frequency tremolo oscillator is not as straightforward as it might seem. Conventional methods of controlling oscillator amplitude (i.e. thermistor or filament lamp) tend to cause serious amplitude "bounce" at low frequencies, and this can take several seconds to die down.

A bounce-free performance can be achieved, however, at the expense of some waveform distortion, by applying "soft" diode limiting to the oscillator. Fortunately, distortion is of secondary importance with tremolo modulation, and something approximating to a sinewave shape will suffice.

## TREMOLO OSCILLATOR

A suggested circuit for a tremolo oscillator is shown in Fig. 5, which employs a Wein network (Cl, C2, VR1, R1 and R2) with diode amplitude limiting introduced between C2 and VRla.

With the combined gain of TR1 and TR2 held down to about five by the ratio of R4 and R5 values, positive feedback is applied from the collector of TR2 to the gate of TR1 via one arm of the Wein network.

There is sufficient loop gain to ensure reliable oscillation over the full frequency range without excessive waveform distortion.

Ganged potentiometer VR1a and $b$ in Fig. 5 gives an approximate frequency coverage of 1 to 20 Hz , and maximum oscillator output is in the region of 400 mV r.m.s. VR2 should be adjusted for six volts between the can of TR2 and the common rail. Capacitors Cl and C 2 can be of moulded polyester type.

When setting up the tremolo oscillator and modulator, with a signal on modulator Input 1, VR1 in the modulator is first adjusted for an output null with oscillator depth control VR3 at minimum output, then modulator VR1 is rotated clockwise with VR3 at maximum for an undistorted signal with full depth tremolo.

Thereafter, no further adjustments to the modulator will be necessary. Various signals can now be applied to modulator Input 1 and the result of varying rate and depth controls noted.


## ENVELOPE SHAPING

Envelope shaping is a very fruitful source of sound effects, and this has been touched upon briefly in connection with Fig. 6. A music or voice signal applied to modulator Input 1 can be chopped and shaped to create interesting percussive rhythms and notes which seem to be unrelated to the original sound. Alternatively, musical instruments can be imitated electronically with an envelope shaper by careful choice of control pulse shape and tone waveform.

Obviously, the need here is for a simple and flexible control pulse generator which can be coupled to the modulator to achieve a variety of envelope shaping requirements.

## CONTROL PULSE GENERATOR

The suggested control pulse generator circuit of Fig. 7 will offer variable attack and decay of pulses of 20 milliseconds to one second duration at repetition rates of 1 to 25 Hz .

With S2 in the repeat position, C2 charges through VR1 and R2, and discharges through VR2 and R3 when the unijunction TR1 conducts. The cycle then repeats. TR2 is switched off during capacitor discharge.

In the manual mode, single-shot operation can be initiated by pressing the key switch (or one of several keyboard switches wired in parallel) and a tone on Input 1 of the modulator will then be sustained for as long as the key remains closed and will decay after the key is released.

If the pulse generator is to be used only for manual operation, R1, TR1, and VR1 may be omitted from circuit, with R2 wired straight to the positive supply rail.

The basic envelope shapes given by the control pulse generator and modulator are listed A to E in Fig. 7. Other shapes can be generated by adjusting the modulator preset VR1.


## RHYTHM GENERATOR

A novel rhythm generator can be made by combining the tremolo and envelope shaper units as shown in Fig. 8. Unlike standard "dance" rhythm generators based on switching circuits, which provide pre-determined beat patterns, the principle here is to allow two independent modulating frequencies to interact and give a slowly changing relationship of amplitudes and intervals. The result can be "jungle drum" rhythms of great subtlety and variety.

Looking at Fig. 8, a steady tone input applied to the tremolo stage is modulated by the sinewave output from the tremolo oscillator; this yields a tone of varying amplitude which is then applied to the input of the shaper stage.

The control pulse generator "chops" the tone and imparts to it a percussive envelope shape (A in. Fig. 7). Tone frequency and waveform will determine the pitch and character of the drum sound, while the relative frequencies of tremolo oscillator and pulse generator will establish the rhythm.


## AUDIO RING MODULATOR

The audio modulator can be converted into a ring modulator quite simply by nulling a music or voice signal on Input 1 , and applying a tone signal from an audio oscillator to Input 2, see Fig. 9. Depending on the frequency of the tone, voices are made to sound like "Daleks" or are unintelligibly scrambled, while music is invested with a bell like or warbling quality. For best results, the signals on both modulator inputs should be kept below 100 mV .


## FREQUENCY DOUBLER

Fig. 10 shows the audio modulator employed as a frequency doubler. The modulator is first adjusted for a null output, then Input 2 is connected to Input 1 in series with a $0.1 \mu \mathrm{~F}$ capacitor.

Frequency doubling is of limited usefulness with complex audio signals because the ear interprets a strong second harmonic signal as severe distortion, but a small amount of frequency doubled signal could be mixed back in to achieve a deliberate discordancy.

The case is somewhat different, however, with tone signals lacking in character. For example, if the doubler is added to a simple electronic organ, and adjusted for a partial null, the sound output could be made more interesting. Guitar notes might also benefit from the addition of some second harmonic content.


## AUTOMATIC FADER

A smooth fade up or down, without clicks or scratching noises, gives a professional touch to amateur recordings and home disco sessions. With a few extra components, the audio modulator can be converted into a switch operated fader, with fade up times of 0 to 15 seconds, and fade down times of 0 to 30 seconds, see Fig. 11.

The modulator is adjusted for a null output with S 1 held in the fade down position. When S 1 is switched to FADE UP, Cl slowly charges to nine volts via VR1, and a proportion of the capacitor voltage, appearing at the junction of R1 and R2, is applied to Input 2 to increase the output level from the modulator.

For fade down, Cl slowly discharges through VR2 and returns the modulator output to its original null level.


## VOICE OPERATED FADER

Voice operated faders are often used by radio disc jockeys to suppress the music signal while talking, this leaves the hands free for cueing discs, and allows bursts of music to punctuate the voice during awkward pauses.

In the circuit of the voice operated fader Fig. 12, the capacitor C 2 controlling the voltage on modulator Input 2 is quickly discharged by TR1 at the commencement of a voice signal, thus "killing" the music signal. When the voice ceases, C 2 is allowed to charge through R1 and fades up the music in about 1.5 seconds.

TR1 is switched hard on by an intermittent d.c. bias obtained by rectification of the a.c. signal voltage from the microphone pre-amplifier. The fluctuations of base bias are smoothed out by C2. To adjust the fader, VR1 in the modulator is adjusted for a null whilst blowing into the microphone.


ELECTRONIC flash equipment is now available to one and all at quite reasonable prices and can be used with almost all types of camera. However, the only information normally available to a user about exposure details is in the form of so-called "guide numbers" which relate the flash unit to particular film speeds and camera-subject distances.

This can provide quite usable black-and-white pictures when using direct flash where the flash and camera both point in the same direction, but is not really ideal for use with colour film, which is much more sensitive to exposure conditions, or with "bounce" flash.

What is needed here is a meter which will measure the value of light produced by the flash under any circumstances and indicate to the user the aperture required for a given film speed.

Commercially available flash or "strobe" meters fulfil this function. However their prices tend to be somewhat high, some simpler ones starting at near $£ 40$ and the more sophisticated running into hundreds of pounds.


Fig. 1. The basic circuit concept of the flash meter

The present design evolved from a critical need for something to fulfill the strobe meter function as reasonably as possible. There are probably ways in which the circuit could be made more sophisticated but when calibrated the present equipment, can be accurate to within less than half a stop in measuring incident light. This is more than satisfactory for use with colour reversal film. The component cost should be in the region of £14-£16.

## PRINCIPLE OF OPERATION

The circuit use three 741 operational amplifiers as main active components. These amplifiers were developed for use in analogue computers and the meter is in fact just such a computer. Fig. 1 shows, in block form, the heart of the unit.

Capacitor Cl is connected to the operational amplifier ICl to form an operational integrator. In such an arrangement current flowing from the inverting ( - ) pin of the amplifier induces a voltage across the capacitor that is proportional to the total current flow in relation to time. When the flow of current is stopped the capacitor will retain its charge, and hence the voltage across it, for some considerable time.

The output of the integrator will now be a positive voltage which provokes a steady current to flow through the meter MEI. The reading on the face of this meter will be an analogue integral of the current flow from the inverting terminal of the amplifier for the duration of that flow.

Diode DI is a silicon photodiode (reverse biased) that allows a reverse current to flow through it in proportion to the level of light falling upon its sensitive junction area at any instant in time. Being a silicon diode its normal reverse leakage is infinitesimal and can be ignored.

Switch S1 is included in the circuit since we are only interested in a pulse of current flowing as a result of a flash discharge acting on D1. SI is

Fig. 3. A linear (above) and a curved (right) scale for the display meter. The lower figures are the actual scale values in microamps on a $100 \mu \mathrm{~A}$ meter
normally open, closes at the commencement of, or slightly before, the flash discharge and re-opens again soon afterwards thereby isolating the integrator from any current flow produced as a result of ambient light striking D1.

## CIRCUIT DETAILS

Fig. 2 shows the full circuit in block form. Here the integrator is formed by amplifier IC3 and the isolating switch (S1 of Fig. 1) is a transistor, TR1, that is normally biased off. TR1 conducts for a brief period dictated by the monostable. The monostable is fired by closing S3 and as well as switching TR1 it also triggers the thyristor, CSR1 which is connected into the trigger circuit of an external flash unit by means of a long synchronising lead. This provides the necessary synchronism with the flash for exposure determination.

The inclusion of amplifiers IC1 and IC2 considerably increases the usefulness and flexibility of the unit. Both operate strictly as amplifiers in this case.
Thus a useful range of apertures in photograhy could be considered to be those from $\mathbf{f} 2.8$ down to f22. Each f-stop is equivalent to a change in light level by a factor of two and in the range just mentioned there are six such steps. In practical terms this means that a scene requiring a lens aperture setting of f 2.8 is in fact $1 / 64$ th as bright as a scene requiring an aperture of f22 for correct exposure of the film. If one tried to display all this range on one scale one would arrive at a very cramped bottom end on a meter display face, a reading of only $1.5 \%$ of full scale representing f 2.8 and $6 \%$ f.s.d. representing f5.6.
$f 8$


TWO RANGES
Amplifier ICl overcomes this problem by providing two switched gains. The meter face is altered to display two ranges, a high range from $\mathbf{f} 22$ down to f 8 and a low range from f 8 down to f 2.8 . When the flash pulse is sufficient to use the high range S1 is switched to VR1 but if the light level registered on the meter falls below f8 then S1 is switched to VR2, which is set to increase the gain of the amplifier. This allows the lower light level (obviously of a repeated identical flash) to be displayed well up the scale.

Thus the lowest marking on the scale is as much as $12 \%$ of f.s.d. and the half stop divisions are easy to see.

A brief inspection of Fig. 3, which shows two specimens for the face markings. will make the whole thing clear. Resistors VR1 and VR2 are made variable and are adjusted in practice such that a pulse of light producing a reading of f 8 on the Low range, i.e. at the top of the scale, will produce, when repeated, a reading of $f 8$ at the bottom of the High range. In this way continuity is assured.

## FILM SPEED VARIATION

The flexibility of the instrument is further increased by the inclusion in circuit of amplifier IC2. This has an input resistor R1 and a parallel arrangement of nine switched feed-back resistors, R2 to R10, which control the gain of the amplifier.


Any one of these resistors can be selected by the nine-way single-pole rotary switch S 2 . The function of this amplifier is to process the signal passing from ICI to the integrator IC3 in such a way that the final analogue display of the aperture required for correct exposure is a function of the sensitivity to light of the particular film in use.

Put more simply, one can set the meter for a particular ASA film-speed and the display will be suitably adjusted.

The relationship of the feed-back resistor values to the input resistor Rl is of course important. If one assumes that $\mathrm{R} 2=25 \mathrm{ASA}$ and let it have a value equal to R1 then R10 $=400 \mathrm{ASA}$ and has a value of $\mathrm{RI} \times 16$. So we have a table of relative values as follows:-

| R2 | 25 ASA | , |
| :---: | :---: | :---: |
| R3 | 35 ASA | $\mathrm{R} 1 \times 1.4$ |
| R4 | 50 ASA | $\mathrm{R} 1 \times 2$ |
| R5 | 70 ASA | $\mathrm{R} 1 \times 2.8$ |
| R6 | : 100 ASA | $\mathrm{R} 1 \times 4$ |
| R7 | 140 ASA | R $1 \times 5.6$ |
| R8 | 200 ASA | R1-8 |
| R9 | 280 ASA | R1×11 |
|  | 400 ASA | RI |

This might appear confusing but a short explanation should clear the matter.

Assume. for arguments sake, that you wish to photograph a scene in colour and black and white. In one camera you have Kodachrome ( 25 ASA) and in another you have Tri-X ( 400 ASA ). Setting the meter for 25 ASA (R2) sets amplifier IC2 in the
unity-gain mode. Tripping the flash gives you (let us say) a reading on the low scale of $\mathrm{f} 5 \cdot 6$. If you now set the meter to 400 ASA (R10) amplifier IC2 will be operating with a gain of sixteen (R10 divided by RI equals 16), and will amplify the through signal from another flash discharge by this amount to give a displayed reading on the high scale of f 22 . In both cases the correct exposure is indicated for the relevant film.

In Fig. 2, switch S4 acts as a reset control which shorts out any charge on capacitor Cl after a reading has been taken, leaving the circuit ready for a further reading to be made. Variable resistor VR3 allows the scale of meter ME1 to be suitably set during calibration.

## THE PRACTICAL CIRCUIT

Fig. 4 shows the full practical circuit. TR1 and TR2 constitute the monostable, the pulse width of which is about $1 / 60$ th second determined by C2 and R5. In its quiescent phase the junction of R2, R3 and TR6 base are at positive rail potential and hence TR6 is biased off.

The junction of R6, C2 and TR3 base via R8 are at ground potential so TR3 is also biased off. The monostable is triggered by closing S 2 thus raising TRI base to the positive potential on Cl and thereby turning TR1 off.

The system now changes state. TR6 base goes to ground allowing it to pass current via R24 to the base of TR5, to bias TR5 on for the duration of the pulse. TR5 acts as the isolating switch for IC3


Fig. 4. Full circuit diagram of the flash meter
and allows it to "see" the light sensing circuit for this short time.

Also during this active phase the potential at the junction of R 6 and C 2 rises to the positive rail. This has the effect of throwing TR3 into a state of conduction and allowing $C 4$ to discharge rapidly through R9 and the thyristor CSR1. This pulse of current triggers the thyristor into conduction which in its turn completes the flash-unit trigger-circuit and the flash discharges immediately.

```
GOMPONENTS
Resistors
```



## Potentiometers

## VF1 500 ! skeleton preset

VR2 2.7 k ! skeleton preset
VR3 10k\& skeleton preset
VR4 10 k IS helical precision pot
VR5 10 k ! skeleton preset
VR6 100k !? skeleton preset

## Capacitors

C1 $0.04 \mu \mathrm{~F}$
C2 $0.68 \mu \mathrm{~F}, 20 \mathrm{~V}$
C3 $4 \mu \mathrm{~F}, 10 \mathrm{~V}$ elèctrolytic
C4 $\quad 0.47 \mu \mathrm{~F}$
C5 $\quad 250 \mu \mathrm{~F}, 10 \mathrm{~V}$ electrolytic
C6 $-250 \mu \mathrm{~F}, 10 \mathrm{~V}$ electrolytic
C7 $22 \mu \mathrm{~F}$. Tantalum. 16 V

## Diodes

D1 BPY68 Phofodiode (A. Marshall)
D2 6.2V Zener, $\frac{1}{4} \mathrm{~W}$

## Transistors

| TR1, 2 and 6 | 2N3702 (3 off) |
| :--- | :--- |
| TR3 | 2N2926 |
| TR4 | ZTX501 |
| TR5 | ZTX300 |

Integrated Circuits
IC1; 2 \& $3 \quad 741$ operational amplifiers ( 3 off) and 3 i.c. sockets (8-pin)

## Switches

S1 SPDT slide
S2 Friedland bell push (modified)
S3 DPST slide
S4 SP 9-way rotary
S5 friedland bell push (modified)

## Miscellaneous

Veroboard, battery connectors (2), batteries, meter ( $100 \mu \mathrm{~A}$ 3 $\frac{3}{4}$ in face), case (aluminium 2in 4 in . 6in), nuts and bolts, glue, resin, table-tennis ball, camera flash sync socket.


## Photograph showing the completed flash meter

The remainder of the circuit is fairly straightforward. D1 is a BPY68 photodiode and has a Zener (D2) stabilised negative voltage across it of 6.2 V nominal. The High and Low ranges are selected by switch SI which is in circuit with the feed-back resistors VR1 and VR2. RII is the input resistor to IC2 and S 4 is the nine-way switch in circuit with the ASA feed-back resistors R13-R21.

TR4 and R22 constitute an emitter-follower connected into the feed-back loop of IC2. The purpose of this is to avoid possible loading of $I C 2$ output and consequent distortion of the through-signal. R23 is a current-limiting resistor in the event of a possible short-circuit of the output to ground.

R25 is the input resistor for IC3. C7 is the integrator charge-storage capacitor which can be discharged by switch 55 thus annulling any meter reading. VR6 is a skeleton preset which limits the current output from the integrator to the $100 \mu \mathrm{~A}$ meter. VR3, VR4, VR5 are preset resistors to balance current off-sets in the 741's. C5 and C6 are large electrolytic smoothing capacitors.

C3 is a stabilising capacitor for the monostable. Some electronic flash units produce quite violent voltage irregularities during charging and if the resulting spikes get to the meter circuit this can cause triggering of the monostable. This of course would fire the flash which would be triggered by each following spike in a never-ending cycle. C4 thus acts as an anti-"hiccup" device.

Switch S3 is of the double-pole variety and is inserted in the positive and negative supply rails as at a and b. Power is supplied by two PP3 batteries in series and centre-tapped. They will have virtually shelf-life since the unit in use is usually not kept switched on for more than a half-minute or so at a time.

## CONSTRUCTION

Circuit construction is straightforward. A piece of $0 \cdot 1$ in matrix Veroboard $3 \frac{3}{4}$ in $\times 2 \frac{1}{2}$ in is used with the copper strips running the length of the board. The practical layout is shown in Fig. 5.

The breaks in the strips should be made first, the i.c. sockets soldered in place, and then the wire links attached. The other components can then be assembled and soldered in as their positioning is more easily determined if this order is adhered to.


Fig. 5. Veroboard component layout complete with lead details

Solder in C5 and C6 last of all as these lie over other components. D1 should be left on longish leads ( $\frac{3}{5}$ in to $\frac{1}{2}$ in) to allow it to be angled horizontally above the board in final assembly. This component is a reversible diode so there are no polarity checks to be made before soldering in place. Keep all flying leads of a good length. say 3 inches, pending final assembly.

Resistors R13 to 21 are mounted on the back of swich S4. VR4 is a 10 k !2 precision helical preset potentiometer which is mounted on the instrument case so that final calibration adjustments can be made after assembly.

In practice VR1 and VR2 might have to be components of slightly different value. The values quoted are those used in the prototype and were found to give sufficient range of adjustment with the actual diode D1 and light-diffusing dome employed. Similarly a value of $22 \mu \mathrm{~F}$ was used for C 7 but this is not critical. A capacitor of (say) $4 \cdot 7 \mu \mathrm{~F}$ would be equally suitable: the only difference it would have on circuit characteristics is that it would tend to charge to a correspondingly higher voltage but even so is still unlikely to "saturate" under operating conditions, i.e. ramp as far as supply potential limits.

The prototype unit was housed in an aluminium box measuring 2 in deep, with the lid retained by two screws. On the front ("bottom" of the box) were mounted the display meter (a $3 \frac{1}{8}$ in squarefaced $100 \mu \mathrm{~A}$ meter was used for large-scale ease of legibility), the On/Off switch S3, the High/Low switch S1, the Flash and Reset buttons S2 and S5, and the nine-way rotary ASA selection switch S4. The illustrations show the general arrangement and Letraset markings-the latter varnished after application for protection. On the "top", that is the face towards the target area, of the case are mounted the flash co-axial socket, the helical preset potentiometer VR4 and a light-collecting diffusion dome behind which is situated the BPY68 (D1).

The size of case selected is dictated mainly by the size of meter used. Equally, if a smaller meter is used then the size and location of the switches becomes critical. As can be seen from the illustrations, the prototype layout follows current commercial practice with all the controls and the meter on an "upper" face and the light sensitive zone facing away from the operator using the instrument. Of course, a constructor can vary this arrangement to suit his needs and parts availability.

## COMPONENT MOUNTING

The mounting of most of the items is straightforward. The co-axial socket SK1 can be taken from a flash extension synchronising lead which can be bought at photographic shops. Use the "male" end and glue it into a wooden or plastic collar about lin long that has sufficient surplus at either end for mounting bolts to pass through, In the prototype the collar was of plywood and mounted on spacing columns so that the socket terminated just flush with the surface of the case. The socket was inserted into a tight-fit hole through the collar and made fast with Bostik quick-setting epoxy resin. Screened (co-axial) cable was used to connect it to the thyristor CSR1.
The prototype flash and reset buttons were adapted from Friedland bell-pushes. These have a fairly deep "box" moulded onto their bases which were carefully sawn off with a hacksaw so that only a thin platform of plastic (which holds the actual make/break assembly) remained. The sawn-off portion of each switch, that is the parts that normally sleeve the mounting screws, can be saved. These serve as spacing pillars on the front of the switches and allow them to be mounted with bolts so that they lie flush with the surface of the case. The effect is neat. Again, other push or toggle switches may be used if desired.

## LIGHT RECEPTOR

An important item that has to be specially adapted for the instrument is a light-collecting diffusion hemisphere. For this the prototype used half a table-tennis ball. This in itself is fragile but it can be strengthened.

Cut a table-tennis ball not quite into halves with a hot knife. Ensure that the larger to be used portion. is clean and free from any printed matter. Mix about a teaspoonful of clear epoxy resin with hardener and accelerator (resin such as is used for embedding objects or else the clear resin to be found in fibre-glass repair kits is suitable), and pour this into the ball half.

Roll it around so that it coats the whole of the inside surface of the ball and continue to move it thus as it sets so that it is distributed in an even layer.

Repeat this two or three times so that the resin is built up to a thickness of about $\frac{1}{8}$ in. Moderate warmth such as is provided by a fan heater will speed setting.

Now take a piece of coarse wet and dry abrasive paper, wet it and place it face-up on a flat surface and press the cut edge of the table-tennis ball gently on the abrasive surface. Steady, circular movements with moderate pressure will soon wear away the resin and the ball itself.

Cease grinding when you have a hemisphere, and the abraded surface is clean and flat. Dry off the hemisphere, varnish the outside surface with a clear varnish and glue it firmly with Bostik over the aperture in the case through which the BPY68 "looks".

The aperture itself should be about lin diameter. On the inside surface of the case fix one or two layers of translucent diffusing material. This should preferably be plastic and have similar diffusing properties to grease-proof paper. Unfortunately grease-proof or tracing papers are not suitable as they might "yellow" in time and their light-transmitting properties change. The BPY68 should be positioned about $\frac{1}{4}$ in behind this translucent screen and facing towards its centre.

## FINAL ASSEMBLY AND SETTING-UP

When the circuit has been built and checked and the case completed, the two can be connected together. Take especial care that the flying leads are connected up correctly, that the meter polarity is observed and that the power switch (S3) is off. Connect up the two PP3 batteries and switch on.

Set VR1 to approximately its mid position and select the High range with SI. Working in a low level of light, measure the voltage at the output (pin 6) of IC1. Adjust VR3 until the output is zero with respect to 0 V .

Press the Reset button S5 and observe the $100 \mu \mathrm{~A}$ meter that serves as the display. The pointer should come to rest at zero but may start to drift up or down the scale. Adjust VR5 until the drift over a period of 10 to 15 seconds is negligible.

Place the back on the case temporarily, press the Reset button in order to zero the pointer again, select 400 ASA with S4 and give the Flash button S2 a jab, do not hold it down. The pointer will probably jump up or down the scale and hold a new position. Reset, adjust VR4 and repeat.

Continue this procedure until pressing the Flash button causes the pointer to move not more than 1


Photograph showing light-collecting diffusion dome
or 2 micro-amp divisions in either direction-ideally it should not move at all. This completes the preliminary setting up of the equipment and the time has come to calibrate it.

## THE METER-FACE

First the meter-face must be adapted. Remove the front cover and carefully move the upper scale and stop wire across to the right so that if the pointer were to swing across against the stop it would be indicating about $115 \mu \mathrm{~A}$. Next undo and remove the small bolts that hold the meter face in position and carefully slide it up and away.

Do not lift it until it is well clear of the pointer as otherwise the meter bearings might be damaged if the pointer is snagged. A new set of characters can now be drawn on a sheet of white paper, using a tracing of the original meter face as a guide and glued accurately in position on the meter face-plate. This is now slid back into position, secured with its bolts and the meter cover replaced.

It must be emphasised that care should be taken in this in fact comparatively simple but rather delicate operation.

The actual calibration of the meter might seem somewhat hit-and-miss, relying initially on the guide number system (the very system we are trying to supersede); however it is quite satisfactory to start with and small adjustments can be made later.

## CALIBRATION

Set up an electronic flash unit and position the flash meter, with the hemisphere pointing towards the flash, at such a distance (computed from the guide number) that an exposure of $f 8$ using 400 ASA film will be correct. Set VR6 to roughly mid-position. Set the ASA to 400 and connect up the meter to the flash-gun via a long trailing extension sync-lead.
Switch on and press the flash button. If the flashgun does not fire reverse the polarity of the synclead (the thyristor is polarity conscious). The easiest way is by using a two-pin connector in the sync lead line.

Reset, switch to the Low scale and press the Flash button again. Adjust VR2 until the pointer comes to rest at f 8 on the top end of the scale, the adjustments being made between flashes.

If, regardless of high VR2 settings, the pointer does not swing as high as f 8 but consistently comes to rest somewhat lower on the scale then do not


Fig. 6. Graphic representation of clipping of the pulse
reset and in fact reduce the value of VR6 so that the pointer tries to move well off the top of the scale. That is to say it is seated firmly against the end stop.

Now reset and continue flashing and adjusting VR2 as before until the pointer comes to rest centrally on f8 at the top of the scale.

## CLIPPING

When this has been achieved switch to 200 ASA , reset and flash again. Check that the pointer stops at 556 . If it does not and comes to rest higher than f5.6 it means that the signal at the output of IC2 is being clipped to the rail potential at the higher ASA settings. In Fig. 6 "A" represents the optimum pulse characteristics whereas " $B$ " shows clipping of the signal.

If clipping does occur then first reset and reduce the effective resistance of VR6 slightly. Now switch back to 400 ASA and adjust VR2 so that once again a flash gives the correct reading of 88 .

When this has been achieved again reset, switch to 200 ASA and confirm that the indicated exposure is f5.6. It might prove necessary to repeat this procedure two or three times to get the relationship correct.

## CORRELATION

When it is correct move on to correlating the High scale with the Low scale. To do this, reset, switch to 400 ASA and switch to the High scale. Adjust VRI between flashes until the pointer comes to rest on f 8 at the bottom of the scale.

Finally do all the checks again: zero voltage at pin 6 of 741 (1) (adjustments on VR3), pointer drift over 10 secs (adjustments on VR5), pointer jump when Flash button depressed and flash-gun not connected in circuit (adjustment externally on VR4) and then the calibration checks should be run through as outlined. From time to time as the meter is in use carry out the pointer jumping test and make adjustments to VR4 as necessary. The adjustments should not be very great.
If, after a period of use, the meter seems to be reading slightly high or low then small adjustments should be made to VR6 to compensate. It should not be necessary to adjust VR1 or VR2 since again the correction required will probably be very small.

## THE BATTERIES

The two PP3 batteries specified will adequately power the unit for several months provided the unit in use is not left switched on for any great length of time. When the batteres are replaced always fit two fresh ones together: never use one fresh and the other exhausted.

## SOLID-STATE TV CAMERA

What is claimed to be the world's most sensitive solid-state television camera has been developed by engineers at General Electric Company in the USA.

The new camera is light and compact and is capable of taking pictures by the glow of a candle.

The heart of the camera is a quarter-inch square, metal-oxide semiconductor chip. It is covered with 10,000 pairs of miniature capacitors, each pair functioning as an individual light-sensing device. As light strikes the chip each capacitor pair collects a charge proportional to the light striking it. Each pair of capacitors is then addressed by scanning circuits to release the charge into the base of the chip.

The tiny chip does the same job as the tube in a conventional camera.

Potential applications for the camera include use in surveillance systems and eventually it could find its way into consumer electronics, possibly combined with a small video recorder and television set to form a home TV studio.

## BRITISH NATIONAL DATA BUOY

Acontract to design and develop the Department of Trade and Industry's national data buoy has been awarded to the SEATEK group of companies which includes Hawker Siddeley and EMI Electronics.

Based on the original design by the Institute of Oceanographic Sciences, this 25 ft diameter wave-riding buoy is the first large oceanographic and meteorological sensor-carrying buoy devised entirely in Britain.

The buoy will be moored off the Norfolk coast and data will be transmitted via a telemetry link to a shore station at Lowestoft.

The major function of the buoy is to provide a platform for development of advanced sensors to monitor both oceanographic and meteorological parameters. The buoy will also form Britain's contribution towards the proposed European network of data buoys.

## OXFORD STREET STORE

THE opening in April of a new store in London's Oxford Street marks the integration of two top names in the radio and hi-fi business G. W. Smith $\&$ Co. (Radio) Ltd. and Laskys Radio Limited who merged in 1972.

Claimed to be the first of its kind in Europe the store will cover 14.000 sq ft of selling area, have 70 specially trained staff and will be stocked with a wide range of consumer electronic products including audio, tape recorders, television receivers and associated video equipment, in-car equipment and calculators.

There will be fully equipped stereo and 4 -channel demonstration rooms and accessory bars where items such as replacement styli, cartridges and record cleaning equipment may be purchased. Other departments stock d.i.y, accessories and records, pre-recorded cassettes, and 8-track cartridges.

This showpiece store is to be the forerunner of a series of hi-fi centres around Britain.
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## ENVIRONMENT AND SPACE

Nearly seventeen years have passed since the first artificial satellite went into orbit and looking back through the literature that has now accumulated it is clear that much new knowledge is now on record. Side by side with space developments there has been much speculation as to how the techniques can be turned to the benefit of the human race.

Not least of these endeavours is the attempt to assess the Earth's resources. At the same time the doom mongers prophesy dire problems of over population, shortage of food supplies while at the same time and almost in the same breath they talk of the decimation of the population from pollution and other hazards.

Since the boundaries are now not merely the Earth but the Solar system it is necessary to readjust thinking of the environment. The Earth is but a part of a composite whole which includes near space, middle space and beyond the Solar system outer space. Much has been learned of the effects of the Sun upon the Earth and the time is ripe for a good look at what technology can do.

It is inevitable that certain areas come in for most attention. These are the areas where there appears to be a lack of the necessities of life. Yet most of this is because technology is not used to the best advantage, or that an intermediate technology is more appropriate to certain situations. Since all these things are a part of a whole, some
of the new thinking must revolve around the use of natural resources and the application of man's ability to readjust to a situation.

A good deal has been written on some aspects of space dangers but little of a constructive nature about adaptation. It is proposed that some of the effects of space exploration side effects should be examined. To do this, separate aspects of some of the present problems must be examined.

## ENERGY FROM SPACE

One of the activities which will naturally be to the fore is the harnessing of energy. It is within the parameters of Spacewatch to do this and also within the electronics field. It is fitting then to start with a method of utilising the power of the Sun, which for the human race is an almost limitless source ( 12,000 million years anyway), of energy.

Many years ago ideas were put forward, for the setting up in space of a vast reflector of sodium which would beam the rays of the Sun down to the Earth in a concentrated ray. Speculation was rife as to how this could be used by one power or another to burn up the cities of the enemy.

The first of a similar type of reflector to beam power derived from the Sun and transmit it to Earth, for peaceful use, has already been funded, see Fig. 1.

The National Aeronautics and Space Administration of the United States have awarded nearly 200,000 dollars to a consultancy working with three American firms. These firms specialise in space platform engineering, solar cells and microwave equipment.

Experiments have already been carried out in America with the transmission of power by microwaves. A microwave beam was used to keep a model helicopter aloft by this means. The new project study is now being set up in space in a geostationary position. The basic idea is to set up a collector for the solar energy. The power would be transferred to a converter by cable where microwaves would be generated.

On the same unit would be the control system which would enable an aerial to be kept facing the Earth and the collector facing the Sun. An aerial on the Earth covering an area six by six miles would collect the microwave energy for distribution. The effective amount of energy that would be available for distribution would be $10^{7}$ kilowatts.


Proposed space power station

This may well seem to be an extremely ambitious project but it is not beyond the present technology. The improvement in solar cell performance is enough to justify the feasibility study, and since the effects of the beam of microwave power would cause less disturbance than a direct beaming of solar energy it would seem to be a better choice.

The microwave beam. however. could raise certain difficulties. Apart from the fact that some precaution would be needed to avoid passing through the beam, space vehicles would need to avoid it (particularly space shuttles with passengers) and the fact that the ionosphere would have its temperature raised.

Some work has already been carried out from Aricebo in firing about a hundred kilowatts at one layer of the ionosphere and raising the temperature thereby. This was reported in Spacewatch some time ago. The effect of megawatts might well cause changes. No doubt the doom mongers will have much to work on here.

## EARTH BASED SOLAR POWER STATIONS

The method of utilising natural resources just described may be the extreme end of the scale of new endeavour but is nevertheless to be considered. A more immediately possible alternative is the utilisation of solar energy at ground level.

Later Spacerwath articles will deal with the practical systems in use at the present time and those projected for the future.


# By A. P. Stephenson 

This series, spocially written for the beginner, takes you step-bystep through transistor circuit design in a simple, nonmathematical way.

Design of a small signal ampliffer will be followed by a Class $B$ amplifier and the series will conclude with a constructional project so that your theoretical knowledge can be put into practice.

LAST MONTH some of the general principles of transistor circuit design were described. This month we will look more closely at the transistor itself and examine how the characteristics of this device influence carcuit design.

We shall also look at the main circuit components which influence voltage gain.

As mentioned in the previous part the principles to be discussed refer only to small signal amplifiers, power amplifiers requiring a different approach.

### 2.1. THE 0.6V VOLTAGE DROP - VBE

The transistor requires the base/emitter diode to be forward biased. In the case af silicon transistors the voltage drop across the junction will appear "locked" at about 0.6 V . Germanium "locks" at about 0.2 V .

This voltage is an intrinsic value and is predictable from a rather involved discussion on the physics of the $p n$ junction.

## EFFECTS OF TEMPERATURE

The figure is almost, but not quite, a constant because at normal ambient temperature (usually specified as 25 degrees C) the variation is typically berween 0.55 V and 0.72 V .

The effect as temperature increases is a reduction in this voltage according to the following scale:

## For every degree C rise, $V_{\mathrm{BE}}$ falls by 2 mV

For example, a transistor operating at 125 degrees C will have a $V_{\text {Be }}$ of about 200 mV less than normal, i.e. 0.4 V .

For the majority of design work, however, transistors are running at the 25 degrces C norm, the only exception being the larger power stages.

## Vee AND CIRCUIT DESIGN

It is very important for the amateur designer to fully appreciate the significance of the 0.6 V lock and to ram home this point the circuit in Fig. 2.1 may be used.

Large variations in VR1 will not have much effect on the voltage $V_{\mathrm{BE}}$. However, if VR1 is reduced to too low a value, the dreaded "thermal runaway" will soon cause the death of the transistor.

In fact, if you are measuring $V_{\mathrm{FE}}$ with a voltmeter and you notice a reading approaching, say, IV the transistor is either already dead or soon will be!

Fig. 2.1. Changes in VRf will have little effect on the base to emitter voltage


### 2.2. THE COLLEGTOR SATURATION VOLTAGE -Vge(sat)

It is easier to define collector saturation voltage by reference to Fig. 2.2. As the slider of VR1 is moved towards the IOV line, Ic increases and the collector voltage falls because of the voltage drop across $R \mathrm{c}$.

At some point on the slider the output voltage stops falling, becoming locked at a certain lower limit. At this limit the collector current can rise no further, in spite of rises in the slider.

## DEFINITION

The collector saturation voltage, $\gamma_{\text {CEisat }}$ may be defined as follews:
The collector becomes "saturated" or "bettemed" when the collector current has reached its maximum possible value because its vollage has reached its minimum possible value.

> This minimum voltage to which the collector can fall is called the collector saturation voltage - VCE(sat).

A typical figure to indicate the order of magnitude would be 0.5 V . The actual value for $V_{\text {CE(sat) }}$ is dependent on the value of $I \mathrm{c}$ at this voltage which in turn is clearly dependent on $R_{\mathrm{s}}$.

## MANUFACTURERS' FIGURES

It is inconvenient for a transistor manufacturer to give $V_{\mathrm{C}(\text { Isui }}$ values in terms of $R_{\mathrm{c}}$, because the supply voltage would also have to be quoted. Instead the $V$ czassal) figures are quoted for a given Ic.
For example in the case of the ECLO8:
$V_{\text {CE(sst }}=90 \mathrm{mV}$ for $I_{\mathrm{c}}$ at 10 mA measured but $V_{\text {CEIssit }}=200 \mathrm{mV}$ for $I_{\mathrm{C}}$ at $200 \mathrm{~mA} 4 \begin{aligned} & \text { with } \\ & I_{\mathrm{C}} / I_{\mathrm{B}}=20\end{aligned}$
Note that $V_{\text {CE(sat) }}$ rises with larger collector currents, which may be taken as a general rule. It also rises with increase in temperature.

Fig. 2.2. As the slicer of VR1 is moved upwards, VEETalls to minimum of VCE(eat)


### 2.3. THE PARAMETER JUNGLE

The academic way of explaining and measuring what goes on inside a transistor is to use a sel of "network parameters" which are supplied by the manufacturer for each type of transistor. A few years ago a system called "T-network parameters" were in use.

## T-NETWORK PARAMETERS

These parameters consisted of strange little resistances which were alleged to inhabit the corridors of the crystal lattice and given names such as $r_{e}, r_{0}, r_{c}$ which stood for the emitter resistance. base resistance and collector resistance respectively.
In addition to these little resistances there was a ratio called "alpha" (symbol xi which was a current gain desperately trying to equal unity but never quite making it.

## H-PARAMETERS

T-parameters gradually became unfashionable and gave way to a modernised set known as "h-para-
meters", the term " $h$ " meaning hybrid. There were originally four in the set: $i_{11}$, the input resistance: $h_{21}$-the current gain; $h_{12}$-the voltage feedback ratio; and $h_{22}$-the output admittance. It is not difficult to see why they are called hybrid!

A breakaway group was formed causing a change in symbolism. The general symbol $h_{12}$ tecame $h_{\text {ie }}$, $h_{h_{i t}}, h_{\text {ic }}$ according to whether the transistor is used with grounded emitter, base or collector.
Similarly $h_{21}$ became $h_{\mathrm{fa}}, h_{\mathrm{rb}}$ or $h_{\mathrm{fe}} ; h_{12}$ became $h_{\mathrm{re}}$, $h_{\mathrm{tb}}$ or $h_{\mathrm{rc}} ; h_{22}$ became $h_{\text {se, }} h_{\text {mb }}$ or $h_{\text {oc }}$.
To reassure the amateur designer, we shall try to avoid any further reference to any of them with the exception of $h_{\mathrm{t}}$ which is the most generally useful of all h -parameters and $r_{e}$ which is still a most useful term because it can so easily be calculated from the collector current.

The design of any circuit, within the capabilities or the amateur, can be carried out with those two alone the rest of the tribe will be left to the academic types.

### 2.4. THE RATIOS $h_{\text {F }}$ AND $h_{\text {fo }}$

The ratio $h_{\mathrm{FE}}$ is one of the important pieces of information given in the manufacturers' literature. Its full title is "large signal forward current transier ratio in grounded emitter configuration" which is a bit of a mouthful and is simply collector curreat divided by base current thus:

$$
h_{\mathrm{FE}}=\frac{I_{\mathrm{C}}}{I_{\mathrm{B}}}
$$

It is useful in setting up the d.c. bias conditions because for a given collector current and $h_{\text {FE }}$ the required base current can be found.

## RANGE OF VARIATION

Unfortunately it is not a very reliable constant and shoulc not be taken at its face value. This is net to say that manufacturers issue false information, in fact ihey always stress that their figures for $h_{\text {Fe }}$ are typical
and often give minimum and maximum expected values. The production line spreads however are very large as carn be seen from the example of the BCl 08 where $h_{\mathrm{FE}}=125$ to 5010 which means that picking one of these at random the hec could be anything within this range.

In addition to the production line spreads there is also another variation which is an added annoyance for the designer. The mean collector current also affects $h_{\mathrm{re}}$.

This means that manufacturers always specify the mean collector current when stating $h_{\mathrm{r}} \mathrm{E}$. In the
above example for the BCl 08 the collector current was taken as 2 mA .
In general the $h_{\text {re }}$ increases as mean collector carrent increases up to a certain upper limit and then tends to fall again

## THE RATIO hte

The ratio $h_{\text {fe }}$ is similar to $h_{\text {fe }}$ but is strictly a small signal ratio. All the remarks above appiy except for this difference. The values given for $h_{\text {te }}$ and $h_{\text {FE }}$ are usually atout the same anyway.

### 2.5. THE 25/Jc EQUATION

The base-emitter junction in a transistor must be forward biased in order to operate the device as an amplifier.

Since forward bias causes the junction to pass current it is a relevant question to ask what is the resistance across the base-e mitter junction when it is conducting.

## THE INTERNAL EMITTER RESISTANCE

The answer is not straightforward, because most of the emitter current ignores the base altogether and flows upwards to the collector.

It is not altogether surprising therefore to learn that the value for $r_{e}$ is not a constant but depends on the collector current 'c.

In fact the formula is complea involving logs to the base e, Bolteman's constant and various other unpleasant things including the absolute temperature. Fortunately, for most practical purposes at room temperature the formula can te reduced to a very simple form:

$$
r_{\mathrm{e}}=\frac{25}{I_{\mathrm{C}}} \quad \begin{aligned}
& \text { where } I_{\mathrm{c}} \text { is in milliamps, } \\
& \text { and } r_{\mathrm{e}} \text { in ohms. }
\end{aligned}
$$

$$
\text { Fer example } \begin{aligned}
I_{\mathrm{C}} & =1 \mathrm{~mA}, r_{\mathrm{e}}=25 \Omega \\
I_{\mathrm{C}} & =10 \mathrm{~mA}, r_{\mathrm{e}}=2.50 \\
I_{\mathrm{C}} & =0.1 \mathrm{~mA}, r_{\mathrm{e}}=250 \Omega .
\end{aligned}
$$

## DYNAMIC RESISTANCE

Note that one should not make the mistake of thinking that $r_{\mathrm{e}}$ is a simple resistance which can be measured with multimeter set to ohms. It is a "dynamic small signal value" which only comes to life when the transistor is passing collector current.

From the designer's viewpeint, the equation is very useful because it shows how to make $t_{e}$ any desired value by suitable choice of collector current.

However, it is not $r_{e}$ itself which is important but the fact that it appears in equations for voltage gain and input resistance.

### 2.6. THE LOAD WHICH THE SIGNAL SEES

For a grounded emitter amplifier circuit the signal is applied betweer the base and ground (see Fig. 2.3).
The input resistance, $r_{\mathrm{in}}$, is the load which the signal sees. Superficially this would appear to be simply $r_{\mathbf{e}}$. However, it musi te remembered that only a small fraction of the total emitter current is supplied by the base, the actual fraction being $1 / h_{\text {te }}$. The apparent input resistance, as far as the signal is concerned is thereiore much higher than $r_{\mathrm{e}}$, in fact the equation is

$$
r_{\mathrm{IN}}=h_{\mathrm{e}} \times r_{\mathrm{e}}
$$

For example if the collector current is 1 mA , $r_{e}=25 \Omega$ which makes $r_{\text {IN }}=$ hife $\times 25$. If the transistor has an $h_{\text {re }}$ of 100 , the input resistance is $2 \cdot 5 \mathrm{k} \Omega$.

It is unusual in practice to ground the emitter directly. For stability purposes, an external emitter resistor $R_{\mathbf{E}}$ is commonly employed, see Fig. 2.4

Since $r_{0}$ and $R_{E}$ are in series across the signal, the modified equation for $r_{1 N}$ is

$$
r_{\mathbf{I N}}=h_{\mathrm{re}}\left(r_{\mathrm{e}}+R_{\mathbf{E}}\right)
$$

Fig. 2.4. The addition of RE increases the load


### 2.7. THE STAGE INPUT RESISTANGE

Although the equation $r_{\mathrm{IN}}=h_{\mathrm{f}}\left(r_{\mathrm{e}}+R_{\mathrm{E}}\right)$ will describe the load presented to a signal by the transistor, this is not the complete story.

Before an amplifier can amplify, the base must be supplied with forward bias, which means extra resistive networks. These extra resistors are a nuisance because they form an additional load on the signal.

For example the forward bias is often provided by a voltage divider chain R1, R2 as shown in Fig. 2.5 .

The problem now is to obtain an overall equation for the total resistive load on the signal, in other words the stage input resistance $R_{\text {in }}$.

## SERIES OR PARALLEL?

It appears fairly obvious that $r_{I N}$ and $R 2$ are in parallel but the position of R 1 is not quite so easy. Is it in series with R2 or in parallel?

From the d.c. bias viewpoint there is no doubt that R 1 is in series with R 2 .
From the signal viewpoint, however, $R \|$ is in parallel with R2 because of the large smoothing capacitor Cs (shown dotted in the diagram because it is part of the power supply). The capacitor is a short circuit as far as varying signals are concerned, which makes the supply rail a ground line.

The input circuit which the signal finally sees is shown in Fig. 2.6. From this it is clear that
$R_{\text {IN }}=R_{1}, R_{2}$ and $r_{\text {IN }}$ in parailel


Fig. 2.5. The divider chain resistors add to the load which the siginal sees


Fig. 2.6. The stage inpút resistance $R_{\text {IN }}$ is effectively $R_{1}, R_{z}$ and $r_{1 N}$ in parallel

### 2.8. VOLTACE GAIN EQUATIONS

The voltage gain of a grounded emitter stage is the ratio of the output voltage swing (measured between collector and ground) to input voltage swing (applied between base and ground).

The symbol " $A$ " will in future be used to represent this ratio thus

$$
A=\frac{V_{\text {out }}}{V_{\mathrm{in}}}
$$

The strictly relevant components which decide this gain are shown in the skeletal diagram of Fig. 2.7.

An equation for finding the voltage gain which has the merits of simplicity and reasonable accuracy is the following:

$$
A=\frac{R_{\mathrm{C}}}{r_{\mathrm{e}}+R_{\mathrm{E}}}
$$

Although this equation is true as far as the amateur design is. concerned there are times when an even simpler pair of equations are good enough.

If. $R_{\mathrm{E}}$ is chosen to be very much larger than $r_{e}$, then the following equation is allowable:

$$
A=\frac{R_{\mathrm{C}}}{R_{\mathrm{E}}}
$$



Fig. 2.7. Skeletal diagram showing components which control voltage gain

If $R_{E}$ is not there at all, either short circuited or bypassed the equation reduces to

$$
A=\frac{R_{\mathrm{C}}}{r_{\mathrm{e}}}
$$



A selection of readers' suggested circuits. It should be emphasised that these designs have not been proven by us. They will at any rate stimulate further thought.
This is YOUR page and any idea published will be awarded payment according to its merits.

## UNIVERSAL FLASHER PANEL

ORiginally designed for Christmas-tree illumination. the unit here described provides a method of turning each of four lamps or relays on and off in continuous sequence. But the unit has a surprising variety of other applications.

Using cheaply priced semiconductors, the circuit shown in Fig. 1 combines a clock-pulse generator: a divide-by-four counter and a decoder which drives the lamps. With the value of Cl given, the "on"
put will be low. But if either one or both inputs is low, then the output will be high.

In order to interpret the bistables' four states, each pair of inputs is arranged to go high in turn so that the four outputs go from high to low and back again in sequence to give the count. Whichever output is low then produces base current through its association AC128 thereby turning on a lamp.

Fast rise time clock pulses for driving the SN7474 are generated by a type of oscillator developed by the author.

Briefly, the circuit operation is that C1 charges via R1 and the base/emitter junction of TR1 thereby turning TRI hard on and TR2 off. But when the initially high charging current decreases, the $V_{\text {ce }}$ of TR1 rises and begins turning on TR2.

The capacitor then begins discharging via the path TR2, VR1, R2, but in so doing, develops a positive feedback voltage across VR1, R2 which completely turns off TR1 in a rapid, cumulative action. But a negative feedback voltage developed across R4 while TR2 is conducting eventually becomes greater than the falling positive feedback and re-initiates the cycle by turning TR1 on again in another cumulative process.

VR1 controls the repetition rate and R2 is simply a value greater than the minimum necessary to sustain oscillation.

Ideally, the supply voltage should be +5 V but for children's toys, battery operation is obviously desirable. The unit will operate from either a $4 \frac{1}{2} \mathrm{~V}$ or a 6 V battery but the supply is best kept below 7 V .


Fig. 1. Complete circuit diagram for the universal flasher unit. The arrangement for connecting relays in the circuit is shown dotted
time of each lamp is variable from about $0 \cdot 2 \mathrm{sec}$ to 0.8 sec but increasing the value of Cl allows this time to be extended to minutes.

The divide-by-four function is accomplished by serial connection of the two edge-triggered bistable circuits contained within the SN7474.

The outputs of the two bistables will continuously sequence through all of four possible states so long as clock pulses are applied.

The logic of any NAND gate in the SN7400 used for decoding, is that if both inputs are high, the out-

Where long repetition times are required, VR1 may be replaced with a $25 \mathrm{k} \Omega$ pot. This will allow smaller values of Cl electrolytics to be used at some loss of time consistency.

If relays are employed (shown dotted) an OA202 or similar diode should be connected across each relay coil.

N. Naughton,<br>Moston,<br>Manchester.



An introduction to a new technology

LIQUID crystal materials have been known for some 80 years. However, it is only in recent years that their special property of turning a clear liquid into a milky liquid under the influence of an applied electric field has been exploited to produce information displays.

These displays are basically a sandwich of liquid crystal material between two sheets of glass. The exceptionally low power requirements of these displays gives them a considerable advantage where power is at a premium. The purpose of this article is to introduce the reader to this new and interesting technology.

## WHAT ARE LIQUID CRYSTALS?

Liquid crystals are materials which over some part of their temperature range have properties intermediate between a solid and a liquid state. This special state is known as the mesophase or anisotropic liquid state.

| solid phase | liquid phase |  |  |
| :--- | :---: | :---: | :---: |
| solid crystalline <br> state | anisotropic liquid <br> state <br> ('Mesophase'") | isotropic liquid <br> state |  |
| Melting Point | Clearing Point | Temperature |  |

Fig. 1. Showing what happens to a liquid crystal material as its temperature is increased


Anisotropic materials have a rather peculiar property in that they exhibit different refractive indices to light which passes through them from different directions. By comparison a normal liquid is isotropic which means that it has no special optical properties. Light behaves the same no matter from what direction it passes through the liquid.

The anisotropic property of liquid crystal materials is the feature which is exploited to produce the change from a clear liquid to a milky liquid on the application of a voltage.

Fig. I shows diagrammatically what happens to a liquid crystal material as its temperature is increased.

The material is solid at low temperatures. If the temperature is then slowly increased its melting point will be reached after which it enters the mesophase state. Further increase in temperature brings the liquid to its clearing point when it loses its special properties and behaves like an ordinary liquid.

If the temperature is now reduced the material will pass back through the same states until it is again a solid material. In other words the temperature effect is reversible.

The materials of particular interest are those which exhibit a mesophase range which covers our normal range of ambient temperatures.

## LIQUID CRYSTAL MATERIALS

There are three main types of liquid crystal material. In all cases the molecules have an elongated form and can best be considered as microscopic greasy transparent sausages. Fig. 2 shows diagrammatically the three types of material.
(a) Smectic type. In this type of material the molecules exist in discrete parallel layers.
(b) Nematic type. The molecules all have their long axes parallel to each other but are free to move in any axial direction.
(c) Cholesteric type. Adjacent molecules appear to be similar to the nematic type but if the material is examined over a greater distance it will be found that a continuous twist is superimposed on the parallel arrangement.

Fig. 2. Three main types of liquid crystal

[^3]The materials in current practical use are organic compounds of the nematic type.

## DESCRIPTION OF BASIC DISPLAY

Fig. 3 shows a simple cell consisting of two glass plates separated by insulating spacers which also serve to retain the liquid in the sandwich. The inside surfaces of the glass are coated with transparent conducting electrodes. The Siemens AF25250 is an example of such a simple cell.


Fig. 3. Simple liquid crystal cell in section
Typical dimensions for a working cell would use glass 2 or 3 mm thick with an extremely thin conducting layer on its surface. The thickness of the liquid crystal material in the sandwich between the two glass plates will be in the range of about 6 to $25 \mu \mathrm{~m}$ depending upon the application.

At this stage another property of the liquid crystal molecules which make up the nematic liquid crystal material must be introduced. This is that the individual molecules in the cell also have electrical dipole axes which are at 90 degrees to the molecule axis. This is shown diagrammatically in Fig. 4.


Fig. 4. Molecule and dipole axes

Fig. 5. What happens in the cell when a voltage is applied


We now have to consider what happens when a potential is applied across a cell. Fig 5 is a simplified representation of what in reality is a very complex interaction of conflicting forces in the cell.

Because the individual molecules are all individual dipoles when a potential is applied the molecules are turned at right angles to the field so that their dipoles lie in line with the field. However, things are not as simple as this and free ions present in
the liquid start to migrate to the electrodes with the result that in the liquid they locally neutralise the electrical field set up by the electrodes. This then allows local groups of molecules to return to their perpendicular state.
This flow of ions and the resulting turbulence in the liquid is what gives us the practical applications of liquid crystal materials.

## REFRACTION

Now it will be remembered that earlier on we referred to the property of liquid crystal materials known as the anisotropic liquid state. In this condition the liquid has different refractive indices depending on the direction in which light passes through it.

A beam of light passing through the turbulent liquid in the cell of Fig 3 encounters local differences in refractive index because of the different orientations of the different groups of molecules and hence is randomly scattered. This scattering of the light beam leads to the milky or "ground glass" appearance of the material when a potential is applied to it.


Fig. 6. Two types of display. From the left: transmissive and reflective

## TYPES OF DISPLAY

There are two basic types of display. Those that operate by transmitted light and those which operate by reflected light.

Fig. 6 left shows a cell designed for use by transmitted light. It is shown with two separate conducting sections. One without voltage applied which allows the light to pass straight through it without scattering. The end of the cell which has been activated by the application of a voltage causes the light entering it to be scattered.

Similarly Fig. 6 right shows the operation of a cell designed to operate by reflected light. These cells have integral mirrors which are preferably designed so that they do not give direct reflections of objects and light sources in front of them. The reflective type of display is particularly attractive for use in battery powered equipment as no internal light source is required.

## TYPICAL DISPLAYS

Fig. 7 shows a typical seven bar display which can be a single unit on a panel such as the Siemens AN1301 or a unit in a row of units on one panel such as the Siemens AN4131. A particular digit is displayed by applying a potential between the required bars on one glass plate (usually the front) and the other glass plate which will normally be a common electrode to all the bars.


Fig. 7. Typical seven bar digit

A potential of about 0.5 V per $\mu \mathrm{m}$ is required to initiate the onset of the milky effect in the liquid crystal material. The cloudiness becomes more intense and reaches a saturation level at about 5 V per $\mu \mathrm{m}$. Fig. 8 shows how the milkiness or contrast ratio increases with applied voltage for a typical display.
The electrical power consumed by typical displays is only in the order of $100 \mu \mathrm{~W} / \mathrm{cm}^{2}$ of character area. To quote a practical example the AN4131 display referred to above will take about $2 \mu \mathrm{~A}$ per bar of the display at an operating voltage of 25 volts peak ( 50 volts peak to peak).


Fig. 8. Effect of voltage on contrast ratio

## TYPICAL APPLICATIONS

Typical applications for liquid crystal displays are:

1. Portable equipment where low power consumption is essential.
2. For information display in situations requiring low power, intrinsically safe circuits (circuits in which voltages and currents are limited to values below which they are capable of producing an explosion initiating spark).
3. Aircraft, car and similar situations in which varying light conditions make other forms of display unsuitable. A major advantage of liquid crystal displays over other forms of display is that when using either transmissive or reflective displays an external light source however bright will not mask the display. This is because the display works by scattering light and a bright external source of light such as sunlight will automatically give a bright display.
4. Coloured displays are possible by use of coloured light sources or coloured filters.
5. Direct interface with mos and cos/mos integrated circuits.
6. Digital instruments, cash registers, calculators clocks.
7. Analogue displays such as thermometer strips in which successive elements are activated. These have applications in temperature instruments, speedometers and tuning dials.


Fig. 9. A typical viewing arrangement for viewing a transmission display

A typical arrangement for viewing a transmission display is shown in Fig. 9. In this it will be noted that the light source is located outside the field of view of the user and that a matt black background is used.

## METHOD OF OPERATING DISPLAYS

A brief mention must be made here about the way in which the displays are operated. In the examples given earlier d.c. circuits were implied or shown for simplicity of explanation.

In practice crystal displays of the types considered here should always be operated with a.c. as d.c. across the cells will polarise them and seriously shorten their operational lives.

To run the displays on a.c. a sinusoidal supply as such is not essential. It is only necessary for the display to "see" a.c. This is usually arranged to be a square wave a.c. as this is both easy to produce and gives the most efficient form of drive.


ADVances in the design of battery chargers have been minimal since their introduction many years ago. Modern components have helped to reduce the size and to increase the reliability but otherwise no significant improvements have been made.

The design presented here seeks to bring the humble charger up to date.

## CONVENTIONAL CHARGER

First consider the conventional battery charger of Fig. 1. A more basic circuit could not be imagined. However, there are a number of drawbacks.
For example, if the output leads should be wrongly connected to a battery to be charged, that is changed over positive for negative, a heavy short circuit current will flow. This can occur even with the charger switched off if there is no switch in the output lines as here.


Fig. 1. A conventional battery charger circuit


Fig. 2. Circuit of the basic concept used in the new battery charger

Although a fuse, if included, can give a measure of protection, replacement of this item is tedious and some damage can often occur due to heavy surge currents flowing before the fuse blows.

If the output leads of the charger become connected together while the unit is switched on, then once again a short circuit current will flow, and previous remarks concerning fuse protection apply here also.

Lastly, a simple charger will carry on charging after the battery is fully charged, leading to excessive gassing and perhaps battery damage.

## A FRESH LOOK

The charger design presented here incorporates reverse connection and output short circuit protection, and cuts the charging rate down heavily when the battery reaches its fully charged potential.
In this way an unskilled person can connect up the charger to the battery without danger and it will not matter if, for example, the charger is inadvertently left connected all night when perhaps only a few hours charge was required.

## THE SYSTEM

The block diagram of Fig. 2 shows the new charger in principle whilst Fig. 3 shows the full circuit diagram.
In Fig. 2 a switch S1 is operated in time with the output of an oscillator which is itself controlled by the voltage at the tap on a potential divider connected across the battery to be charged. Power to the oscillator is fed via a diode. The switch, when closed, connects the output of the transformer secondary, via a rectifier, to the battery under charge.
Until a battery is connected with the correct polarity to the output terminals, the oscillator will not run and the switch will remain open so that no charging takes place. Charging starts when a partially discharged battery is correctly connected to the output terminals, and stops when the correct battery voltage is reached by switching off the oscillator.

In fact, two silicon controlled rectifiers CSR1 and 2 are used for the switch, and these, together with two diodes D1 and D2 also form a bridge rectifier as in Fig. 3.

With the mains applied but no battery connected, CSR1 and 2 are unable to conduct as long as no triggering voltage is fed to their gates. Without $\operatorname{CSR} 1$ and 2 conducting, the remainder of the circuit is unable to provide any triggering potentials.

## BATTERY CONNECTION

This apparent stalemate is circumvented by introducing a battery at the output terminals.
A partially discharged battery of correctly connected polarity is required to bring the unit to life. Whilst it might be thought that this is a drawback as a completely flat battery can apparently not be re-charged with this charger, a completely flat battery occurs very rarely indeed. Even if a vehicle's lights have been accidentally left switched on for a day or so, so that there is apparently no output from the battery, removing the load and allowing a period for the battery to recover usually results in there being sufficient output to switch the charger on in the way to be described.

In addition, a simple modification will be described which enables conventional charging to be done.

## CIRCUIT OPERATION

As soon as positive battery volts appear at the supply rail, diode D6 conducts. If the battery is connected reversed then D6 does not conduct and the unit remains dormant. With D6 conducting, C3 charges through R7 and within a few milliseconds the voltage at TR3 emitter exceeds the voltage at the slider of VR1.

Both TR3 and TR4 then conduct very heavily, discharging C3 through R10 thus giving a short positive pulse at the bases of TR2 and TR1. These
latter are connected as emitter-followers and so convert the pulses to a suitable low impedance for driving CSR1 and CSR2 gates. They also provide isolation between the gates.

A positive pulse at a CSR gate converts it to a conducting state. Thus, the bridge network, consisting of D1, D2, CSR1 and CSR2 becomes fully conducting and current passes to the battery for the remainder of the mains half-cycle.

As soon as C3 discharges, it starts to recharge again through R7 and soon another output pulse is generated. This has no effect whilst the CSRs are already conducting, but at the end of each mains half-cycle they revert to a blocking state as the supply voltage is passing through zero. Consequently, fresh pulses are required early in each mains half-cycle and these the unit generates at a rate of about 1.4 kHz , so keeping the CSRs conducting as required.

Eventually with rising battery voltage the voltage at TR3 emitter balances that at VR1 slider because of the action of the Zener diode D5 which limits the voltage to which C3 charges to 10 V .

Thus TR3 and TR4 do not conduct, no pulse output is generated and the two CSRs are not triggered into condition. Charging ceases at a voltage determined by the setting of VR1.

As soon as charging stops the battery voltage will momentarily fall and charging will re-start only to be stopped again by the mechanism described. This leads to the charger giving a trickle charge of less than 1A, which causes no overcharging while at the same time ensuring that the battery is maintained in a fully charged condition.

The two resistors R1 and R2 provide an external d.c. connection from CSR gate to cathode to ensure reliable triggering. Diodes D3 and D4 prevent any spurious negative pulses from being applied to the gates which could otherwise lead to device destruction.




Fig. 4. Veroboard and component layout for the battery charger

The lamp LP2 lights whenever a battery is con ${ }^{2}$ nected with correct polarity. If the battery is connected in reverse the diode D7 is reverse biased and no current flows. In the prototype, a 6 V lamp of $0 \cdot 1 \mathrm{~A}$ rating was employed, together with an appropriate resistor acting as a voltage dropper. This mode of operation is preferable to the use of a 12 V lamp connected directly since a nominal 12 V battery on charge can reach over 14 V and this could lead to shortened lamp life. Here, the value of R11 is chosen to apply no more than 6 V to the lamp with a 14 V supply.

An ammeter ME1, scaled $0-5 \mathrm{~A}$, is connected in series with the positive output. The use of this is really optional. In the prototype, the cheapest to be found was employed for the meter is at best required only to give an approximate indication of the level of charging current, and to show that the unit really is charging.

## CONSTRUCTION

Constructional details follow a conventional pattern. A suitable housing for the charger is a metal case (see parts list) measuring $6 \frac{1}{2} \mathrm{in} . \times 5 \mathrm{in} . \times 4 \frac{1}{2} \mathrm{in}$. If readers prefer to employ an alternative, then it is suggested that this should be no smaller than that used here to avoid possible cramping and heating problems.

## PRECAUTIONS

Make certain that all mounting holes for diodes and CSRs are free of burrs which could puncture any mica insulation used when mounting to heat sinks.


Photograph showing the layout of the components within the case

In the prototype the diodes were mounted using p.t.f.e. washers and two mica washers supplied, and each CSR was mounted on its own individual heat sink. These were insulated, using one mica washer and two insulating bushes on each CSR.

If available, a light smear of silicone grease should be applied to either side of each mica washer before tightening up. This ensures better thermal contact.

Before proceeding further use an ohmmeter to check that there is no electrical contact between heat sink and any part of the diodes and CSRs.
When the appropriate components have been located on the Veroboard layout of Fig. 4, this latter should be mounted on the diode heat sink and both large sinks are then fixed down to the case bottom, again using silicone grease at metal-to-metal joints for best thermal contact.

All wires that are to carry output current must be of at least 5A rating. Wires to the Veroboard and the indicator lamp can be lighter gauge if found convenient.

## TESTING

When wiring up and assembly are finished, testing can be carried out. Two precautions are worth taking at this stage. An "automobile ", type fuseholder fitted with a 5A fuse should be included in one of the leads to the battery to guard against accidental short circuits of the battery when the leads are disconnected from the charger. It is probably worth leaving this fuse in circuit during later use.

The second precaution need be included only during initial testing. Quite simply-it consists of a 12 V bulb of 2 A or similar rating, connected temporarily in one output lead. Thus the greatest current that can flow in any circumstances, even with faults present will be the current appropriate to the bulb selected.

Connect a 12 V car battery to the charger observing polarity. The indicator lamp should light. Now switch on the mains supply to the charger and advance the setting of VR1. A charge, at a rate depending on the actual transformer employed, the state of the battery and the setting of VR1, will be indicated.

Back off the setting of VR1. The charge rate will fall, and further slight rotation will cause it to cease altogether. The actual setting of VR1 must be found by experiment. If a fully charged battery is to hand, set VR1 so that a small, trickle charge flows. Otherwise, set VR1 to give a charge in the range 2 A to 4 A and monitor the rate over a period of a few hours. When the battery is gassing freely, re-set VR1, again to give a trickle charge.

Some additional slight re-adjustment of VR1 setting may be called for, but experience will soon dictate this.

After some hours of supplying an output of 2A or more, the heat sinks will be warm to the touch and so too will the bottom of the case to which they are bolted. Accordingly, ensure that nothing obstructs the free flow of air through the holes in the rear panel.


The rectifier diodes and CSRs employed in the prototype were obtained as " $50 \mathrm{~V}, 3 \mathrm{~A}$ " devices. Alternatives of similar rating may have different physcal characteristics, e.g. the CSRs may be stud mounting, but will probably prove to be suitable since the circuit shown is very tolerant of component variations.

It is suggested that all major components be obtained before drilling any holes.

The Zener diode D5 specified is rated at 10 V but a 12 V type was also found to function satisfactorily, although resulting in a different setting of VRI. It was noted that R 7 could be any value from about $33 \mathrm{k} \Omega$ to $100 \mathrm{k} \Omega$ ? and that a C3 of twice the value, i.e. $0 \cdot 02 \mu \mathrm{~F}$ functioned adequately.

The use of such variations is left to constructors of an experimental turn of mind; others should employ the values given.


Fig. 5. Alternative wiring to compensate for a completely flat battery

## THE FLAT BATTERY

The possibility of a completely flat battery being unable to activate the charger's circuitry was mentioned earlier.

Accordingly, the modification shown in Fig. 5 is suggested. The resistors R12 and R13 provide sufficient gate current to the CSRs to cause them to conduct without the presence of gating pulses. Consequently, charging takes place regardless of the state of the battery and the setting of VR1, when the switch S2 is closed.
The switch S2 should be opened to return the charger to normal operation as soon as possible.
Constructors may find that the initial charging period includes a time during which the charge-rate fluctuates fairly violently. Where this has happened it has died out after a short time and in any case appears to be a function of the state of charge of the battery and also is physical condition.


A selection of readers' suggested circuits. It should be emphasised that these designs have not been proven by us. They will at any rate stimulate further thought
This is YOUR page and any idea published will be awarded payment according to its merits.

## VOLTAGE CONTROLLED SELECTIVE AMPLIFIER

THE unit shown in Fig. I was constructed with the P.E. Synthesiser in mind. It provides a versatile Wha-Wha effect in conjunction with the synthesiser and keyboard.

The filter can be described as bandpass, suitably modified for voltage control. If used in conjunction with the envelope shaper some unusual effects can be obtained.

VR3, 1M12, is a gain (bandwidth) control and the user will discover that this provides interesting results in manipulation. VR2 is a skeleton preset and to set this up VRI should be set to maximum whilst listening to the filter output with either headphones or an amplifier. VR2 is adjusted so that the output whistle is at its highest frequency. and then is backed off slightly.
N. Campbell,

Lee Park, Liverpool.


Fig. 1. Circuit diagram for the Waa Waa effects unit. The i.c. pin connections are shown on the right



## MICROWAVES

Now when it comes to microwave technology this really is one of the black arts. All that waveguide plumbing and strip-line circuitry, the exotic vacuum glassware, the strangely named solid state devices, the newest associated developments such as surface acoustic waves, the exciting application such as satellite communications, the secretive ones such as electronic warfare, not to mention culinary ones such as microwave ovens, are now adding up to a sizeable industry though as yet fragmented. On a world scale the business in microwave parts of complete systems is running at about $£ 1,000$ million and employs some 50,000 qualified engineers.

But again there are signs of greater togetherness emerging from international conventions and exhibitions. The world's biggest ever microwave get-together is at Montreux next September and while in Switzerland recently, I had the opportunity of a long chat at the Federal Polytechnic, Lausanne, with Professor Freddy Gardiol who is Chairman of the "Microwave '74"' Conference which, although the fourth in a European series, is the first one to have a full-scale commercial exhibition grafted on to it.

The new innovation has caused some controversy in academic circles but Gardiol, now an academic himself but with plenty of industrial experience in the United States (during which he got a master's degree from the famous M.I.T.) is unrepentant. He believes in the highest level of University/ Industry interface and full-blooded co-operation.

As chairman of the conference he guides the programme selection
committee and it is clear that "Microwave '74", while in no way neglecting the more academic aspects of the technology, will have a fine selection of down-toearth practical applications papers plus a potent hardware exhibition where delegates can see and handle the latest developments as well as merely talking about them.
Sitting in Gardiol's office a hundred metres from the shore of Lake Geneva I could see, looking out of the window into the grounds of the Polytechnic, a beautifully constructed test track for linear motors on which the Electrical Department is conducting much research. Gardiol, as well as teaching, leads a microwave research team investigating loaded waveguides, microstrip circuits and industrial applications of microwave technology. His team of seven workers come from five different countries in three continents, an example to us all in our world of conflict. He is also a conservationist, being a member of the Swiss League for the Protection of Nature and of the World Wildife Fund.

## SET-BACK

Britain's Skynet II military comsat didn't get into earth orbit properly, let alone arrive at its designated position in outer space. There was a fault in the second stage firing of the Delta launcher from the Eastern Test Range at Cape Canaveral.

It was really tough luck for the dedicated team of Marconi men who built the satellite under a Ministry of Defence contract at Marconi Space and Defence Systems Ltd. works at Portsmouth. I have seen such satellites being built and tested. No human child has more loving care, no parents are more proud of their progeny, and no nursery is so immaculately clean as the air-filtered final assembly bay.

But there was a crumb of comfort for the designers and craftsmen involved. After six days of being "lost", a tracking station in the Pacific picked up strong telemetry signals which showed that the Skynet on-board systems were still functioning despite the 60G forces and overheating imposed by the faulty launch.

The satellite was now in a severely elliptical orbit, only 65 miles from earth at perigee. An attempt to fire the on-board apogee boost motor to get the satellite well clear of the earth's atmosphere failed because although the motor fired successfully the attitude of the spacecraft was wrong and it descended even lower and is presumed burnt up in the atmosphere.

The programme is now delayed some months but a second flight model is nearing completion and will be launched, one hopes with better success, later in the vear. Nevertheless, the failure of the launch vehicle was a bitter blow to all concerned.

But off-setting the bad news of Skynet II, Marconi Space and Defence Systems has won important new contracts for radar "blindfire" trackers for the British Rapier surface-to-air missile. Rapier, produced by British Aircraft Corporation, is a huge success story with over $£ 200$ million worth of orders, half of this for export. Sharing in the bonanza for electronic sub-contracts are Decca Radar (primary radar and command link) and Cossor Electronics (IFF system).
Why big exports of weapons systems are desirable (though in many people's eyes deplorable) is the extremely high export value for a very low import content, even more important with today's high commodity values for Britain's balance of payments.

## AEROSPACE STILL TOPS

Rapier, of course, is still only one product in a whole range of British aerospace projects which are doing well. Redifon has taken £9 million of orders for flight simulators from Japan, China and Singapore alone within the past twelve months.

Plessey has $£ 7$ million of orders in hand for navigation aids and has formed a completely new business for this sector under the management of Stan Kyte, an old hand in Plessey Radar who joined the company from Decca in 1965. One of Kyte's new activities in Plessey Navaids will be selling airfield electrical and electronic packages which average $£ 4$ million a shot. Nice work if you can get it!

Then there's the Multi-Role Combat Aircraft (MRCA) with nine prototypes readying for flight test. This huge multinational project with its on-board electronics complexes and ground support equipment will keep many a factory busy in the years ahead.

## that three day week

My own researches last January and February showed the electronics industry as ingenious in keeping production going as in the design and execution of its products.
I can report practically no loss of production and where some people were laid off it was shortage of components, not of power, that did the damage.

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## Phantom Photos

Another off-beat subject which just fringes on ESP. in my opinion. concerns the existence of an "aura" around living things. The subject has been in dispute for centuries. and until comparatively recently only clairvoyant persons could be depended upon to detect it. Not any more, apparently.

In the 1950's some Russians claim to have discovered a way of photographing something which seems to resemble the so-called aura, or, as it might be termed. a living force-field. Claim. I say. because I am sure I have heard of earlier reports of the technique used in photography.

Ordinary film is placed on top of an insulating layer laid on a metal base plate. A volunteer's hand is then placed on another insulating layer. The bottom plate is connected to one pole of a highvoltage. high-frequency generator. and the other pole of this generator is connected to the subject's body. High-frequency energy is turned on in bursts.

All this is done in darkness, to avoid the film being exposed by any light from the room. The film is then developed in the normal way. and. if it is a negative, then prints are taken. The results are quite astounding. Details of the hand are shown up on the film, and curiously enough, the entire outline of the hand is surrounded by an overexposed border which shows distinct "prickles" of concentrated exposure at certain points, e.g. around the finger tips. When colour film was used. the prickles appeared in different colours. depending on the temperament and mood of the subject.

Certain physicists at first thought this to be only an electrostatic concentration of electrical discharge. which might well be expected to take place around the edge of an object in such highly concentrated field conditions. Until, that is, the experiment was performed with a living leaf. Results with leaves
showed all types of detail. far greater than would be seen under lighted conditions. But they also revealed a very mysterious fact. A leaf with a clipped-out portion from one edge was exposed by the "electrography" method, as it is called. The result showed the over-exposed border, but this was unbroken at the point at which the piece of leaf should have been. were it intact! Under colour electrography, the area displayed red blotches, almost as if the wound had bled.

The electrography experiments continue in Russia, where the techniqued is termed "Kirlian photography". after the discoverers. And in the United States, Dr Thelma Moss is engaged in similar researches. Strangely, her team obstinately use different voltages and frequencies from those used by the Russians. This seems strange. because in her reports. which I have read, she often uses these differences as a possible reason for her results not agreeing 100 per cent with the Russian findings.
I think both teams should be congratulated on their perseverence in a subject so frowned upon by people who, in my opinion, should know better. After all. a scientist is not supposed to know it all. His job is to find out more, and this is not achieved by a closed mind.

## Psychokinesis

Research is going on into the field of paranormal phenomena in England too. One such experimenter is Benson Herbert. who is the man behind the Paraphysical Laboratory (or Paralab for short) near Downton, Wilts.
Recently, I was a close observer during a psychokinesis experıment (termed P.K.) in which a very gifted yound lady set her mind to influence the falling of soluble crystals through a glass flask of water.

Two identical glass jars, similar to those used for boiled sweets in sweet shops. were filled almost completely with clean, cold water. and allowed to settle for some hours on the table on which they were to stay, undisturbed, until and during the experiment. Two small quantities of soluble crystals of "viride nitens" were made ready on creased pieces of paper and carefully handled just prior to the experiment. Note: Crystals of viride nitens are very poisonous. but were used because of their dark blue dye-like characteristics when dissolved in water.
A hardboard screen was erected between the two bottles, so that only one bottle could be seen by the "medium", the other bottle
acting as a control, with which any observed effect could be compared The word "medium" is shown in quotation marks simply because it is not known that a spiritual medium is the only type of person who could perform the experiment satisfactory, though Miss Suzanne Padfield, who did the concentrating of mind in this instance, is well known for her abilities to heal, and perform "psychometry" (detection of facts from the past history of inanimate objects by handling them ... to be discussed in a future article in this series).

The medium sat in a chair such that she could see only one of the bottles. and two assistants carefully shook the crystals from the creased papers into the respective bottles simultaneously. The subject was asked to influence, by thought, the falling of the crystals in the bottle she was observing. The crystals took a minute or so to become sufficiently wetted to fall slowly through the water, when they left dark blue "smoke trails"

Both sets of crystals commenced their falling at about the same time. and both bottle tops were at eyelevel, to avoid breath interference. On completion of the leading crystal's fall in Miss P's bottle, the trail could be seen to be prominently "S"-shaped, as viewed from behind the medium. The first trails in the "control" bottle were quite vertical. After some dozen trails had been noted in each bottle, and in each case these were straight in the control bottle and " $S$ "'shaped in the other bottle, we took time to chat, during which time it was noticed that further falls of crystals in both bottles were quite vertical.

We then asked Miss P. to concentrate on "spreading" the falls of crystals in her bottle. which. for the next few minutes. she was able to do simply by concentrating on the bottle once more. Again the control bottle traces continued to be vertical. New trails were easily identified at all times by the expansion of earlier ones, rather like vapour trails in the sky.

Accounts of this experiment, and of many others conducted at the Paralab are published in the "Journal of Paraphysics", obtainable from the Paralab. Downton. Wilts.

The experimenter may care to try the experiment for himself. If so. it would be wise to use less-toxic crystals. e.g. potassium permanganate. which give a purple colour when dissolved in water.

Next month: More Experiments

## 

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## PRACTICAL

## G-HTRONIEG



THis month constructional details for the Crystal Clock are given complete with instructions for final testing.

## CONSTRUCTION

All the circuitry, with the exception of the control buttons and switches, is built on a $3 \frac{3}{3}$ in $\times 5$ in $0 \cdot$ lin matrix Veroboard.

The first thing to do is make all the cuts in the strips on the reverse as shown in the component layout of Fig. 4. Do this with care, and double check before going any further. In fact, if enthusiasm tempts you to hurry along at this or any other of the construction stages, remind yourself that two of the components account for just on 90 per cent of the project's total cost, and both can be remarkably sensitive to constructional errors.

The 14 -pin di.i.l socket won't cause any problems in assembly but do be quite sure it goes in the right way round. IC2 is very sensitive indeed to reversal. and getting the socket the right way round is half the battle. The 40 -pin socket for the clock chip can be quite tricky to fit. The big secret is making sure all the socket pins are quite straight before starting. Any that prove reluctant to go into the Veroboard holes should be gently coaxed into position with a very small screwdriver or something similar.

Do not, under any circumstances, put either i.c. into its socket at this point. This is especially true for the clock i.c. which should be put safely to one side until much later in the construction process.

## NEON, RADIO AND ALARM WIRING

First. take two 10 in lengths of wire of any two differing colours, twist them together, and solder into D50 and G50, following Fig. 4. These wires will eventually carry the mains to the pair of neons mounted behind the 1.c.d. Twist another two 10 in lengths and solder into P19 and P36. These will eventually go to the radio jack plug JKI on the side of the clock case.

Now, take another two differently coloured 10 inch lengths, twist together, and solder into R14 and Z17. These will go to the crystal microphone insert serving as an alarm loudspeaker.

## INPUT FUNCTIONS WIRING

The next stage concerns the preliminary wiring for the various display possibilities, and control of the alarm/snooze operation.

First, take eight differently coloured 12 inch lengths of wire and solder them into the board as described below.

Solder a brown wire into EE41; a red wire into CC41; a white wire into AA41; a pink wire into Y41; a purple wire into X41; a yellow wire into W41; a green wire into V41; and a blue wire into U41.

Check that these soldered positions correspond to pins $24,26,28,30,31,32,33$ and 34 respectively.

If this is so, then these wires have become: brown-snooze input; red-alarm off input; white-

$V_{\text {ss }}$ to control the functions; pink-sleep display input; purple-alarm display input; yellow-seconds display input; green-slow set input; and blue-fast set input.

## L.C.D. OUTPUT WIRING

The final wiring on the Veroboard is the most extensive, and provides the outputs from the clock chip which will drive the liquid crystal display.

The following 12 inch lengths of wire will be required: four lengths yellow; four lengths pink; four lengths brown; four lengths een; three lengths white; three lengths blue; four ngths purple; one length red.

Before commencing solderi.ng n, it is suggested that approximately $\frac{1}{4}$ inch of innlation is stripped from each wire at each e,d. the exposed wire twisted, and the ends tinne. 1 tis will enable the wiring process to proceed more quickly, and will also reduce the possibility of error.

The yellow wires should now be soldered into O49, Q49, X49 and DD49 a pink wire and a brown wire together into P49, with the three remaining pink wires going to T49, AA49. and G(349, and the three remaining brown wires going to U49, CC49, and GG41; the four green wires to R49, Y49, EE49 and U38 (the latter forming the l.c.d. common input); one white wire and blue wire together into Z49, with the two remaining white wires going to S 49 and FF49, and the two remaining blue wires going to U49 and HH41; the purple wires to W49, BB49, HH49 and O41; the single red wire to P41.

This wiring has now provided inputs for the only two segments required for the tens of hours digit; all seven segments for the units of hours, the tens of minutes and the units of minutes; a.m. and p.m. indicators; a colon (which will pulse at a 1 Hz rate) between the minutes and hours; and the l.c.d. common connection.


Rear of unit showing microphone insert and S1 switch positions.

## CLOCK DISPLAY CONSTRUCTION

Soldering the liquid crystal display socket into a piece of Veroboard is the first step, and is basically illustrated in Fig. 5. It will be noted that the cut in the Veroboard does not follow usual practice, and lie along the holes. It runs between them instead. This is because the l.c.d. socket pins are in two rows, each of them on $0 \cdot 1$ inch centres along their lengths. Unfortunately, the rows are also only $0 \cdot 1$ inch apart, which means the pins must go into immediately adjacent holes on the board. Thus the cut on the board to insulate the two rows of pins from each other must be done in the way shown.

The photograph of the interior of the prototype shows the looming that was done. Basically, the wires for the various digits were grouped together by digit, and loomed like that, while the function output wires were also loomed together. The result is five separate wire looms, a neat appearance, but most important of all, ease of handling in the stages to come.

Immediately before starting to connect the wires to the l.c.d. board, make sure it is lying the right way round, as in Fig. 5. In other words, the shoulder


Fig. 5. Upper left shows pin diagram for l.c.d. Related digit segments are on right. To conform with the Veroboard when mounting one layer of pins should be bent by 0.05 in . Pin h connects to a colon which separates the two pairs of digits in the display.
on the socket, against which the shoulder on the l.c.d. lies, should be closest to you underneath the board.

When wiring, note that the wires go straight to the underside (copper strip side) of the l.c.d. board, and are soldered there, rather than passing down through the board from the top. The connections are as follows. The notation is for the l.c.d. Veroboard; the pins mentioned are those on the clock chip.

Solder the yellow wire from pin 1 to segment $f$ at G4; the purple wire from pin 40 to segment e at H 7 ; the pink wire from pin 2 to segment c at 16 ; the brown wire from pin 2 to segment $b$ at J6. This completes the tens of hours digit, plus the a.m. and p.m. indicators.

Solder the yellow wire from pin 3 to segment $f$ at N4; the blue wire from pin 7 to segment d at O4; the green wire from pin 4 to segment $g$ at $P 4$; the white wire from pin 5 to segment a at Q 4 ; the purple wire from pin 9 to segment e at 07 ; the brown wire from pin 8 to segment c at P 7 ; the pink wire from pin 6 to segment $b$ at Q7. This completes the wiring for all seven segments of the units of hours digit.

Now solder the yellow wire from pin 10 to segment fat U4; the blue wire from pin 12 to segment d at V4 (leave the white wire from pin 12 for a moment); the green wire from pin 11 to segment $g$ at W4; now take the remaining white wire from pin 12 to segment a at X 4 ; the red wire from pin 39 to the l.c.d. colon at U7; the purple wire from pin 9 to segment e at $V 7$; the brown wire from pin 15 to segment $c$ at $W 7$; the pink wire from pin 13 to segment $b$ at $X 7$. This completes the wiring for all seven segments of the tens of minutes digit, and also for the colon in the display.

Solder the yellow wire from pin 16 to segment $f$ at BB4; the blue wire from pin 21 to segment $d$ at CC4; the green wire from pin 17 to segment $g$ at DD4; the white wire from pin 18 to segment a at EE4; the purple wire from pin 20 to segment e at CC7; the brown wire from pin 22 to segment c at DD7; the pink wire from pin 19 to segment $b$ at EE7. This completes the wiring for all seven segments of the units of minutes digit.

Finally. make two small wire links-first, from F5 to F6; second, from FF5 to FF6. Then link F7 and FF7 with a longer wire. Lastly, connect the green wire from U38 on the clock board to F4. This has linked in the l.c.d. common connection. The wiring to the liquid crystal display board is now complete.


Fig. 6. Veroboard wiring detail for the d.i.l. switch S1.

## THE DISPLAY FUNCTION BOARD

The display function board contains the two-pole four-way switch used to control the display on the liquid crystal. It consists of a small piece of $0 \cdot 1$ in matrix Veroboard as in Fig. 6. The switch is a very small two-pole four-way, built to correspond exactly to a 16 pin dual in-line package, and thus capable of being soldered directly into Veroboard or a printed circuit having holes at a $\cdot 1$ inch matrix. Manufactured by ERG Industrial Corporation of Dunstable, the switch is remarkably small and remarkably neat. It comes in a number of the standard variations more often found in the much larger rotary wafer switches. The correct description for the switch used in the project is "DIL" DSI6A switch- 2 pole, 4 way.

The wiring of the Veroboard is very straightforward. Note that all wires go to the underside (copper strip) of the Veroboard, just as in the l.c.d display board.

First, take the white wire from pin 28 of the clock chip to position $C 9$ on the board. Before soldering, check that C9 corresponds to one of the poles, and not one of the "ways" of the switch. This can be easily seen by turning the board over and examining the switch. The two poles on one side, joining together two sets of four pins, are very obvious, as are the two sets of four "ways" on the other. Yet another advantage of this miniature switch.

Interior of crystal clock.


Soldering the white wire to $C 9$ connects $V_{B B}$ to the board, ready to control the functions.

Now connect the pink wire from pin 30 on the clock chip to C3; the purple wire from pin 31 to D3: and the yellow from pin 32 to E3. This has now completed the control wiring for sleep countdown display, alarm set time display, and minutes/ seconds display. The last position on the switch is not connected, because this will correspond to normal true time display.

## CLOCK OPERATION CONTROLS

The prototype uses four push buttons to control operation of the clock. They are of the simple push-to-make variety, and each one will eventually receive one of the remaining four unconnected wires from the clock chip. The push buttons will also, of course, receive $V_{s \%}$ as their other connection. However, it will prove more convenient to finally wire the buttons after the various boards are in position in the clock case, and so the various electronic assemblies can now be laid aside, so that work can begin on the case itself.

## CLOCK CASE CONSTRUCTION

Construction is really a misnomer here, for the basic metal box into which the electronics go, and on which the l.c.d. is mounted. is a readily available standard size die-cast box. measuring a nominal $7 \frac{1}{4}$ in by $4 \frac{1}{2}$ in by 2 in . It has a lid, which will become the base in the project, held in position by six small bolts.

It seemed a shame to hide the l.c.d. away inside a box, merely letting the digits be visible, when it would then start to look like almost any other digital clock, with little novelty value. And, of course, there would also be a positive need to illuminate the digits in some way, since they generate no light of their own. It was for this reason that the display was mounted on top of the box.

It should be remembered that the four holes on the top for the push buttons must be drilled with diameter of the intended buttons in mind. No indication has been given of size because of the large number of different push buttons available. The same applies to the holes for the small neons behind the l.c.d.

In the same way, no size has been given for the much larger hole behind which the crystal microphone insert will go for the alarm tone. The type of insert used is unimportant, but again they vary in size.

The insert is held in place with an epoxy resin adhesive, so the hole should be less in diameter than the insert by at least a $\frac{1}{4}$ inch, if not a little more. It should be cut as neatly as possible-a tank cutter is ideal for the job-because this is the only hole in the case in which the edges will be clearly visible.

The dimensions given for the rectangular opening, through which the l.c.d. socket will emerge, are correct for the socket in the prototype, but it might be wise to check carefully against the socket actually obtained. Dimensions are unlikely to vary greatly, but the neatest result is obtained if the box opening fits very snugly round the socket. Modify the dimensions if necessary, therefore.

The four small holes round the l.c.d. socket, the four small holes round the d.i.I. switch opening,
and the six small holes in the base are all of a size to take 12 B.A. bolts.

## PAINTING AND LABELLING

After all the metalwork has been completed, the box should be very thoroughly rubbed down with wet-and-dry paper. It may be necessary first to use a coarse glass paper, or even a fine file, to remove any particularly bad rough edges. It is possible to get a singularly smooth finish finally, before painting. When the metal is completely smooth, it's a good idea to give the box a thorough wash in warm water and washing-up liquid. This will get rid of all remaining dust.

The paint used for the prototype was initially matt black. All the openings were sealed from the inside by masking tape, and the box was sprayed with the base screwed in position. Three coats were given, following the instructions on the spray can.

After allowing a three day drying period following the final coat of matt black, the various controls were labelled with Letraset. This was immediately followed with the first of three coats of matt polyurethane varnish, each coat being allowed to dry before applying the next. The aim, of course, is to give protection to the original matt black, as well as the Letraset.

When the final varnish coat is completely dry, assembly work can commence.

## ASSEMBLING THE CLOCK

The first step is to mount the four feet on the underside of the base. This will keep it away from the working surface, and prevent unnecessary damage to the paint. Mount the feet in the normal way, using appropriately sized bolts and nuts.

Now the clock board can be placed on the base. First, take six 12 B.A. bolts and nuts, and mount them firmly in the six small holes in the base. Then take another six nuts and screw each one down on a bolt until it is approximately $\frac{1}{4}$ inch from the base. The Veroboard will rest on these.

Now take the main clock board and gently ease it onto the six bolts, sliding it down until it is resting on the six secondary nuts. Make any minor adjustments to the nuts to ensure the board is seated firmly without warping.

The board should now be removed from the base for a time, in order to complete the last wiring connection on it, before it is finally mounted. This is the mains input.

Normal three core mains wire should be used, with the earth connection going to the screen tag on the transformer input and case.

## PRELIMINARY POWER TESTING

A point has now been reached in the construction and assembly at which it is advisable to carry out a preliminary test of the power supplies to the board, and on the board.

First, securely tape the ends of the miniature wires from the mains input side of the transformer which will eventually go to the neons. These will, of course, be live when the mains is turned on. Second, make sure the main clock board is standing on a non-conducting surface, and arrange the two subassemblies (l.c.d. display and display control boards) so that they are not shorting to any of the components on the main board.

Finally, remember that neither the clock chip or the alarm noise i.c. are to be inserted at this stage.

The mains can now be applied to the board, so that the voltage between $V_{\mathrm{ss}}$ and $V_{\text {dd }}$ can be measured, in order to make sure that it is approximately the correct value, and therefore unlikely to damage either i.c. when the final tests are done later.

There are a number of convenient points on the top of the main clock board at which this measurement can be done. Probably the best is across the two wire links which run between G2/AA2 ( $V_{s,}$ ) and between $\mathrm{L} 1 / \mathrm{Zl}\left(V_{\text {dd }}\right)$. The reading should be approximately 30 volts. Anything at all higher than 31 volts, or significantly lower than about 27 volts almost certainly indicates a fault in the power supply circuitry, which should be investigated and put right before going any further.

Assuming the voltage readings are correct, the mains can now be turned off, and the main clock board replaced on the six 12 B.A. bolts on the base. A further six 12 B.A. nuts are now screwed on to the bolts, thus holding the board firmly in position.
At this stage, the base can be laid aside, and some preliminary work done on fitting components to the main part of the case. First, put the four push buttons, the two neons, and the radio jack plug in their respective positions, and fix them securely. Next, the crystal microphone insert can be glued into place, using an epoxy resin adhesive.
Now the basic $V_{s s}$ wiring for the push buttons can be done. This involves a ring common connection to one of the terminals on each of the push buttons. $V_{\mathrm{ss}}$ will eventually be connected to this ring.

## FINAL ASSEMBLY AND TESTING

The three pairs of leads from the main clock board for the neons, the crystal microphone insert, and the radio jack plug should be connected to their respective components inside the main case first. Check back to Fig. 4 to make sure the right pair goes to the right place. The function control subassembly board, bearing the d.i.l. switch, should now be fitted.

The fitting of the function control board is done in exactly the same manner as the main clock board was fitted to the base. The optimum position for the d.i.l. switch is sufficiently far through the side wall of the main case to be easily operated, but not so far as to possibly short out the external tags on the side of the switch.

The next step is to wire in the other ends of the only four leads from the clock chip so far unconnected.
These are the brown wire from pin 24 (snooze input); the red wire from pin 26 (alarm off or reset input); the green wire from pin 33 (slow time set input); and the blue wire from pin 34 (fast time set input).

These wires go to the vacant terminal on each of their appropriate push buttons. Rather than attempt a complicated explanation of which button is which when viewed from underneath, the pictures of the outside and inside of the case make the push button positions quite clear.
$V_{\mathrm{ss}}$ must now be supplied to the common ring connection round the four push buttons, and this can be most handily obtained from the display
control board, which is now in position and conveniently close to one of the buttons. $V_{s s}$ is, as will be known, at the point where the white wire is connected to the display control board, and thus another very short lead needs to come from here to the push button common ring connection. The final assembly step comes with the mounting of the 1.c.d. display board into the main case. Again, the mounting procedure is exactly the same as that for the main clock board on the base. Care should be taken, however, to make sure the board is the right way round, though this is virtually guaranteed by the small cut-outs to clear the neons.

## FINAL TESTING OF THE CLOCK

The time has now come for the two i.c.s to be put in position, and a great deal of care should be taken at this stage when fitting the clock chip. To start with, it has 40 pins instead of the more familiar 14 or 16 , and this makes fitting very much more difficult. Secondly, it is extremely sensitive to any small charge of static which may have built up. This is why the chip will probably have been supplied with the pins in a material to prevent this. Handle the chip cautiously therefore, and try to avoid shorting any of the pins out, either with the fingers or a component. The loomed wires each side of the clock chip socket will need to be carefully parted to allow the chip to be slid in to rest on the socket. Once this has been done, make a last check to be sure the chip is the right way round, and then ease the pins home into their respective holes. Once they start to go in, there will be no further problem. The chip should be pushed well down and should finally lie flat.

The smaller i.c. for the alarm noise will present no problem, but it is worth repeating that it is especially sensitive to any accidental reversal in the socket. The normal check to avoid this becomes even more essential therefore.

If the 1.c.d. was tested in position earlier but then removed, it should now be replaced.

## FIRST SWITCH-ON

Once both i.c.s and the display are in position, the mains can be turned on.
This should cause at least two things to happen -first, the neons should light, confirming the presence of power; second, and much more rewarding, the l.c.d. should show some form of display.

If it is showing some form of display, but this appears to be a collection of unrelated segments, then the display is slightly off-centre. Merely slide it fractionally left or right, holding it by the ends, until recognisable digits appear. If the l.c.d. started in the approximate centre of the socket, only a tiny movement will be necessary.

If, by this stage, (a) the neons have not come on or (b) the 1.c.d. display is either non-existent or cannot be made recognisable or (c) the p.m. indicator is not pulsing or (d) the colon between hours and minutes is not pulsing or (e) there is a combination of any of these symptoms, then there is a fault somewhere which must be located and cured before proceeding further.

One point which should be checked without delay is the position of the function display di.i. switch. It should, of course, be set for normal time display.

All other fault-finding will follow the traditional path of checking soldered joints, checking for bridges between the Veroboard strips, checking the polarity and operation of components, and so on. If construction has been done carefully, however, there should be no problems at this point.

## PRESS BUTTONS

Assuming that all is well therefore, the "Set fast" button can now be pressed. This should cause the time to count rapidly forward, at one hour per second, and cancel the pulsing "power interruption" indication, replacing it with a steady indication of either a.m. or p.m. time status. Releasing the button will stop the time movement and normal time counting will begin.

The "Set slow" button should now be tried, and should give exactly the same results, excepting that the time will now count up at two minutes per second.


Showing control button legends and neon placement for best contrast

The display control switch can now be moved to the "seconds" display position. This should result in the previously displayed units of minutes digit moving to the units of hours position, and the total minutes display being replaced with a steadily counting seconds display.

With the display control switch still in the "seconds" position, the "Set fast" and "Set slow" buttons should be tried again. The former should result in the seconds count reverting to " 00 " and holding there, while the latter will merely hold the seconds count at whatever point it is when the button is pressed. This ability is invaluable if the clock is to be set accurately against a known time source, as will be explained shortly.

## ALARM

The display control switch should now be moved to the "alarm" position. This should result in the display changing immediately to " $12: 00$ ", which is the alarm set time until changed. The "Set fast" and "Set slow" buttons can now be used as they were with a normal time display, in order to set
the alarm. It is suggested that the alarm be set two or three minutes ahead of whatever the time display happens to be.

When this is done, and as the normal time display reaches the alarm set time, the alarm should sound. Instead of pressing the "Reset alarm" button, press the "Reset snooze" button. The alarm noise should stop immediately, but should come on again approximately 8 minutes later. This preset time may vary slightly but not a great deal.
When the alarm sounds again, press the "Reset alarm" button, which should turn it off for a full 24 hour period.

## SLEEP

Finally, move the display control di.i. switch to its "sleep" position. This should immediately result in the display changing to a two digit figure. There should be no colon or a.m./p.m. indicator. This display shows the time remaining in minutes before the continuous circuit available via the radio jack plug will be cut.

The "Set fast" and "Set slow" buttons will now have a reverse effect on the display, in that they will cause it to count down, rather than up, but at the normal speeds for each button. When the display reaches " 00 ", it will recycle immediately to "59". However, in normal operation, when it reaches " 00 ", it will latch there and not recycle.

## RADIO

The device is used to control a radio very simply by connecting one side of the radio's battery power via the jack plug. Providing the sleep display shows a time in minutes, the radio will play. When " 00 " is reached, the radio will go off.
If every operation described in this section is achieved, the checking process is completed, as is also the entire project, and the constructor now has a fully operating liquid crystal display clock of which he can reasonably be proud!

## ACCURATE SETTING OF TIME

For ease of explanation, it is assumed that a telephone is handy. The process will be identical, however, for setting the clock against the Greenwich time signal from a radio, or the clock displayed on TV from time to time. The only problem with the latter two methods is the inconvenient times at which the time signals seem to be made available!

First, set the clock to a point several minutes ahead of what the true time is believed to be. Then switch to "seconds" display and press the "Fast set" button, holding it down. This will hold the - time count, and revert the seconds ingredient in the time count to zero. In other words, the clock is being held on a precisely set minute.

Then, while still holding the "Fast set" button down, use the other hand to dial the Post Office Speaking Clock. It is at this point that the final choice of switches for some functions on the clock, but buttons for others, will become abundantly clear!
The rest is obvious-as soon as the third stroke is heard for whatever precise minute is being held, the "Fast set" button is released, and the clock is set to the second.


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Memorex MRX2 cassette tape

## SECURITY LIGHT DIMMER

Apart from being able to control the amount of lighting power being used, the latest light dimmer now being marketed by Rendar Instruments Ltd. also acts as an intruder deterrent.

Designed as a direct replacement for the existing light switch, the Secureye embodies a manual on/off switch, dimmer and an automatic on/off switch operated by ambient light.

As light falling upon the cell is reduced, power is allowed to flow through the switch progressively until approximately half brilliance is achieved. When ambient light level is increased the control automatically operates as a dimmer until power is cut off completely.

Capable of carrying a load of up to 500 W a.c., the dimmer is suitable for use with any type of incandescent lighting. The automatic action is claimed to produce surge-free switching at up to 50 per cent of normal power, thus prolonging lamp life.

The Secureye is ideally suited as a standby lighting; control of home or office lighting during absence; and maintenance of illumination level in hospitals, on stairways and in other areas where safety considerations apply.

The recommended retail price for the Secureye is $£ 5.60$ plus VAT. Addresses of nearest stockists can be obtained from Rendar Instruments Ltd., Victoria Road, Burgess Hill, Sussex.

## CASSETTE TAPE

With so many different processes and differing claims for the large number of tapes now appearing on the market, it is difficult to

# market PLALE 

Items mentioned in this feature are usually available from electronic equipment and component retailers advertising in this magazine. However, where a full address is given, enquiries and orders should then be made direct to the firm concerned.
choose one tape from another. However, we can certainly recommend the new cassette tape from Memorex U.K. Ltd.

By using a highly concentrated iron oxide particle method, the Memorex MRX2 cassette tape is claimed to give better frequency response and signal/noise ratio performance than most current tapes. Using a tape recorder in the $£ 100$ range, recordings on various cassette tapes, including chrome oxide types, showed that the Memorex MRX2 tapes gave better high note performance and in many cases far less tape noise.

The cassettes are available from most audio stores with playing times from 30 to 120 minutes. Prices vary from 62 p for 30 minutes to $£ 1.60$ for 120 minutes, plus VAT.

## EMERGENCY DESK LAMP

Whilst on the subject of power and lighting, readers may be interested in an ingenious desk lamp produced by Industrial Instruments Ltd.

Called the Kleverlite the lamp has a five position selector switch which enables the light to be used during power cuts and also charges the internal nickel cadmium battery during mains operation.

The five positions of the selector switch are as follows: 1. Unit off; 2. Charger only on; 3. Low light intensity; 4. High light intensity; 5. Low light intensity on when mains fail. Should the mains fail, the light will also continue to function if the light is in positions 3 or 4.

The low light position enables approximately four hours' operation under power cut conditions, while the high light position gives normal desk working lighting for approximately two hours.

The built-in charger is a transistor controlled, constant current type, which ensures a high current to recharge the battery as quickly as possible when the mains is restored. Once the battery is charged, the charger reverts automatically to trickle charging.


Kleverlite desk lamp Irom Industrial Instruments

The bulbs used in the light are 6 V 6 W low light and 6 V 18 W high light; replacements being readily available from motor accessory shops. The bulbs are deliberately under run from the 4.5 V battery to ensure long life.

The Kleverlite is available from Harrods or Hastings Lighting Centre, price $£ 49 \cdot 50$. Further information can be obtained from Industrial Instruments Ltd., Stanley Road, Bromley, Kent, BR2 9JF.

## SOLDERING TIPS

It is generally recognised that soldering iron efficiency is largely dependent upon the design and shape of the soldering iron bit, so this year Adcola Products Ltd. are concentrating on producing almost 100 different soldering tip designs as standard off-the-shelf items.

The range includes both copper and iron plated long life bits in a variety of shapes; standard, taper, reduced chisel, conical, screwdriver and special printed circuit types.

In addition to soldering bits for their own tools, the range has been extended to cover other manufacturers' soldering irons.

Further details of the complete range of tips can be obtained from Adcola Products Ltd. Adcola House, Gauden Road, London, SW4 6LH.

## NOTE

With regards to the Radio \& TV Components Stereo 21 System announcement on page 218 of the March issue. The postage and packing should be $£ 1 \cdot 60$ not 16 p as stated. We apologise for this printing error.

## PRTENTE REDTEW

## PRIHTED CIRCUITS

## BP 1329770

Printed circuits conventionally take the form of a network of electrically conducting lines on a nonconductive substrate. Often the substrate is a resin or ceramic material and the conductors are of silver or a precious metal such as platinum, palladium or gold. Application is by painting, stamping or screen printing. The outstanding disadvantage is of course the cost of the precious metal.

In BP 1329770 NL Industries Inc. of New Jersey, USA, suggests an inexpensive alternative approach. Conducting lines or paths, the inventors say, can be formed from liquid compositions which consist of the reaction product of aluminium powder with a liquid vehicle which contains a phos-phate-chromate reaction product.

The base plate or substrate is of conventional type and in one specific example given, a substrate of high density, sintered alumina is taken and a circuit of lines painted on one face.

A composition containing 80 parts of fine aluminium powder mixed with an aqueous slurry containing $32 \cdot 3$ parts $\mathrm{H}_{3} \mathrm{PO}_{4}, 9 \cdot 1$ parts $\mathrm{CrO}_{3}$ and 7.1 parts MgO in water is used for painting, the water being in sufficient quantity to permit reaction of the ingredients and allow easy application of the mix to the substrate. After application the substrate with applied composition is fired to $705^{\circ} \mathrm{C}$ in air and then cooled. It is claimed that this gives a workable printed circuit with good adherence of the lines to the substrate and low electrical resistance. But it is admitted that it is very difficult to solder connections to such lines.

The answer proposed to the soldering problem is that small amounts of conventional silver electrical paste be applied to those points on the conductive lines at which it is desired to solder up connections. The bod is then refired in an oxidisins atmosphere (air will apparently do at a temperature of about $650^{\circ} \mathrm{C}$

It is found that soldered connections can now be easily made at the points where the silver paste has been applied.

If the various claims are justified, the resultant printed circuit board should be relatively cheap to produce.

## LIBHT-OPERRTED LAMPS

Lamps which turn themselves on and off according to the strength of ambient illumination are fairly well known. These, of course, rely on light sensitive switching, but a problem is the confusion which can arise from the lamp light falling on the photosensitive switch. Elaborate shielding is possible but this may cause overheating which causes switch degradation.

In BP $1 \quad 325810$ Kabushiki Kaisha Sankosha of Tokyo describes one way of overcoming these problems. Although the patent claim is obviously directed to a fairly simple elaboration over what is known, the description taken as a whole contains useful information.

In Fig. 1 a lamp is screwed into an adaptor with a screw fitting intended to screw into the mains socket. Obviously UK style bayonet connections could be used instead.

A heat sensitive switch S1 is located inside the hollow body of the adaptor and a light sensitive element is mounted in a torch-like head remote from the adaptor and connected to the adaptor by a fairly long flexible 2 -core cable. The photosensitive head is placed in any convenient position so that its photosensitive surface is screened from receiving direct light from the lamp.

As shown in Fig. 2 the make and break contacts of the heat sensitive switch S1 are in series with the power supply to the lamp and its heater element is permanently connected across the mains supply in series with the Light Dependent Resistor (LDR). This element is of the cadmium sulphide, cadmium selenide or similar type of which the resistance varies with incident light.
While the photosensitive surface of PCC1 is bathed in ambient light, its resistance will be low and a substantial amount of current will pass through the heater element. The switch contacts S1 are open in this condition. When the amount of light falling on the LDR reduces, the electrical resistance of the element increases and the current flow through the heater drops. The switch contacts close and the lamp lights.

Provided that the light sensor has been properly positioned and is sufficiently directional in its characteristics, it will not receive sufficient light from the lamp to re-open the switch.



Fig. 2


## SWANLEY IC TOMORAOW

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| Helme XLK25 (pair) | £18.17 |
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# Riadiant A SELECTION FROM OUR POSTBAG 

## P.E. RONDO

Sir-Forgetting the price increase and maybe putting the E.S.P. article to some use, perhaps you could tell me what happened to the "RONDO" series. I started this unit at a slow steady pace-looking forward to each article as it appeared. only to find it comes to an abrupt end with no further mention of it. After searching the February and March issues to see if there is a temporary postponement of the series. I came up with nothing.

Can you tell me the solution; will it be concluded in later articles?
G. E. Heslop. Carlisle.
In view of the current power crisis it was decided to insert items such as the "100W Power Inverter", lamp inverter and the like, rather than continue the Rondo series which had, in any case, reached the completion of the audio stages.

This series will be resumed next month.

## What-phonic?

Sir-Is it not a little odd that we have "progressed" from stereophony to quadraphony when the former is superior to the latter? Should we perhaps consider the possibility that engineers, who have devised words and used them without regard to their meaning, might have betrayed confusion of mind?
A. D. Blumlein, who is generally regarded as having "invented" stereophony, didn't use the word in his famous patent. He did describe how several loudspeakers might be fed from two channels to restore a measure of three-dimensional repro-duction-suggesting that four loudspeakers might be sufficient. Good engineers are precise, Blumlein was both. He said (in effect)-that two loudspeakers could restore a measure of binaural effect-insofar that azimuth separation, instrument from instrument or instrument from reverberation, could be restored to a certain extent; this is not stereophony because "stereo" means:-"Solid in the sense of three dimensional." As far as I am aware, stereophony has yet to be invented.
A lot depends upon whether the "phony" (meaning voices!) relates to the number of loudspeakers or
to the voices being reproduced; if the latter is intended then monophonic reproduction will become rather boring. If the former is intended, stereophony has been known for years: it has recently been revived and demonstrated by the Sennheiser dummy-head. disk which is able to provide binaural headphone listening. What the fuss is all about is how the binaural signals should be presented through loudspeakers.
With the exception of Blumlein's patent, the remaining technical literature reveals little sign of coherent theory or rigorous experimentation. Quadraphony does have a genuine objective: it is to create a better stereophonic effect. It would hardly be fair to say that the consumer is being "conned" although he is most certainly participating in a mass-experiment to determine which particular system will "win". The effects of advertising and muddled explanations could hardly be seen as beneficial towards ensuring that the experiment is a fair one.
It would be a pity if those of use who are less sensitive to the defects of a particular quadraphonic system should "vote it in" as acceptable, denying others, and ultimately themselves, the benefit of further development because a low-grade commercially acceptable system has been found to provide sufficient turnover without the need for further research or investment. By all means say to the recording companies, "your quad isn't yet good enough for me to buy it" or-"it is so good that I can't tell I'm not in the concert-hall". Some of the uninformed all-black/all-white insensitivities in "Readout" remind me of a bar-room brawl. There are those who swear that beer makes a piano sound better-it doesn't have the same effect in an organ.
A last word: Eric Partridge, in "Usage and Abusage" gives: Quadra-; Quadri-; Quadru-. The first is always wrong

Peter J. Unwin Rochdale.

## Charger/Regulator

Sir-After reading the article on the "Auto-Charger Regulator". (Feb. 1974), I decided to investigate the material presented.

1. Every mechanical regulator irrespective of manufacture (e.g. Lucas, CAV, Bosch, Hitachi, etc.) has current regulation incorporated.
2. Use of a solid state voltage regulator without the current regulator will burn out the dynamo. On test with the battery voltage down to 10 V a C40 dynamo produces 40 amps at the regulation voltage. Max. safe current for C40 is 22 A .
3. Voltage regulation figures vary from 13.8 V to 16.5 V , not about 15 volts.
4. Current output at a safe level for dynamos fitted to very ordinary cars varies from 22A to 34A according to type. Only one is quoted at the 20 A average mentioned.
5 All cable has been supplied in metric sizes for a considerable time.
5. Spade terminals are readily available so there is no need to knock them out of bits of plumbers' copper.
6. The method illustrated is definitely not the way to solder Lucas terminals to cable.
7. No car ever had a connector in the dynamo leads as standard equipment.
8. Positive earth went out with the Ark so why give it as a first choice?
9. A current sensing resistor is available from CAV or any scrap yard.
10. No protection is provided on the unit against voltage surges produced by the vehicle equipment or a worn dynamo.
It would appear, therefore, that whilst the project will work to a limited extent, the application on cars could have been more thoroughly researched. This comment is also applicable to other car projects and I would suggest some form of pre-publication critique to avoid serious mistakes.

May I stress that I welcome car orientated projects and hope that you will include many more as I know from my involvement with the motor trade that these arouse a very great interest.
H. D. Briggs, Telford.

The answers to the points raised are as follows:

1. The article clearly states that if a vehicle having a current and voltage regulator is to be fitted with the solid state unit, then the current/voltage solid state unit should be used.
2. Current limits given for dynamos are continuous rating. Intermittent, or short term rating is considerably higher.

Any battery which cannot be raised from 10 V to regulation voltage within the allowed time before generator heating occurs must be faulty, and probably would not start the engine. Remember that the generator would be starting from cold in this case, and so would have greater thermal capacity. Your reader forgets that with a conventional regulator the output of say 20A is created by a far from constant pulsed current whose peak value is probably well over 30A.
3. The article quite clearly refers the constructor to the maker's manual to set the voltage accurately.
4. The current rating | quoted was that for my own vehicle. I did not dispute that other capacity generators exist but I imagine that any reader who knew enough about the vehicle to fit my circuit to it would be aware of the fact, and would soon find out if-as I sug-gested-the manual was consulted to find the voltage setting; the current capacity would probably be found on the same page.
5. Cable has been available in metric sizes for some time, this does not mean that all "Imperial" stocks were destroyed at the day of changeover. I still have quite a lot of "old" wire. The size 14/0076 was quoted only for robustness, as it actually carries 1 milliamp the application is hardly critical. The main current carrying cable was quoted as " 50 A " which is neither metric nor imperial.
6. I was unable to purchase $\frac{7}{8}$ in. spade terminals from Southampton's main Lucas agent. In my design, construction is simplified by making the spades part of the diode bracket so I see no sense in the comment at all. As for "knocking them out of plumbers' copper', surely that is all the commercial article is! Why not do it yourself and save money?
7. I did not draw the illustration, but, as an engineer, I find that any method which achieves a satisfactory result will not cause me as much indignation as it does to your reader. I would be interested to know what the British Standard way of soldering a Lucas connector is.
8. The only answer to this is "Mine has!" In the age of mass production, when an engine descends into a vehicle, all of the relevant electrics, including the generator cable, are connected up
with one multi-pole plug, to save time. This is the case with mine.
9. I expected the average reader to understand that either polarity version could be built. If your reader is so scornful of the positive earth system then he is under no compulsion to build it. It occurs to me that most of the techniques used in modern cars went out with the Ark, but are still used for economic reasons.
10. So what! Why buy one when the cable can be used? The cable-voltage-drop technique has been used commercially with alternators. (Incidentally my circuit will also work with alternators, simply by omitting the cutout diode. I intend to do this quite soon).
11. My circuit does not allow a worn dynamo to create voltage surges sufficient to cause damage. $A \quad 2 \mu \mathrm{~F}$ capacitor is incorporated to suppress transients, so I hardly consider that there is "no" protection. In my vehicle, the voltage waveform produced is so smooth that the CD inverter spillback masks any other irregularities.
। am pleased that your reader admits, despite its shortcomings, that my unit will work. Mine has been working for 18 months now and has covered about 15,000 miles. I consider that to be adequate research.
I think I have answered any technical queries raised. I feel, however, that I must make a reply also to the tone of the letter, which was, in my opinion, rather provocative. I do not see any valid scientific purpose served by "having a go" in this manner, as to my mind, criticism couched in these terms loses some of its credibility. I assure you that my article is based on sound principles, and 1 avidly await the publication of an article by Mr. Briggs!
J. R. Watkinson, B.Sc., M.Sc.

## Inverter/Charger

Sir-l am writing in connection with Mr Verrill's article (100W Inverter/Charger) in February P.E. There seems to be some error under the section entitled "Transformer Calculations" regarding average and peak values of rectified sinewaves. The first two paragraphs do not make sense.

More serious though is the implication that a transformer designed for mains to low voltage and not vice versa, may be used freely in this or any other inverter circuit. The fact is that unless a transformer has been specifically designed for such an application it is possible
for the inductance of the "new" primary (LT side) to be so low as to allow a large magnetising current to flow. This means that an inverter may take several amps when unloaded, only being reasonably efficient on full load.
Another point regarding the published circuit is the use of 2N3055 transistors which have a high $V_{\text {ce(sat) }}$ when handling high collector currents. Silicon may well be "à la mode" but germanium would save precious watts, thanks to their low on resistance.
Both these points lead me to ask either the magazine or the author to clarify the following points.

1. How many amps does the inverter take when no load is connected?
2. How many hard-earned watts are lost when the inverter is drawing, say 10 amps , in each 2N3055 transistor?
3. How many amps are drawn from the battery to illuminate 80 watts of filament lamps to full brilliance?
In conclusion I should like to point out that these are meant to be constructive criticisms intended to enable constructors to eke the last amount of useful energy out of their batteries in the most efficient manner possible. As such I hope those concerned will reply to these points fully.
L. Cook,

Prescot, Lancs.

## LOW PRIMARY INDUCTANCE

Contrary to Mr. Cook's statement, a low primary inductance is an advantage since a transformer designed with a higher inductance to give lower magnetising current would need more turns of wire and hence would have a higher resistance giving greater losses to load current. This is made clear by the following example :
$\begin{aligned} 12 \mathrm{~V} \text { winding resistance } & =0.1 \Omega \\ \text { Magnetising current } & =2 \mathrm{~A} \\ \text { Load current } & =10 \mathrm{~A} \\ \text { Total current } \bumpeq \sqrt{2^{2}+10^{2}} & =10.2 \mathrm{~A} \\ I^{2} R \text { loss } & =10.4 \mathrm{~W}\end{aligned}$
If the number of turns is now doubled the inductance is multiplied by 4 and the resistance is also multiplied by 4 since for a given volume of wire, twice the length at half the thickness is required.

$$
\text { Totalcurrent } \simeq \sqrt{0.5^{2}+10^{2}}=10.012
$$ $\therefore I^{2} R$ loss $=40 \mathrm{~W}$

To this must be added the increased loss due to the doubled number of secondary turns.

The foregoing calculations refer to the untuned square wave version. It is correct to take the square root of the sum of the squares of the two components of current, since the

$$
\begin{aligned}
& \therefore 12 \mathrm{~V} \text { winding resistance } \quad=0.4 \Omega \\
& \text { Magnetising current }=0.5 \mathrm{~A} \\
& \text { Load current }=10 \mathrm{~A}
\end{aligned}
$$


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\author{[^5]}
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magnetising current lags 90 degrees behind the load current. The fact that the input is a square wave of voltage does not alter this rule, since the 90 degree lag also applies to the harmonics of the square wave.

Note that the 12 V battery does not supply $12 \mathrm{~V} \times 2 \mathrm{~A}=24 \mathrm{~W}$ of power due to magnetising current since this power is effectively an oscillating component which flows in and out of the battery with only a very small nett loss due to the increased copper loss.

In the tuned version the magnetising current is supplied in the secondary (240V) winding by the tuning capacitor C7. The foregoing arguments on increased power loss if the number of turns is increased still apply, but here there is an added advantage in having a high magnetising current since this increases the $Q$ of the tuned circuit giving a better sine wave. It is equivalent to the experiment mentioned in the article, where extra inductance and capacitance was added in parallel with the output.

## USE OF SILICON TRANSISTORS

It is true that Germanium transistors saturate better than Silicon but a 2N3055 carrying 10A with 1A base drive saturates to less than 0.5 V , typically, giving less than 5W power loss from this cause. The 2 watts or so improvement obtained by using Germanium could well be lost by increased leakage current.

The major power losses are $I^{2} R$ losses in the transformer and inductor and approximately 12 W in the base drive circuitry. In this type of circuit where the power transistors are driven by a separate oscillator transformer iron loss is very small.

Some commercially available inverters use a saturating core type of free running oscillator; in these circuits the transformer is 甘riven hard into saturation in alternate directions giving in many cases a low efficiency due to the high hysteresis loss. Such circuits are notoriously difficult to start under load and have a tendency for both transistors to latch on in the output in some cases causing them to burn out.
R. Verrill

## INCREASING THE POWER

Sir-May I congratulate you on the timely and very helpful constructional article on the above. I am building this as an urgent project and would ask you if there is a simple way to increase the output wattage.
I have a fairly massive 200 watt mains transformer which I would like to press into service and while leaving the earlier stages as they are could I fairly easily use additional 2N3055's in place of TR6 and 7? You mention the direct coupling method without using R15 and R17 and would prefer this if possible.

Also is it feasible to double the battery voltage to the output stage only i.e. to 24 volts while leaving the earlier stages as they are on 12 volts, if this would simplify a 200 watt output?

I would be most grateful for a little guidance on the above and any comment if it would then be suitable for running a domestic television set. John S. Aston,

## Dorchester, Dorset.

I have had a report from one person who simply connected two 2N3055's in parallel and was able to run a 140 W colour television with some reduction in performance. For optimum results it is necessary to duplicate the drive circuitry, i.e. everything from the collectors of TR3 and TR4 onwards to the bases of a second pair of 2N3055's with parallel collectors and emitters.

If you wish to go to 24 V input it is not necessary to leave the earlier stages at 12 V ; simply increase R15 and R17 to 22 ohm at 12 watts. If you have a few 2N3055's to spare you could probably risk using them but there is a danger of them breaking down with the increased voltage. A higher voltage type which is suitable is the 2N3442 (RCA or Ferranti) but this is considerably more expensive. R.V.

## NOTES

With reference to the first and third queries raised by Mr. Cook, the no-load current of the inverter appears to vary with the quality of
the driver and switching transistors and the nature of the "tuning" selected for a particular application. We have measured values between 1.04 and 3.04 .

As to current drawn under load, we have not measured this with 80W of filament load alone since an BoW bulb is not standard. However, using a 100W bulb as load again gives varying conditions dependent apparently on the condition of the components and the funing but it should be emphasised that full brilliance (measured by eye in the absence of an accurate photometer) has been obtained with à load current under 10A.

In the section dealing with transformer calculations on page 135 of the February issue a line was printed twice in error. Thus the line " winding is 12 V because of the equal number of" should be replaced with " winding of T1 is a rectified sine wave and since". Additionally, the reference to 12 V in the next paragraph should, of course, be to 2 Vm .

From information received and our own experiments it seems that one or two basic precautions are needed. For example, peak voltages as high as 1,200V have been measured at the output in the inverter mode with the circuit not tuned but using the choke. Thus clearly it is not to advantage to try switching the a.c. output as arc formation can occur at the switch with consequent further spike generation and the risk of component damage.

In fact there seem to be several sources of "spike" and it is suggested that the driver transistors and switching transistors might benefit from protection diodes and the 12 V d.c. rail from the use of a 1W Zener of around 18V rating. One constructor who has met with switching problems has solved noload switching by using a 100W 12 V projector lamp in the d.c. line during switching on and then subsequently shorting it with a switch after all is running smoothly.


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$0 / 5 \mathrm{k} / 50 \mathrm{k} / 500 \mathrm{k} / 5 \mathrm{M}$ 0/5k/50k/500k
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$1 / 5 / 10 / 50 / 250 \mathrm{~mA} / 1 / 5 \mathrm{~A}$ DC.
$0.25 /$ $1 / 5 / 10 / 50 / 250 \mathrm{~mA} 1 / 5 A$ DC. $0.25 /$
$0.5 / 1 / 5 / 10 / 50 / 250 \mathrm{~mA} 1 / 5 A$ AC. Res. istance: $0.5 / 10 / 100 / 200$ ohms $/ 1 / 3 /$ $30 / 300 \mathrm{k}$ ohms. Decibels: -5 to +10 dB
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Thousands sold of this extremely popular mono version. A mic input may be fitted using the VA30 (see below). Low consumption from a $9 V$ battery.
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Only $S A X O N$ can supply such incredible value for money. This unic features 3 kW power handling, full-wave conerol, bass, middle, treble AND master controls. Twin loudspeaker jacks for "ehrough " connections. It may be used free standing or will
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MULTI-PURPOSE MIXERS
M4HL
M6HL

Featuring multiples of our VA30 module, the M4H: and $\mathbf{M 6 H L}$ fulfilthe requirements of all clubs, groups, etc. Where a high quality mixer is required. Each plus volume, treble and bass controls. input. impedances may, if required, be easily changed, The M4HL has four channels. and one output, and the M6HL six channels ( 12 inputs) and a master control and two outputs. Either unit may be used
free-standing or panel mounted. These mixers will reed all types of amplifier. Recommended for their versatility and high periormance, and excellent

## value for moner

VA30 CHANNEL $\mathbf{1 3 . 5 0} \underset{\text { Caree }}{\text { CaOL }}$
This is the basic channel module in the above mixers and may also be used for extra inputs on either the mono or stereo mixers. Fitted with volume, bass and treble controls, requires just a jack and supply (9-100V)


## Sinclair Project 80



Stereo 80 pre-amplifier/control unit


## only $\frac{3^{\prime}}{4}$ ' deep $\times 2^{\prime \prime}$ high

Living with hi-fitakes on new meaning with Sinclair Project 80 . The electronics of these revolutionary new modules are all contained within elegantly designed matching cases no more than three-quarters of an inch deep. They are designed for mounting on any appropriate flat surface by means of 6BA bolts extending from the rear of each module and which pass through suitably drilled holes. Connections are taken away out of sight in a similar manner. The possibilities opened up by Project 80 are endless - superb hi-fi systems can be installed in ways hitherto only dreamed about and never before made practical. No more cutting out and shaping to put modules in position. A few holes drilled with the aid of templates supplied and the job is done. Now you need never again be faced with problems of keeping the hi-fi from clashing with carefully thought-out furnishing schemes. (That will surely please wives!) Slider controls have been introduced in place of knobs and all modules in the range incorporate new up-dated circuitry with emphasis on performance standards and built-in protection against overload and shorting. The aim was to re-think modular construction completely - to make it infinitely more versatile, even simpler and more reliable - the result - Project 80 - another triumph for Sinclair, and the most exciting construction modules ever.
the slimmest,most elegant hi-fi modules ever made

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| Simple battery record player | 2.40 | $\begin{aligned} & \mathbf{£ 5 . 4 5} \\ & +54 p \text { V.A.T } \end{aligned}$ |
| Mains powered record player | Z.40, PZ. 5 | $\begin{aligned} & \mathbf{£ 1 0 . 4 3} \\ & +£ 1.04 \vee \text {. } . T . \end{aligned}$ |
| 30W. RMS continuous sine wave stereo amp. | $\begin{aligned} & 2 \times Z .40 \mathrm{~s}, \text { Stereo } \\ & 80: P Z .6 \end{aligned}$ | $\begin{aligned} & \mathbf{£ 3 0 . 8 3} \\ & +£ 3.08 \vee \text { A.T. } \end{aligned}$ |
| 50W ( $8 \Omega$ ) RMS continuous sine wave de luxe stereo amp | $\begin{aligned} & 2 \times Z .60 \mathrm{~s}, \text { Stereo } \\ & 80 ; \text { PZ. } 8 \end{aligned}$ | $\begin{aligned} & £ 33.83 \\ & +£ 3.38 \text { V.A.T. } \\ & \hline \end{aligned}$ |
| Indoor P.A. | Z.60, PZ.8 | $\begin{aligned} & \mathbf{£ 1 4 . 9 3} \\ & +£ 1.49 \text { V.A.T. } \\ & \hline \end{aligned}$ |

Project 80 FM tuner, decoder, and A.F.U. may be added as required


Mount Project 80 on a bookshelf, a loudspeaker, a lampshade base a false wall with two 0.16 loudspeakers ... almost anywhere.

# new thinking in modular hi.fi 

## Stereo 80 pre-amplifier

 and control unit

Each channel has its own separate tone and volume controls or fixing sliders, enabling ideal environmental matching to be obtained. A virtual earth input stage forms part of the up-dated circuitry that ensures the finest possible quality from all signal sources. Generous overload margins are allowed on allinputs. Clear instructions with template are supplied. TECHNICAL SPECIFICATIONS
Size $-260 \cdot 50 \times 20 \mathrm{~mm}\left(10 \frac{1}{4} \times 2 \times \frac{3}{4} \mathrm{~ms}\right)$
Finish - Black with white indicators and transparent SIIders
inputs - Magnetic pick-up 3 mV RIAA corrected. Ceramic pick-up 300 mV Radro 300 mV : Tape 30 mv
Signal/noise ratio - 60 dL
Frequency range -20 Hz to $15 \mathrm{KHz} \pm 1 \mathrm{~dB} .10 \mathrm{~Hz}$ to $25 \mathrm{KHz} \pm 3 \mathrm{~dB}$
Power requirements - 20 to 35 volts
Outputs $-100 \mathrm{mV}+\mathrm{AB}$ monitoring for tape
Controls - Press button for tape radio and P U. Sliders for volume
bass ( 12 dB to -14 dB at 100 Hz ) treble ( +11 dB to -12 dB at 10 KHz )
R.R.P. f11.95 +f1.19

## Project 80 FM tuner



Making the Project 80 F.M. tuner and decoder available separately gives a wider chorce of systems and saves moriey where stereo receptron may not be required. The tuner is a triumph of electronic design and assures excellent performance. The decoder gives a 40 dB channel separation with 150 mV output per channel. Both units may be used with other than Project 80 systems.
TECHNICALSPECIFICATIONS OF TUNER
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Tuning range -87.5 to 108 MHz
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One I.C. equal to 26 transistors
Distortion $-0.2 \%$ at 1 KHz for $30 \%$ moduiation
4 pole ceramic filter in I.F. section
Aerial impedance-75 $\Omega$ or $240-300 \Omega$
Sensitivity - 4 microvolts for 30 dB culeting
Output -300 mV for $30 \%$ modulation
Power requirements -23 to 33 volts
DECODER
Size $-47 \times 50 \times 20 \mathrm{~mm}\left(1 \frac{7}{6} \times 2 \times \frac{3}{4} \mathrm{~ns}\right)$
One 19 transistor I.C.
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R.R.P. $£ 7.45+0.74$

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## Z. 40 \& Z. 60 power amplifiers

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Z.40 TECHNICAL SPECIFICATIONS

Size $-55 \times 80 \times 20 \mathrm{~mm}\left(2 \mathbf{i} \times 3 \mathbf{t} \times \frac{3}{4} \mathrm{~ns}\right) 9$ transistors
Inpui sensitivity -100 mV
Output -15 watis RMS continuous into $8 \Omega(35 \mathrm{v})$
Frequency response $-10 \mathrm{~Hz}-100 \mathrm{KHz}+1 \mathrm{~dB}$
Signal/noise ratio -64 dB
Distortion - at 10 watts into $8 \Omega$ less than $01 \%$
Power requirements - 12 to 35 volts
Z.60 TECHNICAL SPECIFICATIONS

Size $-55 \times 48 \times 95 \mathrm{~mm}\left(2 \mathrm{t} \times 3 \frac{3}{4} \times 3 \mathrm{ins}\right) 12$ transistors
Input sensitivity - $100-250 \mathrm{mV}$
Output -25 watts RMS continuous into $8 \Omega(45 \mathrm{~V})$. Distortion - typically $0.03 \%$
Frequency response -10 Hz to more than $200 \mathrm{KHz} \pm 1 \mathrm{~dB}$
Signal/noise ratio - better than 70 dB
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