PRACTICAL

NOVEMBER 1975


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\hline ${ }^{7400}$ \& \& 0-11 \& 7448 \& ¢ 080 \& 74150 \& <br>
\hline 7402 \& 11 \& 7450
7451 \& ${ }_{13}^{12}$ \& ${ }_{7}^{74151}$ \& $\xrightarrow{60}$ <br>
\hline 7403

7048 \& ${ }^{11}$ \& 7453 \& 13 \& 24154 \& -05 <br>
\hline ${ }_{7} 74054$ \& ${ }^{13}$ \& 7454 \& 14 \& ${ }^{2} 4155$ \& 95 <br>
\hline ${ }_{7} 706$ \& 22 \& 7464 \& \& ${ }^{74156}$ \& 71 <br>
\hline 7407 \& 22 \& 7465 \& 21 \& ${ }_{7} 7161$ \& <br>
\hline 74 \& 14 \& 7472 \& ${ }_{22}$ \& ${ }_{74163}$ \& 05 <br>
\hline 7409 \& 14 \& 7473 \& 26 \& 74164 \& 1.25 <br>
\hline 7411
7401 \& 11 \& 7474
775 \& ${ }^{266} 4{ }_{41}^{26}$ \& 74465 \& 1.25 <br>
\hline 7443 \& 35 \& 7476 \& 26 \& ${ }_{74173}$ \& 15 <br>
\hline 7415 \& ${ }_{2}^{22}$ \& 7483 \& 70 \& 74775 \& 95 <br>
\hline 74 \& 22 \& ${ }^{7485}$ \& ${ }_{8}^{80}$ \& 24176 \& 95 <br>
\hline 74 \& 11 \& 7489 \& 24 \& 7417 \& ${ }^{85}$ <br>
\hline 7422 \& 22 \& 7489 \& - 4 \& S4181 \& ${ }^{80}$ <br>
\hline 7423 \& 22 \& 7491 \& 81 \& ${ }_{74182}$ \& ${ }^{2.50}$ <br>
\hline 7425 \& 22 \& 7492 \& 44 \& 74184 \& 1.55 <br>
\hline ${ }^{3} 248$ \& ${ }_{22}^{23}$ \& 7493 \& 44 \& 74185 \& 1.45 <br>
\hline 7430 \& 12 \& ${ }_{7} 7995$ \& 49 \& 74191 \& 95 <br>
\hline ${ }^{7432}$ \& 22 \& ${ }^{7496}$ \& 55 \& 74192 \& 90 <br>

\hline ${ }_{7} 748$ \& 21 \& | 74100 |
| :--- |
| 74105 | \& ${ }_{1}^{1.25}$ \& ${ }_{74194} 7$ \& 85 <br>

\hline 7440 \& 11 \& 74107 \& ${ }_{27}$ \& 7495 \& 80 <br>
\hline 7441 \& 60 \& 74121 \& 32 \& 74196 \& 1.00 <br>
\hline 7422
7443 \& 55 \& ${ }^{74122}$ \& 50
55 \& $\begin{array}{r}74197 \\ \hline 7198\end{array}$ \& 75 <br>
\hline 7444 \& 60 \& 74125 \& 50 \& 74199 \& ${ }_{1}^{1.70}$ <br>
\hline 7445 \& 75 \& 74126 \& 50 \& 74200 \& 390 <br>
\hline ${ }_{7} 7447$ \& ${ }_{80}^{85}$ \& ${ }_{74149} 7$ \& ${ }_{75}^{68}$ \& \& <br>
\hline \multicolumn{6}{|l|}{LOW POWER} <br>
\hline (2400 \& ${ }^{0} 016$ \& ${ }_{7}^{7445155}$ \& ¢ 0.16 \& ${ }_{7}^{744990}$ \& <br>
\hline 7403 \& 16 \& ${ }_{7} 74251$ \& ${ }_{18}^{18}$ \& ${ }_{74493}$ \& ${ }_{89}^{80}$ <br>
\hline 74104 \& 18 \& 74172 \& 27 \& 74 L95 \& 89 <br>
\hline (1) \& 18 \& ${ }_{7} 74473$ \& ${ }_{38}^{38}$ \& 74498 \& ${ }_{\text {lis }}$ <br>
\hline 74120 \& ${ }_{16}$ \& ${ }_{74178}$ \& 44 \& ${ }_{74 L} 165$ \& ${ }^{1.53}$ <br>
\hline 7430 \& 16 \& 74485 \& ${ }^{85}$ \& \& <br>
\hline 74142 \& 89 \& 74186 \& \& \& <br>
\hline \multicolumn{6}{|l|}{HIGH SPEED} <br>
\hline 3400 \& ¢ 016 \& ${ }^{74 \mathrm{H} 212}$ \& [0 0 \& $7 \mathrm{7aH55}$ \& <br>
\hline 14H04 \& ${ }_{16}^{16}$ \& ${ }_{7}^{74 H 22}$ \& ${ }_{18}^{18}$ \& ${ }_{7}^{74 \mathrm{H66}} \mathbf{7}$ \& 21 21 <br>
\hline 4M08 \& ${ }^{16}$ \& 74 H 40 \& 16 \& 74462 \& 20 <br>
\hline 24H10 \& 16
16 \& ${ }_{7} 74 \mathrm{HH50}$ \& ${ }_{18}^{16}$ \& \& 32 <br>
\hline 44+20 \& ${ }_{16}^{16}$ \& - ${ }_{344532}$ \& ${ }_{20}^{18}$ \& \& <br>
\hline \multicolumn{6}{|l|}{8000 SERIES} <br>
\hline - 8091 \& -33 \& 8274 \& E0.93 \& \& E. 0.38 <br>

\hline ${ }_{\substack{8092 \\ 8095}}$ \& ${ }_{76}$ \& | 8220 |
| :--- |
| 8230 |
| 8 | \& ${ }_{1} 9.42$ \& \& ${ }_{1}^{60}$ <br>

\hline 121 \& ${ }_{4} 9$ \& 8520 \& 71 \& 8830 \& 142 <br>
\hline 130 \& ${ }^{88}$ \& 8551 \& 91 \& ${ }^{8831}$ \& 1.42 <br>
\hline 300 \& 1.18 \& ${ }_{8554}$ \& 1 \& \& ${ }_{73}^{27}$ <br>
\hline 8220 \& ${ }_{1}^{1.92}$ \& ${ }_{8810} 8$ \& ${ }_{48}$ \& 8880 \& ${ }^{3}$ <br>
\hline \multicolumn{6}{|l|}{\multirow[t]{2}{*}{9000 SERIES}} <br>
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\begin{aligned}
& 9002 \\
& 99010
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9309 \\
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\] \& c 049

49 \& ${ }_{9602}^{9601}$ \& ¢0.54 49 <br>
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| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
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| 74.42 | 1-18 | $74 C 160$ | 1-78 | $80 C 97$ | 82 | 4010A | 38 | 4022A | 78 | 4050A | 42 | 4082A | 25 |
| $74 \mathrm{C73}$ | . 85 | 74C161 | 1-78 |  |  | 4011A | 21 | 4023A | 18 | 4066A | 63 | 4528A | 1-14 |
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| U4324 MULTIMETER <br> High sansitivity. <br> 20,0000py Ranges: $0.6 / 1.2 / 3 / 12 / 30$ $60 / 120 / 600 / 1200 \mathrm{~V}$ DC. 3/6/15/60/150/ 300/600/900V AC. Currant: $0.06 / 0.6 /$ $6 / 60 / 600 \mathrm{~mA} A \mathrm{BADC}$ $0.3 / 3 / 30 / 300 \mathrm{~mA}$ 3A AC. Revistonce: 25/500 ohms/0.5/5/50/500k ohms/5 Mohrms. Decibals: -10 to +12 ds . Size $167 \times 98 \times 63 \mathrm{~mm}$. Suppliod compintituctione. <br> OUR PRICE f10.60p/P \& ins 60p |
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 accuracy $1 \%$. $A C$ 1.5\%. Knife edge
pointer, mirror scie. Complate with sturdy, metal carrying case, leads and Instructions.
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## U4315 MULTIMETER

 Sturdy 43 .rangemultimeter for currentend voltaga in DC AC circuits with frea. 45-20kHz
and DC resisand DC resiz-
tence capaci tance
Ranges,
RC 50 ${ }^{0} \mathrm{C} 50$
$0.5 \mu \mathrm{~A} / 1 / 5 / 26$ /
100/560/2500: DC $75 \mathrm{mV} / \mathrm{IV} / 2.6 / 5 /$ 10/25/100/250/500/1000: AC O.5 2.5/5/10/25/100/250/500/10000: DC $300 \Omega$. DC $5 \mathrm{~K} \Omega / 50 / 500 / 5000$; -15 db +2dB. Complete with stosi OUR PRICE $£ 10.80$
 carrying case and leade. manual.
 OUR OHLC. -20 to +81.5 dB . OUR PRICE E13.50P/P \& Ins 60 p

MODEL AF. 105 VOM 50.000 opv. M
scale. Meter scale. Mbtar

protection. | $0 / 3 / 3 / 12 / 60 / 120 /$ |
| :--- |
| $300 / 600 / 1200 \mathrm{VDC}$ | 0/6/30/120/ $300 / 600 / 1200 \mathrm{VDC}$. $0 / 30 \mathrm{~A}$

$60 / 6 / 6 /$ $60 / 300 \mathrm{~mA} /{ }^{2}$
$12 \mathrm{Amp} .0 / 1 \mathrm{KK}$ 12 Amp. $0 / 10 \mathrm{~K}$ -20 to +17 dB OUR PRICEf13.50P/P \& Ins 60p

## U4313 MULTIMETER

 High sonsitivity(20,000 opvon DC And 2,000 opv on AC and on DC and $2.5 \%$ on AC. R
DC and ${ }_{\text {DC }}^{\mathrm{DC}} \mathrm{and}$
currant
0.6 mA
3/15/60/
300/1.5A:
VC and A
1.5V/3/7.5/
$1.5 / 30 / 60 / 150 / 300 / 600$

16/30/60/150/300/600; DC resis-
tance $1 \mathrm{kohm} / 10 / 100 /, 000 .-10$ to tal
+12 dB , etc. Complete with steel OUR PRICE $£ 14.90$

##  50 OUR PRICE $£ 15.05$ P/P \& ins 60p <br> U4317 MULTIMETER <br>  <br> Knifo edget pointerk 86 m  Ranges 100mVI $0.5 / 2.5 / 10 / 25 / 50 / 100 / 250 / 500 / 1000$ VDC. $0.5 / 25 / 10 / 25 / 50 / 100 / 250$ $5 \mathrm{DC} .00 .5 / 2.5 / 10 / 25 / 50 / 100 / 250 /$     OUR PRICE $18.35 \mathrm{~F} / \mathrm{P} \& \operatorname{lng} 60 \mathrm{p}$ <br> 

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KAMODEN 360 MUL TIMETER High sonsitivity
DC 100 konm
AC $10 \mathrm{kcohm} /$
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OUR PRICE $£ 18.90$ P/P \& Ins 60 p
Modet HT100B4 MUL TIMETER Ovarload protected.
shock proot shock Proot circruits. 9.5uA Mater with
mirror seble Sensivivity
100 kV . Pol irity chana 1wokV. Pol erity ch innge/
switch. Aaness: $0.5 / 2.5$ / 1/550/250/500/1.000
Volts DC $2.5 / 10 / 50 /$ Volts DC. 2. $2 / 10 / 50$,
$250 / 1,000$ Volts AC. DC resistence: 0-20i $200 \mathrm{k} / 2 / 20 \mathrm{Meg}$ ohms
DC current:- $10 / 250$ DC current:- 10/250. A/2.5/25/250 to +62 dB . Operates from $2 \times 1.5 \mathrm{~V}$ batreries. Size: $180 \times 134 \times 79 \mathrm{~mm}$. OUR PRICEE21.50p/P \& Ins 60 p

## MODEL C7080EN



$50 / 250 / 1000 /$
500 DC
$0 / 2.5 / 10 / 50 / 250$
$0 / 2.5 / 10 / 50 / 250 /$
$1000 / 5000 \mathrm{~V} \mathrm{AC}$. $0 / 500 \mathrm{~A} / 1110 / 10$
$100 / 500 \mathrm{~mA} / 10 \mathrm{~A}$

$10+50 \mathrm{~dB}$
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 Handy SWB meter fortransmiter ment, with butitiona ditis 5 strongth moter. Accursicy 5\%, 'mpadinc. 52 . Inc
ator 100 AA OC. Full schate 5 mection colllapsibl
antenna. Sizo $145 \times 50 \times$ 3ntenna,
60 mm.
OUR PRICE f4.55 P/P \& Ins $60_{p}$
U4341 Multimeter \&
 0.6/2/6/20/60/200 Bettery operated, Supplied complote with probes, losch and stoel caryin OUR PRICE $11.85 \mathrm{P} / \mathrm{P} \& \operatorname{Ins} 6^{\circ} \mathrm{O}_{\mathrm{P}}$

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TRANSISTOR TESTER High quality
instrument to
test reversh loak
currant and DC
current. Amplification fector of
NPN, PNP, diodes. transistors, SCR'
atc. $4^{*}$ square cleart scmie meter
Operates from Operates from
internal batteries Complete with

carrying handle.

## OUR PRICE f 18.

S
TRANSISTOR TESTER
100.000 opv. Mirror
scale Overiod

$.0 .6 / 3 / 12 / 30 / 120 /$
$600 \mathrm{~V} 0 \mathrm{C} .0 / 6 / 30 /$
120

$3007 \mathrm{~A} / 6 / 12 \mathrm{~A} D \mathrm{DC}$
$0 / 10 \mathrm{k} / 1 \mathrm{Meg} /$
100 Meg.
100 Meg
$-2010+50 \mathrm{~A}$
$0.01-0.2 \mathrm{MFD}$
 and ICO. Compterto
batteries and leats.
OUR PRICE £22.65p/P \& Ins 60 p CI5 PULSE OSCILLOSCOPE For dieptay of pulsed
and priodic wave
forms in forms in elecertonic
fircuits VERT. AMP.
 Sensitivity at 100 kHz
VRMS $/ \mathrm{mm}: 0.1-25 ;$
HOR HOR. AMP. Ban
width: 500 kHz . widiti
Sonsivity ay 100 kH
VRMS $/ \mathrm{mm}: 0.3-25$

1-3000unec. Frem ranning 20-200
kHz in nine ranges. Calibrator pips:
$220 \times 360 \times 430 \mathrm{~mm} .115-230 \mathrm{AC}$.
OUR PRICE 547.50 P/P


Will measure $A C$ and $D C$ volts. $A C$ and DC current, and resistance in a
total of 20 ranges. The large light emitting diode display will read up to 1999 and automatically indicate polarity. Indication of positive and negative overload is also provided. The instrument is fiteed with a comblned carrying handle and bench stand and sockets are provided for the connection of an extarnal power supply
RANGES:
DC VOLTS: 1 v .10 .100 v .1000 v AC VOLTS: $1 \mathrm{v}, 10 \mathrm{v}, 100 \mathrm{v}, 1000 \mathrm{v}$ 100 mA .1000 mA
AC CURRENT: 1 mA .10 mA
$100 \mathrm{~mA}, 1000 \mathrm{~mA}$.
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OUR PRICE E63.70[P/P \& Int 50p
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on 4 bunds.
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Sine wave 10 V . Sine wave 10 V .
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 or tuner
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win stereo headphone outputs and sparate volume controls for aach
channel. Operates from 9 V battery. channal. Operates from 9 V battery
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Dynamic, remote start/stop,200 ohms, $100-10 \mathrm{kHz}, 6 \mathrm{mV}$ outpuk miniAtLRE TRANSISTOR MOOULES Mieroohone pre-ampllifler Power amplifier
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Super Tweeters, 8 onms, $\mathrm{c} / \mathrm{f} 7 \mathrm{kHz}$,
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128


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(P.E. Feb. 1973 to Feb. 1974)

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Two Linear Yoltage Controlled Oscillators and one Inverter-all 3 circuits: Two Ramp Generators and Two Input Amplifiers-all 4 circuits
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PCB (holds both sircuies)
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Sprine Line unit for Reverb Amp
Ring Modulator
Pakk Leval Meter Circuit
$100 \mu \mathrm{~A}$ Panel Meter
PCB for Rey., R-Mod, \& Meter Ccts.
Envelopo Shimper, 55.24 ; PCB,
Envelope Shaper, 55.24 ; PCB, \&1.42 THE SYNTHESISER KEYBOARD CIRCUITS Can be used without the Main Synthesiser to make an independent musicalinstrument PCB for borh log VCO's Divider, 2 Hold Circuits, 2 Modulation Amplifiers, Mixer and 2 Envelope Shapers $\& 19.46$ PCB (Holds the first 6 circuits) PCB for borh Envelope Shapers Keyboard Stabilised Power Supply Printed Circuiz Board

## SYWTHESISERS AND KEYBDARDS

## P.E. JOANNA

(P.E. May to Aug. 1975)

The new electronic piano that has switchable alternative voicing of Piano Honky-Tonk and Harpsichora. All PCB's are "as published"
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Envelope Shapers
12 sets (full requirement)
Set of 12 PCB's (full requirement)
Voicing and Pre.Amplifier Circuits Voicing and Pre-Amp
PCB for above circuits

Remaining circuits: prices in lists.

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$\begin{array}{lllll}\text { Contact Each Sot } & \text { Set } & \text { Set } \\ \text { SP } & 20 p & 67.40 & 69.80 & 412.20 \\ 2 P & 24 p & 48.86 & E 11.76 & \text { E14.64 }\end{array}$
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C1.32
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C 1.25
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P.E. SYNTHESISER<br>SEE OUR ADVERTISEMENT ON OPPOSITE PAGE

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Will modify an audio signal not only from a guitar but from any audio source, producing 8 different switchable effects that can be further modified by the low-priced sound effects units in our range.
Component set with special foor operated Switches
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$43 \cdot 87$

Component sets include all necessary resistors, capacitors, semiconductors, potentiometer
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Tempo. Timing and Logic circuits
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Printed circuit board
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For use with the above Phasing Unit co automatically control the rate of phasing.
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## P.E. JOANNA

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## WIND AND RAIN UNIT

A manually controlled unit for producing the abovenamed sounds.
Component set incl. PCB

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\&2.20
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Electronic Piano:
Pre-amp PCB (P.E. Oct, 1972)
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CBS SQ Decoder PCB (P.E. Sep
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Multi-function circuits that, with the use of other external equipment, can serve as lie devector,

Pre-Amplifier Module
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Compo
PCB for above components
Power Supply
\&7.16
41.55

## P.E. MINISONIC <br> SEE OUR ADVERTISEMENT ON OPPOSITE PAGE

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9 inch spring unit
Panel meter ( $50 \mu \mathrm{~A}$ ) (opcional)
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Elac din tweeter TW4
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Fane Pop 55 60W 12 in
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Fane Crescendo 15in 100W
Fane Crescendo 8in sin dic roll surr
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Ket DN8
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Kel DN12
Kef DN13
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STC 4001G Super Tweeter
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Radtord FN1ta
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Richard Allan CGAT d/c r/surt
2 in 75 ohm
7 in $x 4 i n ~$
3
$\sin \times \sin 3$ or 8 ohm
$10 \mathrm{in} \times 61 \mathrm{n} \mathrm{3}$. 8 or 15 ohm

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6W IC audio amp with
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5 witched outputs of 6 , $7 \frac{1}{2}$ and $9 V$ at 250 mA ack plug and socket out
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AADIO MODELS
50 mA output with
poppett battery connec-
ors for transistor radios
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64.43.

$\mathbf{4}$.

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$\begin{array}{cc}\text { PZB } \mathbb{E 8 . 2 0} & \text { Project } 80 \text { Quadraphonic decoder } \\ & \mathbf{E 2 0 . 9 7}\end{array}$
S-DECS and T-DECS
S.DeC 62.34.

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$\mu$-DeC $A 44.55$
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## SWANLEY ELECTRONICS

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## IP



MONO ELECTRICAL CIRCUIT DIAGRAM WITH INTERCONNECTIONS FOR STEREO SHOWN


The HY5 is a complete mono hybrid preampllfier, ideally suited for both mono and stereo applications. Internally first contains frequency equatisation and gain correction while the second caters for tone control and balance.

TECHNICAL SPECIFICATION
Inputs: Magnetic Pick-up 3 mV R|AA; Ceramic Pick-up 30 mV : Microphone 10 mV ; Tuner 100 mV ; Auxillary $3-100 \mathrm{mV}$. Inputimpedance $47 \mathrm{k} \Omega$ at 1 kHz . Outpute: Tape 100 mV Main output Ocb (0.775V RMS). Active Tone Controls $0.5 \%$ - 18 Hz Signal/Nolse Rällo: 68 dB Overiod Cepe bility: 40 db on most sensltive input. Supply Voltage $\pm 16-25 \mathrm{~V}$
PRICE $£ 4.75$
$+£ 1 \cdot 19$ VAT
P. \& Pree
TWO YEARS' GUARANTEE ON ALL OUR PRODUCTS
The HY50 is a complete solid state hybrid Hi-F amplifle incorporaling its own high conductivity hestsink her metically sealed in black epoxy resin. Only five connections mere provided input output. power lines and earth.

TECHNICAL SPECIFICATION
Output Power: 25W RMS into $8 \Omega$ Load Impedance 4-16ת. Input Sensitivity: Odb ( $0-775 V$ RMS). Inpul Impedance: $47 \mathrm{k} \Omega$. Distortion: Less than $0.1 \%$ at 25 W typically $0.05 \%$. Signal/Nolse Rallo: Better than 75 db Fraquency Response: $10 \mathrm{~Hz}-50 \mathrm{kHz}=30 \mathrm{~b}$. Supply Voltage mm
DBIP + +1.55 VAT TECHNICAL SPECIFICATIONS

The PSU50 incorporates a specially designed transformer The PSU50 incorporates a specially designed transt


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VALVE MAIL ORDER CO．
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\begin{tabular}{|c|c|c|c|c|c|c|c|c|}
\hline 1N21 & \[
\begin{aligned}
& 39 \\
& 0.17
\end{aligned}
\] & \[
\text { Ar'Z11 } \quad{ }_{10}^{20}
\] & BY： 13 & \[
\begin{aligned}
& \text { Ap } \\
& 0.25
\end{aligned}
\] & 0AZ205 & \[
\begin{aligned}
& 5 \\
& 0.45
\end{aligned}
\] & 281741 & \[
\begin{aligned}
& 8_{0} \\
& 0.10
\end{aligned}
\] \\
\hline in23 & 0.35 & AF\％12 2.00 & BYZı & 0.45 & OAzeog & 0.15 & 2．s271 & 0.18 \\
\hline \(1 \mathrm{~N}_{5} \mathrm{E}\) & 0.88 &  & HY\％ & 0.40 & 0azent & 0.45 & \({ }_{\text {Zr21 }}\) & 0.25
0.25 \\
\hline IN263 & 0.50 & AXY24 0 & ВYZE！ & 0.40 & oazsix & 0.40 & ZTX \({ }_{\text {2rivi }}\) & － 0.12 \\
\hline 1N256 & 0.50 & Antey 0.30 & － yzia \(^{\text {a }}\) & 0.42 & OAZE0S & 0.40 & ZTX108 & 0.04 \\
\hline 1 N 645 & 0.16 & AnY3i 0.25 & BYZ： & 1．25 & OAZSIO & 0.40 & 2TX3m & 0.18 \\
\hline 1N725A & 0.20 & AsY50 0.20 & HY\％\({ }^{\text {\％}}\) & 0.60 & OA\％\({ }^{\text {O }}\) & 0.40 & ZTX304 & 0.24 \\
\hline 1 N 914 & 0.08 & Anyijs 0.40 & BY Y ¢ & 0.10 & OAZさせ！ & 0.45 & ZTX \({ }^{\text {cou }}\) & 0.18 \\
\hline IN400： & 0.12 & Anyīa 0.20 & C111 & 0.55 & OAZz2\％ & 0.46 & 7TX \({ }^{\text {a }}\)［03 & 0.16 \\
\hline 18113 & 0.25 & AYYE5 0.20 & （1knfus & 0.35 & OAZEP4 & 0.45 & 7.2 & 25 \\
\hline 1820 & 0.88 &  &  & 0.50
1.90 & OAZ 24
OAZ \(2+2\) & 0．85 & ITREO & ED \\
\hline 201371 & 0.75 & AHZ21 \(\quad 1.00\) & csios & 3.50 & \(0 \mathrm{O} / 244\) & 0.25 & clacu & \\
\hline 26381 & 0.22 & \({ }^{\text {ASL23 }} 00.75\) & D150以 & 0.15 & OAzCut & 0.15 & ：3010 & 0.18 \\
\hline 20.414 & 0.30 & AC．04 1.00 & L1Duas & 0.15 & OAZ：290 & 0.88 & － \(\mathrm{SnL}_{1}\) & 0.16 \\
\hline 2 C 417 & 0.25 & Al＇yior 1.50 & bluodi & 0.25 & OCl1） & 1.00 & －302 & 0.16 \\
\hline \(2 \mathrm{NH04}\) & 0.22 & \(\begin{array}{ll}\text { BC10\％} & 0.14\end{array}\) & nduat & 0.40 & OClif & 1.00 & －103 & 0.18 \\
\hline \(2 \mathrm{~N}^{695}\) & 0.16 & \(\begin{array}{ll}\text { BrC108 } & 0.18 \\ \text { Be．} 109 & 0.14\end{array}\) & \({ }^{10}\) & 0.38 & Oc19 & 0.50 & T304 & 0.88 \\
\hline 2 N 69 N & 0.80 & \(\begin{array}{ll}\text { Bechas } & 0.14 \\ 0.15\end{array}\) & 1：173 & 0.38
0.10 & OCx & 1.00
1.25 & －\({ }^{\text {ans }}\) & 0.98
0.48 \\
\hline 2 N 703 & 0.12 & \(\begin{array}{ll}\text { BCIIS } & 0.20\end{array}\) & 1：115 & 0.10
0.83 & OC2， & 1.10 & \({ }^{\text {\％}} 807\) & － 0.42 \\
\hline 2N706A & 0.12 & 15116 0.80 & （110x & 0.25 & OC\％ & 0.40 & － 40 N & 0.88 \\
\hline 2 NTON & 0.15 & BC1181 0.28 & （：1）12 & 0.10 & OC\％\({ }^{\text {2 }}\) & 0.40 & \％404 & 0.88 \\
\hline 2 N 704 & 0.40 & Belis 0.80 & （：ETIい！ & 0.50 & \(\mathrm{OCH}^{2}\) & 0.86 & \(2+16\) & 0.18 \\
\hline 2 N 1091 & 0.65 & BC121 0.20 & （：ETIM\％ & 0.40 & 0じっ！ & 0.85 & 2111 & 0.85 \\
\hline 2 N 1131 & 0.25
0.24 & BC122 0.20 & （GETIH & 0.85 & OC3i） & 0.40 & ：112 & 0.80 \\
\hline \({ }_{2}^{2 N 1132}\) & 0.24
0.18 & Bc125 0.68 & CETIA & 0.30 & 0） 0 \％ & 0.55 & 7417 & 0.80 \\
\hline \[
\begin{aligned}
& 2 \mathrm{~N}_{13012} \\
& \text { N } 13003
\end{aligned}
\] & 0.18
0.18 & \(\begin{array}{ll}\text { He＇124；} & 0.65 \\ \text { bel }\end{array}\) & CETIIS & 0.90 & Oc：313 & 0.80 & －116 & 0.88 \\
\hline 2 N 1304 & 0.28 & \(\begin{array}{ll}\text { BC140 } & 0.65 \\ \text { HC14：} & 0.10\end{array}\) & （：ET116 & 0.85 & OC＋1 & 0.88 & －41\％ & 0.88 \\
\hline 2 N 1305 & 0.82 &  & （：ET120 & 0.80
0.80 & O184 & 0.40 & － & 0.18
0.85 \\
\hline 2Ni306 & 0.89 & HCi49 0.10 & （iETrifi & 0.40 & Octid & 0.70
0.20 & －\({ }^{\text {cos }}\) & 0.87 \\
\hline \(2 \mathrm{~N} 130{ }^{\circ}\) & 0.28 & Be＇si 0.14 &  & 0.80 & OC＇4s & 0.17 & － 725 & 0.87 \\
\hline \({ }_{2} 2 \mathrm{~N} 1308\) & U．28
0.78 & belas 0.12 & （EETAN 1 & 0.25 & \(\mathrm{OCH}^{4}\) & 0.20 & 7427 & 0.87 \\
\hline \({ }_{2}^{2 N 2147}\) & 0.78 &  & （：ETMA： & 0.35 & OC45， & 0.18 & －428 & 0.40 \\
\hline 3 N 218 n & 0.78 &  & （aFTMm． & 0.40 & \(0 \mathrm{Oc}+6\) & 0.87 & －436 & 0.16 \\
\hline 2N2\％\({ }^{\text {d }}\) & 0.28 & \(\begin{array}{ll}\text { BCy } \\ \text { BCY } 3: 2 & 0.85 \\ 0.85\end{array}\) & GEX44 & 0.08 & 06\％ & 0.60 & － 432 & 0.97
0.87 \\
\hline 2 N 2211 & 0.25 & \begin{tabular}{ll} 
BC＇Y33 & 0.88 \\
\hline
\end{tabular} &  & 0.45
0.45 & OCat & 0.60
0.60 & － 4 ＋14 & 0.87 \\
\hline \(2 \mathrm{~N}^{23694}\) & 0.18 & \(\begin{array}{ll}\text { Нс＇y34 } & 0.46\end{array}\) & CiJim & 0．80 & Ocest & 0.60
0.60 & －\(+3 \times 4\) & 0.87 \\
\hline \({ }_{2} \mathrm{~N}^{2} 2441\) & 1.9
0.75 & BC＇Y34 0．65 & （iJam & 0.80 & 以ぐ11 & 0.18 & － 440 & 0.22 \\
\hline 2N2F46 & 0.60 & HCY3H 1.60 & （ iJ 5.1 & 0.85 & O6： 1 & 0.18 & It1AN & 0.88 \\
\hline 2 N 2904 & 0.20 & \(\begin{array}{ll}\text { BCY40 } & 0.80 \\ \mathbf{H C Y} \\ 0.80\end{array}\) & iJTM & 0.50 & 0 O－ & 0.28 & －442 & \({ }^{0.78}\) \\
\hline 2 N 2904 A & 0.25 & \(\begin{array}{ll}\text { BCY49 } \\ \text { BCYOf1 } & 0.80 \\ 0.18\end{array}\) & Lid 1105 & 0.60
0.80 & OC\％ & 0.60 & － 4 ， & 0.18
0.18 \\
\hline 2N2906 & 0.20 &  & MATI告 & 0.20
0.20 & Oct & 0．80 & － 403 & 0.16 \\
\hline \(2 \mathrm{~N} 290{ }^{-}\) & 0.28 & BCzin 0.60 & MATIII & 0.25 & OC： & 0.80
0.80 & －454 & 0.18 \\
\hline 2 N 29.4 & 0.18 & BCzzil 0．66 & MAT12 & 0.20 & 以宁 & 0.64 & T－4610 & 0.16 \\
\hline 2 N 2925 & 0.15 & 3 \({ }^{\text {a }}\) & Mati21 & 0.25 & に年 & \(0 \cdot 25\) & 54510 & 0.86 \\
\hline \({ }_{2}^{2 N} 2 \mathrm{Nag} 6\) & 0.12
0.48 & B1123 1．00 & MJF：3才10 & 0.47 & 11－5！ & 0.80 & －472 & 0.88 \\
\hline \({ }_{2} \mathrm{~N} 30046\) & 0.48
0.45 & 1以リ4 0．68 & MJE．：－ & 0.63 & OCx1 & 0.29 & 543： & 0.41 \\
\hline 2N3046 & 0．11 & H2Y11 1.45 & MJE： & 1.87 & 0＊11 & 0.28 & 2474 & 0.42 \\
\hline 2N3：05 & 0.15 &  & MJF35 & 0.77 & （x，wy & 0.20 & －475 & 0.69 \\
\hline 2 N 3 O 106 & 0.11 &  &  & 0.40
0.86 &  & 0.18
0.45 & － & 0.60 \\
\hline 2 N 3707 & 0.18 & Br1＊ 0.85 & M1PF｜o4 & \({ }_{0.35}^{0.86}\) & OMN\％ & 0.45
0.88 & －4＊： & 0.87 \\
\hline \({ }_{2} \mathrm{~N} 3704\) & 0.10 & \(\begin{array}{ll}13 \mathrm{~F} \\ 184 & 0.28\end{array}\) & 31PF10\％ & \({ }_{0}^{0.38}\) & （1） & 0.88
0.25 & － \(4 \times 3\) & 1.10 \\
\hline \[
\begin{aligned}
& 2 N_{3710} \\
& 2 \mathrm{~N}_{3} 111
\end{aligned}
\] & 0.11 & 3FIms 0．22 & －kTl24 & 0.45 & （16x： & 0.27 & ism & 1.00 \\
\hline \(2 \mathrm{~N}_{2} 11\)
2 N 3 HI & 0.11
0.88 & 15194 0.10 & －＜T1建 & 0.80 & 以为4 & 0.80 & Evkij & 0.47 \\
\hline 2 N 4288 & 0.30 & \(\begin{array}{ll}13+195 \\ 3 F 196 & 0.18 \\ 0.15\end{array}\) & NKT：II & 0.25 & O－1 14 & 0.38 & －490 & 0.65
1.00 \\
\hline 2 N 5027 & 0.68 &  &  & 0.25
0.24 &  & 1．00 & －4， & 0.70 \\
\hline \({ }_{2} \mathbf{N} 50\)（0am & 0.38 & BFNGI 0.25 &  & 0.24
0.40 & （R1） & 1.10
0.40 & －4938 & 0.70 \\
\hline 29304 & 0.60
1.16 & НमНяs 0.85 &  & 0.45 & \(0 \cdot 1+11\) & \({ }_{1}^{1.14}\) & 2494 & 0.80 \\
\hline 28304 & 1.16
0.76 & MFX13000 &  & 0.45 & C－14 & 1.80
0.80 & － 4 ＋15 & 0.80 \\
\hline 28703 & 1.00 &  & NKTers & 0.83 & 00］4 & 0.20 & － 7496 & 0.96
8.87 \\
\hline AALEM & 0.80 & \(\begin{array}{ll}\text { 13FXe9 } & 0.88 \\ 1380 & 0.28\end{array}\) & NKTE22 & 0.30 & 0．1\％ & 0.80 & ¢49\％ & 8.87
1.88 \\
\hline AAZ！ & 0.75 & \(\begin{array}{lll}15 \mathrm{X} \\ 18 & 0.98\end{array}\) & －\({ }^{\text {NKT }}\) & 0.25
0.24 & 9017 & 0.30
0.64 & －115： & 0.45 \\
\hline AAZ13 & 0 & \(\begin{array}{ll}13+863 & 0.50\end{array}\) &  & 0.22
0.20 & O） & 1.00 & 741111 & 0.68 \\
\hline \({ }_{\text {ACl }}{ }^{\text {ACl }}\) & 0.61 & BFX 400.26 & 入кт & 0.20 & （1） & 0.80 & 34111 & 0.88 \\
\hline AClizt & 0.88
0.85 &  &  & 0.20 & いでき & 0.68 & －4114 & 0.90 \\
\hline Aclis & 0.15 & 13FXH6 0.85 & －\(\times 1.2\) & 0.20 & 6゙きロ4 & 0.65 & 31119 & 1－68 \\
\hline ACl8： & 0.21 &  & XKT2\％ & 0.25 & \(0{ }^{0}\) & 1.00 & 7412
\(7+1201\) & 0.50
0.70 \\
\hline Acins & 0.20 & \(\begin{array}{ll}13 \mathrm{~F} \\ 10 & 0.60\end{array}\) &  & 0.20
0.85 & 06906 & 1.10 & － \(412 \times 3\) & 1.00 \\
\hline Acyli & 0.40
0.97 & HFYil 0.60 & Хкт：101 & 0.85 & ¢9＊＊ & 0.20 & ＋ \(41+1\) & 0.80 \\
\hline ACYJ & 0.27
0.87 & 18F17 0.40 & Хкт：34 & 0.75 & いくすこ & 0.80 & 74145 & 1.26 \\
\hline ACPy & 0.87
0.82 & \(13+1 / 40\) & －кт＋13 & 0.70 & い「いう & 1．20 & 741510 & 1．75 \\
\hline ACY21 & 0.82 & \(\begin{array}{ll}18+114 & 0.65 \\ 1454 & 0.45\end{array}\) & － CT Tit & 1.00 & OHP10 & 0.60 & I 1515 & 1.00 \\
\hline ACre & 0.18 &  & ※KTti\％ & 0.80
0.80 &  & 0.55 & － & 2.00
1.00 \\
\hline ACY＇\％ & 0.25 & \(\begin{array}{ll}14 \mathrm{~F} 51 & 0.21\end{array}\) & ※кт涪 & 0.80
0.85 &  & 0.48
0.20 & － 415 s & 1.00 \\
\hline \({ }_{\text {ACPY }}{ }^{\text {a }}\) & 0.25
0.78 & 13F151 0.20 & 祖т曻 & 0.88 & cxis & 0.20
0.45 & －\(+15 \%\) & 0.95 \\
\hline ACY\％ & 0.88
0.82 & BFYSE 0．20 & \({ }^{\text {OAF }}\) & 0.78 &  & 0.45 & 741\％ & 2.62 \\
\hline ACY41 & 0.22 & \(\begin{array}{ll}13+153 & 0.17 \\ 13 & 0.54 \\ 142 & 0.88\end{array}\) & OAS & 0.12 & －x \(6+10\) & 0.60
0.76 & i 417 & 1.67 \\
\hline ACH4 & 0.32 & \(\begin{array}{lll}13 \mathrm{~F} 164 & 0.88 \\ 15+464 & 0.81\end{array}\) & OAt\％ & 0.08 & NX6＋1 & 0.76 & \％ \(711 \%\) & 1．10 \\
\hline ADIA & 0.50 & 185190 & OAII & 0.10 & nxise & 0.60 &  & 2．00 \\
\hline ADIsy & 0.60
0.44 & & OATI & 0.20
0.15 & \(\pm \times 644\) & 0.85 & －+141 & 2.00 \\
\hline AD161 & 0.44 & \(\begin{array}{ll}\text { B8X：7 } & 0.60 \\ \text { HSX6II } & 0.93\end{array}\) & OAT3
Qait & 0.15 & － N （its & 0.85 & －4142 & 2.00 \\
\hline ADIGN
AFIog & 0.44
0.30 &  & OAFI4
OAFS & 0.18
0.10 & Ticis & 0.20 & \％ 4194 & 2.00 \\
\hline AF14 & 0.85 & 8یצ\％ 0.17 & （1）AM & 0.18 & V15／314 & 0.78 & i－194 & 1．30 \\
\hline AFIIS & 0.25 & 3N1E\％ 0.20 & （1，Amis & 0.15 & V30／2117 & 0.75 & － & 1.10
1.20 \\
\hline AF116 & 0.95 & H8Y：1 0.60 & 0. ¢4\％ & 0.15 &  & 0.50 & － 4195 & 1.20 \\
\hline AFllis & 0.24
0.67 &  & OAYII & 0.07 & V60！201P & 0.75 & －119\％ & ¢．77 \\
\hline AFliy & 0.20 &  & 0.491 & 0.07 & X． 101 & 0.10 & \(7+194\) & 2.52 \\
\hline AFI24 & 0.80 & 0.75 & 10， \(\mathbf{H}_{0}\) & 0.07 & － & 0.18 & & \\
\hline AF129 & 0.30 & \multirow[b]{2}{*}{MT「\％9／ル01R} & 0．12011 & 0.08 & XA151 & 0.16 & Plug in & \\
\hline AF126 & 0.80 & & 0．AE02 & 0.06 & X Al 15 & 0.15 & & \\
\hline AFl2＇ & 0.30 & 0.76 & 0 CH 10 & 0.20 & XAlfil & 0.85 & & 0.16 \\
\hline AF139 & 0.41 & HTエ̇4／400R & O．AE11 & 0.35
0.50 & X \(\times 18101\) & & 1614111 & \\
\hline \({ }^{\text {AFIFH }}\) AF／9 & 0.56 & \(\begin{array}{ll}\text { Hriog } & 1.10 \\ 0.27\end{array}\) &  & 0 & X 8101
\(\times 180 \%\) & 0.43
0.80 &  & －17 \\
\hline AFian & 0.65 & BY1石 0.14 & OAZ\％O： & 0.45 & X Br 103 & 0.85 & & \\
\hline AF181 & 0.60 & 13Y127 0.12 & 0．7z：03 & 0.45 & X 8113 & \(0 \cdot 30\) & & \\
\hline AF188 & 0.48 & HYIN： 0.85 & oszrot & 0.45 & x Bl 21 & 0.43 & & \\
\hline
\end{tabular}

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Phone or write for details（A S．A．E．helos）
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DLTaEE 0.3 in Red Common Cathode 7 segment LED daplay only 85 p FNDS00 0.5 in Red CC LED \(£ 1.50\) MAN3M 0.13 in Red CC LED 48 p
 WKS0253 4 or 6 digit 12 or 24 hr format alarm clock IC with snooze

AY51224 4 digit clock IC \(\mathrm{ES} \cdot 25\) MM5314 46 diglt clock IC 84．44
SOLOERCON IC PLN SOCKET8
The sensible method for lowest cost sockets for ICs，diaplays，CMOS，TTL（nylon supports avaliable if required；samplea enclosed with any pin order）．Strip of 100 pins for \(50 p ; 400\) for \(£ 2 ; 1,000\) for \(£ 4 ; 3,000\) for \(£ 10 \cdot 50\).

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RELAYS SIEMENS, PLESSEY, E
MINIATURE RELAYS Col ( 1 (
Coil
Cohm Col. (2) Working d.ci, volts Contacts \begin{tabular}{l} 
Price \\
\hline
\end{tabular} HD \(=\) \begin{tabular}{l|l|l|l|l}
9.000 & \(40-70\) & \(2 \mathrm{c} / \mathrm{o}\) & 65 p \\
& 15 k & \(85-110\) & 6 M & \(6 \mathrm{P}^{*}\)
\end{tabular}

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3 h.d. c/o contacts. Price 75p. Post 20p. Octal plug in base \(15 p\) extra.
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\(\frac{20}{3} \mathrm{clo} 5\) amp contacts. Sealed. MIg. ISKRA CI. 25. Post 20 p Base 15 F extra.
CLARE-ELLIOTT TYPE RP7641 GB

Miniature relay. 675 ohm coil, 24 Volt D.C. 2 c/o. 70p post paid.
110 V . 2 cio. 20 amp contacts. \(\mathbf{6 1 . 2 5}\). Post 10 p Many others from stock-phone for details.

\section*{C, O MICRO SWITCH}

VERY SPECIAL OFFER. Mfg. by
C.E.M. 3 amp 250 volt. io amp 125 C.E.M. 3 amp 250 volt. 10 amp 125
volt. 50 for \(\epsilon 3\). Post \(36 p\). 100 for 65.
Post 50 . 1,000 for \(\epsilon 45\). Post paid.
 SUIk purchase means LOW! LO SUB-MINIATURE REED
RELAY 3-9V d.c. 250 ohm Coi RELAY 3-9V d.e.
Single make, size \(1 \frac{1}{2} \times\)
Outstanding Value \& for six, \(\mathrm{Cl} \mid .50\) for ten. Post Ifp. (Min. order

\section*{LATCHING RELAY}

Twin latching relay, "flipoflop" \(2 \mathrm{c} / \mathrm{o}\)
each relay. Mains contacts. 115 V each relay. Mains contacts. 115 V
A.C. or 50 V D.C. operation. 240 V
A.C. with 2.5 K resistor 85 .


\section*{TRIAC}

Raytheon Tag symmetrical Triac. Type TAG.
\(250 / 500 \mathrm{~V}, 10 \mathrm{mp}, 500\) p,i,y. Glass passivated plastic \(250 / 500 \mathrm{~V}\), \(10 \mathrm{amp}, 500 \mathrm{p} . \mathrm{i} . \mathrm{v}\). Glass passivated plastic triac. Swiss precision product for long term
reliability \(£ 1.00\). Post 10 p. (Inclusive of Data and application sheet.) Suitable Diac 18p.

\section*{230/250 VOLT A.C. SOLENOID} Approximately \(1 \frac{1}{2} l \mathrm{~b}\) pull
Price \(£ 1.00\). Post 20 p
HEAVYDUTY TYPE. 10 lb . (approx.) pull. \(\mathbf{\text { 2.50. }}\)

\section*{24 VOLT DC SOLENOIDS}

UNIT containing I heavy duty solenoid approx. 251b pull \(I\) inch travel. Two \(x\) approx. Itb pull \(\frac{1}{2}\) inch
travel. 6 approx. \(40 z\). pull \(\frac{1}{2}\) inch travel. One cravel. 6 approx. 4ox. pull inch travel. One 24 volt d.c.. heary duty single make relay.
E2-50. Post EI. ABSOLUTE BARGAIN.
COIN MECHANISM (Ex London Transport)
Unit containing, selector mechanism for 1p, 2p and 5p coins. Microswirches, relays, solenoid operated hopper. 24 volt D.C. Precision built to high standard. Incredible VALUE at only \(\mathrm{E2}\).50. Post \(£ 1 \cdot 00\).
VAT \(\mathbf{2 5 \%}\). (Total price inc. VAT and post \(£ 4.21\) ).
CENTRIFUGAL BLOWER
Mrg by Smiths Industries. \(230 / 240 \mathrm{~V}\) a.c. Miniature Model. Series SE/200.
size \(95 \mathrm{~mm} \times 82 \mathrm{~mm} \times 82 \mathrm{~mm}\). Aperture 38 mm
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Mfg by Airflow Developments Led
Precision made, continuously rated, smooth running. \(230 / 240 \mathrm{~V}\) a.c. motor, 80 c.f.m. As illustrated but with round aperture, \(\mathbf{£ 6 - 5 0 \text { . Post } 7 5 \mathrm { p }}\)
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Build a Strobe Unit, using the latest type Xenon white light flash tube. Solid state timing
triggering circuit. \(230 / 250 \mathrm{~V}\) a.c. operation. HY-LYGHT STROBE MK III
iarge rooms, halls and urilises a silica tube, printed circuit. Speed adjustable 0-20f.p.s. Light output greater than
Joule) strobes \(\mathbf{E 1 4}\). Post 75 p .
RANGEE OF THREE OTHER STROBE KITS FROM STOCK. FROM C6. 30 to \(\mathbf{6 2 2}\).
S.A.E. (Foolscap) for derails.

\section*{BIG BLACK LIGHT}

400 Watt. Mercury vapour ultra violet la mp Powerful source of u.v. P.F. ballast is essentia Price of matched ballast and bulb \(£ 21\) Post \(61 \cdot 50\). Spare bulb \(\mathbf{4 8}\). Post 65p.
BLACK LIGHT FLUORESCENT U.V. TUBES 4 ft 40 watt, 65.50 (callers only) 2ft 20 watt,
standard bi-pin. MiNI. \(12 i n\). 8 watt, \(E 1.60\). Post 25p. 9in. 6 watt \& 1.30 . Post 25p. Complete ballast unit and holders for 9 in and 12 in tube, \(\mathbb{\ell} 1.70\). Post 30 p . ( 9 in and 12 in ,
measures approx.)

\section*{SQUAD LIGHT}
new conception in
light control. Four
channels each capable channels each capable of spot lights, flood lighes or dozens of small mains lamps. Seven programs all speed controlled plus flash modulation, effectively giving I 4 different displays. Makes sound-tolight obsolete. Completely electrically and mechanically noise free. Can be used on same circuit as radio mikes or sensitive amplifiers. A whole new range oflighting effects possible With astounding results. Already in use in discos. Conforms to all R.F.I. tests, including Common Market rezulations. Supplied in tough, well designed case with embossed
(Foolscap) for further detail

\section*{POWER RHEOSTATS I I 1}

\section*{Superior Quality Precision Made}

\section*{NEW POWER RHEOSTATS}

New ceramic construction, vitreous
enamel embedded winding, heavy duty brush assembly. continuously
rated WATT \(10 / 25 / 50 / 100 / 150 / 250 / 500 / 1 \mathrm{k} / 1 / 5 \mathrm{k} / 2 \cdot 5 \mathrm{k}\) ohm. 61.70 . Post 20 p .
50 50 WATT I/5/10/25/50/100/250/500/1k ohm E2.10. Post 25p. 100 WATT \(1 / 5 / 10 / 25 / 50 / 100 / 250 / 500 / 1 \mathrm{k} / 1.5 \mathrm{k} / 2 \cdot 5 \mathrm{k} /\) \(3.5 \mathrm{k} / 5 \mathrm{k}\) ohm 43.30 . Post 35p.
Black Silver, Skirted knob calibrated in Nos. I-9 Black Silver, Skirted knob cal abrated 22p each.

\section*{VAT}

VAT AT 8\% MUST BE ADDED TO ALL ORDERS FOR THE TOTAL VALUE OF GOODS INCLUDING POSTAGE UNLESS OTHERWISE STATED
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\section*{METERS NEW}

90 mm diameter
Type 65C5. 2A D.C. M/C; 5A D.C.M/C
1OA D.C. M/C:20A D.C.M/C. 10A D.C. M/C; 20A D.C. M/C. 50A D.C. M/C. MII: \(6 L L\) ABOVE C2.50, POSt 30 M
M/I; ALL ABOVE C2.50. Post 30p.
TYpe \(65 \mathrm{L5} .300 \mathrm{~V}\) A.C. R/M/C; C2.75. Post 30p
REVERSIBLE MOTOR
General Electric, 230V
p.m. 0.25 A. Complete with ant vibration mounting bracket and
capacitor. O/A size \(110 \mathrm{~mm} \times 95 \mathrm{~mm}\). spindle is in. dia. 20 mm long. Ex equipment rested, E3. Post 50p.

230/240 VOLT A.C. MINIATURE MOTOR. 20 R.P.M. Price EI, Post 20p.

\section*{BODINE TYPE N.C.I.} GEARED MOTOR
 amp. (Type 2) 28 r.p.m, torque 20
1b, in Reversible \(1 / 80 \mathrm{th}\) h.p. 50 cvele 0.28 amp .
The above two precision made U.S.A. motors are offered in 'as new' condition. Inpur voltage of motor \(115 V\) A.C. Supplied complete with transformer for Price, either type \(\mathbf{6} 6,25\). Post 75 p or less trans. frice, ermer 63.75. Post 650

CARTER' 230 VOLT A.C. GEARED MOTOR
\(230 / 240 \mathrm{~V}\) A.C., smooth, powerful, .om or \(110 \mathrm{r} . \mathrm{p} . \mathrm{m}\). Either eype ©4.75. Post 50p

ROTARY VACUUM AIR PUMP AND COMPRESSOR
Carbon vane, oilless, \(100 / 115 \mathrm{~V}\) a.c.. \(1 \frac{1}{2}\) h.p. motor, SO/60 cycle, 2875/3450 r.p.m., 20 in vacuum, comp. 10 p.s.i. (approx. figures).

New unused surplus stock. 5 upplied with electrical connection data. FRACTION OF MAKERS'PRICE \(\mathbf{E l}^{\prime} 2\). Post \(£ 1\) 00. 5uitable \(110 / 240 \mathrm{~V}\), 150 watt auto transformer \(\mathbf{6 3 - 5 0}\). Post 50p. (Both items together Post E(1.25)

\section*{PROGRAMME TIMERS}
\(230 / 240 \mathrm{~V}\) a.c. 15 r.p.m. Motor
Fach cam operates a c/o micro
witch. Ideal for lighting effects, 2 inf ineme:" animated displays, etc. Ex equipment rested.
2 cam model. \(15 \mathrm{r} . \mathrm{p} . \mathrm{m}\). \(\mathbf{4 2 . 0 0}\) post 35p 4 cam model. \(15 \mathrm{r} . \mathrm{p} . \mathrm{m}\). 62.50 post 40 p
8 cam model. \(20 \mathrm{r} . \mathrm{p} . \mathrm{m} . \mathrm{C} .75\) post 60 p 8 cam model. 20 r.p.m. 24.15 post 60 p

A.C. MAINS TIMER UNIT Based on an electric clock, with 25 amp. single pole switch. Which can be preset for any
period up to 12 hrs . ahead period up to 12 hrs. ahead
to switch on for any length
of time, from 10 mins. to 6 hrs. then switch off. An additional 60 min. audible
timer is also incorporated. Ideal
 Electric Blankets, etc. Attractive satin copper finish. 40p. (Total incl. VAT and Post \(£ 2 \cdot 87\) ).

\section*{TIME SWITCH}
'Horstmann' Type V. Mk. II Time
switch. \(200 / 250\) volt A.C. Two on/two switch. \(200 / 250\) volt A.C. Two on/two set time. 30 amp contaets. 36 hour spring reserve in case of power failure. Day omitting device. Fitted in heavy high impact case, with glass observation window. Built to highest Electricity Board Spec. individually tested. Price
\(\mathbf{E 7} \cdot \mathbf{7 5}\). Post 50 p. (Total inc. VAT \(£ 8.91\) ).


B/-PAK

\section*{SEMICONDUCTORS}

\section*{CARBON RESISTOR PAKS}

These Paks contain a range of Carbon Resistors, assorted into the ollowing groups
RI 50 Mixed 100 ohms -820 ohms R2 50 Mixed 1 K ohms- 8.2 K ohms R3 50 Mixed lok \(1 / 8 \mathrm{sh}\) W. 0.60 (10K ohms-82K ohms R4 50 Mixed 100 K ohms-820K \begin{tabular}{ll}
0 \\
\hline \(1 / 8 \mathrm{th}\) \\
\(1 / 0.60\)
\end{tabular} R5 30 Mixed 100 ohms -820 ohms R6 30 Mixed IK ohms 82 K ohms R7 30 Mixed 10 K ohms -82 K ohms R8 30 Mixed \(100 \mathrm{~K}^{2}\) ohms-820K 0.60 THESE ARE UNREPEATABLE
LOW COST CAPACITORS
500
SAV Elect
0.09 each \(\begin{array}{llll}500 & H F & 50 V & \text { Elect } \\ 001 & 0.09 \text { each } \\ 0.400 V & 0.03 \text { each }\end{array}\)

\section*{REPANCO CHOKES \& COILS}

\section*{\(\begin{array}{lll}\text { RF Chokes } & \mathrm{CHI} & 2.5 \mathrm{mH} \\ & \mathrm{CH} & 7.5 \mathrm{mH} \\ & \mathrm{CHS} & 1.5 \mathrm{mH} \\ & \mathrm{CH} & 5.0 \mathrm{mH}\end{array}\) \\ \(\mathrm{CH}_{2} \mathrm{CH}_{4} \quad 50 \mathrm{mH}\)}

COILS DRXI Crysral sec \(0 \cdot 29\) DRR2
0.27
0.29
0.26
0.28
0.31 range
0.42

CAREON POTENTIOMETERS
\(4.7 \mathrm{~K}, 10 \mathrm{~K}, 22 \mathrm{~K}, 47 \mathrm{~K}, 100 \mathrm{~K}, 220 \mathrm{~K}, 470 \mathrm{~K}\)
IM, \(2 M\). Single Less 5 witeh
VC 2 Single D.P. Switch
\(\begin{array}{ll} \\ V C & 0.14 \\ \end{array}\)
VC 5 look anti-Log

\section*{HORIZONTAL CARBON}
0.1W 0.06 each 00, 220, 470, IK, \(2 \cdot 2 \mathrm{~K}, 47 \mathrm{~K}, 10 \mathrm{~K}, 22 \mathrm{~K}\),

REPANCO TRANSFORMERS 240 V . Primary. Secondary volrages available from selecred tappings \(4 \mathrm{~V}, 7 \mathrm{~V}, 8 \mathrm{~V}\). \(10 \mathrm{~V} .40 \mathrm{~V}, 50 \mathrm{~V}\) and \(25 \mathrm{~V}-0-25 \mathrm{~V}\).
\begin{tabular}{|c|c|c|c|}
\hline Type & Amps & Price & \(P\) \& \(P\) \\
\hline MT50/: & & \$1.79 & 0.45p \\
\hline MT50/I & I & 62.24 & 0.48 \\
\hline
\end{tabular}

COIL FORMERS \& CORES NORMAN: Cores \& Formers 0.07 p

WITCHES
DP/DT Toggle 0.28p SP/ST Toggle 0.22p

\section*{FUSES}
and \(20 \mathrm{~mm} .100 \mathrm{~mA}, 200 \mathrm{~mA}, 250 \mathrm{~mA}\) 500 mA . IA. \(1.5 \mathrm{~A}, 2 \mathrm{~A}\) QUICK BLOW Anti-serge 20 mm only \(\quad 0.08 p\) each

\section*{VEROBOARDS}

VBl containing approx. 50 sq.ins various
sizes all 0.1 matrix
\(\begin{aligned} & \mathbf{~} 0.60\end{aligned}\) B2 containing approx. 50 sq . ins various

DECON-DALO 33PC Marker
tch resistans printed circuit marker pen. Full instruetions supplied with
\(\quad * 0.92 p\)

\section*{BATTERY HOLDERS}
akes 6 H.P. 7 s complece with terminal clip and lead \(\quad \star 0.31\) peach

\section*{CABLES}

CP 2 Twin Common Scree
\(\star 0.08\)
four Core Common
CP 5 Four Coreindividually
CP 6 Microphone Fully Braided hree Core Mains Cable P 8 Twin Oval Mains Cable CP 9 Speaker Cable

\section*{INSTRUMENT CASES \(\star\)}
(In 2 sections, Black Vinyl covered top and sides and bezel)
No. Length Width Height Price
 \begin{tabular}{ll} 
BV2 & \(6^{\prime}\) \\
BV4 \\
& \\
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> fi:

\section*{ALUMINIUM BOXES \(\star\)}
\begin{tabular}{|c|c|c|c|c|}
\hline & th & Width & Height & Price \\
\hline BAI & \(5{ }^{4}{ }^{\text {² }}\) & \(21^{\circ}\) & & +0.45 \\
\hline BA2 & 4* & \(4 *\) & \(1 \frac{1}{2}\) & * 0.45 \\
\hline BA3 & \(4{ }^{\prime}\) & \(2 \frac{1}{4}^{-}\) & \(1 \frac{1}{2}\) & * 0.45 \\
\hline BA4 & 5: \({ }^{\text {\% }}\) & \(4 *\) & \(1 \frac{1}{2}{ }^{*}\) & \(\star 0.54\) \\
\hline BA5 & 4" & 21** & 2 & + 0.45 \\
\hline BA6 & 3" & 2 " & 1 " & * 0.39 \\
\hline BA7 & \(7{ }^{-}\) & 5 " & \(2 \frac{1}{2}^{\prime \prime}\) & \(\pm 0.79\) \\
\hline BAS & \(8{ }^{\circ}\) & \(6 *\) & - \(3^{*}\) & + \(\$ 1.02\) \\
\hline BA9 & 6" & \(4 \times\) & 2 " & \(\star 0.65\) \\
\hline
\end{tabular} (Each complere with \(\frac{1}{3}^{-}\)deeplid \& screws) PLEASEADD 20P PACH BOXAND PACKING FOR EACH BOX.

\section*{COMPONENT BOXES}

Pak

\section*{No. Qty. Description Price
C1 200 Resistorsmixed values approx.}

C2 150 counc by weight \(\quad 0.60\)
C2 150 Capacitors mixed values C3 50 Precision Resistors mixed C4 75 Values \(1 / 8 t h\) width Resistors mixed 0.60 C5 preferredvalues \(\quad 0.60\) C5 5 Pieces assorted Ferrite Rods 0.60 c
c
C7 1 Pak Wire 50 metres assorted 0.60 C8 10 Reed Swirches \(\quad \begin{array}{ll} & 0.60 \\ & 0.60\end{array}\) \begin{tabular}{lll} 
C9 & 3 & Micro Switches \\
Clo & 0.60 \\
\hline
\end{tabular} Cll 5 Jack Sockers \(3 \cdot 3 \cdot 5 \mathrm{~m}, 2\) C12 30 Prandard Switch Type 0.60 0 eypes mixed values \(\quad \mathbf{0 . 6 0}\) C12 20 Electrolytics Trans. types 0.60 Cl4 1 Pack assorted Hardware. Nurs/Bolss, Grommers. 0.60 C15 5 Mains Slide Switches. 2 Amp. 0.60 C16 20 Assorted Tag Strips \& \(\begin{array}{lll}\text { C17 } 10 & \text { Panels } & \\ \text { Assorted Control Knobs } & 0.60 \\ 0.60\end{array}\) Cis 4 Rorssy Wave Change witches
\(\begin{array}{ll}C 192 & \text { Relays } 6.24 \mathrm{~V} \text { Operating } \\ \mathbf{C} & \mathbf{0 . 6 0}\end{array}\) 20 Sheets Copper Laminate approx. 200 sains.
\(+0.60\) Please add 20 p post and packing on all component packs. plus a furth

\section*{AVDEL BOND}
*
SOLVE THOSE STICKY

\section*{PROBLEMS}
with
CYANOCRYLATE C2 ADHESIVE
The wonder bond which works in seconds-bond plastic, rubber. transistors.

OUR PRICE ONLY 60p for 2 gm phial
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ACCESSORIES \\
BIB HI-FI
\end{tabular} & - \\
\hline & Stylus and turntable clean & Price \\
\hline & Tape head cleaning kit & 太 68 p \\
\hline \(P_{9}\) & Hi-Fi cleaner & * \({ }^{\text {30p }}\) \\
\hline 31 & Wire stripper & \\
\hline 32 & Tape editing kit & + \({ }^{\text {Cl }} 1.64\) \\
\hline & Stylus balance & * 61.24 \\
\hline & Record stylus cleaning kit & \\
\hline 42 & De Luxe Gr & \\
\hline 43 & Record carekit & \\
\hline 45 & uto changer groove cleane & \\
\hline 46 & Spiric level & \\
\hline 58 & Mi-Fi stereo & \\
\hline &  & \\
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\section*{ANTEX EQUIPMENT}

SOLDERING IRONS Model G. 18 W 18 W
15 W
King CCN 240. I5W
SK2. Soldering Kir
( +62.45
+62.70
+62.90 \(\star<2.70\)
+62.90
\(\mathbf{~} 63.90\)

\section*{BlTS AN}

Bit No model CN240 3/32-
102 for model CN240 \(3 / 16^{\circ}\)
104 for model CN240 3/16" 1100 for model CCN240 3/32
1101 for model CCN240 3/8" 1102 for model CCN240 \({ }^{+}\) 1020 for model G240 3/32" lo20 for model G240 3/32" 1022 for model G240 3/16 50 for model \(\times 253132^{\circ}\) 51 for model \(\times 251 / 8^{\circ}\)
52 for model \(\times 253 / 16^{\prime \prime}\) 52 for model \(\times 253 / 16^{\prime \prime}\)

\section*{ELEMENTS}

Model ECN240
Model EG240
Model ECCN240


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\section*{DIN 2 pin (Speaker)}

DIN 3 pi
OIN 4 Pi
DIN 5 Pin 190
4 DIN 5 Pin 180
5 DIN 5 Pin 240
DIN 6 Pin
DIN 6 Pin
DIN 7 Pin
Jack 2.5 mm Screened
Jack 3.5 mm Plastic
Jack \(\frac{1}{*}\) " Plastic
Jack \(\frac{1}{4}\). Screened
Jack Sterco Screened
Car Aerial

INLINE SOCKETS
PS 21 DIN 2 Pin (Speaker)
PS 23 DIN 3 Pin
PS 23 DIN 5 Pin 180
PS 25 Jack 2.5 mm Plastic
PS 26 Jack 3.5 mm Plastic

PS 28 Jack \(\frac{1}{2}\) Pl Screened
PS 29 Jack Stereo Plastic
PS 30 Jack Stereo Screene
31 Phono Screened
PS 33 Co-Axial
SOCKETS
PS 35 DIN 2 Pin (Speaker)
PS 36 DIN 3 Pin
PS 37 DIN 5 Pin 180
PS 38 DIN 5 Pin 240
PS 39 Jack 2.5 mm Switched
Jack 3.5 mm Switch
Jack Stereo Switche
Phono Single
Phono Double
PS 46 Co-Axial Surface

\section*{P.C.B. KITS \& PENS}

PROFESSIONAL D.I.Y. PRINTED CIRCUIT KIT \(\mathbf{E T} .80\)
Containing 6 sheers of \(6^{\circ} \times 4^{*}\) single sided laminate, a generous supply of etchant powder. etching dish. erchant measure. tweezers, etch resistant marking pen. high qualicy pump drill with spares, cutting knife with spare blades. 6" metal ruler. plus full easy to follow instructions.
Spare container of etchanc for above
PCB Pens \(2 \times\) Quality marker pens specifically designed for drawing fine line etch resistant circuits on copper lamin. ate. Complete with full instructions. \(\star 41.53\) per pair

\section*{LOW NOISE CASSETTES}
\(C 60\)
\(C 90\)
\(C 120\)

\section*{AUDIO LEADS}

S221 5 pin DIN plug to 4 phono plugs ength 1.5 m
41.08

52225 pin DIN plug to 5 pin DIN 5237 socket Dingth 5 pin Ding to 5 pin DIN plug mirror image length 1.5 m \&1.20 S238 2 pin DIN plug to 2 pin DIN 5268 socket length 5 m, 68p \(1 \& 4\) and 3 \& 5 length 1.5 m \& 1.00 S270 2 pin DIN plug to 2 pin DIN S271 5 pin DIN plug to 2 phono plugs connected to pins 3 \& 5 length 52755 pin DIN plug ro 2 phono sockers to pins 3 \& 5 length 5 pin DIN socker to 2 phono plug connected to pin 3 \& 5 length 23 cm
5404 Coiled stereo headphones ex \(5217{ }^{3}\) pin DIN plug to 3 pin DIN Plue length 1.5 m 80p S219 5 pin DIN plug to 5 pin DIN plug S474 3.5 mm . 5 mm to 3.5 mm Jack length 56005 pin DIN plug to 3.5 mm Jack connected to pins 3 \& 5 length
5 pin DIN plug co 3.5 jack connec ted to pins 184 length 1.5 m

\section*{CROSSOVER NETWORK}

K4007 1/P impedance 8 ohms. Insertion \(\begin{aligned} & \text { (2-way) Loss } 3 \mathrm{~dB} . \text { Crossover Frequency } \\ & 3 \mathrm{KHz}, \\ & \text { PRICE } £ 1.12\end{aligned}\)

\section*{H!PHONE JUNC. BOX}

H1012 Enables change over from loud-(3-way- speaker to headphone listening stereo) Also has a centre posicion for orh ourputs PRICE \(\& 1.7\)

\section*{ALL PRICES EX \\ PLEASE ADD VAT ITEMS EXCEPT \\ GIRO NUMBER 388-7006 \\ Postage \& Packing Add extra for airmail. \\ handbooks}

TRANSISTOR DATA BOOK
DRANSISTOR DATA BOOK DTE I 227 Pages packed with information
on European Transistors. Full specification including outlines. \(\quad\). Price \(\leqslant 2.95 \mathrm{each}\) TRANSISTOR EQUIVALENT
BPE 76256 Pages of cross references and equivalents for European. American and Japanese ransistors. This is the mos comprehensive equivalents book on the market today and has an introduction in DIODEEQUIVALENTBOORK DE 74144 Pages of cross references and equivalents for European. American and Japanese Diodes, Zeners. Thyristors, Triacs, Diacs and LED's.
THE WORLDS BROACASTING STATIONS
WBS 75 An up to the minute guide for those interested in DX-ing. Contains al and LW, as well as European FM/TV stations. \(A\) Price 6.56 each TTL DATA BOOK DIC 75 Now complete Data book of 74 series TTL (7400-74132). Covering 13 main manufacturers in the U.S.A. and Europe, this book gives full dara as well A full range of technical books available A full range of technical books available

\section*{PO BOX 6, WARE, HERTS. \\ AL 60 \\ 50W. PEAK (25W. R.M.S.)}


\section*{ONLY £3•95}

Max Heat Sink temp \(90^{\circ} \mathrm{C}\). Frequency Response 20 Hz to 100 kHz . Distortion better than 0.1 at 1 kHz . Supply voltage 15.50 V . Thermal Feedback. Latest Design Improvements. Load 3,4,5 or 16 ohms. Signal to noise ratio 80dB. Overall size \(63 \mathrm{~mm} \times 105 \mathrm{~mm} \times 13 \mathrm{~mm}\).
Especially designed to a strict specification. Only the finest components have been used and the latest solid state circuitry incorporated in this powerful little amplifier which should satisfy the most critical A.F. enthusiast.

\section*{STABILISED POWER MODUEE SPM80}

SPM80 is especially designed to power 2 of the AL60 Amplifiers, up to 15 watt (r.m.s.) per channel simultaneously. This module embodies the latest components and circuit techniques incorporating complete short circuit protection. With the addition of the Mains Transformer BMT80, the unit will provide outputs of up to \(1.5 A\) at 35 V . Size: \(63 \mathrm{~mm} * 105 \mathrm{~mm} \approx 30 \mathrm{~mm}\).
These units enable you to build Audio Systems of the highest quality at a hitherto unobtainable price. Also ideal for many other applications including:-Disco Systems. Public Address Intercom Units, etc. Handbook available 10 p .

PRICE \(£ 3.00\)
TRANSFORMER BMT80 \(£ 2 \cdot 60\)


\section*{CLUDE VAT}
add 20p* overseas Mimimum order 75p


\section*{STEREO 30 COMPLETE AUDIO CHASSIS}

\section*{\(7+7\) WATTS R.M.S.}

The Stereo 30 comprises a complete stereo pre-amplifier, power amplifiers and power supply. This with only comprises a complete sterer or overwind, will produce a high quality audio unit suitable for use with a wide range of inputs, i.e. high quality ceramic pickup, stereo tuner, stereo tape deck, etc.
tape deck. etc. simple co instapable of producing really first class results, this unit is supplied with full instruc. tions, black front panel, knobs, mains switch, fuse and fuse holder and universal mounting bracker, enabling it to be installed in a record plinth, cabinets of your own construction or the cabinet vailable.
Ideal for the beginner or advanced constructor who requires Hi-Fi perfor mance with a minimum of installation difficulty. (Can be installed in 30 mins .) PRICE: 615.75 plus 45p postage and packing. TRANSFORMER: 62.45 plus 45 postage and packing. TEAK CASE: 63.65 plus 45p postage and packing.

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\section*{A COMPELLING FORCE}

E
lectronics means many things to many people, as our post bag makes abundantly clear. Obviously the technology is a powerful force since it unites all manner of individuals who may have but little else in common. The powerful attraction offered by this highly technical and intellectual subject to vast numbers of persons who are not by trade or training "professionals" must be one of the more interesting and striking social phenomena of present times.

Particular areas of applied electronics have from time immemorial attracted their own devotees. These amateur enthusiasts have been easily identified as radio hams, audiophiles, radio control modellers or musical instrument builders. Outside such well defined areas the position is rather nebulous, the field is extremely wide and the applications too numerous for easy general classification. The equipments themselves are usually strictly functional and by and large fall within the definitions of sensing, measuring and controlling devices and systems. Highly valued for the duties they perform, they are installed and forgotten, or brought into use as a tool as the occasion demands. This is the comparatively new area of the hobby, and unlike the more traditional areas the end-products do not themselves directly foster any further creative or entertaining activities on the part of the owner.

As we all know this area is under constant exploration by the inventive minds. The probing into the vastness of this "application space" will continue, that's for sure, and will bring forth more and more rewarding discoveries. The enthusiast who pursues this kind of search may owe no allegiance to any well labelled group or sectional interest. He might be simply a devotee of electronics-just that; a member of a species that did not exist in any strength, outside the research laboratory, a couple of decades ago.

What should we call this free-ranging individual who respects no artificial bounds or limits in his pursuit of electronics in the common cause? The very universality of applied electronics gives the measure of the task. Frankly, we see no easy answer to our own question. But readers may think otherwise and may have their own ideas of an appropriate label.

\footnotetext{
Yet, as someone said, what's in a name? The bond of common interest in electronic circuitry and techniques is exceedingly strong and unites this huge band of enthusiasts, at least so far as the means are concerned. The end to which the technology is applied is entirely a personal affair. Here it must be admitted divergent views do sometimes emerge and even heated arguments can ensue between the various "specialists". But we should be tolerant in our approach to another man's uses or appropriations of electronics, and appreciate that the technology is free for all to apply and enjoy or benefit from, as they will.
F.E.B.
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THE tuning of keyboard instruments to the equal tempered scale is a task requiring considerable skill. The professional piano tuner uses a single tuning fork to obtain absolute pitch and then tunes all other notes by a system ascending tempered fifths and descending octaves.

The instrument described here uses two of the latest m.o.s. chips to produce 84 discrete tones from a single master oscillator. These tones can be used directly to tune any instrument by eliminating the beat frequency. Also included is a direct mixing input, which uses novel techniques to obtain the beat frequency. Also included is a direct mixing connection from an electronic organ, synthesiser, etc., or a microphone pick-up from any acoustic instrument, will produce visual beats via an l.e.d.

\section*{THE TEMPERED SCALE}

Before describing the instrument, it may be helpful if we delve a little way into the theory behind the equal tempered scale. The scientifically orientated reader who has taken up a musical instrument may have been surprised by the distinct nonlinearity in the musical scale. This is particularly apparent on the guitar, for if the scale of C is played on a single string, there is a separation of only one fret (a semitone) between E, F and B, C whilst all other notes have a whole tone separation requiring double fret intervals. On keyboard instruments this irregularity is tidied up by arranging the black notes in the familiar groups of two and three.

Why then is the scale in this form, and why don't we notice it? We don't notice it largely because we are accustomed to it. The formation of our scale goes back to Pythagoras in the 6th Century B.C. who discovered that strings harmonised best when their lengths were in simple ratios to each other. Thus the most consonant arrangement was a ratio of \(2: 1\),
the octave, followed by the ratio \(2: 3\) known as the fifth. The Pythagorean scale was based on just these intervals. In fact, there were several other scales in use until relatively recent times, but with the development of harmony (playing two or more notes together) most of these have become non-runners.
With the advent of keyboard instruments there was the requirement for playing in more than one key on a single instrument, without favouring a particular key. Clearly, the solution was to adopt a constant interval for the semitone. For this reason, the equal tempered scale was adopted in the 18th century, the only perfect intervals being the octaves and all semitone intervals equal to 1.0594, the twelfth root of two.

\section*{TUNING}

What all this means as far as we are concerned is that the traditional method of tuning a piano is not easy, for the professional will' tune in ascending fifths flattened by just the required amount. Moreover, as far as our tuning aid is concerned, it means that we cannot derive digitally from a master source, the exact frequency we require; the notes are not in exact simple ratios. We can, however, produce a set of notes of sufficient accuracy if we start with a very high master frequency and divide it by a set of large number integers.


Fig. 1. Block schematic of Tuning Fork

The AY-1-0212 master tone generator was developed specifically for this purpose, its application being electronic organs of course. The block schematic is shown in Fig. 1; a frequency of 841.28 kHz is produced by the master oscillator and fed directly to the tone generator chip. The outputs give directly the eleven notes of the highest octave. The lower octaves are produced by repeated division by two, a 6 stage divider chip AY-1-6721/6 being switched in directly.

Table 1 lists all the frequencies available from the instrument together with the pin number inputs for the master tone generator chip.

\section*{CIRCUIT DESCRIPTION}

The circuit design for the basic tuning aid is shown in Fig. 2. The master oscillator feeding pin 2 of IC1 is basically a Colpitts oscillator with certain refinements. The additional capacitor C3 in series with the inductance L1 gives a substantial improvement in the frequency stability of the oscillator with respect to variations of the transistor parameters with temperature and supply voltage.

Also the ratio of C 4 to C 5 sets the tapping point for minimum tuned circuit loading and improves the circuit ' Q '. The use of an f.e.t. rather than a bipolar transistor further reduces the circuit loading. The inductor L1 is a standard single tuned



\section*{Capacitors}
\begin{tabular}{|c|c|}
\hline C1, C2 & \(0.22 \mu \mathrm{~F}\) ceramic \\
\hline C3 & 100 pF silver mica \\
\hline C4 & 220 pF silver mica \\
\hline C5 & 820pF \\
\hline C6 & 82 pF \\
\hline C7 & 10pF \\
\hline C8 & \(100 \mu \mathrm{~F}\) elect. 35 V \\
\hline C9 & 10 nF \\
\hline C10 & 220pF \\
\hline C11 & \(0.22 \mu \mathrm{~F}\) ceramic \\
\hline C12 & \(100 \mu \mathrm{~F}\) elect. 35 V \\
\hline C13 & 100 pF \\
\hline C14 & \(100 \mu \mathrm{~F}\) elect. 35 V \\
\hline C15 & 100 pF \\
\hline
\end{tabular}

Semiconductors
\begin{tabular}{|c|c|c|}
\hline IC1 & AY-1-0212 & \\
\hline IC2 & AY-1-6721/6 & \\
\hline IC3 & 70914 pin di.i.l. & \\
\hline IC4, 5 & 7418 pin d.i.l. & \\
\hline IC6, 7 & 72314 pin d.i.l. & \\
\hline TR1 & 2N3823 f.e.t. & \\
\hline TR2 & 2N3823 f.e.t. & \\
\hline TR3 & BC214 & \\
\hline TR4 & BC184 & \\
\hline D1-D2 & 1 N914 & \\
\hline D3-D6 & Miniature moulded (400V 1A) & bridge rectifier \\
\hline D7-D10 & Miniature moulded ( 400 V 1 A ) (Both from R.S) & bridge rectifier \\
\hline D11 & TILzo9 & \\
\hline
\end{tabular}

Transformer
T1 Min. mains transformer-Sec. 20V (2 windings) 3VA/winding (RS)

Inductor
L1 Denco IFT13 470 kHz coil

\section*{Miscellaneous}

Diecast box \(73 \sin \times 4 \frac{3}{4} \mathrm{in} \times 2 \frac{1}{4} \mathrm{in}\)
Veroboard \(7 \mathrm{in} \times 3 \frac{3}{4} \mathrm{in}, 0.1 \mathrm{in}\) pitch
SK1, SK2 standard jack sockets
S1 12 way, single pole wafer switch
S2 7 way single pole wafer switch
14 pin i.c. sockets ( 4 off), 8 pin i.c. sockets (2 off)


470 kHz i.f. transformer with the parallel capacitor removed.

The high input impedance of the m.o.s. chip ICI allows it to be directly connected to the drain of TRI, eliminating the usual buffer transistor. The tone output from IC2 is taken through the attenuator R4. R5 which serves the dual purpose of reducing the 12 V amplitude to a suitable power amplifier input level whilst protecting the output of IC2 from short circuits.

Referring to the circuit in Fig. 1, the incoming musical waveform, from a microphone say (SK1) is amplified by IC3 which has a gain of 1,000 . The output from IC3 is a.c. coupled into IC4 which further amplifies it by a factor of 100 . These two stages utilise operational amplifiers in standard configurations. The first stage, because of its very high gain, utilises a 709 with its attendant frequency compensation components. For the second stage we can manage with the limited bandwidth of a 741.

\section*{HIGH AMPLIFICATION}

The reason for the high amplification of the signal is twofold. Firstly, it enables a magnetic pick-up or microphone with a sensitivity of a few millivolts to be used. Secondly, the over-amplification can be utilised to overcome the decay in amplitude, characteristic of stringed instruments. This is very important when tuning a piano, as the top notes decay very quickly; without this facility the beats would not be easily observed. There will be some initial squaring of the waveform, but this will not matter to us. The sampling circuit can cater for waveforms of any shape.

Passing on to the sample and hold circiuit, a signal from the output of the tone generator (pole of S2) is amplified by TR3 to give a square wave of amplitude limited by the rail voltages. The leading edges of the square wave are differentiated by C10, R18 to give positive going pulses rising from -15.5 V and rising to +12 V . The diode D2 serves to suppress the negative going pulses generated at the trailing edge. These positive spikes turn on the f.e.t. TR2 and allow the hold capacitor C9 to store the output of IC4. The attenuator R11, R12 is necessary to ensure that the signal voltage on the drain of TR2 exceeds the gate voltage by the required 8 V when TR2 is off.


Fig. 2. The complete Tuning Fork circuit


Fig. 3. Waveforms showing action of sampling circuit

\section*{SAMPLING WAVEFORMS}

The action of the sampiing circuil is depicted in Fig. 3. A simplified musical waveform (a) with period 4 ms , frequency 250 Hz is sampled by waveform (b) with period 3.5 ms , frequency 285.7 Hz . (c) is the output of the sample and hold circuit which corresponds in shape to the original waveform (a). (d) is formed from waveform (c) by the squaring circuit consisting of IC5 connected as a zero threshold comparator. The period of (c) and (d) is 28 ms corresponding to a frequency of \(35 \cdot 7 \mathrm{~Hz}\). This is the beat frequency as \(285 \cdot 7-250=35 \cdot 7 \mathrm{~Hz}\). Finally, the output of IC.5 feeds directly to the l.e.d. driver TR4 providing a visual beat display.

\section*{CONSTRUCTION}

The main circuit is built on a single piece of Vero stripboard as shown in the accompanying photographs.

Note that the pin connections to the 723 refer to the 14 pin DIL package; if it is intended to use the 723 in a TO case, the pin numbering will be different and a different layout will be required.
When completed the output voltages should be measured. The tolerance on the supplies is \(\pm 1 \mathrm{~V}\) on the +12 V rail and +I .5 V on the -15.5 V rail: With 2 per cent resistors for the potential dividers R25, R26 and R22, R23 the outputs should be in tolerance. If, for any reason, the outputs require adjustment, these are the resistors to vary.

With the power supply working satisfactorily, the rest of the circuit can confidently be built. The layout conveniently splits in two, so that the oscillator and divider circuit can be built and tested first.

It is strongly recommended that the expensive master tone generator i.c. is mounted in a socket. The divider i.c. will have to be soldered indirectly as it is packaged in a TO case. As this is an m.o.s. device precautions should be taken whilst soldering it. The metal spring ring, which shorts the leads together in transit, should be left in place until soldering is complete.

Mount the decoupling capacitors Cl and C 2 close to the oscillator components and ensure that the tracks around the oscillator are broken, thus preventing the high frequency oscillations being picked up in other parts of the circuit. The construction of the rest of the circuitry is straightforward.


Photograph of Fork interior showing board assemblies and ancillary wiring to control switches. Note the use of a film canister for screening the microphone socket.


Fig. 4. Main board showing component assembly and copper track cuts required

As can be seen from Fig. 4 the p.s.u. regulators are mounted at the end of the main board.

The bridge rectifiers and smoothing capacitors are mounted on a small piece of board affixed to the side of the case as shown in Fig. 5 using countersunk screws and stand-off pillars.


Fig. 5. Additional board for rectifiers and smoothing components

The complete instrument is housed in a diecast box. The microphone jack socket should be mounted close to the input connection to IC3. Also, shielding of this socket is necessary to prevent pick-up from the oscillator section. In the prototype, the complete socket was mounted inside a small aluminium canister and the connection to pin 5 of IC3 was made with screened lead.

\section*{TESTING}

After carefully checking your wiring, plug the output into an audio amplifier and switch on. You should have 84 musical notes at your command. If you possess an oscilloscope you can ensure you have the correct input to ICl by looking at the oscillator output ( ICl pin 2). This signal, which is not a sine wave, should have an amplitude which lies between the limits of +10 V and +12 V for the crest and +2 V and -8 V for the trough.

Before the visual beat circuitry can be tested, the instrument will have to be roughly calibrated against a known source; a musical instrument roughly in tune will suffice.

Select the note and octave required (middle \(C\) lies in range 4) and adjust the core of Ll for an audio beat frequency of about 1 beat per second.

Disconnect the audio output and connect a good quality magnetic microphone to the input jack; with the microphone in the vicinity of the musical instrument, the beat frequency should be clearly visible.

\section*{CALIBRATION AND USE}

If you have access to a digital frequency counter, you will have no difficulty in calibration,

Table 1: AY-1-0212 Master Tone Generator
\begin{tabular}{|c|c|c|c|c|c|c|c|c|c|c|}
\hline Pin No. & Note & Divisor & Frequency \(f\) Range 7 & \[
\begin{gathered}
f / 2 \\
\text { Range } 6
\end{gathered}
\] & \begin{tabular}{l}
\(f / 4\) \\
Range 5
\end{tabular} & \[
\begin{gathered}
f / 8 \\
\text { Range } 4
\end{gathered}
\] & \begin{tabular}{l}
f/16 \\
Range 3
\end{tabular} & \begin{tabular}{l}
f/32 \\
Range 2
\end{tabular} & \begin{tabular}{l}
\(f / 64\) \\
Range 1
\end{tabular} & Correct frequency for \(f / 8\) \\
\hline 8 & A & 239 & 3520 & 1760 & 880 & 440 & 220 & 110 & 55 & 440 \\
\hline 7 & G \# & 253 & 3325.22 & 1662.61 & 831.30 & 415.65 & 207.82 & 103.91 & 51.96 & 415.31 \\
\hline 11 & G & 268 & 3139.1 & 1569.55 & 784.78 & 392.39 & 196.19 & 98.10 & 49.05 & 392.00 \\
\hline 12 & F\# & 284 & 2962.25 & 1481.12 & 740.56 & 370.28 & \(185 \cdot 14\) & 92.57 & 46.29 & 369.99 \\
\hline 6 & F & 301 & 2794 & 1397.48 & 698.74 & 349.37 & 174.68 & 87.34 & 43.67 & 349.23 \\
\hline 5 & E & 319 & 2637.24 & 1318.62 & 659.62 & 329.66 & 164.83 & 82.41 & 41.21 & 329.68 \\
\hline 13 & D \# & 338 & 2488.99 & 1244.50 & 622.75 & 311.12 & 155.56 & 77.78 & 38.89 & 311.13 \\
\hline 14 & D & 358 & 2349.94 & 1174.97 & 587.49 & 293.74 & 146.87 & 73.44 & 36.72 & 293.67 \\
\hline 4 & C\# & 379 & 2219.74 & 1109.87 & 554.93 & 277.47 & 138.73 & 69.37 & 34.68 & 277.18 \\
\hline 15 & C & 402 & 2092.74 & 1046.37 & 523.18 & 261.59 & 130.80 & 65.40 & 32.70 & 261.62 \\
\hline 16 & B & 426 & 1974.84 & 987.42 & 493.71 & 246.85 & 123.43 & 61.71 & 30.86 & 246.92 \\
\hline 3 & A \# & 451 & \(1865 \cdot 37\) & 932.68 & 466.34 & 233.17 & 116.59 & 58.29 & 29.15 & 233.07 \\
\hline
\end{tabular}
\(2\left(f_{0}\right)\) input frequency \(=841 \cdot 28 \mathrm{kHz}\)
\(1+12 \mathrm{~V}\) supply
\(9 \quad-15.5 \mathrm{~V}\) supply
10 OV
however, do not connect the counter to the oscillator output at pin 2 of ICl as the loading effect will give a false reading. The best accuracy can be obtained by using the counter in the period timing mode with the instrument switched to the low frequency \(A\) of 55 Hz .

Alternatively, a tuning fork can be used-it may be worth purchasing one if the instrument is to be used a lot. Finally, if absolute pitch is not essential, the 50 Hz mains frequency may be used.

Referring to Table 1 we see that the lowest note G has a frequency of 49.05 Hz , in fact the correct frequency should be very near 49 Hz . All that is required to pick up some mains hum, is a looped lead placed near the mains transformer. With the lowest G selected, tune L1 for 1 beat per second,

\section*{Pick-up for steel stringed instrument}

check that you are tuning to 49 Hz and not 51 Hz by switching to the adjacent G\# which should give approximately two beats per second.

\section*{ELECTRO-MAGNETIC PICK-UP}

An alternative pick-up for steel stringed instruments is a coil wound round a permanent magnet as used for electric guitars. This type of pick-up gives a superior performance to a microphone for the very low strings on a piano and has advantages for tuning in noisy environments. The device shown in the photograph was fashioned from an old telephone earpiece of 1936 vintage. One of the coils was removed, together with the pole-piece and magnet. The components were mounted on a suitable handle in much the same way as in the original earpiece.

\section*{CONCLUSION}

The Electronic Tuning Fork has been used to tune several instruments, including a piano and a 37 string autoharp. The visible beat indicator proved to be a boon as it allowed piano tuning to proceed without switching off the sacred TV set! This particular piano, being a poor specimen, was incapable of holding its tune unless the tension on the strings was relieved by tuning about three semitones below concert pitch. This was easily facilitated by simply loosening the note select knob, and advancing it three semitones.

Finally, don't be put off by the fact that the instrument is not perfect, for if you wish to be fanatical about your tuning, the utmost precision can be obtained by tuning to a specific number of beats. Referring to Table 1, we see that for a perfect A, G has the largest error of 0.39 Hz at a frequency of 392 Hz . If the instrument is tuned for 39 beats in a period of 100 seconds, then it will be precise. This is not possible for a stringed instrument, as the note will decay too soon. However, it might be worthwhile for tuning a divider organ where there are only twelve master frequencies to adjust. \(\star\)

\section*{ICE AGES}

Talk of weather and ice ages has been increasing of late, so it is refreshing to have some new information on the subject. Recently. Professor McCrea, one of our most meticulous astronomers, has made certain suggestions regarding the advent of ice ages. He postulates that in the journey round the galaxy by the solar system, the Sun may pass through areas rich in dust. The infalling dust could reduce the effect of the Sun's output.

In McCrea's study, he did not attempt to explain how the changes occur, but looked at the evidence of the recurrence of ice ages related to the cosmic year. Support for this idea came from G. E. Williams of Australia, who quite independently made a detailed exami nation of the Sun's position during the cosmic year without offering any evidence of a link or mechanism for the ice ages.

These two approaches within a few weeks of each other has caused more attention to be given to a new model. In McCrea's calculations, orders of magnitude and estimates of the intervals were made. Williams, on the other hand, used geological evidence to an accuracy of a few tens of millions of years. This produced a fairly rigorous time scale for the glaciations.

In a summary, he indicates six principal occurrences. One of these has just been completed, one occurred in the Permo-Carboniferous period about 295 millions of years ago, another in the late Ordivician period some 450 millions of years ago and three in the late preCambrian. That is 615,770 and 940 millions of years ago. This reveals a gap at about 150 million years ago which was, however, a cool period.

From the evidence that Williams offers, a cosmic year for 303 mil lions emerges. This agrees with the impressions that the ice ages occur twice in the cosmic year. Williams offers the suggestion that this might be due to the bending of the galaxy due to the tides raised by the influence of the Magellanic clouds.

\section*{NEW PLANETS}

The news recently released that Russian theoretical astronomy has examined a computerised experiment dealing with perturbations or orbits reveals the existence of one or maybe two planets beyond Pluto. There has always remained some doubt that all the corrections have not yet been made to the discrepancies in the Uranus predictions.

At the Institute of Theoretical Astronomy in Leningrad, this experiment has been carried out under the direction of Professor Chebotaryov,

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a specialist in celestial mechanics, who has been computing the effects on the comet 1862-3. The parameters of the orbit make it an en" ticing argument that a planetary body would produce the data to fit the observations.

The invisible planets would have positions way out beyond the Plutonian orbit. One would have a diameter of about 12,000 miles, which would give it a mass of about the same as that of the Earth, and would be 54 times as far away from the Sun as the Earth. The other would be twice as large as the Earth and at a distance of a hundred times that of the Earth from the Sun.

The ancients have always held that there were other planets; perhaps one of these will be called Lilith also, if and when they are optically observed. Is this perhaps a good argument for a very large telescope aboard an orbiting observatory.

\section*{SPACE BIOLOGY}

The Apollo-Soyız programme dealt with a number of joint biological tasks. One of these was concerned with fish embryonic development, with the object of discovering what effects weightlessness would have. Similar fish. Danyo Rerio, had been taken to outer space by Sovuz 16.

The main object was to study the vestibular apparatus. On the ground, living things learn from birth to distinguish the difference between up and down, by the use of this mechanism. In space these notions become non-existent and this seems to cause changes in the organ.

Another experiment concerned the growth of micro-organisms, since weightlessness had a considerable effect on the vital activity of monocellular life. The organisms chosen for the experiment were Chlamy Domonada, Flowers Crepsis, Arabysopsis and Protea Vulgaris.

Two experiments with microbial exchange were made from the USA module and the Russian Soyuz. Exchange was made from the astronauts themselves also. These will be compared back on Earth to discover how the microflora has changed.

This was an important joint venture to discover the effects of mutual exchange between the astronauts. It could reveal the processes by which illness develops especially during the first few days of a mission. It would also be of value to determine conditions of cross-infection between the individuals on Earth. Thus another spin-off appears for the benefit of mankind.

\section*{COMET TO ASTEROID}

A rare phenomenon, the transformation of a comet into an asteroid has been observed by Soviet astronomers.

A comet of the 15 th magnitude is moving between Jupiter's orbit and the asteroid belt. The comet has been named Tamara SmirnovaNikolai Chernkh, and was discovered in March this year.

Unlike most comets, this one does not follow the elongated elliptical orbit but moves in an almost circular one, its path close to the asteroids. It would seem, therefore, that the theory of capture obtains here as it does with other material particles.

If the comet has gathered enough material round its icy nucleus to attain a mass sufficient to make it also a minor planet, it may become another of the random bodies under the control of the gravitation of Jupiter or a lesser place in the asteroid belt.

\section*{POLES APART}

It is calculated that the magnetic poles are changing position. This will necessitate considerable changes in charts towards the end of the century.
If the Soviet scientist Nikolai Medvedev is correct, the Earth has already entered a new epoch of magnetic inversion. When it attains a maximum, the north magnetic pole will be in the area of the Persian Gulf, with the Southern pole in the area of the Phillipines.

The theory is based on the fact that the poles drift with the migration of the Earth's nucleus which is known not to coincide with the geographical poles.


LAST month the construction and wiring of circuits involving static tests, that is tests without the engine running, were described. The remaining circuits involve dynamic tests on the engine under normal running conditions, and the first of these, to be described this month, is the dwell and tachometer board. The dwell period is the portion of the ignition timing cycle during which the contacts are closed, and current is allowed to build up in the ignition coil primary. Too short dwell time will mean that, at high engine speeds, the current will not reach a sufficiently high value to produce a good spark and mis-firing will result.

Dwell is usually quoted in degrees of rotation of the distributor shaft. Since in one 360 degree rotation, the distributor connects once to each sparkplug, the distributor angle per contact breaker cycle is \(360 / \mathrm{N}\) degrees, where \(\mathrm{N}=\) number of cylinders. Hence, for a four-cylinder engine the maximum dwell angle is 90 degrees, for a sixcylinder 60 degrees and so on. On this instrument the meter scale for dwell has been calibrated 0 to 100 per cent of the maximum angle so that it applies to all engines. Of course, no engine will run with maximum dwell angle since the contacts would be closed all the time. The normal dwell angle for most cars is around 40 to 60 per cent of the total period.

\section*{INPUT WAVEFORM}

Both r.p.m. and dwell time measurements make use of the pulses obtained from the contact breaker terminal on the ignition coil. Much of the circuit, shown in Fig. 2.1, is common to both measurements.

The squared letters on the circuit diagram refer to the waveforms shown in Fig. 2.2 occurring at the points indicated. The waveforms and circuit are described relative to the negative terminal of the battery, normally connected to chassis. The signal obtained from the contact breaker is waveform. A.

When the contacts are closed, the terminal of the contact breaker is connected to chassis. When the contacts open the terminal is connected through the low voltage section of the coil to the positive side of the battery. At the instant the contacts open the energy stored in the coil inductance, due to the current flowing in it, is converted into potential energy and produces a series of high voltage oscillations (waveform A) which last for about 3 milliseconds. It is these oscillations which generate the high voltage energy necessary to produce the spark.

\section*{CARS WITH POSITIVE EARTH}

On cars which have the positive pole of the battery connected to the chassis, the c.b. terminal on the coil will go to minus 12 volts when the contacts open, and waveforms A and B of Fig. 2.2 will be inverted. One section of IC2 (4 NOR gates) is used as an inverter, brought into circuit by S 2 , so that the \(^{2}\), the input presented to pin 5 of IC1 is the same as waveform B.

\section*{METER SIGNALS}

At this point it is worth digressing to consider what signals are required to produce meter readings

\section*{SPECIFICATION•••}
- Ignition
timing
By strobe lamp fired from inductive coupling to No. 1 spark plug lead.
- Tachometer
- Dwell measurement

Ohmmeter
- Battery Charger
- Voltmeter
- Condenser check

Power input

0 to 2500 r.p.m. on \(1,2,4,6\) or 8 cylinder engines.

10 per cent to 80 per cent at 1000 r.p.m.

0 to 1000 ohms. 150 ohms centre scale.

12 volt 4A (High rate) or 2A (Low rate) or 6 volt 4A (High rate) or 2A (Low rate).

0 to 25 volts d.c. \(\pm 5\) per cent f.s.d.
\(0.22 u \mathrm{~F}\) condenser is sutstituted across contact breaker.

240 V a.c. at 50 Hz or \(12-16 \mathrm{~V}\) d.c. at 1 A .
proportional to engine speed and dwell time. If regular current pulses are passed through a meter. the meter will read current equal to the average value of the current pulses. Therefore, the meter


Fig. 2.2. The oscillograms to be found on test points of the dwell and r.p.m. circuit
reading will depend on the ratio of on to off time of these pulses. For instance, if current pulses of 1 mA flow for 50 per cent of the time, the meter will read 0.5 mA . Hence, considering waveform E of Fig. 2.2, it can be seen that if the dwell time is made the on part of the waveform, the meter will give a reading directly proportional to dwell angle. The 90 degree maximum dwell angle shown at \(E\) is for a fourcylinder engine. A six or eight-cylinder engine would have a maximum dwell angle of 60 degree and 45 degree respectively. If engine speed is varied.

\section*{TACHOMETER AND DWELL CIRCUIT}

Fig. 2.1. Board A, the dwell and r.p.m. circuit. The squared letters refer to the oscillograms in Fig. 2.2. For Veropin connections see Fig. 1.2

both the on and off parts of the waveform will change proportionally, and the average meter indication (dwell angle) is independent of engine speed.

Waveform C, is a fixed length pulse, produced once every time the contacts open. Here, changing engine speed does not alter the on time, but makes the pulse occur more or less often, depending on whether engine speed is increased or decreased. Thus doubling engine speed will double the current through a meter. These pulses (waveform C) are used to give an indication of r.p.m.

\section*{CIRCUIT DESCRIPTION}

Now to consider Fig. 2.1, the sircuit required to produce these two trains of pulses in a form suitable to give the required indications on a \(0-1 \mathrm{~mA}\) meter. The contact breaker signal is clipped by a \(5 \cdot 1 \mathrm{~V}\) Zener diode D13 so that it is at the correct level for use with standard 5 V logic integrated circuits. This produces waveform B at the input to ICl (pin 5). This integrated circuit is a monostable multivibrator with a level detecting trigger input. Once fired, it will ignore any further input triggering pulses occuring during the time constant of the timing components C6, R14-21.
Thus the pulse chopping on waveform B caused by the coil ringing is ignored by the multivibrator, and the output at pin 1 of IC 1 is waveform C. This signal is fed through series resistors R12 and VR2 to the meter, which will give a reading proportional to pulse repetition rate, i.e., r.p.m. The length of this pulse limits the maximum r.p.m. which can be measured, but if it is made too short there will be a risk of the i.c. double-firing and giving a false meter reading. About 3 milliseconds is the shortest pulse that can be safely tolerated. The integrated circuit has a maximum duty cycle of 60 per cent, which means that for 3 milliseconds on time, the total pulse period will be 5 milliseconds.
The formula for the maximum r.p.m. which can be measured with this circuit is given by
\(60 \times\) max. duty cycle
Pulse length \(\times \frac{1}{2}\) No. cylinders.
from which it will be seen that the greatest limitation will be on engines with the highest number of cylinders For an eight-cylinder engine, the limit will be
\[
\frac{1,000 \times 60 \times 0.60}{3 \times 4}=3,000 \mathrm{r} . \mathrm{p} . \mathrm{m} .
\]


For ease of use and calibration it was decided to make the range the same ( 2,500 r.p.m.) for all types of engines and accordingly, the time constant associated with ICl has been made switchable for a range of engines with differing number of cylinders from one to eight. This means that all carburettor adjustments are carried out with the r.p.m. meter reading near the centre of the scale, around 1,000 revolutions per minute.

As already described, dwell angle is measured by waveform E, which is a cleaned up version of B. The cleaning up is achieved by feeding waveform \(B\) and D into the two inputs of IC2a, or NOR gate A NOR gate produces " O " output when either of its inputs is a " 1 ". Hence, since the positive going output of IC1 holds one input positive during the ringing on the other waveform, this ringing will be removed from the output. IC2b is a second NOR gate on the same i.c., used to invert the signal so that the correct portion of the waveform is in the on state to drive the meter positively.

From examination of waveform E it will be seen that 100 per cent dwell will be represented by constant zero voltage and zero dwell by constant positive level. This fact is made use of later in the calibration process.

The board is supplied from the 10 V regulated supply which is further dropped to 5.1 V for the integrated circuits by R22 and Zener diode D7.

\section*{CIRCUIT BOARD CONSTRUCTION}

The circuit for r.p.m. and dwell angle measurement is constructed on a piece of \(0 \cdot 1\) in pitch Vero board measuring \(4 \cdot 8\) in long by \(2 \cdot 7 \mathrm{in}\) wide. Fig. 2.3 shows the layout of components on the top side of the board, and the points at which the printed circuit track should be cut.

Tags are inserted at all points where the circuits show connections to switches. If the constructor does not require all the facilities provided by the switching, links may be soldered between appropriate tags to set the circuits for one particular type of car, e.g. four-cylinder, negative earth. 14-pin d.i.1. sockets are recommended for the two i.c.s in preference to direct soldering to the board as there is a risk of damage by overheating with direct soldering. Both i.c.s are mounted with their orientation identity, i.e., pin 1, towards the top edge of the board. Two small angle brackets are fixed to the bottom corners of the board for bolting to the chassis, beside the power supply regulator board.

\section*{WIRING IN}

There is quite a considerable amount of wiring associated with this board. Fig. 2.3 shows the pin identities on the board and should be used in conjunction with Fig. 1.2 in the first article when wiring in. As well as the supply connections, which are made from the 10 V regulator board via the 150 mA fuse, connections must be made to the meter function, positive/negative, and number of cylinder switches and to the contact breaker socket on the front panel. A lead having a banana plug on one end and a crocodile clip on the other should be made up for connection to the contact breaker terminal.


\section*{Build an oscilloscope.}

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Fig. 2.3. Component layout and cutting details for Board A

\section*{TEST AND CALIBRATION}
lt only now remains, to set the two calibration potentiometers VR2 and VR3 so that the meter indicates correct dwell and tachometer readings. It has already been noted that 100 per cent dwell is represented by contacts permanently closed. To set the dwell calibration, switch the meter to 'Dwell', Earth switch to negative and connect the contact breaker socket to supply negative. The meter should read approximately full scale. Adjust VR3 to set the meter reading to 100 per cent. Check that with contact breaker socket connected to +12 V , the meter reading falls to zero. With the earth switch set to \(+v e\), the readings should be reversed, i.e., 0 with c.b. socket connected to supply - ve and 100 per cent with c.b. socket connected to +12 V .

To calibrate the tachometer scale, the 50 Hz mains supply is used. The r.p.m. corresponding to 50 Hz is \(50 \times \frac{60}{-2}=1,500\) r.p.m. for a four-cylinder engine. Set the function switch to r.p.m., the No. of cylinders switch to 4 , and connect the c.b. lead to one 12 V a.c. terminal of the bridge rectifier. Adjust VR2 to give a meter reading of 1,500 on the r.p.m. scale. Check that the reading falls to 1,000 r.p.m. on sixcylinders and 750 r.p.m. on eight cylinders.

\section*{DWELL MEASUREMENT}

To make measurements of dwell angle, the lead from the c.b. socket is connected to the c.b. terminal on the coil. A lead should also be connected between instrument chassis and car chassis. From the foregoing description it will be seen that the pulse produced by ICl forms part of the 'off' portion of the dwell waveform (E of Fig. 2.2). As the engine speed increases waveform \(E\) contracts to a shorter time scale.

This pulse therefore sets the upper limit of dwell which can be measured, since the off section of the waveform cannot be less than D. It should be noted that waveforms \(C\) and \(D\) are only 3 milliseconds
for the eight-cylinder engine setting, being increased in width for less cylinders in order to maintain tachometer calibration. However, the period between sparks is also proportionally longer for less cylinders at a given engine speed, and waveform \(D\) will remain the same proportion of total period of waveform \(E\) for all engines, and will limit the maximum dwell measurement to the same percentage in each case. The following table should clarify this point.

Table 2.1.
\begin{tabular}{|c|c|c|c|}
\hline No. of cylinders & Tach. pulse D & Period of E at 1,000 r.p.m. & Max. dwell at 1,000 r.p.m. \\
\hline 1 & 24 ms & 120 ms & 80 per cent \\
\hline 2 & 12 " & 60 " & \(80{ }^{\text {* }}\) \\
\hline 4 & 6 ", & 30 & 80 \\
\hline 6 & 4 ", & 20 ," & 80 ", \\
\hline 8 & 3 " & 15 ," & 80 \\
\hline
\end{tabular}

Increasing engine speed will reduce the maximum measurable dwell since it reduces the periodic time of waveform E. This limitation can be overcome to some extent, on less than eight-cylinder engines by leaving the "No. of Cylinders" switch on 8 for all dwell measurements. It must be remembered however, that the switch must be returned to its correct position when making tachometer readings.

\section*{SIX-VOLT SYSTEMS}

In a 6 V system the amplitude of the pulse obtained from the contact breaker will be only 6 V instead of 12. It may therefore be necessary to reduce the value of R10 in Fig. 2.1 to around 50!. The full value of \(240 \Omega 2\) must be used when setting the calibration of the tachometer and dwell scales, if this is done from 12 V .

Next month: Strobe circuit and instructions for using the Analyser

\title{
 UPDATIE monnwens
}

ULN3006T
IM5200
DD10R

\section*{HALL SWITCH}

We see less and less magnetic components in our circuits these days, coils are being replaced by active filters using only C's and R's, and relays are giving way to logic gates and thyristors in a general trend away from electromagnetics and towards electronics.

This state of affairs is not going to be tolerated for long by the ever loyal band of magnet-fanciers, and already they are launching a counter attack into the very heart of logic-land with a new device from Sprague called a Hall-Effect Switch.

Now for all you deviants who have forgotten what the Hall-Effect is, it goes something like this: If a current is made to flow from end to end of a bar shaped conductor, and a magnetic field is introduced perpendicular to the direction of current flow, then the effect of the field will be to deflect the end to end current flow, and to cause a small voltage, to be developed across the width of the bar. This principle has been known for a long time, and has been put to good use in the past to measure the strength of particular magnetic fields.

The new use for the Hall-Effect is in a very small magnetically actuated switch with no moving parts, and the trick has been to integrate a Hall-sensor cell with a silicon integrated circuit and then to put the whole thing in a tiny three pin plastic package. The device code is ULN3006T, and providing that all the chips that come off the assembly line aren't gobbled up immediately by industry, this one could be a very useful addition to any amateur's component box.

The chip contains an amplifier which converts the tiny Hall voltage into a logic swing at the output. Switch ON point is about 500 Gauss, and switch OFF point is about 225 Gauss, making operation with any kind of magnet possible.

The standard use for this device will be as a keyswitch for teletypewriters, where a small permanent magnet mounted in the key top will activate the sensor when the key is depressed, but, of course, other uses are limited only by your own imagination! And most readers are not short of this.

\section*{D.I.Y./L.B.I.}

We all know that l.s.i. (large scale integration) can squeeze very large logic arrays into very small spaces, but up to now this sort of production economy was strictly for the specialists, with no chance of the small man dictating chip design because of the high tooling cost involved for each new circuit. One glimmer of hope has been present for a number of years in the form of PROMs, or Programmable Read Only Memories, which are factory produced arrays which can be content-programmed by the user.

An ingenious new chip from Intersil, the IM5200, may at last bring the benefits of custom I.s.i. to everyone because it is the first Field Programmable Logic Array (F.P.L.A.) ever produced. A sort of Programmable Logic Array has been available in the past, but with these early devices programming had to be carried out at the final manufacturing stage. These were a help, because instead of having to order a new device in thousands, you could order in hundreds, but the new F.P.L.A. takes this idea much further because it is now economic to make a new device in only one-off quantities, and not only that, because programming is done by the user, a ready programmed chip can be edited or modified at will.

The IM5200 has 14 logic inputs which are buffered and inverted internally and then fed to a programmable array of 48 AND gates which can each have any combination of the 14 input terms used as inputs.

The outputs of the AND gate array are fed to an OR gate array where each of eight OR gates can have any combination of AND terms as their inputs, and finally the eight OR ouptuts can be further programmed to give active high or active low outputs from the chip.

The secret of the new device lies in the A.I.M. (Avalanche Induced Migration) links used to hook-up the gate array when high current programming pulses are applied momentarily. As supplied, all A.I.M. links appear as open-base
npn transistors (i.e. open circuits) but after programming a link is partially shorted to form a diode, and so from the outside of the package it is possible to selectively "wireup" the gating arrays.

There seems to be no reason why these devices should not be used by amateurs once a suitable (and simple) programmer has been built, and since each F.P.L.A. can replace up to 250 t.t.l. packages, do-it-yourself will take on a new meaning!

\section*{TEN TO THE BAR}

A modern trend is to replace all "old-fashioned" analogue displays with a trendy bunch of seven-segment l.e.d.s, to achieve a mathematically precise numerical readout. Of course, we all rather fancy a digital multimeter to replace the old faithful moving coil job, but in many circumstances the change to a digital type display actually docreases the amount, and clarity of available information; imagine for example the difficulties involved in reading a car speedo' which gave a digital output!

Now that the novelty of little flickering numbers is wearing off, the l.e.d. manufacturers are turning their skills to producing a new kind of analogue display, which, while being more suitable for use with today's advanced circuits than a moving coil meter, still gives a simple to read analogue information display which is not subject to misinterpretation.

An example of this practical thinking is the DD10R ten bar l.e.d. array from ITT which comes in a 20 pin d.i.I. package that can be stacked end to end to produce an array of light bars to any desired length. The anode and cathode of each of the ten l.e.d.s are individually accessible to ensure the utmost flexibility of application.

The usual way to display analogue variables such as speed, height, or depth on this sort of array is to illuminate more and more bars as the measured quantity increases, so as to give a variable length of column of light. Stacking several arrays alongside each other forms a histogram type display, and you can't do that with Nixie tubes!

Top 500 Semiconductors From the Largest Range in the U.K
\begin{tabular}{|c|c|c|c|c|c|c|c|c|c|c|c|c|c|c|c|}
\hline 2N456 & 0.901 & Orange & 0.12
0.25 & 2N5192 & 1.24
1.45 & AF 106
AF 109R & 0.40
0.40 & BC186
BC187 & 0.25
0.27 & BF159
BFI60 & 0.27
0.23 & LOO5T1 & 1.50
1.10 & OC35 & \\
\hline 2N456A & 0.85 & 2N3053 & 0.25 & 2N5195 & 1.46 & AF109R & 0.40 & BC187 & 0.27 & BFI60 & 0. 23 & LM380 & \(1 \cdot 10\) & OC42 & \\
\hline 2N457A & 1.20 & 2N3054 & 0.80 & 2N5245 & 0.47 & AF114 & 0.35 & BC207 & 0.12 & BF163 & 0.32 & LM381 & \(2 \cdot 20\) & OC45 & \\
\hline 2N490 & 4.00 & 2N3055 & 0.75 & 2N5294 & 0.48 & AF115 & 0.35 & BC208 & 0.11 & BF166 & 0.40 & LM702C & 0.75 & OC71 & \\
\hline 2N491 & 4.36 & 2N3390 & 0.45 & 2N5295 & 0.43 & AF 116 & 0.35 & BC212K & 0.18 & BF167 & 0.25 & LM709 & & OC72 & \\
\hline 2N492 & 5.00 & 2N3391 & 0.28 & 2N5296 & 0.48 & AF117 & 0.35 & BC212L & 0.16 & BF173 & 0.27 & TO99 & 0.40 & OC81 & \\
\hline 2N493 & \(5 \cdot 20\) & 2N3391A & 0.29 & 2N5298 & 0.50 & AF118 & 0.35 & BC214L & 0.18 & BF177 & 0.29 & 8 OH & 0.38 & OC83 & \\
\hline 2N696 & 0.22 & 2N3392 & 0.15 & 2N5457 & 0.49 & AF 124 & 0.30 & BC237 & 0.18 & BF178 & 0.35 & 14 DIL & 0.40 & ORP12 & \\
\hline 2N697 & 0.16 & 2N3393 & 0.15 & 2N5458 & 0.46 & AF125 & 0.30 & BC238 & 0.15 & BF179 & 0.43 & LM710 & 0.47 & R53 & \\
\hline 2N698 & 0.82 & 2N3394 & 0.15 & 2N5459 & 0.49 & AF126 & 0.28 & BC239 & 0.15 & BF180 & 0.35 & LM723C & 0.90 & SL414A & \\
\hline 2N699 & 0.59 & 2N3402 & 0.18 & 2N5492 & 0.58 & AF127 & 0.28 & BC251 & 0.25 & BF181 & 0.36 & LM741 & & SL610C & \\
\hline 2N706 & 0.14 & 2N3403 & 0.19 & 2N5494 & 0.58 & AF 139 & 0.65 & BC253 & 0.25 & BF182 & 0.35 & TO99 & 0.40 & SL611C & \\
\hline 2N706A & 0.11 & 2N34 14 & 0.20 & 2N5496 & 0.61 & AF 186 & 0.46 & BC257 & 0.18 & BF183 & 0.55 & 8016 & 0.40 & SL612C & \\
\hline 2N708 & 0.17 & 2N3415 & 0.21 & 2N5777 & 0.45 & AF200 & 0.85 & BC258 & 0.18 & BF184 & 0.30 & 14DIL & 0.39 & SL620C & \\
\hline 2N709 & 0.42 & 2N3416 & 0.24 & 2N6027 & 0.45 & AF239 & 0.65 & BC259 & 0.17 & BF185 & 0.30 & LM747 & 1.00 & SL621C & \\
\hline 2N711 & 0.50 & 2N3417 & 0.29 & 3N128 & 0.73 & AF240 & 0.90 & BC261 & 0.25 & BF194 & 0.12 & LM748 & & SL623 & \\
\hline 2N718 & 0.23 & 2N3440 & 0.59 & 3N 139 & 1.42 & AF279 & 0.70 & BC262 & 0.25 & BF195 & 0.12 & 8DIL & 0.60 & SL640C & \\
\hline 2N718A & 0.28 & \({ }^{2} \mathbf{N} 3444\) & 0.97 & 3N140 & 1.00 & AF280 & 0.79 & BC263 & 0.25 & BF196 & 0.13 & 14DIL & 0.73 & SL641C & \\
\hline 2N720 & 0.57 & 2N3442 & 1.40 & 3N141 & 0.1 & AL102 & 1.00 & BC300 & 0.38 & BF 197 & 0.15 & LM3900 & 0.70 & SN76003N & \\
\hline 2N914 & 0.39 & N3638 & 0.15 & 3N200 & 2.49 & AL 103 & 1.00 & EC301 & 0.34 & BF 198 & 0.18 & LM7805 & 2.00 & SN76043N & \\
\hline 2N916 & 0.28 & 2N3638A & 0.15 & 40361 & 0.40 & BC107 & 0.14 & BC302 & 0.29 & BF200 & 0.40 & LM7812 & 2.50 & SN76023N & \\
\hline 2N918 & 0.32 & 2N3639 & 0.27 & 40362 & 0.45 & BC108 & 0.14 & BC303 & 0.54 & BF225 & 0.23 & LM7815 & \(2 \cdot 50\) & SN76033N & \\
\hline 2N929 & 0.37 & 2N3641 & 0.17 & 40363 & \(0 \cdot 8\). & BC109 & 0.14 & BC307 & 0.17 & BF244 & 0.21 & LM7824 & 2.50 & ST2 & \\
\hline 2N930 & 0.22 & 2N3702 & 0.12 & 40389 & 0.48 & BC113 & 0.15 & BC308A & 0.15 & BF245 & 0.45 & MC1303 & 1.50 & TAA263 & \\
\hline 2N1302 & 0.19 & 2N3703 & . 13 & 40394 & 0.56 & BC115 & 0.17 & BC309C & 0.20 & BF246 & 0.58 & MC4310 & 2.50 & taA300 & \\
\hline 2N1303 & 0.19 & 2 N 3704 & 0.15 & 40395 & 0.65 & BC116 & 0.17 & BC317 & 0.12 & BF247 & 0.65 & MC1330P & 0.90 & TAA350 & \\
\hline 2 N 1304 & 0.28 & 2N3705 & 0.15 & 40406 & 0.44 & BCi16A & 0.18 & BC318 & 0.12 & BF254 & 0.19 & MC1351P & 0.80 & tAA550 & \\
\hline 2N1305 & 0.24 & 2N3706 & 15 & 40407 & 0.35 & EC117 & 0.21 & 8С337 & 0.20 & BF255 & 0.19 & MC1352P & D. 80 & TAA611C & \\
\hline 2N \(\uparrow 306\) & 0.31 & 2N3707 & 0.18 & 40408 & 0.35 & BC118 & 0.14 & BC338 & 0.20 & BF257 & 0.47 & MC1466 & 3.50 & TAA621 & \\
\hline 2N+307 & 0.30 & 2N3708 & 14 & 40409 & 0.52 & EC119 & 0.29 & BCY30 & 0.80 & BF258 & 0.53 & MC1469 & 2.75 & TAA661B & \\
\hline 2N1308 & 0.47 & 2N3709 & 0.15 & 40410 & 0.52 & BC121 & 0.35 & 8CY31 & 0.85 & BF259 & 0.55 & ME0402 & 0.20 & TBA641日 & \\
\hline 2N1309 & 0.47 & 3710 & 15 & 40411 & 2.00 & BC125 & 0.16 & BCY32 & 1.15 & BFR39 & 0.24 & ME0404 & 0.1 & TBA651 & \\
\hline 2N1671' & 1.54 & 2N3711 & 0.15 & 40594 & 0.74 & BC126 & 0.23 & BCY33 & 0.85 & BFR79 & 0.24 & ME0412 & 0.11 & TBA800 & \\
\hline 2N1671A & 1.67 & 2, 3712 & 1.20 & 40595 & 0.84 & BC132 & 0.30 & 8CY34 & 0.79 & BFS21A & \(2 \cdot 30\) & ME4102 & 0.11 & TBA810 & \\
\hline 2N1671B & 1.85 & 2N3713 & 1.20 & 40601 & 0.67 & BC134 & 0.13 & BCY38 & 1.00 & BFS28 & 1.36 & ME4104 & 0.11 & TBAB20 & \\
\hline 2N1741 & 0.45 & 2N3714 & - 30 & 40602 & 0.61 & EC135 & 0.13 & BCY39 & 1.50 & BFS61 & 0.27 & MJ480 & 0.95 & tBa920 & \\
\hline 2N1907 & 5.50 & 2N3715 & . 50 & 40603 & 0.58 & BC136 & 0.17 & BCY40 & 0.97 & BFS98 & 0.25 & M. 1481 & 1.20 & TIL209 & \\
\hline 2N2102 & 0.60 & 2N3716 & 1.80 & 40604 & 0.56 & BC+37 & 0.17 & BCY42 & 0.28 & BFX29 & 0.30 & MJ490 & 1.05 & TIP29A & \\
\hline 2N2147 & 0.78 & 2N3771 & 2.20 & 40636 & \(1 \cdot 10\) & BC140 & 0.68 & BCY58 & 0.30 & EFX30 & 0.27 & MJ491 & 1.45 & T1P29C & \\
\hline 2N2148 & 0.94 & 2N3772 & . 80 & 40669 & 1.00 & BC14 & 0.68 & BCY59 & 0.32 & BFX84 & 0.24 & M 29555 & 1.00 & TIP30A & \\
\hline 2N2160 & 0.90 & 2N3773 & 2.65 & 40673 & 0.73 & EC142 & 0.23 & BCY70 & 0.17 & BFX85 & 0.30 & MJE340 & 0.48 & TIP30C & \\
\hline 2N2218A & 0.22 & 2N3789 & 2.06 & AC126 & 0.20 & BC143 & 0.25 & BCY71 & 0.22 & BFX87 & 0.28 & MJE2955 & 1.20 & TIP31A & \\
\hline 2N2219 & 0.24 & 2N3790 & 2.40 & AC127 & 0.20 & BC147 & 0.14 & BCY72 & 0.15 & BFX88 & 0.25 & MJE3055 & 0.75 & TIP31C & \\
\hline 2N2219A & 0.26 & 2N3791 & \(2 \cdot 35\) & AC128 & 0.20 & BC148 & 0.14 & BD115 & 0.75 & BFX89 & 0.90 & MJE370 & 0.65 & TIP32A & \\
\hline 2N2220
2N2221 & 0.25 & 2N3792 & 2.60 & AC451V & 0.27 & BC149 & 0.15 & BD146 & 0.75 & BFY50 & 0.23 & MJE371 & 0.75 & TIP32C & \\
\hline 2N2221 & 0.18 & 2N3794 & 0.24 & AC152V & 0.49 & \(\mathrm{BCH}^{83}\) & 0.18 & BD121 & 1.00 & BFY51 & 0.23 & MJE520 & 0.60 & tip33A & \\
\hline 2N2221A & 0.21 & 2N3819 & 0.37 & \({ }^{\text {AC }}\) C153 & 0.35 & ECT54 & 0.18 & B0123 & 0.82 & BFY52 & 0.21 & MJE521 & 0.70 & TIP33C & \\
\hline 2N2222 & 0.20 & 2N3820 & 0.64 & AC153K & 0.40 & BC157 & 0.16 & BD124 & 0.67 & BFY53 & 0.18 & MP8111 & 0.32 & TIP34A & \\
\hline 2N2222A & 0.25 & 2N3823 & 0.78 & AC154 & 0.25 & BC158 & 0.16 & ED134 & 0.40 & BFY90 & 0.75 & MP8112 & 0.40 & TIP34C & \\
\hline 2N2368 & 0.25 & 2N3904 & 0.27 & AC176 & 0.30 & BC160 & 0.60 & 8D132 & 0.50 & BRY3s & 0.38 & MP8113 & 0.47 & TIP35A & \\
\hline 2N2369 & 0.20 & 2N3906 & 0.27 & AC176K & 0.40 & BC1678 & 0.15 & ED135 & 0.43 & BSx20 & 0.21 & MPF102 & 0.39 & TIP36A & \\
\hline 2N2369A & 0.22 & 2N4036 & 0.67 & AC187K & 0.35 & BC1688 & 0.15 & ED136 & 0.47 & ES×21 & 0.29 & MPSA05 & 0.25 & TIP4IA & \\
\hline 2N2646 & 0.55 & 2N4037 & 0.42 & ACIP8K & 0.49 & BC168C & 0.15 & ED137 & 0.55 & BU104 & 2.00 & MPSA06 & 0.31 & TIP41C & \\
\hline 2N2647 & 0.98 & 2N4058 & 0.18 & ACY18 & 0.24 & BC1698 & 0.15 & BD138 & 0.63 & BU105 & 2.25 & MPSA12 & 0.35 & TIP42A & \\
\hline 2N2904 & 0.22 & 2N4059 & 0:15 & ACY19 & 0.27 & BC169C & 0.15 & 8p139 & 0.71 & C106D & 0.65 & MPSA55 & 0.25 & 11P42C & \\
\hline 2N2904A & 0.24 & 2N4060 & 0.15 & ACY20 & 0.22 & BC170A & 0.15 & ED140 & 0.87 & CA3018A & \(0 \cdot 85\) & MPSA56 & 0.31 & TIP49C & \\
\hline 2N2905 & 0.25 & 2N4061 & 0.15 & ACY21 & 0.26 & BC171 & 0.18 & BD529 & 0.80 & CA3020A & 1.80 & MPSU05 & 0.65 & TIP53 & \\
\hline 2N2905A & 0.26 & 2N4062 & 0.15 & ACY28 & 0.20 & BC172 & \(0 \cdot 17\) & ED530 & 0.80 & CA3028A & 0.79 & MPSU06 & 0.58 & TIP2955 & \\
\hline 2N2906 & 0.19 & 2N4126 & 0.21 & ACY30 & 0.58 & BC177 & 0.28 & BDY20 & 1.05 & CA3035 & 1.37 & MPSU55 & 0.63 & TIP3055 & \\
\hline 2N2906A & 0.21 & 2N4289 & 0.34 & AD142 & 0.57 & BC178 & 0.27 & EF115 & 0.36 & CA3046 & 0.70 & MPSU56 & 0.80 & TYY43 & \\
\hline 2N2907 & 0.22 & 2N4919 & 0.95 & AD143 & 0.68 & BC179 & 0.30 & 8F117 & 0.55 & CA3048 & 2.11 & NE555V & 0.70 & ZTX300 & \\
\hline 2N2907A & 0.24 & 2N4920 & 1.10 & AD149V & 1.20 & BC182 & \(0 \cdot 12\) & BF121 & 0.35 & CA3052 & 1.62 & NE556 & \(1 \cdot 30\) & ZTX301 & \\
\hline 2 N 2924 & \(0 \cdot 20\) & 2N4921 & 0.83 & AD150 & 1.15 & BY182L & 0.12 & BF123 & 0.35 & CA3089E & 1.96 & NE560 & 4.48 & 2Tx302 & \\
\hline 2N2925 & 0.20 & 2N4922 & 1.00 & AD161 & 0.50 & BC183 & 0.12 & BF125 & 0.35 & CA30900 & 4.23 & NE561 & 4.48 & 21 \(\times 500\) & \\
\hline 2N2926 & & 2N4923 & 1.00 & AD162 & 0.50 & BC183L & 0.12 & EF152 & 0.20 & LM301A & 0.48 & NE565A & 4.48 & 2TX501 & \\
\hline Green & 0.12 & 2N5190 & 0.92 & AD161 & PR & BC184 & 0.13 & BF153 & 0.25 & LM 308 & 2.50 & OC23 & 1.35 & ZTX502 & \\
\hline Yellow & 0.12 & 2N5191 & 0.98 & AD162 & 1.20 & BC184L & 0.13 & BF754 & \(0 \cdot 20\) & LM309K & 1.88 & OC28 & 0.78 & \(\underline{21} \times 530\) & \\
\hline
\end{tabular}

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 \begin{tabular}{ll|l|} 
& SN7402 & 0.16 \\
SN7403 & 0.18 & SN741 \\
SN7404 & 0.19 & SN7417
\end{tabular}

\begin{tabular}{ll|ll|ll|ll|llll} 
SN7406 & 0.45 & SN7423 & 0.29 & SN7445 & 0.90 & SN7473 & 0.36 & SN7490 & 0.45 & SN74121 & 0.37 \\
SN7407 & 0.45 & SN7425 & 0.29 & SN7446 & 0.95 & SN7474 & 0.36 & SN7499 & 0.85 & SN74122 & 0.50 \\
SN7408 & 0.19 & SN7427 & 0.29 & SN7447 & 0.95 & SN7475 & 0.50 & SN7492 & 0.45 & SN774123 & 0.
\end{tabular}
\(\begin{array}{lll}\text { SN74145 } & 0.95 \\ \text { SN74150 }\end{array}\) SN74150 SN74153 0.85 \(\begin{array}{ll}\text { SN74153 } & 0.85 \\ \text { SN74154 } & 1.50\end{array}\) \(\begin{array}{ll}\text { SN74154 } & 1.50 \\ \text { SN74155 } & 1.50\end{array}\) \(\begin{array}{ll}\text { SN74155 } & 1.50 \\ \text { SN74157 } & 0.95 \\ \text { SN74 }\end{array}\) \(\begin{array}{lll}\text { SN74160 } & 1 \cdot 10 \\ \text { SN }\end{array}\) \(\begin{array}{ll}\text { SN74161 } & 1 \cdot 10 \\ \text { SN }\end{array}\) \(\begin{array}{ll}\text { SN774162 } & 1 \cdot 10 \\ \text { SN74163 } & 1.10\end{array}\) \(\begin{array}{ll}\text { SN74163 } & 1.10 \\ \text { SN74164 } & 2.01 \\ \text { SN74165 }\end{array}\)

SN74167 SN74174
SN74175 SN74176 SN74180 SN74181 SN74181 SN74191
SN74192 SN74192
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SN7419 SN74196
SN74197 SN74197
SN74198
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\section*{(P) MIEROPROEESSOR PART Z}

LAST month we covered the fundamentals of the microprocessor, the hexidecimal coding system, and the function of some of the pins of the Motorola chip. This concluding article deals with the programming language used in the processor and the various types of memory.

\section*{the language of the mpu}

All instructions within an MPU system are in the form of binary words-more commonly referred to as machine code. However, the sequence of instructions which form a programme are normally first written in mnemonic form, using the list of instructions illustrated in Fig. 6, and are then converted into machine code.
A programme consists of a series of statements each comprising an instruction (mnemonic operator) and the operand. The operand can be a numerical value, an address, or an address where a numerical value can be found. Sometimes the operand is only present by implication. For instance, the instruction to add the contents of accumulator A to the contents of accumulator B and to place the result in accumulator \(A\), represented by the mnemonic ABA, does not need an operand because it is obvious from the instruction that the operands must be in the accumulators.
Depending on the particular instruction an operand can be specified or located in a number of different ways:
Immediate: An example of immediate addressing is LDA A 05 (Load accumulator A with the number 05). In immediate addressing a programme statement comprises two bytes of machine code; in our example LDA A will form one byte and 05 the second. The machine code for LDA A in the immediate addressing mode is 86 , so the whole statement becomes 8605 .
Direct: A statement using the direct form of addressing is a two byte statement comprising a single byte which specifies the instruction and a second byte which is the address where the operand will be found.
For example, the instruction LDA A 05 in the direct form of addressing would tell the MPU to load accumulator A with the content of memory location 0005 . In machine code this is 9605.
The second byte of a statement employing the direct addressing mode forms the least significant
*Director, MOS Marketing, Europe. Motorola Inc.
eight bits of the address and the most significant eight bits are assumed to be 00 . Therefore, with direct addressing it is possible to address the memory locations from 0000 to 00 FF (the first 256 locations).
Extended: The only difference between extended and direct addressing is that three bytes instead of two are employed so that a full address can be incorporated in a statement. For example, LDA A 12 05 (load accumulator A with the contents of memory location 1205 using extended addressing) becomes B6 1205 in machine code.
Indexed: The MPU contained a register called the index register which is used in indexed addressing. The index register holds a two byte address which can be set to any value by the programmer. In indexed addressing the operand is found at the

address specified by the index register plus the number which is contained in the second byte of the instruction.

For example, the instruction LDA \(A+6\) in the indexed mode would load accumulator \(A\) with the contents of the memory location which is six locations higher than the location which is addressed by the index register. In machine code this is represented by A6 06 .

If the index register held the address 1202 , the instruction A6 06 would load accumulator A with the contents of memory location \(1202+6=1208\).

Relative: Certain instructions, namely branch instructions, for the MPU employ the relative mode of addressing. These instructions enable the MPU to carry out an instruction which is not the next one in sequence. Consider the following instruction programme:

\section*{LOOP DEC B \\ BNE LOOP \\ NEXT INSTRUCTION}

In this sequence \(D E C B\) causes \(a l\) to be subtracted from the content of accumulator \(B\). Instruction BNE compares what is left in accumulator \(B\) with zero and causes the next instruction to be carried out if the result is zero. If the accumulator is not zero BNE causes the MPU to branch back to "LOOP" and again subtract 1 from the accumulator. In machine code this instruction sequence is represented by
\begin{tabular}{lll}
0010 & 5A & (DEC B) \\
0011 & 26 & (BNE) \\
0012 & FD & \((-3)\) \\
0013 & Next instruction
\end{tabular}

At memory location 0011 the MPU encounters the instruction 26 (BNE). Memory location 0012 contains the relative address of the next instruction should the conditions for a branch be met. If they are, the relative address ( \(\mathrm{FD}=-3\) ) is added to the contents of the programme counter, so that the programme branches back to 0010 (DEC B): i.e. after reading the \(B N E\) instruction and the relative address the programime counter contains 0013 , to branch, the relative address is added to the programme counter; 00 \(13+(-3)=0010\).

Numerical information within the machine, as has been mentioned before, is represented in binary form. For relative addressing, and for some mathematical operation the numerical value is in the form of a signed two's complement number.

In signed two's complement representation the most significant bit (left hand bit) of a number is used to indicate the sign of the number: all numbers beginning with 1 are negative and all numbers beginning with 0 are positive. The remaining seven bits are used to indicate the value of the number.

Therefore while unsigned binary numbers of 8 bits can have any value from 0 to 255 (decimal), signed binary numbers can have values from +127 to -128 .

To find the signed two's complement of a negative number three steps are necessary.
For example, what is the two's complement of -23 ? Forget about the sign and express the number in binary form: \(-23=00010111\). Complement the number (change 0 to 1 and 1 to 0 ) so that it equals 11101000 . Add 1 to the result, \(11101000+1=\) 11101001 and so -23 is equal to 11101001 or E9 (hexidecimal) in signed two's complement form.

The MPU can also operate on decimal numbers in binary coded form. As each decimal number requires four bits, each byte of binary code can represent two decimal numbers.

\section*{PROGRAMMING THE MPU}

It is possible to programme the MPU directly in machine code, although this is rather a tedious and time consuming .business. The industrialist who intends to use the MPU in an instrument, control system or in other equipment will use a "full-sized" computer to assist him in the preparation of his microprocessor programme.

Basically the process involves writing the microprocessor programme using the mnemonic instruction set (this is called the source programme), and then using the main computer to convert it into a sequence of machine coded instructions known as the object programme.

The whole conversion process is known as "assembly" and the programme which is run on the main computer to perform the conversion is called an assembler. The assembler, and some associated programmes, enable the source programme to be easily edited and manipulated by the programmer.

\section*{THE MEMORY}

The MPU has, as we have seen, a 16-bit address bus for addressing locations in memory and input/ output units (Peripherals). It has also been explained that a 16 -bit binary word can have 65,536 different values, therefore the memory can have 65,536 locations.

Each 1,024 memory locations is referred to as 1 K (it being generally accepted that in data processing \(1 \mathrm{~K}=1,024\), while in electronics \(1 \mathrm{k}=1,000\) ) therefore, the memory for the Motorola MPU can have a capacity of up to 64 K bytes.

This memory will normally be made up from RAMs (Random Access Memories), ROMs (Read Only Memories) and PROMs (Programmable Read Only Memories). All these memory devices are semiconductor integrated circuits.
RAMs: A RAM consists of a number of storage cells-each cell capable of storing one bit-together with the necessary control and addressing circuitry. The design of the cells and the control circuit allows information to be written into, or read from, the memory at will.

ROMs: Like the RAM a ROM comprises a number of memory cells complete with the addressing and control circuitry. However, the design of the cells in a ROM is fundamentally different. The content (a binary 1 or 0 ) is set during the final stages of manufacture of the device and can never be subsequently changed.

The actual binary content of the ROM is determined by the customer who usually sends the ROM manufacturer a paper tape, or a set of punched cards, which specifies the content of the ROM. ROMs are only economic when a large number with the same binary content have to be manufactured.

PROMs: The difference between a PROM and a ROM is that the content of the memory is entered electrically by the user. Once a PROM has been programmed the contents cannot be altered.


Most microprocessors will employ RAMs, together with some form of read only storage. The ROM section of the store is used to hold frequently used programmes.

The RAM in Motorola's microprocessor set is known as the MCM6810 and is shown in Fig. 7. It will store 128 bytes and has eight bi-directional buffers for connecting to the microprocessing system's data bus. The 128 memory locations necessitate seven address inputs in order that each location can be individually selected. In addition, the MCM6810' has six chip select ( \(E\) ) inputs to facilitate easy memory expansion.

The ROM intended for use in the Motorola microprocessor will store 1 K bytes (1.024 8-bit

Fig. 9. PIA Block diagram



Fig. 8. ROM Functional block diagram
words). It is known as the MCM6830 and. as shown in Fig. 8, it has four enable inputs and eight buffer stages for connection to the data bus.

\section*{THE OUTSIDE WORLD}

A microprocessing system must be able to communicate with external equipment before it can be used. This equipment can take very many forms from simple relays to magnetic tape units, keyboards. video display units. printers and the like. Designing a circuit that will allow the microprocessing system to communicate with any or all of these is, to say the least, difficult.

In the Motorola microprocessor the problem is solved by a universal interface circuit known as the "Peripheral Interface Adaptor" (PIA) with the type number MC6820.

This is a complex LSI circuit of which a block diagram is given in Fig. 9. It is. as can be seen, divided into two sections: section \(A\) and section \(B\). Connection to the microprocessor is made via the data highway ( \(\mathrm{D}_{4} \mathrm{D}_{7}\) ), the address highway (A0, 1. 2. 13 and \({ }_{14}\) ) and several control lines. The peripheral can be connected to the PIA by two bidirectional data highways \(\left(\mathrm{PA}_{0}\right.\); and \(\left.\mathrm{PB}_{0-7}\right)\) and four control lines.

Each individual line in the two peripheral datal highways can be programmed to be either an input or an output by binary words held in the data direction registers. Binary words held in the control registers set the function performed by the various PIA control lines to adjust the function performed by the PIA to suit the peripheral to which it is connected.

\section*{PIA IN ACTION}

Let us consider an actual example of the PIA in action, as graphically illustrated in Fig. 10.
(1) Fig. 10(a). The peripheral tells the PIA that it wishes to input data by taking the PIA input ready line to logic " 1 ". This causes the left hand bit in the


Fig. 10(a). Data is presented on the A side by an external device. The Input Ready signal sets a status bit and pulls down IRQA. The Interrupt response routine will identify this interrupt by polling status bits

A control register to be set to 1 . This bit is called a "flag" bit.
(2) The flag bit forces the system's "Tnterrupt request" (TRQ) line to logic " 0 ". Normálly, all IRQ lines will be connected together (wire-OR). including the \(\overline{I R Q}\) input to the MPU.
(3) The MPU will go into an interrupt request sequence as described earlier. After storing the contents of all its internal registers away in the stack, the MPU will set the interrupt mask bit in the condition code register before going to a known location within memory for the first instruction in the interrupt programme.
(4) This programme will cause the MPU to examine each flag bit in all units capable of causing an \(\overline{I R Q}\) in turn (this is known as "Polling").
(5) Fig.10(b). The MPU "sees" the PIA A control register flag at logic 1 and instructs the PIA to write its data onto the data highway.
(6) This action resets the flag in the PIA control register. The \(\overline{I R Q}\) line goes high, the M.PU resets the interrupt mask bit to 0 , recalls the content of the stack and proceeds with its normal programme.

The situation resulting from a request from a peripheral for data is illustrated in Figs. 10(c) and \(10(d)\). The sequence starts with an "output request" from the peripheral. The reader should by now be able to trace the sequence without the assistance of the printed word.
The PIA can be used in many other ways apart from the one that has been described here.

Motorola have another interface device for use with their MPU which is called the Asynchronous Communications Interface Adapter (ACIA). This is another very complex device and it is not proposed to describe it in very great detail here. However. its function will be discussed in principle.

All information transfers discussed so far have been carried out in parallel (all eight bits at the same time). However. when data is to be transferred over a single line it has to be done serially (one bit at a time). The ACIA receives each input word one bit at a time (serially) from the reception line.


Fig. 10(b). The Interrupt Response routine reads the \(A\) data. This action automatically clears the interrupt and sends the Input Acknowledge signal


Fig. 10(c). An external device requests data on the \(B\) side with Output Request. This sets a status bit and pulls down IRQB


Fig. 10(d). The Interrupt Response routine identifies the interrupt by checking status bits. A read operation is used to clear the interrupt. Writing output data to the \(B\) side presents the data to the external device and automatically generates the output ready signal
performs certain checks, then transmits the data in parallel ( 8 bits) over the data highwaty to the MPU. In the reverse direction the MPU sends all eight bits of a word simultaneously to the ACIA and the ACIA re-transmits each word one bit at a time over the serial transmission line to the remote equipment.

Information on the serial transmission line appears as a string of is and 0 s in the form of electrical pulses and is carried out in accordance with rules which are internationally agreed. Pulses of different length to the data pulses are inserted to identify the beginning and end of each word transmitted. The ACIA performs in accordance with the agreed rules. If required, the ACIA can be used whenever it is necessary to interface the MPU with other equipment over a single pair of wires.

\section*{MORE MICROPROCESSORS}

We have examined the Motorola microprocessors in some detail. It was mentioned at the start of this article that there are now some thirty different microprocessors available. While we do not intend to mention all of these, it would be worth while having a brief look at some of them.

All the microprocessors consist of a central processing unit. a memory and suitable arrangements for input/output operations. In some cases the MPU itself has to be programmed to carry out the necessary handshaking routines with peripheral devices.

\section*{FIRST TYPES}

Intel were the first semiconductor company to produce a microprocessor. It consists of four integrated circuits which together made up a complete 4 -bit processing unit, known as the 4004 N . The basic four-chip set comprises the central processing unit (CPU), a RAM, a ROM and an I/O unit.

Using the basic building blocks a 4004 system can have up to \(4 \mathrm{~K} \times 8\)-bit words in the ROM, 1280 4-bit words in the RAM and up to \(1281 / \mathrm{O}\) lines. With the addition of a few gates' the system can be expanded to incorporate any combination of RAM and ROM up to 48 packages and to handle up to 192 1/O lines. The system has an instruction set comprising of 45 commands.

From the 4004 Intel progressed to the 8008, an 8 bit parallel central processing unit complete with associated integrated circuits. The 8008 was designed as a generat purpose processing unit. which can directly address up to 16 K bytes of memory.

The CPU unit itself comprises an 8-bit accumulator, two 8 -bit temporary registers, four flag bits and eight 14 -bit address registers. The instruction set consists of 48 commands which allow subroutine nesting up to seven levels. Asynchronous or synchronous operation with external memory and multiple interrupts are standard features.

\section*{THE 8080}

The next development from Intel was a more powerful and faster 8 -bit microprocessor which was designated the 8080 .

The 8080 provides 74 basic processor instructions including decimal and binary arithmetic and can address up to 65 K bytes of memory. Up to 256 input/output units can be interfaced directly with the , single chip processor unit. A basic 8080 system
consists of the CPU and seven other i.c. packages. A second 4-bit machine, known as the 4040, has been introduced by Intel. This is electrically identical to the 4004 but has an improved performance.

\section*{SERIES 3000}

For applications where greater speed is required there is the series 3000 . This employs Schottky bipolar circuitry throughout, and consists of a central processing element (3002), microprogramme control unit (3001) and seven other i.c.s including a look-ahead-carry generator and an interrupt control unit.

The main circuits are the 3001 and the 3002 which together form a 2 -bit section of a high speed processing unit expandable up to 320 bits. One microprogramme control unit is required for every eight central processing elements. Each of the i.c.s within the system have multiple logic systems which can be rearranged by microprogramming.

\section*{MICROPROGRAMMING}

Microprogramming is a technique which means that the function of the processor is determined by a series of short microprogrammes held in a RONi. The instruction set available is determined by the microprogramme and the user virtually chooses the instruction set he requires by writing the appropriate microprogrammes. Each instruction (called a microinstruction). when implemented, calls up, a microprogramme consisting of a series of microinstructions. The time taken by the machines to perform a microinstruction is called a microcycle.

\section*{BCD OPERATION}

Most microprocessors work in binary. following mainframe computer practice. However. a system has been introduced by Advance Electronics Ltd which operates in binary coded decimal (BCD). The decimal processor is contained on two LSI chips divided as follows: Chip 1-programme store (ROM) and input interface. Chip 2-arithmetic unit, data store ( RAM) and output interface. The unit is intended for use in applications where fairly large quantities are required, as the ROM section has to be programmed by the manufacturers and. once programmed, cannot be altered.

\section*{CMP8}

National Semiconductors have a fairly new microprocessor, designated CMP8. consisting of an 8 -bit arithmetic and logic unit (ALU). two 16-bit index registers with auto increment/decrement plus four 8 -bit registers. There are separate programme and address counters, a 16 -bit stack pointer and direct memory access (DMA). It has the capability of directly addressing 65 K bytes of memory. and comes in a 40-pin package.

Other microprocessor products available from National Semiconductors are designed around two p.m.o.s. I.s.i. chips which can be assembled in different combinations to form computer systems ranging from a simple 4-bit processor for elementary control functions upwards.

These two chips are the register, arithmetic and logic unit (RALU) and the control read-only


A miniature, battery operated, six-channel-into-two stereo mixer

SOUND mixing has, over the years, become more and more a part of the audio scene and is, of course, an essential prerequisite of the recording industry. Mixers of one form or another have always been in demand by the amateur constructor and find their place in p.a. work, discos, on stage live performances, and in the home studio. One of the difficulties has been to find a mixer which fits equally well into all these spheres of activity and which does not cost the earth to build.
Additionally the growth of interest in high fidelity reproduction and latterly, the boost given to interest in the creative manipulation of sound by the advent of instruments like the synthesiser has given rise to a fresh demand for sound mixers offering a little more than the "add or take away" requirement of a year or so ago.
It is with the synthesiser in mind, therefore, that the Minimix 6 has been designed but, at the same time, with the proviso that it should prove capable of employment in a diversity of applications.

\section*{USE OF THE 741}

Essentially the Minimix 6 is a tine mixer based on the ubiquitous 741 operational amplifier. However, the line preamplifiers have been given a gain range of up to +40 dB and thus each line channel is capable of being operated satisfactorily by devices such as crystal pickups or crystal microphones. It is fairly generally recognised that the 741 operational amplifier is unsuitable for quality audio applications because the typical input noise figure of around \(20 \mu \mathrm{~V}\) creates problems particularly when any degree of gain is involved. A large proportion of the signal to noise problems can be overcome, however, by careful specification of signal levels and circuit layout.

The Minimix 6 provides unity gain as a true line mixer, i.e. for a \(1 \mathrm{~V}(0 \mathrm{~V} . \mathrm{U}\).) input the output is also 1 V . In these circumstances, providing the input noise of the active units does not exceed \(20 \mu \mathrm{~V}\) then the theoretical signal/noise ratio is slightly less than -86 dB . In practice the measured value on the prototype instrument proved to be -80 dB , a figure which is likely to be more than adequate for the purposes specified. With a 10 mV input, i.e. operating at maximum gain for a 1 V output, the signal to noise ratio deteriorates to -70 dB . Again, a figure which would be acceptable for most purposes.

\section*{SPECIFICATION}
\begin{tabular}{|c|c|}
\hline equency Res & \\
\hline 1 V (0 V.U.) input & 14 Hz to 42 k \\
\hline 0.1 V input & 14 Hz to 16 \\
\hline
\end{tabular}

Signal to Noise Ratio (Input channels)
\[
\begin{array}{ll}
0.1 \mathrm{~V} \text { input } & -80 \mathrm{~dB} \\
0.01 \mathrm{~V} \text { input } & -70 \mathrm{~dB}
\end{array}
\]

Crosstalk
\begin{tabular}{ll}
100 Hz & -43 dB \\
1 kHz & -40 dB \\
10 kHz & -23 dB
\end{tabular}

Overload capacity
Input Amplifiers +14 dB
Output Amplifiers +14 dB
Master Fader Rejection \(\quad-80 \mathrm{~dB}\)

\section*{CROSSTALK}

Crosstalk between channels is another problem, particularly when an attempt is made to compress all the necessary circuitry into a confined space. In the Minimix 6 the crosstalk at 100 Hz is -43 dB deteriorating to -23 dB at 10 kHz . Although the latter figure leaves much to be desired it compares favourably with the crosstalk in almost any stereo cartridge and in practice, with both channels driven there appears to be no discernible intrusion or interaction between channels. If only one channel is driven with a signal containing significant high audio-frequency elements the master fader rejection of -80 dB ensures that the undriven output channel does not receive any unwanted signal.

\section*{BLOCK SCHEMATIC}

A block schematic of the Minimix 6 is shown in Fig. 1. In all there are six input channels grouped in pairs as shown by channels one and two in the diagram. Signals from the input preamplifier are routed via the channel fader to the wiper of a panpot the ends of which are linked into the left and right pan busses. These are in fact virtual earth leads into the left and right group output mixer amplifiers. The group outputs are routed via the master faders into their respective output stages.

\section*{P.F.L. FACILITY}

Cueing and monitoring facilities are often left out of home built mixers but are nevertheless frequently found to be useful, if not indispensible, in some circumstances. Aural and visual monitoring are provided in the Minimix 6 and may be switched so that the operator can monitor the outputs or any input irrespective of whether that input is actually driving into the group mixers. This latter facility is

known as Pre-Fade Listen (P.F.L.), and is extremely useful when setting up channel gains and signal levels prior to fading into the mix.

The monitoring of input signals is very much geared to the arrangement of the input channels which, as was explained above, are arranged into groups of two. For stereo operation the odd numbered channels 1, 3 and 5 are associated with the left output channel, while the even numbered channels 2,4 and 6 are associated with the right output channel.

When a stereo signal is being mixed from a prerecorded source (a tape or disc) it would normally be expected that the pan controls would be hard over to left or right since the stereo imaging would have been carried out during the original recording.

When mixing mono signals to form stereo, however, the pan controls on each channel have to be adjusted in order to give a subjective spatial position for the individual mono signals in the final stereo image. In these circumstances the signal has to be monitored twice. Firstly the input channel has to be adjusted for gain and the subjective quality of the sound assessed.


Fig. 1. Block schematic of the P.E. Minimix 6


Fig. 2. Full circuit diagram of the P.E. Minimix 6. Note that channels 3-4 and 5-6 are identical to channels 1-2 (shaded areas)

\section*{BULK COMPONENTS}

This list has been compiled to allow readers to take advantage of ordering all the components at once, and thus possibly obtain a quantity discount.

\section*{Resistors}
\begin{tabular}{ll}
\(l\) & \\
\(10 \Omega \Omega\) & 2 \\
\(100 \Omega \Omega\) & 6 \\
\(1.2 \mathrm{k} \Omega 2\) & 4 \\
\(10 \mathrm{k} \Omega 2\) & 2 \\
\(12 \mathrm{k} \Omega\) & 2 \\
\(20 \mathrm{k} \Omega\) & 6 \\
\(100 \mathrm{k} \Omega \Omega\) & 40 \\
\(3.9 \mathrm{M} \Omega\) & 6
\end{tabular}

Capacitors
680 pF Ceramic 2
2,000pF Ceramic 2
\(0.47 \mu \mathrm{~F} 35 \mathrm{~V}\) Tantalum 6
\(1 \cdot 0 \mu \mathrm{~F} 35 \mathrm{~V}\) Tantalum 2
\(2 \cdot 2 \mu \mathrm{~F} 35 \mathrm{~V}\) Tantalum \(\quad 2\)
\(10 \mu \mathrm{~F} 16 \mathrm{~V}\) Tantalum 2
\(330 \mu \mathrm{~F} 10 \mathrm{~V}\) Elect. 2
Potentiometers
\(20 \mathrm{k} \Omega\) log. Tocosa 35 mm stereo slider controls \(10 \mathrm{k} \Omega\) log. miniature rotary pots ( 16 mm dia.) \(10 \mathrm{k} \Omega \mathrm{lin}\). miniature rotary pots ( 16 mm dia .) 6
Integrated Circuits
7418 pin d.i.l. 12
MFC4000B 250 mW power amplifiers 2
Semiconductors OA80 4

\section*{Miscellaneous}

PP6 type battery press studs 2 prs
Printed circuit board \(200 \mathrm{~mm} \times 110 \mathrm{~mm}\)
Case: Instrument case type 22 (R.S.
components) or Vero case type 652523E (Vero Electronics)
Instrument case type 22
Rubber mounting feet, self-adhesive
5-way 180 DIN sockets
0.25 in. stereo; jack socket

Flush mounting V.U. meters type 3
Jean Renaud 2 pole changeover swtiches
10 -way mounting frame 2-way mounting frame
Round push button (red, green, grey) 1 each
Control knobs for slider controls9

Control knobs for rotary pots ( 4 mm spindle) 12
Sundry 6BA nuts, bolts, washers
2 mm dia \(\times 3.2 \mathrm{~mm}\) spacers for mounting 18 slider pots


This is done by closing the monitoring switch on the input channel in question and also by changing over the input/output monitor switch. In these circumstances the input signal is routed to both meter and monitoring amplifiers.

\section*{MONITORING ARRANGEMENT}

Whilst it is not too important to have two visual indications of signal level, it is nevertheless very important to the overall assessment of the sound to have it heard by both ears rather than one alone. The arrangement of the monitoring or P.F.L. switches on paired channels is such that closure of either switch alone routes the input signal to both monitoring amplifiers, while closure of both switches routes the odd channel input signals to the left monitor amplifier and the even channel input signals to the right.
The second stage of stero monitoring consists of checking the signal level and balance between output channels. This is done by switching the monitoring amplifiers so that they are coupled directly to the left and right group output mixers and adjusting the channel faders and panning level and, equally as important, that the spatial positioning of the respective signals appears to be correct.

\section*{CIRCUIT DETAILS}

The circuit diagram is shown in Fig. 2. It can be seen that, with the exception of the line inputs, each amplifier is connected in the inverting mode. This is quite important as far as the bus amplifiers. are concerned since the fact that the busses themselves are connected directly into the virtual earth points means that they are at very low impedance and therefore less likely to suffer from the induction of stray signals and/or hum.

The input channel or line amplifier has a variable gain and this would normally be set so that, with an input signal in the range 10 mV to 1 V , the output would be IV. The bus or group mixer amplifiers have a gain of -14 dB to allow for circumstances in which all channels might be routed at full level into one group amplifier and this is compensated for by the fact the output stage and output monitoring option have gains of +14 dB . Thus with only one input signal per channel the output stage is still capable of producing a 1 V signal.

\section*{BATTERY CHECK}

The meter driving section incorporates a momentary action battery check switch in order that the battery condition may be conveniently monitored. With the values given a good battery will give a reading between 0 V.U. and +1 V.U. Below 0 V.U. however the battery voltage is beginning to fall off and although the instrument will still operate satisfactorily with a reading of -IV.U. it would be imprudent to use the instrument on an important assignment with the batteries in this condition.

The headphone amplifiers are based on the MFC 4000 B and have an effective gain of -10 dB thereby providing an output signal of around 300 mV when the monitored channels are fully driven. This signal is quite sufficient to drive a pair of low impedance headphones to a listening level which is more than adequate.
Next month: Construction, testing and using the Minimix 6

\section*{PROTECTIMG IDEAS}

Although this is essentially a patent column, concerned with new British patents for interesting electronic inventions, brief reference must now be made to an alternative approach to patenting. This is prophylactic, protective or defensive disclosure, by the deliberate publication of invention details, so as to block for ever the possibility of a patent on the idea.

The defensive disclosure approach is usually adopted by firms or individuals who can no longer afford to patent every development that they make, but are, understandably, worried over the possibility of a rival firm coincidentally coming up with the same idea, patenting it, and thereby securing a wide monopoly. Especially, now that the British Government is flying in the face of advice from the patent profession, industry and inventors, by raising the official fees payable on patents to unprecedented levels, the concept of deliberate disclosure may be of interest to readers.
Once details of a development have been published, for instance in these pages as a constructional article, it will be quite impossible for anyone (the author of the article included) to apply for a British patent to cover his idea. Because this magazine will find its way onto the shelves of libraries all round the world, such publication will likewise block the possibility of future patents in most civilised countries.

Only a fraction of the ideas conceived by inventors and research teams find their way into magazines, and it would be unwise for anyone to rely on such publication to defend their idea against patenting by others. It is far safer for an inventor who has taken a positive decision not to apply for a patent on a new idea to have brief details published as quickly as possible in some other manner.

One possibility open to firms with a house magazine is that a few pages of the magazine should regularly be set aside for the purpose of disclosure. As every single publication made in the United Kingdom (from children's comics to bus timetables to local borough
council minutes) must be deposited with the British Library, and because the existence of a single copy of a document in a public library can constitute legal publication, details in a house magazine will carry heavy legal weight in the U.K. But house magazines may not find their way into foreign libraries, and thus foreign patents may not always be blocked.

To achieve reliable, widespread foreign blocking, it is necessary to ensure that published details are placed on the shelves of public libraries throughout the world. Research Disclosure, of Homewell. Havant, Hampshire, has a longstanding system of swiftly publishing (at a price, of course) details of any invention, on request of the inventor, and sending them automatically to strategic libraries in virtually every country of the world.

Inventors and small firms desperate over the continually increasing cost of patenting and the worry of what others may do, should at least consider the possibility of protective disclosure in all or some of the ways mentioned above.

\section*{FUSBLE LINK \\ BP 1395971}

A clever new type of fusible link for protecting circuitry against overheating is described by ITT Creed Ltd., in BP 1395971.

A circular wheel turns about a central pivot and has an inner wall and an outer wall which together define an annular cavity. Two pairs of contact pins protrude through the outer wall of the wheel into the cavity. One pair of contact pins at the top of the wheel engage fixed contact plates mounted outside and adjacent to the wheel. A solid slug of low melting point alloy bridges the ends of these pins where they protrude into the annular cavity.

The whole arrangement is mounted vertically and the power supply to the equipment under protection is passed through the electrical circuit formed by the series connection of these contacts and pins.

The wheel is in thermal contact with the equipment under protection, and if the latter overheats the alloy slug melts and falls under gravity to the bottom of the cavity.

Here it solidifies again to bridge the gap between the other pair of contact pins. Thus, as the power supply connection between the first pins is broken, an alarm circuit connection between the second set of pins is made, for instance to ring a bell or light a warning lamp.

Once the fault has been corrected, the wheel is turned about its central pivot through 180 degrees, so that the slug bridging the bottom set of pins is moved to the top position to bridge the power supply contacts again. The isolated contacts are simultaneously moved into the alarm circuit at the bottom. Thus the link may be re-set as new after each fusing operation.

\section*{MOVE INDICATOR \\ BP \\ 377381}

The Italian company Adriasud explains in BP 1377381 that problems arise in international bridge games, due to confusion at the moment of making a declaration. These problems are aggravated when the game is played by contestants speaking different languages.

The patent describes a circuit arrangement for enabling each contestant to positively indicate his declaration, with no possibility of thereafter altering it.

A series of indicator lights displaying the playing card numbers and symbols is shown in the patent. The lights are controlled using logic circuits, by a series of correspondingly marked switches. The circuitry is duplicated for each light and switch.

Each lamp is in series with a respective \(n p n\) transistor across a supply line. The base of each transistor is connected via a resistor to the supply line and also via a diode to a bistable circuit. Initially a capacitor keeps the bistable supply briefly at zero so that all bistable circuits assume a start condition blocking their respective transistors and extinguishing all lights.

The patent goes on to describe in detail the effect of operating the keyswitches and how the circuit ensures that the first key operated has an inhibiting effect on all other keys in the same series of card symbol lights.


\section*{GUITAR AMPIIFIER}
- Fifty watts output into 8 ohms
- Two independent input channels
- Bass and treble controls on each channel
- Output short circuit protection
- Low distortion

A 50 watt r.m.s. two-channel mono amplifier for use in pop groups/bands. Suitable for lead and bass electric guitars, electronic organs and can also be used for P.A. work


MTI B H

A low power, interference free design for the random flashing of lights either for Christmas tree or shop window displays. Flash rate is completely con-


\section*{UNIVERSAL THERMOSTAT}

A temperature controller with a host of useful applications. Simple to build and easy to calibrate

PRACTICAL
= =THTRNIES
DECEMBER ISSUE ON SALE NOVEMBER 14, 1975- PRICE 35p
 THERE have been many descriptions of enlarging exposure-meters, but the one described in this article has several unique properties.
It does not employ a milliammeter or microammeter (which can be expensive and easily damaged).

The components are cheap and readily obtainable: the cost being mainly determined by the "hardware"

It is designed from the point of view of the photographer and unlike expensive and sophisticated instruments it need not, nor in fact is, calibrated in terms of absolute illumination levels, which are seldom of interest to the practical amateur. It could best be described as an "illumination comparator"

Sufficiently sensitive to respond to weak illumination from dense negatives or shadow areas, and with a range of 5 stops or the equivalent in exposure time at any given stop. This is a ratio of at least \(32: 1\) with reference to any given illumination level.

Its usual method of use is to "calibrate" it in arbitrary terms of exposure with a given stop under "average" conditions, i.e. an average degree of enlargement with a negative of average density and contrast ratio. On any particular negative the contrast ratio can be determined because the light-sensitive cell area is so small. The operator can then determine whether it is more important for his purpose to expose for the shadows or the highlights or for any intermediate effect.

Comparison of the density of negatives can be made at a convenient "standard" stop and degree of entargement; then changes in ratio of enlargement is a simple matter of calculation. The great majority of competent amateurs are aware of these facts.


Fig. 1. Circuit diagram of the simple light comparator

The instrument is powered by small batteries twhich in the writer's instrument are housed in the case) and which, owing to the very low power requirement, have practically "shelf life".

\section*{PHOTODIODE}

Since one never obtains anything (except trouble!) for nothing, there is a price to pay for these advantages. As a very small photo diode (Mullard BPX90) is used (for "spot" readings) considerable amplification is required, which results in a limited range of light intensity readable. The range is \(1: 32\), i.e. " 5 stops". However, the intelligent photographer will know how to extend this range by operating procedure.

The circuit is shown in Fig. 1. The heart of the circuit is a 741 operating in the differential mode. In order to obtain a reasonably linear scale the potentiometer VR2 is logarithmic and used "backwards", i.e. with the high resistance end at the start of the reading. The preset potentiometers VRI and VR3 are adjusted for best range and linearity of scale. It is not possible to obtain a close approximation to linearity, but this is not very important. having regard to the way in which the instrument is intended to be used.

\section*{:TRIGGER CIRCUIT}

The transistors TR1 and TR2, with resistors R5 and R6, form a "trigger-circuit" which operates abruptly when the potential at the base of TR2 is about 150 mV with respect to 0 V , and lights the lamp LPI.

The lamp used in the prototype is a 6 V 0.04 A cycle-lamp bulb, which is also under-run, to minimise the load on the battery. The lamp is normally
on for only a few seconds at a time. Otherwise the current drain on the batteries is only of the order of a couple of milliamperes. The battery B1 is a PP6, since it has to supply the lamp current. B2 can be a PP3, since it only has to supply the photocell current and the-negative feed to the amplifier.



\section*{CONSTRUCTION}

The potentiometer VR2 is provided with a blank scale and a good pointer (a slip of "celastine" or thin Perspex, with a scribed line filled with Indian ink makes an excellent index. mounted with a suitable adhesive on the underside of the knob). The photocell is mounted on the end of a "probe". A piece of wood (such as a lolly-stick) with a protective coating of varnish was used in the prototype with the photocell covered by a layer of stretch-and-seal film. The leads from the probe can be of any convenient length, preferably of very thin flexible wire such as gramophone pickup lead. Be careful not to put tension on the photocell leads, and anchor the attachment of the external leads securely. The probe is then quite robust.

The lamp is mounted close to the scale, and most of the bulb is blacked out with black enamel, except for a small clear portion to allow illumination of the scale. The circuit is constructed on a piece of Veroboard \(2 \frac{1}{2}\) in \(\times 1 \frac{1}{2}\) in ( \(60 \times 40 \mathrm{~mm}\) ) Fig. 2, and the whole instrument, including batteries, is contained in a plastic box with outside dimensions 5 in \(\times\) \(2 \frac{3}{\mathrm{j}} \mathrm{in} \times 1 \frac{3}{\mathrm{~J}} \mathrm{in}(130 \times 70 \times 45 \mathrm{~mm})\).

\section*{CALIBRATION}

Set up the enlarger in the darkroom. Place the probe in the centre of the image space. Have the enlarger lens at maximum aperture and only the enlarger light switched on. (Even the "safelight" should be off.) Set VR1 and VR2 at about half scale. Set VR1 (the indicating scale) at "maximum". i.e. fully clockwise.

Switch the instrument on by S1. The lamp will light. Press the "Reset" button S2. The lamp should go out. If it does not, raise the enlarger lamphouse (to reduce the illumination intersity on the easel) until when the reset button is pressed, the lamp goes out. It should be possible, with careful adjustment of the height of the lamphouse to cause the lamp to come on when the potentiometer is turned slightly anti-clockwise. Make a mark with a pencil on the scale. This is a trial datum. Stop the lens down one stop.

Press the reset button. The lamp should go out. Move the potentiometer slowly further anti-clockwise until the lamp again lights. Make another pencil mark on the scale here.


Fig. 2. Component layout and Veroboard cutting details

Repeat this procedure to five stops down. You will have six marks on the scale, at whole stop intervals. If the first and last marks don't come near the ends of the scale. repeat the procedure after adjusting VRI and VR3, until the scale looks like Fig. 3. Having found the right values for these presets, they can be mounted inside the instrument and/or replaced by equivalent fixed resistors, and having finally obtained a convenient scale, make the markings permanent.

\section*{TIME SCALE}

These markings (representing stop numbers) can now for convenience have an added time scale. One can either mark the whole scale in time values, or mark the individual stop spaces with interpolating marks. or both. This is a simple mathematical exercise. It is probably most convenient to mark the individual stop spaces, having regard to the uses to which the instrument will usually be put. Each individual stop space will then be marked as in Fig.


Fig. 3. Scale markings for the unit

Fig. 4. Rear of front panel showing theinterwiring between controls and leads to the circuit board

3. Each stop space gives a time ratio of 2:1. Reading from right to left from the stop mark, we have time ratios of \(1: 1 \cdot 2,1: 1 \cdot 4,1: 1 \cdot 6,1: 1 \cdot 8,1: 2\) (next stop mark), each stop space being marked as in Fig. 3.

\section*{USE OF THE EXPOSURE METER}

Now as to method of use. You select a negative from which you have made an entirely satisfactory enlargement (by the method of "test strips" or trial and error). With the negative in the enlarger, decide whether you intend to establish your datum on the shadows (the brightest part of the image) or the highlights (the dimmest part of the image) or intermediately. Place the probe to receive the light at the selected part of the image (making a permanent note of what part of the image you have selected). Also make a permanent note of the paper used and the exposure time when making an entirely satisfactory print. This is your "standard" for that particular paper. Make a permanent note of the meter reading under these conditions. This is your "standard" meter reading.

\section*{EXAMPLE}

A standard meter reading is obtained as above (e.g. 2-2) and the standard exposure time is, say, 10 seconds. The conditions are then changed (but not the paper). For example, a larger or smaller degree of enlargement, denser or lighter negative, etc. One then chooses whether it is more important to expose for the highlights or the shadows. The probe cell is then placed at the most important point of the image and the reading taken. Say the reading obtained in this fashion is \(3 \cdot 4\).

\section*{MULTIPLICATION FACTOR}

The difference between this and the standard \(2 \cdot 2\) is 1.2 stops. The exposure multiplying factor is therefore \(2 \cdot 4\), i.e. mutiply by 2 for the one stop difference and by 1.2 of that value for the extra 0.2 of a stop. So one can either leave the stop unchanged and give 24 seconds exposure, or open the lens 1 stop and give 12 seconds. or close the lens 1 stop and give 48 seconds, according to choice.

If the reading goes the other way (due to a smaller degree of enlargement and/or a lighter negative, etc.)
say to 1.6 , there is a difference of one stop plus 0.4 in terms of time. The multiplication factor is then \(\frac{1}{2}\) (for the one stop difference) which is 5 seconds, and then this is multiplied by 0.4 for the extra 0.4 of a stop, making 7 seconds.

This may look like a clumsy way of reckoning, but it is a simple way that avoids mistakes, and is the reason why the stop interpolations are not marked over the scale (each stop space being marked as shown in Fig. 3). This last reading of 7 seconds is of course with an unchanged stop. If the lens is stopped down 1 stop the time would become 14 seconds. If another reading is now taken (taking care to place the probe in exactly the same place on the image) a reading of 2.6 would be obtained; that is to say, 10 seconds, the standard exposure, multiplied by 1.4 making 14 seconds.

\section*{STANDARD READINGS}

With a given enlarging paper, any set of conditions may be adopted as "standard". If a number of different makes and grades of paper are in use, it will be convenient to determine the standard exposure time and meter réading for each, making a permanent record. It would be a further convenience to arrange conditions so that either a convenient exposure time (say 10 seconds) or a convenient meter reading (say 2 or 3 ) is involved.

\section*{TEMPERATURE CONSIDERATIONS}

To minimise cost, no attempt has been made to design the instrument for use over a wide temperature range, but this is not usually important because darkrooms are usually occupied at normal room temperature for other reasons. If there is very strong light on the easel, the rise in temperature of the photocell may affect the reading, but this cannot occur at any light level that the instrument is capable of reading.

Other ways of using the instrument (e.g. classifying negatives with respect to density) will be obvious to the photographer. If the light level used during the exposure is below the range covered by the instrument, the datum can be established at a light level within the range and allowance made for the change in terms of stop settings. This is not so accurate as a direct reading, but is quicker than guesswork or the time taken to make test strips.


THIS mains-powered unit was originally built to replace the battery in a transistor radio and thus solve the recurring problem of deciding when the sound distortion caused by the falling battery voltage was noticeable enough to justify throwing away a far from dead battery and replacing it by a new and increasingly more expensive one.

The unit is very simple and lighter than the battery it replaces and generates virtually no heat. It is also over-load proof and will suffer no harm if a short circuit is. connected to its output indefinitely. It is therefore also a useful unit to use when doing repair or experimental work as accidental short circuits can easily occur at such times.

\section*{CURRENT LIMITING}

The circuit consists of series capacitors (2/C3 which controls the output current, in this case approximately 40 mA , followed by a bridge rectifier. smoothing capacitor, series resistor and Zener diode. Each rectifier is shunted by a capacitor to eliminate interference caused by the rapid switching on and off of each diode and a capacitor is also connected across the mains input to prevent mains wiring interference getting into the receiver (See Fig. I).

If a lower or higher voltage output is required it can be obtained by changing the Zener diode to one
with the desired voltage, but if higher, also change the electrolytic capacitor to one with a higher working voltage. The current will remain the same. For service work switched Zeners could be used. say 6V. 9 V , 18V. If a higher current is required, C2/C3 must be changed to a larger value. The current output is roughly proportional to the capacity of C2/C3, doubling its value will provide approximately twice the output current. For example, in a unit needed for an output of about 70 mA a \(1_{\mu} \mathrm{F}\) capacitor was used. The Zener should also be changed to one of a higher current rating.

\section*{SAFETY PRECAUTIONS}

It must be remembered that the output is connected to the mains via C2/C3 and the bridge rectifiers and it is therefore extremely important that safety precautions be taken. It would be unwise for instance to use the unit in a radio with exposed metal connected to the chassis. It is also safer to connect the live mains lead to C2/C3 as the current drawn from the mains is then at least timited to 40 mA or so and a non-reversible mains connector should therefore always be used.
If the unit is used for service work it would normally be used on a mains supply from an isolating transformer and such safety problems would not a rise.

\section*{COMPONENTS . . .}
```

Resistor
R1 270\Omega 1W
Capacitors
C1 4,700pF 400V polyester film
C2 }\quad0.15\mu\textrm{F
C3 0.47\muF 400V polyester film
C4-C7 4,700pF polyester film
C8 1,000\muF elect 25V
Diodes
D1-D4 BY127 (4 off)
D5 BZY88/C9V1

```

Miscellaneous
Mains lead and four self-tapping screws


Fig. 1. Circuit of 9 V.s.u.

The circuit components are easily accommodated in the metal case of the battery it replaces. Remove the inside of the battery and retain the metal case and end pieces.

All the components of the unit can be mounted on a piece of insulated board cut and drilled as shown in Fig. 2. The wire ends of the components are pushed through small holes drilled in the board and after soldering is completed all components are selfsecured. The two end pieces saved from the original battery are mounted on to the ends of the board by screws tapped into it. The Zener diode is soldered directly on to the two output connections, a small slot having been cut in the end of the insulating board to accommodate it. Make sure all components are well clear of the metal case.

\section*{COMPONENTS}

As in all projects, if satisfactory and trouble free operation is required, good grade components operating within their limits must be used.

The writer has used Mullard polyester film capacitors in all positions, except the electrolytic, and they have proved to be satisfactory. C2/C3 is made up from one \(0.47 \mu \mathrm{~F}\) and one \(0.15 \mu \mathrm{~F}\) capacitor in parallel. They are subjected to approximately the peak of the mains voltage, about 350 volts, so 400 volt working voltage types were used. The same working voltage was also chosen for the other film capacitors. The voltage across the \(1.000 \mu \mathrm{~F}\) electrolytic is about 20 and a 25 volts working type was therefore chosen. It is probable that a smaller calpacity would be satisfactory for most purposes so long as the working voltage is high enough.


If the unit is switched on and no current is taken from it then the full 40 mA passes through the Zener diode. The Mullard type BZY88 is rated at 400 mW up to an ambient temperature of \(50^{\circ} \mathrm{C}\). In this unit the 9 volts Zener passing 40 mA will dissipate 360 mW under no load; the worse conditions. As soon as a load is applied less current will flow through the Zener and its dissipation will reduce. It will therefore always be within, normally well within, its rating.


Fig. 2 Component assembly and board cutting details


The silicon diodes used are much better than necessary but are of proven reliability and easily available.
The current through the 270 ohm resistor remains at approximately 40 mA regardless of the output load current as it merely divides between the Zener and the load. If the ouptut is short-circuited all the 40 mA flows through the short circuit. If it is open-circuited all the current flows through the Zener. The power dissipated by the resistor is therefore just over 0.4 watts.

When the mains unit is completed and inserted into the metal case the metal can be swaged back over the bottom end pieces by lightly tapping with a small hammer. The unswaging and re-swaging of the metal case was found to be very much easier than expected and needs little skill.

\section*{USE AS A CHARGER}

The circuit can also be used in other applications where its current-limiting feature is important, such as a charger for re-chargeable cells. A conventional charger supplies a high current into a discharged cell. This may be too high until the cell becomes charged. and before that overheating and damage could result. With this circuit the current is constant. Components for the charger can be chosen as outlined for the above unit.
The capacitor size to establish the current and the Zener diode should be a few volts higher than the fully charged voltage of the cell or cells to be charged. The purpose of the Zener in this case is to limit the output voltage if no load was applied to the charger while it was switched on. The electrolytic capacitor need not be included.
A number of the 9 volts units have been made up and used during the last two years and have per'formed satisfactorily. Hum is negligible, indicating that the \(1,000 \mu \mathrm{~F}\) electrolytic is probably larger than necessary.

\section*{MICROPROCESSORS}

\section*{continued from page 903}
memory (CROM). The design is such that one to four CROM's may be used to control a single RALU and yet one CROM may control up to eight RALU's. Therefore, there are many possible combinations.

\section*{RALU's}

Within the RALU, which is a 4 -bit wide slice of a processor, are seven general-purpose registers, a status flag register, a 16 -word stack, an ALU, an I/O multiplexer, and three data buses. RALU's may be interconnected to form larger systems. by means of control lines. For example, when four are put together to form a 16 -bit computer, they all receive an instruction in parallel and all carry out the requisite fetches and data manipulations in parallel at the full system word size with a carry bit being transferred from the most significant bit of one RALU) to the least significant bit of the next one where required.
The logic elements within the RALU are interconnected by the data buses, so that data may flow from any element into any other element. The RALU is completely general in design; the flow of data through the chip and, thus, the instruction set of the processor, is not fixed by the RALU at all. but is defined from outside by the second chip, the CROM.
The CROM is a form of ROM. coupled with some masking and sequencing logic. It contains the microprogramme control commands that direct the RALU's and define the instruction set of the system. National Semiconductors have available standard CROM's which contain the basic instruction set.

\section*{THE HOME CONSTRUCTOR AND MICROPROCESSORS}

While difficult, these problems are not insurmountable. Likely applications home experimenters will have for microprocessors are electronic games, model railway control and television games.

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\section*{WORLD-BEATER?}

The Ferranti affair is now almost forgotten since, short of cash flow, it had some public money pumped in. In the face of bigger and more newsworthy commercial and indus. trial disasters such as British Leyland, Ferranti mercifully dropped into the background and keeping a comparatively low profile went quietly about its business.

Now Ferranti has popped up again in the news to let the world know that although the company might not have been very clever at holding down losses in the heavy power transformer division, it has some very bright people in the electronic component division and particularly in that area devoted to integrated circuits.

Their latest venture in I.s.i. has to be a winner. It uses the Collector Diffusion Isolation (CDI) technique which gives bipolar speeds with all the benefits of low-cost processing and small power consumption of unipolar devices. The particular device I have in mind is a single chip incorporating some 1,500 transistors which forms the central component of a whole range of modern digital instruments.

Initially, there are nine instruments of varying complexity in the range, each using the same chip which may be likened to a tiny central processor around which peripheral circuits may be added as required to provide various functions in the instrument. The chip includes circuits for input and output gating, the clock, decade counters, time base, plus all the associated logic circuits, and if all these circuits were realised with discrete components you would need something like a thousand.

The single-chip instrument is therefore less costly to assemble
and should be far more reliable in service. Of course, such a chip costs a small fortune to develop and this has to be amortised over a long production run and so the instruments are not at greatly reduced prices though very competitive.

Co-sponsors of this project are Racal Instruments Ltd. who will, of course, have exclusive use of it. First showing of the instrument range was at Racalex '75 which opened in London on September 23. Racal Instruments is naturally delighted with the whole project and expect considerable export business as well as brisk home demand. Ferranti, too, has all the satisfaction of seeing another success for the CDI technique and a quickening of interest in its potentialities.

\section*{OPTICAL COMMUNICATIONS}

Ten years ago research started on communications by light through optical fibres as a possible alternative to electrons through metallic conductors. When it all started nobody knew whether a fibre could be made with low enough losses, whether a fibre could be made economically in commercial quantities, whether if a fibre could be made it would be capable of being handled and jointed, and nobdy knew if l.e.d.s and lasers of suitable type would become available as the transmitting source or whether suitable photodiodes would be produced.

Now, at the IEE headquarters in London we have had the first fullscale international conference on the subject and although there are still problems ahead they are comparatively minor. A whole new industry seems to be in the buildup stage. Some modest systems are already in use, for example in warships and other military applications, but despite the considerable volume of experience that has been accumulated, especially in the last five years when research has accelerated sharply, it is unlikely that really big business in optical communications by fibre will start for another seven, possibly ten years. But when it comes it could be the biggest thing for the electronics industry since the electronic computer.

\section*{TANKER RISKS}

Nobody likes to see disaster at sea but recent incidents involving supertankers do highlight the need for human navigating skills to be supplemented by the very bet electronic aids. Decca Radar has just announced that their Channel Approach Aid is now operational at the big oil terminal at Milford

Haven after some two years of costly R and D and sea trials.

The Channel Approach Aid is portable and is taken on board by the ship's pilot. Working in conjunction with transponder beacons at shore sites, it provides the pilot with a digital read-out of ship's speed, a measurement of deviation from the channel centre line and distance-to-go to pre-determined points of turn.

Both the philosophy of the Channel Approach Aid and its engineering realisation are first class. It was developed by Decca in association with the Admiralty Surface Weapons Establishment and the Radar Research Establishment and so has an excellent pedigree. But I fear that despite all its abvious virtues it may not prove commercially successful, even after the fine example set by Milford Haven in installing the first system.

\section*{CMOS RELIABILITY}

To kill rumours that CMOS plastic i.c.s are unreliable at supply voltages of 15 V and above, Motorola has produced some remarkable figures on randomly selected devices tested at the higher end of the voltage range and at elevated temperatures. Military types operated at 18 V and at 125 deg \(C\) survived with only four device failures in \(1,200,000\) device hours. Industrial quality circuits operated at 15 V and at 85 deg \(C\) had eight failures in \(5,819,500\) device hours. The devices, say Motorola, were ordinary production items taken straight from the line.

\section*{CUTBACKS}

Top spender in the industry is the Post Office as regular readers will already know. When the PO hiccups it reverberates all the way down the line and the PO with its unhappy financial situation is almost bound to trim its huge reequipment programme regardless of urgency. GEC and Plessey are reported to have developed a nervous twitch at the prospect, while STC with the big TXE4 electronic exchange programme is reported as being fairly relaxed on the assumption that this prestige project will remain unharmed.

Of the trio, Plessey has a strong export business in cross-bar equipment and hopes to alleviate any home cut-backs by increasing overseas sales. But whichever way the cookie crumbles the Post Office must, in the end, have the equipment it so badly needs, the only trouble being that it will cost more later. Which could, of course, mean dearer telephone charges which could mean less subscribers with shorter and fewer calls which could put up unit costs which

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\section*{* ELECTRONIC PIANO KIT * SYNTHESISER KIT \\ * ELECTRONIC C.ZGAN KITS}


WE are all tired of the oft quoted words--depressed economy. setback, downward climb, gloomy times-most of these could be applied to the music industry when the imposition of VIAT was applied. When I visited the British Musical Instrument Trade Fair (August 17-21) at Connaught Rooms, Bloomsbury Centre and Hotel Russell. I fully expected a reprise of last year's exhibits from a stagnant industry. No way-there was a lot of product range expansion notably at the low end of the organ market. Here, of course, electronics gimmickry, wizardry if you like. prevails.

It's an oft quoted fact that more than 90 per cent of the population do not own an instrument and for that matter cannot read music. With the range of "all singing all dancing" fun machines available and "at a glance" back up, music teaching software, the organ distributors have designs on reducing this statistic.

\section*{ANYONE CAN PLAY}

The low end of the market consists mainly of instruments anyone can play. In the majority of cases this is true even if you are unfortunate enough to have one foot and two fingers. Usually what they have in etceteras they lack in voicing and balance.

An addition to the Philips range, the "Automagic", has none of these problems. This has a professional specification, and most appealing to the beginner, illuminated touch controls that enable rapid and easy switching of preset registration and automatic bass chords coupled to rhythm.


The Thomas Majestic Royale 1157. Features of this include "Preset" and "Memo Chord" tabs, with these an ensemble of voices in chosen chords can provide an automatic accompaniment in time with the rhythmn

\section*{}

\section*{ORGAN INDUSTRY PIONEER}

To the cognoscenti the name Hammond is synonymous with organ excellence both in product and sound produced. Over 40 years they have moved from tone wheel generation to the multiple derivative divider system still maintaining, arguably, that distinctive Hammond sound.

The tone wheel, in fact, pioneered the organ industry as we know it today.

In 1972 the incredible Concorde made its appearance combining 1.s.i./m.d.d. technology with harmonic tone bar registration. The spin-off from this was evident in the Cougar and X2/X5 models. These offer tonebar performance, the latter being portable with a professional specification.

\section*{RHYTHM GENERATORS}

Another development with roots in Concorde is the Autovari 64 which introduces almost lifelike realism to rhythm patterns. Completely different from the crashingly boring ryhthm units attached to most organs.

That there is a demand for this type of unit is evidenced by the singular selling success of the Powerhouse drum rhythm unit when introduced this year at the Frankfurt International Spring Fair.

This consists of eight pre-recorded double-track tapes from which it is possible to obtain up to 64 different drum rhythms. Again, the emphasis is on authentic sound with each rhythm having a 32 bar sequence Distributors for this unit are Benelux Musical Instruments Ltd.

Before leaving this subject one would have to mention the Farfisa "Super-partner". A rhythm box that allows the organist to augment rhythmic sequences. An extra facility makes this box rather remarkable. It adds a variety of instrumental accompaniments such as trumpet, guitar, trombone, etc. to the lower manual in conjunction with the selected rhythm. Different sounds either singly or in combination are predetermined by whichever rhythm is selected.

The unit is available with the new Farfisa Beresford and Buckingham, the latter being a clubland theatre organ.

\section*{SYNTHESISER ULTIMATE}

Another extremely stylish clubland instrument, elegant in plastic and chrome, is the new EX42 from Yamaha. At \(£ 9.375\) worthy of a mortgage but this paled into insignificance compared to its similarly clad companion. the \(£ 30,000 \mathrm{GXI}\) fully polyphonic synthesiser.

If you watched "Tomorrow's World" on the lith of September you would have seen the American bandleader/arranger, George Fleury, give a commendable account of himself and the instrument. Orchestral instrument synthesis was convincing, particularly the big band brass tuttis. Unfortunately any one player can only skim the surface potential of this multi-million pound research development.

\section*{OTHER SYNTHESISERS}

1 was told that a derivative of the technology is the new SY-2 synthesiser which, like the ARP Pro-

Soloist, has a whole range of preset instrumental voices and effects. All of these can be modified with a variety of filters and shapers. Obviously, this type of synthesiser makes an excellent addition to an organ. Unless you are lucky enough to have a Wurlitzer with integrated Orbit.

Weighing in at 151 lb and checking out at around \(£ 460\) is the ARP Axxe. My introduction to this was the recreation of the unmistakable fired phasor and torpedo sounds from starship Enterprise by a dextrous Boosey and Hawkes demonstrator.

This is probably one of the simplest synthesisers to get to know; the fascia labelling and colour coded slide controls make this possible. An interface addition to the Axxe is "Little Brother" which can be slaved to provide extra effects. I was told that Little Brothers could be stacked endlessly, which promotes some speculation.

\section*{HOHNER KEYBOARDS}

New Hohner keyboards shown by Hornby-Skewes included the HiPiano String. I find these string ensemble units hard to resist as the impression is of playing in a cathedral. A melody picked out with piano or harpsichord voicing seems to have a full reverberant orchestral backing.

\section*{P.A. GEAR}

In P.A. equipment it seems that the old type column is out and the


system column and bin box in. This usually consists of h.f. horns and mixed diameter speakers and h.f. and woofer horns for the bin.
The diminutive Min Bin from Carlsboro is representative of the genre measuring a mere \(35 \times 20 \times\) 20 in but capable of pushing out 100W.
This company also had on display the comparatively low priced 35W Scorpion combination amplifier. This is described as the most exciting innovation in the small amplifier market for years.

Marshall provided continuous demonstrations of their Lead 100 , 100W transistor amplifier with guitarists Bert Kirby, Jim Wilmer and Steve Thomas.

\section*{EFFECTS UNITS}

Distortion is big business. Per-
haps this is a harsh term for electronic effects; some purists might prefer colourisation. From reader reaction to this magazine we can tell where the interest lies and have provided many designs that rival commercial specifications.
One of the biggest producers of effects units is Solasound. New additions to their range are Chuck-a-Wah, an automatic wah-wah that is responsive to speed and dynamics in performance and Phase Pedal 4 for that way out rushing sound. An extension to this unit's capability is Supa Phaze which gives a passable Leslie imitation.
Increasingly popular is the foot controlled v.c.f. providing as jit does an almost limitless range of tone colours in combination with voice or instrument. Two examples shown were the Univex Synthi Pedal and the Korg Traveler.


The Hohner Hi String, an electronic keyboard that synthesises the many sounds of a string orchestra


The Kimball Swinger 200. One of the new breed of two finger fun machines. One finger on the upper manual picks out the melody-one finger on the lower gives you rhythmic chords

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600 WATTS PER CHANNEL Connects to your loudspeaker or loudspeaker socket. The unit can be connected to your existing spotlight fittings or to our type A or B fittings.

\section*{4
4
5 \\ including channel output plugs} and mains input socket.
ALL PRICES INCLUOE V.A.T. and POST \& PACKING ALL PRICES aply to the United Kingdom only)

\section*{Twin Bank 6 LIGHT}

(less lamps)
B.C. FITTING £ 9.55 (each)

Length \(141 / 2\) inches
E.S. FITTING
£10.35 (each)
Type A
(less lamp)
B.C. FITTING £1.95 (each)
E.S. FITTING
£2.12 (each)
100 WATT SPOT LAMPS Minimum
 blue, clear. \(\mathrm{O}^{-18}\) each £3.54 TRATALSA, STANDISH STREET, Only 1 B.C. or E.S. Fitting

Type B B BaNK UNT


FITTING £6. 90 (each) E.S. FITTING £7.26 (each)

Length \(31 \frac{3 / 4}{4}\) inches \(\mathrm{JNIT}^{2}\)

(each) B.C. FITTING £15.60 (each) E.S. FITTING £17.00 and 20 p for illustrated leaflet \& price list. BURNLEY, LANCS.
\begin{tabular}{|c|c|c|c|c|c|}
\hline  & &  & \begin{tabular}{l}
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WINDSO \\
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\end{tabular} & \begin{tabular}{l}
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\end{tabular} & \begin{tabular}{l}
 What STOCKA. LOU RAYCES, \\
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 \\
 BRRCTAFCARD AGCRSS \(A\) MOST. \\

\end{tabular} \\
\hline \multicolumn{3}{|l|}{Dinitita Displays} & \multicolumn{2}{|l|}{TRANSISTORS 8 DIODES} & \multirow[t]{2}{*}{\[
\begin{aligned}
& \text { VERO PINSX36 } 28 \mathrm{p} \text {. NWMW RRIASS! } \\
& \text { COPPER CLAD VEROBOARD OI" }
\end{aligned}
\]} \\
\hline IGIT O-9DP & 709
555
DILIL
TIM &  & Price each & \multirow[t]{2}{*}{\(\begin{array}{lr}\text { MATCBING } & 16 \mathrm{p} \\ \text { INS: BUSH } \\ \text { SET10p }\end{array}\)} & \\
\hline GREEN\&YELLOW & \(703 \mathrm{RF} / \mathrm{IF} \quad 28 \mathrm{p}\) & LM381 \(2 \times \mathrm{Pre} \mathrm{E} 2\) & AC187 \& 188 19p & &  \\
\hline JUMBO LED \(0.6^{\prime \prime} 7\)
DISPLAY \&2.25 ea. &  & LM3900 \(4 \times\) OPA69p &  & TIP 41 70p & \multirow[t]{3}{*}{\begin{tabular}{l}
DIL IC's BOARDS \(6 \times 4 \frac{1}{2}\) § \(\$ 1.50\) \\
24 way edge connector 60 p . \\

\end{tabular}} \\
\hline 3015F 0-9D & 710 DIL 14 34p & MC1306 49p &  & TIP 2955 90p & \\
\hline  &  & MC1 3102LEDE 2.65
\(M C 1312\) SQ E 2.10 & BC109 10p & TIP 3055 55p & \\
\hline & 741 DIL 14 29p & MC1330 69p & \(\begin{array}{ll}\text { BC147/8/9 } & 10 \mathrm{p} \\ \mathrm{BC} 57 / 8 / 9 & 12 \mathrm{p}\end{array}\) & \[
z T \times 109 \& 301 \quad 13 p
\] & \multirow[t]{2}{*}{} \\
\hline & 741 T099 \({ }^{\text {29p }}\) & MC1339 \(2 \times\) Pre \({ }^{1} 1\) & BC167/8/9. 12 p & 1N4001 & \\
\hline (1) S (0) &  & MC1350 55p & BC177/8/9 18p & 1 N 4004 \& ? 7p & \multirow[t]{3}{*}{PRINTED CIRCUIT BOARD KIT \&1.69 DECON NO MESS ETCH PAK NET 69p decon desolder braid reel 59p} \\
\hline & 7805
780
5V
¢1.40 & NE540 Driver ¢1 & BC182/3/4A\&L10p & \multirow[t]{2}{*}{\(1 N 4148\)
2N697} & \\
\hline LEDS 209 STYLE ONLY 13p ea & 7812 \& 15 £1.40 & NE550 2vRef & & & \\
\hline TIL 209 WITH CLIP RED 15p ea & 76013 6W AF \(£ 1\) & NE555 Timer 55 p & BD131 \& 13239 p & \begin{tabular}{l}
2N70688 \\
2N2646 UJT
\end{tabular} & \\
\hline TIL 211 a CLIP GREEN 29p ea & 8038 SIG GEN \&3 & NE556 \(2 \times\) " 11.20 & BFR51 & 2 N 2904 \& \(5{ }^{20}\) & \multirow[t]{2}{*}{} \\
\hline Large 0.2" \& Clip red 17p ea & CA3028 ¢1 & NE560 PLL ¢3.15 & BFR50/51 23p & \multirow[t]{2}{*}{2N2926royg \({ }_{\text {2N3053 }} \begin{array}{r}\text { 9p } \\ 17 \mathrm{p}\end{array}\)} & \\
\hline  & CA3046 55p &  &  & & TV3/TO3 16p. EXTRUDED 4" 4 Y1 29p \\
\hline INFRA RED LED \(21.2 N 5777\) 33 & СА3052 £1.50 & NE565 PLL ¢2.69 & BFR88 250 V 29p & \(2 \mathrm{~N} 3563{ }^{\text {¢ }} 64\) & \multirow[t]{3}{*}{TGS308 GAS DETECTOR \(£ 1.80\) ea. logic probe tTl tester pen \(\mathfrak{c} 5\) CAPACITORS} \\
\hline & 054 & SN72709 709 28p & BFY50/1/2 15p & \multirow[t]{2}{*}{2N3614 \({ }_{2}\) 2 3702 \& 3} & \\
\hline  & LM300 2-20V ¢2 & SN72741 741 26p & BSX19/20/21 16p & & \\
\hline 2 рното amp/SCMIT & \(\begin{array}{ll}\text { LM301 } \\ \text { LM304 } & \text { OPA } \\ 0-40 V \\ 45\end{array}\) &  & MJE2955 \({ }_{\text {MJE3055 }}\) & 2N3704:5 10p & \multirow[t]{4}{*}{CERAMIC 22pf to \(0,1 \mathrm{uf} 50 \mathrm{v} 5 \mathrm{p}\), ELECTROLYTIC: \(10 / 50 / 100\) uf in 10 v 5 p .25 v 6p. 50 v 8p. \(2 \mathrm{uf} / 10 \mathrm{v} 5 \mathrm{p}\).
\(1000 \mathrm{uf} / 25 \mathrm{v} 18 \mathrm{p} .200 / 50025 \mathrm{v} 9 \mathrm{p}\),} \\
\hline \multirow[t]{2}{*}{DRIVER or LED TTL INTERFACE 81p} & \multicolumn{2}{|l|}{LM307 OPA 49p SN76611 IF¢1.25} & \multirow[t]{2}{*}{MPU1 31 PUT
OA9
OA81} & 2N3706 \& 7 & \\
\hline & LM308 H1Bo 95p & TAD110 \& 1 F ¢ & & 2N3708 \& 9 & \\
\hline & LM309K 5V \(£ 1.48\) & TBA810 7WAF 99p & OA81* OA91 6p &  & \\
\hline & LM372 IF \(\{1.80\) & 2N414 RX ¢1.09 & \(\begin{array}{lllll}\text { TIP } & 29 \\ \text { TIP } & 31\end{array}\) & 2N3823E FET 17p & \multirow[t]{3}{*}{POTENTIOMETERS (POTS) AB or EGIN DUAL 45p. SLIDERS 29p. STEREO 57p KNOBS 7p, Presets 6pResistors} \\
\hline & \multicolumn{2}{|l|}{SPECIAL OFFERS} & \multicolumn{2}{|l|}{\multirow[t]{2}{*}{TIP 31 \& 32 69p 2N3904/5/6 15p full selection in our free lists,}} & \\
\hline FLUORESCENT LIGHTS 12 V MADE IN UK 8 WATT 13" £3. \(1.3 \mathrm{~W} 22^{\prime \prime} £ 3.50\) & \multicolumn{2}{|l|}{2N3055 FULL HIGH SPEC 115W 37p 741C 8PIN DIL 27p:MFC4000B 33p} & & & \\
\hline 8! & \multicolumn{2}{|l|}{NE555 TINER 55p.ZN414 RX £1,09 BC109 9p. 2N3819e 16p. BFY51 15p} & \multicolumn{2}{|l|}{NEW TRAMPUS FULL SPEC PAKS} & \begin{tabular}{l}
KNOBS 7p. PRESETS 6PRESISTORS 1 1p \\
SWITCHES: SPST 18p. DPDT 25p.
\end{tabular} \\
\hline  & & \multirow[t]{2}{*}{阿T} & \multicolumn{2}{|l|}{\multirow[t]{3}{*}{}} & \multirow[t]{2}{*}{Din plugs all 12 p, Sockets 10 p, ALI CASES AB5/AB7 50p. AB13 65p. TRANSFORMERS 1A \(6 v 6 v\) or \(12 v 12 \mathrm{v}\)} \\
\hline & 18 & & & & \\
\hline & & 7473/74\%76 29p & & & \multirow[b]{2}{*}{DK Sachets} \\
\hline mathanics & 7400 GATES \(13 p\)
7404 INVERT 17 p & & \multicolumn{2}{|l|}{BZY88 400mW 1a/50V SCR 360} & \\
\hline & 7401/2/10etc 14p & 7491/2/3/4 59p & \multirow[t]{2}{*}{CENER DIODES 9p} & TAG1/400 55p & \multirow[t]{2}{*}{\begin{tabular}{l}
TEXAS GOLD \\
Low PROFILE
\end{tabular}} \\
\hline 8 8tk Cartridge mechanism & 7413 SCMITT 31p & 7410074175 \&1 & & \(C^{106}\) \& 7 SCR D1 & \\
\hline STEREO CASSETTE, MECHANISM \(£ 13.75\) & 7440 BUFFER 14 p & \(\begin{array}{ll}74121 & 32 \mathrm{p} \\ 74123 & 59 \mathrm{p}\end{array}\) & 1 A 50 V . 20 & 4A/400V
SC146D
53p & 8,14,\& 16 PIN 13p \\
\hline Suitable for 'PW ASCOT' recor
with heads etc & 7447
7470 &  & BR 100 DIAC 250 &  & \begin{tabular}{l}
SOLDERCON STRIPS: \\
100 PINS 50p.1K £3.
\end{tabular} \\
\hline
\end{tabular}


\section*{16 AND 14 PIN LOGIG CHECKER}
\(N^{0}\) doubt P.E. readers will have noticed the "Logic Testers" that have come onto the market recently. Whilst being a worthwhile investment for laboratories etc., the price renders them unsuitable for amateurs with limited resources to fall back on.
The display being critical as regards both cost and acceptability, an oscilloscope display was adopted. This necessitates a \(Z\)-modulation facility in the scope being used. This is easily incorporated into 'scopes without this facility by simply coupling a one-valve grounded cathode stage to the c.r.t. cathode by a suitably high voltage capacitor of about 0.1 uF .

IC4 and IC5 together with, one NAND gate from 1 C 3 form a sixteen-to-one line multiplexer, IC4 and 5 being alternately enabled by output \(D\) and the inverted output \(\bar{D}\) of 1C2. The input pins of the multiplexer are wired to a sixteen-pin d.i.I. i.c. test clip. When this is clipped onto an i.c., the multiplexer output will go low if the pin being addressed by the multiplexer is low or grounded.

The resistors R2 to R4 together with IC2 from a staircase generator, which, when applied to the "X" amplifier of the oscilloscope forms a row of dots. Connecting the D output to the Y amplifier produces a double row of dots.

If the output of the multiplexer is now connected to the Z -modulation input, and if a given pin on the i.c. being checked is at logic 0 , then the dot being drawn in the corresponding position on the oscilloscope face will be extinguished, thus indicating the state of the pin.
As can be seen from the circuit diagram, one of the NAND gates is being used to decode 7 (0111) and

15 (1111). These points in the cycle correspond to pins 9 and 8 respectively. If Sl is closed then on 7 and 15 the \(Z\) output will be forced low through DI, thus extinguishing pins 8 and 9, and modifying the output format to 14 pin.

The display was found easier to read using badly-focussed dots, and has proved invaluable in diagnosing faults in digital circuits.
G. Butler, Hertford

TRIFFID POWER SUPPLY


\section*{Fig. 1}

AVERY simple circuit which can be used to provide the 1.3 V necessary to power the ZN414 i.c. radio (as shown in P.E. Feb. 73) is shown in Fig. 1. Resistor RI provides current to forward-bias the silicon diodes D1 and D2 to provide about 1.3 V , and capacitor Cl decouples any noise to earth. The diodes can be any general purpose silicon types such as iN4002, 1N4148, etc.
S. Newington-Bridges.

Ampney Crucis.

\section*{SIMPLE ELECTROLYTIC COMPARATOR}

Asimple circuit which can be used to roughly determine the capacitance of an electrolytic capacitor is shown in Fig. 1.

A 555 timer in its monostable mode is used to provide a pulse, the duration of which being proportional to the unknown capacitor C4 and resistor R3. One simply times it with a capacitor of known value. the length of the pulse and compares Switch Sl commences the timing action.

A relay can be used instead of an l.e.d. which will make timing easier as one listens for the "click" of the relay dropping off whilst looking at a watch.
D. Lal, Amsterdam

\section*{MUSIC GENERATOR}

THE circuit of Fig. 1 can be used to play either repeating tunes of 32 notes or random tunes. Although the notes in the sequence cannot be individually determined, they are determined by the settings of only five controls. This simplifies both the circuitry and the operation.

The output is produced by a unijunction transistor oscillator, whose emitter is connected through five potentiometers to the outputs and input of a 4 -bit binary counter IC2, IC3. If pulses are applied to the input, 32 different states are obtained in


Fig. 1
sequence, and therefore 32 different notes. Diodes are connected in series with the potentiometers so that each potentiometer is isolated when the corresponding output is low.

The pulses for the counter can be produced either manually with a push button or automatically by a lowfrequency astable consisting of TR1 and TR2, whose tempo can be controlled by two independent potentiometers. The sequence can either be fixed in a 32 -note cycle or random. An and gate (IC1) is used to select these four modes.

With the switch on "fixed", one input to the gate is permanently high, and the other is connected to the
astable or push button. The output will be high when the input is high and low when it is low. With the switch on "random", one input is connected to an r.f. oscillator consisting of TR3 and TR4. When the other input, connected to the astable or push button, is high, the output is r.f. oscillation, and the counter counts at this frequency. When the voltage at this input drops, the counter will remain at the state at which it was before the voltage fell. Since the r.f. is much greater than the automatic pulsing frequency, this state cannot be predicted, and is therefore random.
J. Samson,

Bishop's Stortford


Fig. 1

SUPERSOUND 13 HI-FI MONO AMPLIFIER A superb solid state audio amplier. Brand new componente throughout. Sificon transistors transjatorsin push-pull, Full wave rectification. Output approz. \begin{tabular}{l}
13 watts r.m.s. Into \\
\multirow{1}{c}{ ohms. Frequency }
\end{tabular} response 12 Hz . 30 KHz tadb. Fully integrated
separate Volume, Bass boont and Input for ceramic. or crystal cartridge. Sensitivity approx. 40mV for full output. Supplied ready built and tested, with knobs, eacutcheon panel, input and output pluge. Overall size \(3^{*}\) high \(\times 6^{*}\) wide \(\times\)
AC 200/250V. PRICE \(£ 15.00\). P. \& P. 65 .
DE LUXE STEREO AMPLIFIER
 A.C. mains
\(200-240\)
\(\forall\) U a it
heary
duty heally izolated maning er with ful wave recti-
llcastion giving ade-
alve line-up:-2 \(\times\) ECLA6 Triode Pentodegigible hum. as rectifier. Two dual potentiometers are provided for bass and treble control, glving bass and treble boost and cut. A dual volune controlis used. Balance of the leftand right hand channels can be adjuated by means of a sepa. rate 'Balance' control fitted at the rear of the chassis Input sensitirlty is approximately \(300 \mathrm{~m} / \mathrm{v}\) for full peak out put of 4 watts per channel ( 8 watts mono), into 30 hm seakers. Full nexative reedback in a carefully calculated circuit, allows high volume le vels to be used with negligible
distortion. Supplied complete with knobs, chesig siz \(11^{\circ} \mathrm{w} \times 4^{*} \mathrm{~d}\) Opled complete wien knobs, chassis size huilt and tested to a high standard. inie.50, P. \& P. 85 p. ALL PURPOSE POWER BUPPLY OKIT 200/240v. A.C nput. Four switched fully smoothed D.C. out Fitted insulated output terminala amp on load
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\(\times 8 \ln\) deep by 21 in high. Bupplled ready bult, fully \(\times 8\) in deep by 21 in high. Supplied ready bullt, fully Price \(£ 27 \cdot 50\). Post and Packing \(£ 1 \cdot 00\).
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Ready built. Pre-aligned and teated
Rena. \(20-560 \mathrm{mV}\) for 9.16 Y Sena. \(20-560 \mathrm{mV}\) for \(9-16 \mathrm{~V}\) neg.
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\section*{Bridge that gap}

Sir-Gas ignitors of identical design to that featured in the July issue of Practical. Electronics have been in commercial production for some considerable time now. During this time 1 have been called upon to repair quite a few. In most cases the spark gap component was not functioning.

As l was unable to obtain direct replacements for these components, I replaced them with 200 V thyris tors, which worked just as well. The thyristor was connected directly in place of the spark gap. anode to cathode of rectifier and cathode to output transformer primary. The gate was not connected and the lead was cut off. Although the P.E. article mentions that a current of "several tens of amps" may pass through the spark gap under pulse conditions. a 3A thyristor was found to suffice. This is probably due to the extremely short time for which the current flows.

I feel sure that this information may prove useful to some of your readers who wish to construct a gas ignitor from your design.
S. Champion.

King's Langley.

I have been aware of the technique of using a thyristor for some time but because of the difficulties outlined below, did not use it in our design. However, in case any readers encounter problems in using the technique, I feel that I should briefly outline the drawbacks.

Although this solution will work in particular cases, semiconductor manufacturers have informed me that there are two drawbacks to triggering thyristors by applying excess voltage between anode and cathode:

Typically the excess voltage required between devices may vary from a few volts to twice the rated voltage of the device.

The magnitude of excess voltage is temperature dependent.
Therefore, anybody contemplating using this technique may have to experiment to find a suitable device.

With regard to the current carrying capability of the device, Mr Champion is generally correct in stating that a 3A device is suitable because of the relatively short duration of the pulse.-R.D.B.

\section*{Highest reward}

Sir-Having just completed building a \(£ 200\) synthesiser using Dewtron modules and our own bits and pieces. I recommend the v.c.o.. v.c.f., envelope shaper, etc. but warn people beware of gimmicks.
P.E. must be congratulated for stimulating interest in the field of musical synthesis; we think the patching used in both projects, i.e. plugs and sockets is unsatisfactory; we used slide switches along with slider potentiometers.

I think that the first project was too bulky and expensive; the "P.E. Minisonic" was good except for the use of a touchboard, which you yourself admitted leaves something to be desired. The actual choice of modules for Minisonic was good. the ring modulator, noise generator and v.c.f. are essential along with the sawtooth v.c.o.

The "Symbiosis" piece by Malcolm Pointon (June-July 1975) was very interesting and is the kind of use synthesisers should be put to. Synthesisers should explore new sounds rather than imitate conventional ones. I hope "Symbiosis" prompts other owners/composers to attempt their own recordings along these lines.

My synthesiser is designed for "experimental" sound exploration rather than conventional music and has virtually all patching routes possible, stereo output, noise. ring modulation. low frequency oscillator, v.c. phaser along with the more conventional modules I mentioned earlier.

Synthesisers give the highest reward from an involvement in electronics. a creative, artistic product. unlike some projects I could name.
R. D. Martin.
Congresbury.
Bristol.

Mr Martin raises some interesting points in his letter but I do feel that a number of them need some slight qualification.

I believe that synthesisers fall into two classifications. In the "live" performance area the instrument requires flexibility and yet has to be easy to play. In these circumstances some form of switch patching is ideal since it enables changeovers to be accomplished swiftly and accurately. The disadvantage to this method is that it provides a considerable restriction to the overall versatility of the instrument.

In the studio the requirement to achieve speed in patch changeover is not nearly so important and a method of patching can be adopted which allows the user to maximise on the interconnection options available. In these circumstances I strongly believe that a patch cord system is the ideal. In general terms the greater contact area and relatively wide spacing minimises noise and crosstalk problems and these are distinct advantages over the neater, but relatively costly, matrix patch boards now available.

The P.E. Sound Synthesiser was an attempt to provide an introduction to both types of instrument. The modular system was essentially a studio instrument featuring cord patching while the Keyboard Unit was geared for live performance and required no patching at all other than the options of coupling it directly to external processing devices if required. In the case of the Minisonic it was the aim to provide a design, aimed particularly at the younger constructor, which would offer a maximum of flexibility with the lowest possible price. The "touch" keyboard simply provided a very economical way of getting the constructor off the ground and there is always the option of adding a conventional keyboard at a later date.

Finally, a word on the use of the synthesiser. Mr Martin feels that the instrument should be used in an imaginative sense rather than in an imitative one. To a certain extent I go along with this but I make the qualification that, because of its inherent versatility, the synthesiser is not an easy instrument to master. If it is used in an entirely imaginative way there is a danger that performances will become stilted, at best, or that the listening public will become frightened off by the unaccustomed and rather weird sounds which can be produced. I believe that imitative sound synthesis is an extremely good exercise in the use of the instrument and serves to train the user to exploit the full potential of the instrument in terms of dynamic range and register.-G.D.S.

\section*{Funt control}

Sir-Mr Carter's fuzz effiect circuit (see Ingenuity Unlimited. August) is similar to a design of my own, and readers may be interested in a few further ideas. Mr Carter's "Fuzz" control only effectively reduces the length of sustain. which in itself is a continuous \(\pm 0.6 \mathrm{~V}\) square wave.
If a variable resistor is put in series with the diodes a very different range of effects is produced. At Lero setting \(\mathrm{R}=\) O!?, the effect is the same as Mr Carter's, However, if the resistance is increased the amount of clipping is reduced as the 8.2 k ! resistor and the variable resistor split the voltage between the amp output and the diode voltage.


\footnotetext{
Modified Fuzz
If the potentiometer is around 100 k IS the effect will be variable from square wave to almost no fuzz at all. A \(\log\) potentiometer is best wired so that half way \(=10 \mathrm{kS}\).

It now becomes necessary to look at another point; the amp is able under some signal conditions. to clip itself, which in the no fuzz mode, is undesirable. The important factor is the ratio between rail volts and diode volts. This ratio can easily be improved by increasing rail volts and/or easier still. using lower voltage diodes, germanium selenium or micro alloy.

> G. C. Cleasby.
> Reigate.
}


\section*{Suggested D/A converter improvement}

\section*{Right beam}

Sir-1 must congratulate A. C. Ainslie on the 8 -channel. irace multiplier design in the August issue of P.E. The facilities provicied by most reasonably priced oscilloscopes are inadequate, when working with logic circuitry, and techniques like Trace Multiplying help to fill the gap.

May I suggest an improvement to the \(D / A\) converter of rig. 3. In practice, it is essential to be able to compare any two traces by overlaying them at the same vertical position. particularly for well separated channels. The attached circuit permits this facility, by switching VRI to the appropriate channel.

I would also like to point out the trend in commercial instruments towards presenting displays of the actual logic states of a circuit by strobing the states at an appropriate time, using units such as the Hewlett-Packard pattern analyser.
While such instruments are obviously outside the range of the amateur. the use of digital timebase generation does allow some fairly powerful circuit analysis methods to be used. if the time base is designed to be accessible. For example, the time-base counter inputs to the internal D/A converter can be replaced by inputs from up to five test points in the circuit under test. This will immediately show up any disallowed combinations of logic levels on the five inputs, since each combination corresponds to a particular X-defection.

Also, the response of another test point to the allowed combinations can be checked by connection to the Y-amp, or Z-mod. Such displays are simpler to interpret than timing diagrams in most cases.

Other advantages are the presentation of the correct display is often automatic. without any knob twiddling. The display is independent of changes in clock rate. Direct readout of time (in terms of number of clock pulse.) between events is feasible, for example on 7 -segment l.e.d.s.

The addition of a data selector i.c.. e.g. a 74151 to the time base counter allows the states of up to eight test points to be shown in an
easily readable format. The timing diagram display can be more difticult to decipher if the logic levels on the channels do not change during the sweep. particularly with the smaller sizes of display tube.
As a user of a combined trace multiplier and digital time-base unit for some time. I have found the trace multiplying section rarely preferable to the more direct methods indicated above.

\section*{J. R. Keneally, Weymouth.}

Mr Keneally's simple D/A Converter was of the type tried originally but which had to be scrapped because of poor pulse response. This would appear to be because there is no defined system impedance, whereas with the ladder network a characteristic impedance, \(R\) (the ladder being \(R, R 2, R\), etc.) can be chosen for optimum performance. This type of converter, however, does not lend itself to providing trace shifting.
Trace overlay is usually used to give more accurate comparison between times than is possible with a spaced display. However, should modern c.r.t.'s and internal graticules still leave one in doubt with a separated display, it is a simple matter to trigger from one of the signals under consideration, there then being no ambiguity as to which of the two channels is "in the lead". Modern high performance 'scopes with dual timebases and trace expansion also simplify comparison.
The instruments to which Mr Keneally refers using digital discrete step timebase are most us eful and I have used one for several months from a commercial manufacturer. With little space it is not possible to go fully into their advantages but by using memories they allow complex and accurate investigation to be performed.
For the majority of uses, however, it is considered that a simple timing diagram display holds virtually "all the answers" in an unambiguous form, and it was for this reason that the Trace Multiplier was developed; to supplement, rather than replace, existing and costly digital instrumen-tation.-A.C.A.



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