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## CONSTRUCTIONAL PROJECTS

PROGRAMMABLE WASH/WIPE CONTROLLER by D. W. Lee A car wiper delay unit with a difference ..... 112
SOIL SATURATION METER D. W. Lloyd A simple electronic answer to "green fingers" ..... 116
CAR/CARAVAN CLOCK by M. Fischer
A crystal controlled clock with some unusual extra facilities ..... 130
OPTO COUPLED REV COUNTER by D. S. Bradbury
$0-25,000$ r.p.m. meter for aeromodellers ..... 144
GENERAL FEATURES
USING CMOS DIGITAL I.C.s-2 by D. B. Johnson-Davies \& A. M. Marshall
The transmission gate; logic gates; part numbering systems; the quad analogue switch ..... 123
SEMICONDUCTOR UPDATE by D.W. Coles
A look at some recently released devices ..... 143
INGENUITY UNLIMITED
Surround Sound Matrix-White Noise Generator-Simple Touch Switch—Better Figures- Night Light-Car Theft Alarm-Audio Signalling-Cycle Lighting Control- Charger for Nickel-Cadmium Cells-'Instant" Digital Stop Watch Conversion ..... 150
NEWS AND COMMENT
EDITORIAL-Excommunication ..... 111
SPACEWATCH by Frank W. Hyde
Details from the Venus probe ..... 120
NEWS BRIEFS
Calculators for the Blind-Sky Communications ..... 126
MARKET PLACE ..... 129
POST OFFICE RESEARCH CENTRE
A look at some of the work being done at the new centre ..... 142
BOOK REVIEWS
Selected books we have received ..... 148
INDUSTRY NOTEBOOK by Nexus
What's happening inside industry ..... 149
PATENTS REVIEW
Thought provoking ideas on file at the British Patent Office ..... 159
READOUTA selection of readers' letters160

Our March issue will be published on Friday, February 13, 1976
(for details of contents see page 119)

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input voltage 33-40V a.c
Output voltage 33V d.c. nomiñal
Output current $10 \mathrm{~mA}-1.5 \mathrm{~A}$
Overloed current 1.7A approx.
Olmenalon $105 \mathrm{~mm} \times 63 \mathrm{~mm} \times 30 \mathrm{~mm}$
Transtorm er BMTBO $\mathbf{~ E 2} \cdot 60$ plus 52p postage

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TYPE QUANTITY


| 7404 | 0.12 | 0.09 | 7453 | 0.12 | 0.10 | 74145 | 0.70 | 0.65 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| 7405 | 0.10 | 0.09 | 7454 | 0.12 | 0.10 | 74150 | 1.30 | 1.25 |


| 7405 | 0.10 | 0.09 | 7460 | 0.12 | 0.10 | 74151 | 0.65 | 0.60 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| 7406 | 0.28 | 0.25 | 7470 | 0.26 | 0.24 | 74153 | 0.65 | 0.60 |


| 7406 | 0.28 | 0.25 | 7470 | 0.26 | 0.24 | 74153 | 0.65 | 0.60 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| 7407 | 0.28 | 0.25 | 7472 | 0.22 | 0.20 | 74154 | 1.30 | 1.25 |


| 7408 | 0.14 | 0.10 | 7473 | 0.26 | 0.22 | 74155 | 0.65 | 0.60 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| 7409 | 0.14 | 0.10 | 7474 | 0.27 | 0.23 | 74156 | 0.65 | 0.60 |


| 7410 | 0.08 | 0.07 | 7475 | 0.44 | 0.40 | 74156 | 0.65 | 0.60 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| 7411 | 0.20 | 0.18 | 7476 | 0.28 | 0.25 | 74167 | 0.90 | 0.80 |
| 7412 | 0.20 .95 | 0.85 |  |  |  |  |  |  |


| 7412 | 0.20 | 0.18 | 7476 | 0.28 | 0.25 | 74160 | 0.95 | 0.85 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| 7413 | 0.20 | 0.18 | 7480 | 0.40 | 0.35 | 74161 | 0.95 | 0.85 |

$\begin{array}{llllll}7413 & 0.26 & 0.25 & 7481 & 0.95 & 0.90 \\ 7416 & 0.28 & 0.25 & 7482 & 0.72 & 0.62\end{array}$

| 7422 | 0.25 | 0.23 | 7485 | 1.20 | 0.95 | 74166 | 1.20 | 1.10 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| 7423 | 0.26 | 0.24 | 7486 | 0.30 | 0.25 | 74174 | 1.00 | 0.90 |


| 7425 | 0.26 | 0.24 | 7489 | 2.70 | 2.50 | 74174 | 1.00 | 0.90 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| 7426 | 0.26 | 0.24 | 7490 | 0.38 | 0.32 | 74176 | 0.95 | 0.85 |
| 7426 | 0.26 | 0.24 | 7491 | 0.80 | 0.95 |  |  |  |


| 7426 | 0.26 | 0.24 | 7490 | 0.38 | 0.32 | 74176 | 1.00 | 0.95 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| 7427 | 0.26 | 0.24 | 7491 | 0.60 | 0.50 | 74177 | 1.00 | 0.95 |
| 7428 | 0.36 | 0.34 | 7492 | 0.43 | 0.35 | 74180 | 1.00 | 0.95 |

$\begin{array}{lllllllll}7428 & 0.36 & 0.34 & 7492 & 0.43 & 0.35 & 74180 & 1.00 & 0.95 \\ 7430 & 0.12 & 0.10 & 7493 & 0.38 & 0.35 & 74181 & 2.00 & 1.80 \\ 7432 & 0.25 & 0.22 & 7494 & 0.45 & 0.40 & 74182 & 1.00 & 0.90\end{array}$

| 7432 | 0.25 | 0.22 | 7494 | 0.45 | 0.40 | 74182 | 1.00 | 0.90 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| 7433 | 0.35 | 0.32 | 7495 | 0.58 | 0.50 | 74184 | 1.50 | 1.40 |
| 7437 | 0.25 | 0.22 | 7496 | 0.68 | 0.63 | 74190 | 1.40 | 1.30 |

$\begin{array}{lllllllll}7438 & 0.25 & 0.22 & 7496 & 0.68 & 0.63 & 74190 & 1.40 & 1.30 \\ 7440 & 0.12 & 0.10 & 74100 & 1.00 & 0.90 & 74191 & 1.40 & 1.30\end{array}$
$\begin{array}{lllllllll}7440 & 0.12 & 0.10 & 74101 & 0.30 & 0.25 & 74192 & 1.10 & 1.00 \\ 7441 & 0.64 & 0.58 & 74105 & 0.30 & 0.25 & 74193 & 1.10 & 1.00 \\ 7442 & 0.60 & 0.52 & 74107 & 0.30 & 0.25 & 74194 & 1.00 & 0.95\end{array}$

| 7443 | 0.95 | 0.90 | 74110 | 0.50 | 0.45 | 74194 | 1.00 | 0.95 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| 7444 | 0.95 | 0.90 | 74111 | 0.80 | 0.75 | 74195 | 0.75 | 0.70 |

$\begin{array}{lllllllll}7444 & 0.95 & 0.90 & 74111 & 0.80 & 0.75 & 74196 & 1.00 & 0.95 \\ 7445 & 0.75 & 0.55 & 74118 & 0.90 & 0.82 & 74197 & 1.00 & 0.95\end{array}$

| 7446 | 0.95 | 0.85 | 74119 | 1.30 | 1.20 | 74198 | 1.90 | 1.80 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |

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# FEBRUARY SPECIALS 



# R <br> $T-v$ <br> 近FOR SPEAKERS AT FANTASTIC REDUCTIONS 

## ELIZABETHAN STEREO TUNER AMPLIFIER

This compact Tuner Amplifier gives you full medium wave and V.H.F. coverage and FM stereo. With injuts for vour turntable and tape recorder it has rotary tuning, Volume, Balance. Bass and Treble controls and pusls button selection switches for Phono/tape FM Stereo, FM mono. Medium wave and A.F.C. has built-in stereo beacon and switched headphone socket.
Technical Specifications 15 transistors. 11 diodes, integrated circuit. Power output 8 watts. Size of tuner amplifier $4^{\prime \prime} \times 10^{\prime \prime} \times 15_{2}^{\prime \prime}$ approx. Finished in selected rosewood veneer with brushed aluminium front panel and matching controls.


## BSR DELKS WITH PINTIHS AT FANTASTIC REDUGTIONS <br>  <br> C123 inot mustratad). Auto. $£ 10.00$ ation crystal cartridge p\& p $£ 1.50$ <br> All Plinths tinished in malching Teak veneer

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VISCOUNT IV STERED SYSTEM
System 1a. $£ 69.00$
The new $20+20$ watt Stereo Amplifier incorporating the latest silicon transistor solid state circuitry. the RT-VC VISCOUNT IV gives you a poweriul 20 watts RMS per channel into 8 ohms. Superb teakfimished cabinet, with anodised fascia to harmonise with any decot. Polished trim and knobs.
The VISCOUNT iV has a comprehensive range of controls - volume, bass, treble, balance, mono/stereo, mode selector, and scratch filter.
Front panel socket for stereo headphones. And a host of sockets at the rear - for left and right speakers, tape recorder, auxiliany, tuner, disc and microphone.
SPECIFICATION: 20 watts RMS pei channel 40 watts peak. Suitable $8-15$ ohms speakers. Total distortion w 10 watts better than $0.2 \%$. Six switched inputs: 1 . Magnetic PU. - 3 millivolts e 47 K ohms (R.IA.A): 2. Crysta/ceramic P.U. 50 millivolts $\quad 50 \mathrm{~K}$ ohms (R.IA.A): 3, 4. 6. Tape Tuner/Aux - 140 millivolts 50 K ohms (flat frequency response); 5 . Microphone - 3 millivolts e 50 K ohms /flat frequency response).
CONTROLS: Push button ON/OFF. stereo/mono. scratch filter. 6 position rotary selector. Individual rotary controls tor treble. bass, balance and volume. Headphone socket, tape out socket. Aux. mains output. Frequency respense: 25 Hz to 25 KHz fuil rated output, Signal to noise ratio: better than -50 dB on all inputs. Tone control range: Bass $\pm 15 \mathrm{~dB}$ a 50 Hz . Treble $\pm 12 \mathrm{~dB}$ y 10 KHz Power requirements: $200-250 \mathrm{~V}$ AC mains 60 watts. Approx. size: $154^{\prime \prime} \times 3^{\prime \prime} \times 10^{\prime \prime}$. MP60 type deck with magnetic cartidge, de luxe plinth and cover.
Two Duo Type lla matched speakers - tnclosure size approx $19 \frac{1}{2}^{\prime \prime} \times 10_{\frac{3}{3}} \times 7 \frac{3^{\prime \prime}}{}{ }^{\prime \prime}$ in simulated teak. Orive unit $13^{\prime \prime} \times 8^{\prime \prime}$ with $3^{\prime \prime}$ tweeter. 15 watts handling. 30 watis peak.
Complete System with these speakers $\mathbf{f 6 9 . 0 0}+\mathbf{f 6 . 5 0} p$ \& $p$.

System 2. £85.00
Viscount IV amplifier (As System 1a) MP60 type deck (As System 1a)

Two Duo Type III matched speakers - Enclosure size approx. $27^{\circ} \times 13^{\prime \prime}$ $\times 11 \frac{1^{2}}{2}$. Finished in teak simulate. Dive units $13^{3} \times 8^{\prime}$ bass driver, and two $3^{\text {in }}$ (approx.) tweeters. 20 watts RMS, 8 abms trequency range | RMS. |  |
| :--- | :--- | :--- |
| 20 Hz to | ohms | 8.000 Hz.

20 Hz to 18.000 Hz .
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Complete System with these
speakers $£ 85.00+\mathbf{f}^{-} 7.60^{-}$\& $p$.

PRICES: SYSTEM Ia
Viscount IV R103
amplitier $£ 27.50+£ 1.90 \mathrm{p} \& \mathrm{D}$ 2 Duo Type Ha
speakers
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Total if purchased Total if purchased
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Available complete tor only $\mathbf{f 6 9 0 0}$ f6.50 $\&$ \& p.

## PRICES: SYSTEM 2

Viscount IV R103
ampltier $£ 27.50+£ 1.90 \mathrm{p} \& \rho$.
2 Ouo Type III
2 Ouo Type III
speakers $£ 46.00+£ 7.50 \mathrm{p} \&$ MP60 type deck with Mag. cartricge

Total it purchase
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EASY BUILD SPEAKER KIT
A compact bookshelf speaker system giving a high electro accoustic efficiency for the low powered amplifier.
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The cabinet measures $12^{\prime \prime} \times 9^{\prime \prime} \times 5$ " deep approx finished in simulated teak, incorporating a quality $7^{\circ} \times 4^{\prime \prime}$ elliptical speaker, power handling 4 watts, flux density 30.000 maxwells, impedance $8-15$ ohms nominal, voice coil dia $\frac{3}{4}$ " magnet size $2 \frac{73}{6}$ " approx.
f6.00

## EASY TO BUILD SPEAKER KITS

These superb simulated teak-finished speaker kits have been specially designed by RT-VC fo! the cost-constious hi-fi enthusiast who wants top quality speakers but doesn't want to spend the earth. Built to EMI's exacting specification. these new RT-VC speakef kits ( 350 type kit) incorporate $13^{\prime \prime} \times 8^{\prime \prime}$ wooter. $3 \frac{1}{4}^{\circ}$ tweeter and matching crossover.
Easily put together with just a few basic tools.
Specification (each speaker): Impedance 8 ohms. Power handling 15 watts RMS ( 30 watts peak). Response $20-20,000 \mathrm{~Hz}$. Size $20^{\prime \prime} \times 11^{\prime \prime} \times 9 \frac{1}{2}^{\prime \prime}$ approx. Comparable built units (EMI LE3) sold elsewhere for over $£ 45$ pair.
$£ 22.00$ pair complete + f5.20 $\mathrm{p} \& \mathrm{p}$
Complete with crossover Components and circuit diagram


System consists of a $13^{\prime \prime} \times 8^{\prime \prime}$ approx. woofer with a $3^{\prime \prime}$ tweeter, crossover components and circuit diagram. Frequency response: 20 Hz to 20 KHz . Power handling 15 watts RMS into 8 ohms. (Peak 30 watts.)

Complate with crossover
Components and circuit diagram

## PUSH BUTTON CAR D Eaced

 NO SOLDERING REQUIREDNOW BUILD YOUR DWN PUSH BUTTON CAR RAOIO
Easy to assemble construction kit comprising fully completed and tested printed circuit board on which no soldering is required. All connections are simple push fit type making for easy assembly. Fine tuning push button mechanism is fully built and tested to mate with printed circuit board. TECHNICAL SPECIFICATION: (1) Output 4 watts RMS output. For 12 volt operation on negative or positive earth. (2) Integrated circuit output stage, pre-built three stage IF Module

Controls volume manual tuning and five push buttons for station selection, illuminated tuning scale covering full, medium and long wave bands. Size chassis $7^{\prime \prime}$ wide 2" high and $4 \frac{3}{4}^{*}$ deep approx. $\quad \mathbf{f} 9.50+£ 1.05 p \& p$. Speaker including baffle and fixing strip $£ 2.00$ $+45 p$ p \& p. Car Aerial Recommended - fully retractable $£ 1.60+40 p p \& p$.
The Tourist I Kit For the experienced constructor If you can solder on a printed circuit board you can build this model. Same technical specification as Tourist $\Pi$. Price $\mathbf{f 8 . 2 0}+£ 1.05 \rho \& p$.

## * A (REO 

Stereo 21, easy to assemble audio system kit: No soldering required.
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tull, easy to follow assembly instructions.
Specifications - For the technically minded
Input sensitivity 600 mV . Aux. input sensitivity 120 mV . Power output 2.7 watts per channel. Output imperdance $8-15$ ohms. Stereo headphone socket with automatic speaker cutour. Provision for auxiliary inputs - radio, tape, etc. and outputs for taping discs. Overall Dimensions. Speakers approx $15 \frac{1}{2} \times \times 8^{\prime \prime} \times 4^{\prime \prime}$. Complete deck and cover in closed position apporax. $15 \frac{1}{2}{ }^{\prime \prime} \times 12^{\prime \prime} \times 6^{\prime \prime}$
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$\mathbf{£ 2 0 . 0 0}+£ 1.35 \mathrm{p}$ \& p.

## 8 TRACK HOME CARTRIDGE PLAYER for use with your stereo system. Compatible with Viscount iv system, Unisound module and the Stereo 21 Technical specification Mains input. 240 V . Output sensitivity 125 mV . <br> Yours for only <br> f16.20 $+£ 1.70 \rho \& p$. <br> <br> PORTABLE DISCO CONSOLE*

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INCORPORATES: Pre-Amp with full mixing facilities, including switched input for mic with volume control. switched input for auxiliary with volume control. bass and treble controls, volume control and blend controi for turntables. Twa B.S.R. MP60 type single play professional series decks. fitted with crystalicartridges.
technical specification:
Preamp - Output $\quad 200 \mathrm{mV}$. Auxiliary inputs 200 mV and
750 mV into 1 meg . Mic input - 6 mV 750 mv into meg . Mic input - mm Turntables cspacity - $7,10^{\circ}$ or Turntabies capacity - . . 10 or 12 records. Rumbie, wow and flutter Rumble Better than -35 dB . Wow Better than $0.2 \%$. Fluter Bette
$0.06 \%$ (Gaumont kalee meter).
$0.06 \%$ (Gaumont kalee meter).
Finish - Satin black mainplate with Finish - Satin black mainplate with
black turntable mat inlaid with brushed aluminium trim. Tonearm and controis in black and brushed aluminium.


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Inputs: Magnetic Pick-up 3 mV RIAA; Ceramic Pick-up 30 mV : Microphone 10 mV ; Tuner 100 mV : Auxillary $3-100 \mathrm{mV}$ Input/Impedance 47 kn at 1 kMz . Active Tone Controls: Trebla -12 db at 10 kHz : Bass $\pm 12 \mathrm{db}$ at 100 Hz . Distortlon: $0.5 \%$ at 1 kHz . Signal/Noies Ratlo: 68 db . Overioad Capablity: 40db on most sensitive input Supply Voltege: $\pm 16-25 \mathrm{~V}$


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PLESSEY Semiconductors

 for 16 years, so I can claim to know something about them. The first aim of Home Radio's staff was to provide a first rate catalogue of electronic components that was easy to use. Next, they made it easy for you to order. They provide a simple order form, or for a small charge-you only pay for the stamps-they will send you six order forms and six prepaid envelopes. And nowadays everyone who has a catalogue can start a credit account. Send off an order at any time and settle your account with one monthly cheque. They even have the answer phone so credit customers can ring up any hour of the day or
night, seven days a week. A further incentive for credit account customers is that after a year you get a new catalogue, free! I feel sure that by now you'll want one of these indispensable catalogues. Just fill in the coupon and send it off with your cheque or postal order. The cost is 85 p plus 45 p postage and packing, but remember they give 14 coupons with every catalogue, each one worth 5 p. So there's 70 p you can get back! It certainly is a gilt-edged investment!


## EXCOMMUNICATION

ANCIENT and modern are brought together in a news report concerning an archeological "dig" planned at Babylon. We learn that the resources of present day technology, including magnetometers and computers, will aid the team of investigators in their search for the remains of the Tower of Babel, that ancient international talking shop for affairs of culture and commerce.

The world has seen many changes since 2,000 b.C. The modern "Tower of Babel" is represented not by a single centre, but by an immense network of global telecommuni-cations-its threads, material or immaterial, spreading out in all directions, in all media. Today any "confusion" is only apparent-not real-for it is a highly organised scheme thanks to 20 th century electronics. Yet demands for communication in all tongues, including new computer-age languages, are never fully satisfied. Technology is constantly being stretched to new frontiers in order to provide additional channels and more sophisticated signalling methods to permit a greater amount of intelligence to be transmitted further distances with, of course, utmost reliability.

Almost coincidental with the recent official opening of the new Post Office Research Centre, dedicated especially to meet the great demands for increased, improved, and additional services in telecommunications, comes a thought-provoking objective view of the anti-social effects some forms of telecommunications have produced in Amercia in the century since Alexander Graham Bell took out his patent for a telephone in 1876.

In the fourth of his Reith Lectures (BBC Radio 4, December 3, 1975) Daniel J. Boorstin said:
... In the following century, every new advance of electronic technology-from the telephone to the radio to television-tended increasingly to isolate individual Americans and keep them at home."
In this broadcast the speaker also stated:
". . . advancing technology tends to have a proportionately much greater effect on large quantities than on small. The longer the distance to be covered, the greater the power of technology to reduce the required time. This means that, within the short distances that circumscribe man's everyday community, the powers of this technology are negligible.";

The above quoted comments are surely as applicable to Britain as America, though we have yet to feel the deeper effects of isolation and segregation of citizens that apparently is commonly experienced in parts of the U.S.

Telecommunications through its manifold services has proved a benefit to most people, in some way or another; it is an indispensable mainstay of modern life. But what is its likely long term effect upon personal relations; and can we hope to escape total enmeshment within this network with its battalions of automated peripherals? Ah, Mr Bell, little did you realise just what would develop from your original magneto-telephone. How strange and absurd it seems that an invention intended to overcome distance and to bring people "together" seems likely in its ultimate achievement actually to isolate persons from one another at the local community level. Is there a remedy, and in whose hands is it likely to be found?
F.E.B.

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Fig. 1. Circuit diagram of the controller

S1 causes the washer motor to operate and the relay to close which starts the windscreen wiper in motion. C 1 is discharged through both the VR1, R2 and D2, R3 combinations. VR1 thus determines the time the wipers continue to operate after releasing S1. DI prevents Cl from discharging through the washer motor when Sl is released. With the components selected the delay before the wipers self-park can be varied between 4 and 11 seconds.

Switch $\$ 2$ can either be a separate switch or be ganged to VR2. Both alternatives have been tried
but the former was found to be slightly more convenient. If a warning light is required $S 2$ should be a double pole switch and the supply for the light being taken from before D4 in order to preserve the decoupling action of D 4 and C 5 .

## CONSTRUCTION

The circuit may be conveniently constructed on a $3.8 \times 2.4 \mathrm{in}(80 \times 60 \mathrm{~mm})$ piece of 0.1 in pitch Veroboard (Fig. 2). If the suggested layout is followed


Fig. 2. Veroboard cutting details and component layout


Fig. 3. Various wiper motor arrangements
and corresponding connections to the unit

Fig. 4. Washer motor wiring details for positive earth vehicles, before and after connection to the unit


the only breaks necessary are the four beneath the i.c. and the three between the relay pins.

Construction should commence with cutting and trimming the board to fit into the case, then the three holes for the relay should be enlarged with a small round file or drill.

When the board has been assembled connect S2 and VR2 and then connect points $a$ and $b$ to $a$ suitable power supply (a 9 V battery will suffice). Set VR3 to maximum resistance, close S2 and after a short delay the relay should be heard clicking on and off. If all is well fit the circuit board into the case using insulating spacers, and connect the flexible leads to the connecting strip as in Fig. 2.

## INSTALLATION

It will be necessary to determine the type of windscreen wiper motor used and the method of wiring it, before the unit can be installed. Various configurations and the corresponding connections to the correcting strip are shown in Fig. 3.

The relay specified has contacts rated at 5 A , which have proved quite adequate since they are hardly ever required to break the supply to the wiper motor. For the washer motor the connection to point e from SI should be taken from the motor side of the switch for negative earth vehicles. For positive earth vehicles a slight modification will be needed, see Fig. 4.

## SETTING UP

Having installed the unit with VRI and VR3 set at maximum resistance, VR2 should be set to minimum resistance and S2 closed. After a short delay the wipers should make one sweep and then self-
park. The value of VR3 should be reduced until there is about a 1 second delay between the wipers parking and the relay closing to initiate the next sweep. It is advisable to hinge the wipers away from the screen to prevent any damage caused by "wiping" a dry screen.

Next VRI can be adjusted; closing SI should cause the washers to operate and the wipers to work continuously for about I| seconds. The value of VR I should be reduced so that the wipers operate for long enough to clear the screen and so that the relay contacts open half way through a sweep of the wipers, in order to minimise the possibility of the relay contacts having to break the supply to the wiper motor.

## USES

The three functions: continuous wipe, variwipe, and wash-wipe can be used independently. With the wipers operating continuously, closing $\mathrm{S} \mid$ washes the screen as nonmal. With the variwipe in action, closing SI results in the programmed wash-wipe after which the wipers revert to the variwipe action. If both the wipers and the variwipe are off, olosing SI results in the programmed wash-wipe.

Finally, with the wipers off or on variwipe a very quick flick of St will start the wipers working for the preset interval. without giving the washer motor time to spray more than a few drops of water onto the screen. This last feature is very useful for clearing the screen of spray from passing lorries, etc. whilst on the motorway.

For cars without electric windscreen washers, components SI, D1, R1, D2, C1, VR1 and R2 can be omitted. They can be retained, however, to preserve the screen olearing action as described above.


# SOIL SATURATION METER By D.W. LLOYD 

GROWING plants today is rapidly becoming a necessity with the ever increasing cost of food. Also being a very popular pastime, it is surprising how many people still leave the success of their labours to either luck or their possessing those coveted "green fingers".

Plants can wither and die in harsh dry summers, but in the other extreme, over watering can also be damaging since it can lead to rotting of the roots. To enable the gardener to control and maintain a careful watch on the moisture content of the soil, the Satometer described in this article was produced.

The instrument is a small hand unit and is very simple to use. It consists of a small hand-held box with a meter display and is powered by a small 9 V battery. Firsty, a probe is pushed into the soid to

## CIRCUIT OPERATION

The circuit diagram is shown in Fig. 1. The unit consists of a multivibrator whose output feeds a diode bridge circuit via a capacitor. The capacitor is ircorporated to isolate d.c. thus preventing possible polarisation of the probes and ensuring repeatable readings. If any d.c. flows through the probes a gradual exide formation takes place which can slowily after the calibration of the instrument.

The multivibrator design used is conventional, except for the inclusion of Zener diodes D1, D2 and D3. These have been added so that circuit operating conditions cio not vary as the battery ages.

Soil saturation is displayed on a $100 \mu \mathrm{~A}$ meter connected via a calibrating resistor across the diode


Fig. 1. Circuit diagram of the Satometer

COMPONENTS . . .

\section*{Resistors <br> | R1 | $4 \cdot 7 \mathrm{k} \Omega$ |
| :--- | :--- |
| R2 | $4.7 \mathrm{k} \Omega$ |
| R3 | $82 \mathrm{k} \Omega$ |
| R4 | $82 \mathrm{k} \Omega$ |
| R5 | $4 \cdot 7 \mathrm{k} \Omega$ |
| R6 | $4 \cdot 7 \mathrm{k} \Omega$ |
| R7 | $68 \mathrm{k} \Omega$ |}

All 10\% $\underset{4}{2}$ W carbon

Potentlometers
VR1 $22 k \Omega \operatorname{lin}$.

## Capacitors

C1-2 $\quad 0.1 \mu \mathrm{~F}$ plastic or ceramic
C3 $\quad 0.47 \mu \mathrm{~F}$ plastic or ceramic
C4 $\quad 0.1 \mu \mathrm{~F}$ plastic or ceramic

Semiconductors
D1-3 5.1V Zener diode (BZY88 5V1)
D4-7 1 N914 or 1N4148
TR1-2 BC107

## Miscellaneous

S1 3-way 2-pole switch
ME1 $\quad 100 \mu A$ meter movement (SW100)
SK1 Din socket
PC1 Din plug
$9 \mathrm{in} . \times \frac{1}{2} \mathrm{in}$. dia. ( $230 \mathrm{~mm} \times 10 \mathrm{~mm}$ ) copper tube, 11 in . ( 280 mm ) 6 BA threaded rod, tin. dia. ( $8 \mathrm{~mm} \times 20 \mathrm{~mm}$ ) brass rod, 9 in . ( 230 mm ) flexible insulation (an old p.v.c. cable sheath of the correct size will suffice)
bridge. Capacitor C4 smooths the supply to the meter.

## CONSTRUCTION

Constructing the unit is straightforward, the most difficult part being the large hole in the front panel for the barrel of the meter. In the absence of a proper tool to make this, one can use the standard method of drilling small holes round the circum-


## SATOMETER WIRING



Fig. 2. Internal layout and Veroboard details


Fig. 3. Defails of the probe
ference of the hole to be made, knocking the centre out and then filing down the rough edges with a round file.

## CASE

Plastics boxes obtainable from most hobby shops give a very professional appearance to the finished instrument. The one used in this article was supplied with a front panel ready for drilling. The front pancl is held by four posts, one at each corner of the box and these were also usefully employed to anchor a piece of bent aluminium which provided a battery space. Foam draught excluder was then used to hold the battery in place.

The only other piece of metal work is the calibration pot bracket which, as can be seen, is very simple. This completes all of the hardware. It can all be assembled in the box or on the front panel as appropriate. The component board should now be dealt with, and when complete this fits on the rear of the meter.

The component board is constructed on Veroboard, and the layout is given in Fig. 2.

## PROBE DETAILS

The probe is made from a tube of copper piping and a solid brass or copper tip, with an insulating coat along the whole length, except for the area indicated in Fig. 3. The tip is insulated from the main stem by an old plastics "Biro" stem as shown. Brass is a good material as the noninsulating parts require no treatment against rust, and the wire connections at the blunt end can be soldered direct. To ease insertion into the earth the end is filed into a point.

Insulation of the rod is important and a coating that will not come off when the probe is inserted into the ground is essential. A good hard paint well keyed onto the rod is therefore used. Finally, a din plug and socket and a length of wire are used to connect the probe to the instrument.

## SETTING UP

Testing of this unit is done by obtaining a bucket of soil from your garden and watering it until it can absorb no more. The soil is then saturated and with the probe inserted to about half the bucket's depth, the calibration potentiometer is adjusted untid the meter reads 100 per cent saturation. The unit is now ready and calibrated for use.

## USE OF THE SATOMETER

The full benefits of the Satometer cannot be realised unless plant requirements are known. A trip to a local library would therefore be worthwhile to give you all the information required, as it is a far reaching subject which is impossible to cover here. However, for the majority of plants a 50 per cent level is usually adequate. Roses, for example, will be happy with anything from about 30 per cent to 70 per cent. Marrows are watery plants though and prefer an average of 80 per cent with only plus or minus 10 per cent variation for good results. So by adjusting your watering techniques you should be able to get results as good as the experts.


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## PRACTICAL <br> ELECTRONICS

MARCH ISSUE ON SALE FEBRUARY 13, 1976

## VENUS PROBE

Another milestone in space and astronomical history was made by the arrival of Venus 9 and 10 , the two Soviet spacecraft with the facilities of landing two descent craft on the surface of Venus, the mystery planet. The history of this mission follows a more or less normal pattern; the vehicles were launched according to plan and made their swift journey from Bikaner to the vicinity of Venus.

The five-month journey was completed without mishap and then the drama began. Previous attempts had been made to land descent craft but none had succeeded in reaching the surface of the planet. On this occasion, however, two descent craft were landed, one from Venus 9 and the other from Vemus 10 . The two landings were made at different points on the surface but at almost the same time.

## TOUCH DOWN

The descent craft landed at a speed of between 7 and 8 metres a second. Pictures were taken immediately after landing for, as it turned out, there was no dust to be disturbed, as on Mars and the Moon.

The descent craft from Ventus 9 landed on a plateau which was approximately 2,500 metres above what might be termed the Venusian "sea level". The descent craft from Venus 10 landed on a plain some 2,200 kilometres away from the other craft. It is, therefore, a very unique situation where two levels of the planetary surface could be assessed at the same time. The Venus 10 craft sent back some very remarkable photographs which could be turned over to the newspapers without further processing.

Once again here was a situation for which the planners were not prepared, yet the results so dramatically obtained were the result of very special design techniques. Since the atmosphere of the planet was so dense it had been assumed that the light reaching the surface from the Sun would be at a very low level. The cameras were, therefore, designed for very low level lighting. The result was perfect pictures of the Venusian surface.

## FIRST PICTURES

The first picture sent back was from the craft which landed on the plateau. This showed a scattered collection of rocks, large and typical of young, mountainous regions. The next picture from the 'plain was quite different, showing rocks from an older type of mountainous landscape. It would seem that from the examination of the rocks that they are laminar and sedimentary in nature. These are well known types with which geologists are familiar.


The landscape showed rock outcrops among the more general debris. A Soviet planetologist, Dr Mikhail Marov, said that the plain showed a stony desert. This means that Venus can be classed as a young and still living planet. So though the same pattern appears as for all the solid planets, the surface of Venus is arid and uninviting. It is also very hot and the crust is probably pliant for this reason.

The formations on the plateau show that it is probably tectonic and the fact that some of the rocks show recent fracture may indicate some volcanic activity. Considering the nature of the accumulations, the dark colour and content of radioactive elements, these rocks would appear to be similar to the basalt rocks on Earth. Similarly, the history of the crust formation like the Moon and the Earth confirms that there is a standard geochemical process on all the solid planets. Thus, though having a different appearance from each other they are still a standard type.

## ATMOSPHERIC STUDIES

During the lowering of the descent craft which took about 75 minutes physical and chemical parameters were measured. The optical nature of atmosphere was studied and also the structure as the descent craft went lower and lower towards the surface of the planet.

During the time that signals were received, which lasted some 65 minutes for each descent craft, measurement of the light was recorded. The photography of the terrain and an examination of the nature of the rocks near the craft was also made. In both the pictures returned it was possible to see the curved horizon
of the planet. The actual temperature was somewhat higher than had been suspected. This was $465^{\circ} \mathrm{C}$; the wind velocity at the surface was 3.5 metres $/ \mathrm{sec}$, and the pressure of the atmosphere 92 atmospheres.

## SPECIAL TECHNIQUE

The photography was by a specially developed system which consisted of a panoramic television system with opto-mechanical scanning. The pictures were relayed via Venus 10 . The pictures will, of course, be the subject of extensive study for they have in the words of Chief Topographer, Boris Nepoklonov, "we can surely dismiss already the old idea of Venus as a desert created by constant wind erosion, and an extreme range in temperature".

Rocks were not sharp but resemble pancakes with sections of cooled lava or weathered rock debris in between. From the standpoint of geochemistry, Alexander Badilevsky, says that everything points to a planet that is "living". However, this is something to be studied both in the pictures and from the results of other experiments which are part of the Venus mission.

## SECOND GENERATION

The two main spacecraft Venus 9 and Venus 10 could be said to be the second generation of automatic stations intended for a deeper study of Venus.

The special heat resisting shell is cast off after the first stage of braking; this makes it possible to instal experiments outside the shell of the craft, thus providing greater facilities for study of the planet. Also, it was possible in the new design to instal better shock absorbing equipment which could have a high degree of stable orientation.

Many problems arose in the redesign of the shell which had to be small and capable of withstanding temperatures up to $2,000^{\circ} \mathrm{C}$ and a frontal force of more than 300 tons when entering the Venusian atmosphere. Further, it had to be easy to jettison the heat shell.

Perhaps it would be right to conclude this story of the new and very exciting chapter in the exploration of the so-called mystery planet by "iting Dmitry Grigoryev's words"We geologists of the Earth find it difficult at first glance to suppose that we are seeing on these photographs outcrops destroyed in situ. It looks more likely that some unknown force has scattered these rocks over the planet's surface. Perhaps they fell or slipped down from the surrounding rocks. It could also be caused by meteorite craters of gigantic size. They do resemble the sedimentary rocks well
known to us."


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#  <br> digital I.Cs 



By D.B. JOHNSON-DAVIES \& A.M. MARSHALL b.a. PART 2

The opening article of this series described the main basic element from which complementary mos i.c.s are constructed - the complementary inverter pair. This is an arrangement where $n$ - and $p$-channel mosfets are connected in series to operate in push-pull fashion across the supply rails (Fig. 2.1).

When one device is on, the other is off and the idling or quiescent power consumption is a few nanowatts. The switching characteristic of the cmos inverter very closely approaches that of the ideal switch. The switching thresholds of the two devices are typically 45 per cent of the supply voltage. Thus cmos has a high degree of immunity to noise-a powerful attribute in industrial and automotive applications.

Switching power consumption increases linearly with operating speed. At 2 MHz a typical cmos gate will consume 1 mW at 5 V -the same as a low-power TTL gate. At 20 kHz the power consumption of the cmos gate will reduce to 0.01 mW , while the consumption of the Trl gate remains at 1 mW .

Finally, it was shown that cmos operates from a single positive supply which, for standard devices, can be anywhere in the range of 3 to 15 V . The positive rail is called $V_{\text {DD }}$, since the drains of the $p$-channel devices are connected to it. The other rail is generally referred to as $V_{S S}$, since the sources of the $n$-channel devices are connected to it. It is normally 0 V but can be negative if the signal has a negative swing.

## THE TRANSMISSION GATE

Another way of arranging the $n$ - and $p$-channel devices is to connect them in parallel instead of in series as in the inverter. This forms the cmos transmission gate which has a considerably superior performance over any other form of semiconductor switch. It behaves as a bi-directional single-pole, single-throw switch with a very high off/on resistance ratio of $10^{9}$ ohms. It can handle both analogue and digital signals.

All cmos i.c.s are constructed from the two basic building blocks; the inverter and the transmission gate. The transmission gate enables cmos to do things that are virtually impossible with other logic families.

The basic transmission gate circuit is shown in Fig. 2.2. The transmission gate is on when the gate of the $p$-channel device is at 0 V and the gate of the $n$-channel device is at $\mathrm{V}_{\text {DID }}$. When the levels at the gates are reversed, the transmission gate is off and a resistance of about $10^{11}$ exists between the input and the output.

In the basic form shown in Fig. 2.2 the resistance between the input and the output when the transmission gate is on ( $\mathrm{R}_{\mathrm{oN}}$ ) is several hundred ohms; the actual value varies rather sharply with the voltage applied at the input. The addition of a third $p$-channel device to delay the turn off of the $n$-channel device, gives a much flatter $\mathrm{R}_{\mathrm{ON}}$ versus $\mathrm{V}_{\mathrm{IN}}$ curve. This enables the transmission gate to switch analogue signals with very low distortion.
The advantage of having opposite polarity devices in parallel, is that the analogue signal can swing over the whole cmos supply range. With a single device, the signal swing is limited by the gate threshold. Furthermore, with the complementary pair, the signal can flow both ways.
The circuit of Fig. 2.2 is used extensively in the fabrication of flip-flops, latches, shift registers and counters. It enables these functions to be implemented much more economically than the equivalent bipolar functions, which means that more logic can be integrated on a cmos chip. As an example, a D-type flip-flop in cmos requires an eighth of the chip area as the same function in TTL.

## THE BILATERAL SWITCH

When the parallel pair is combined with an inverter, as in Fig. 2.3, we have the cmos bilateral switch. This is operated by the application of a " 0 " $\left(\mathrm{V}_{\text {SS }}\right)$ or a " 1 " ( $\mathrm{V}_{\mathrm{DD}}$ ) to a single control terminal. When the control pin is high, both the $n$ - and $p$ channel devices are on; thus the switch is on. Similarly, both parallel devices are off when the control terminal is low.

## LOGIC GATES

NOR and NAND gates are readily formed by combining the complementary inverter pairs in series-parallel-arrangements.

If $p$-channel devices are connected in series from the positive rail to the output, and the $n$-channel devices connected in parallel from the 0V rail, Nor gates are created (Fig. 2.4).

On the other hand, if the $p$-channel devices are put in parallel and the $n$-channel devices in series, Nand gates are formed (Fig. 2.5).

For clarity these arrangements are represented in Figs. 2.4 and 2.5 by interlocking pushbuttons. It can be seen, in the case of the NOR gate, that if all inputs are low, then all the series transistors are on and the output is high. If one input is high, then one of the series transistors will be off and the output will be low, whatever the state of the other inputs. Similarly


Fig. 2.2. Paraliel connection of the complementary pair forming the basic transmission gate. It is not only a basic building element in CMOS i.c.s but also makes an excellent switch for analogue signals


Fig. 2.3. The смоs bilateral switch, formed by adding an inverter as a control element. It is equivalent to a fully-floating s.p.s.t. switch and both digital and analogue signals can pass in either direction. Transmission bandwidth is at least 50 MHz

Fig. 2.1 (left). Complementary $p$ - and $n$-channel devices connected to form the basic cMOS element, an inverter


Fig. 2.4. Simplified representation of seriesparallel connection of CMOS inverters to form 3-input NOR gate. When all inputs are "low', the output is 'high'. If any input goes "high"', the output goes "low"


$$
=-8
$$

Fig. 2.5. CMOS 2-input NAND gate; the complement of the NOR gate. The $n$-channel devices are now in series instead of parallel. If either input goes "low', the output goes "high"
with the NAND gate; if all inputs are high, the output will be low since the $n$-channel devices in series will all be on and connect the output to the 0 V rail. If any input goes low, then the output will be high.

## FAN-IN AND FAN-OUT

смоs has limited fan-in and almost unlimited fanout. As shown above, each extra input to a gate requires an additional complementary pair. This in practice limits the number of inputs, for standard gates, to four. The reason for this is that what is known as pattern sensitivity at the output, becomes a significant problem as the number of elements in a gate is increased.

Pattern sensitivity can be understood by referring to Fig. 2.4. When all the inputs are low, the output is connected to $\mathrm{V}_{\mathrm{In}}$ via the series $p$-channel devices. If the output impêdance is to be held within reasonable limits, then as more elements (i.e. inputs) are added, the on resistance of each series device must be increased. This is done by enlarging their geometries or chip areas. Similarly, the $n$-channel devices in the nand gate (Fig. 2.5) have to be progressively increased in size.

## BUFFER SOLUTION

The solution to pattern sensitivity is to buffer the gate output with two inverters. This need not increase the chip area, since smaller geometry transistors can be used to generate the logic function while only two large transistors are required for the final output inverter.

Apart from eliminating patterning, output buffering improves cmos performance in several other ways. Since the input geometries are small, the input capacitances are reduced thus increasing the performance at high speeds. Output drive can be truly symmetrical, since there is only one sink or source transistor regardless of the number of inputs. The added buffer stages also provide increased voltage gain, which further improves the switching transfer characteristic and increases the noise immunity (Fig. 2.6).

Fan-out is limited only by the effect on operating speed of paralleling output capacitances. The d.c. fan-out amply covers any designer's practicable requirements; being more than 100 .

## PART NUMBERING SYSTEMS

This series of articles is concerned with the standard cmos logic family, which was originated by RCA as the CD4000 series under the trade mark COS/MOS. In 1971 Motorola made a firm commitment to the production of cmos with the announcement of not only second-source versions of some of the CD4000 series, but also some special Motorola designs. The Motorola trade mark is McMOS.

Today Motorola and RCA are, by far, the two largest producers of the standard cmos logic family. This series of articles will, therefore, be confined to the products, and their various designation systems, of these two companies.

The situation is that both companies offer a range of cmos devices which are pin-for-pin replacements for one another, although the precise specification
and the internal construction may vary between the two companies. Also, both companies manufacture смоs devices which are special in-house designs but which are not necessarily second-sourced by the other.

## PREFIXES AND SUFFIXES

RCA prefix смоs devices, CD4-.-; while Motorola uses MC14--. Thus the CD4001 is equivalent to the MC14001. The infix 5 appears in parts originated by Motorola and has no other significance. Thus the MC14518 is equivalent to the CD4518.

Suffixes indicate type of packaging and operating temperature and voltage range. The ordinary commercial plastic packaged device is the type that will be used throughout in these articles.

For this type of device Motorola uses the suffix CP which specifies an operating voltage range of 3 to 16 V , and an operating temperature range of -40 to $+85^{\circ} \mathrm{C}$.

RCA uses the suffix AE or BE. AE specifies 3 to 15 V operation over the temperature range -40 to $+85^{\circ} \mathrm{C}$, while BE specifies 3 to 18 V operation over the same temperature range and incorporates output buffering (see above under fan-in).


Fig. 2.6. Comparison of the switching characteristic of buffered and unbuffered gates. Feeding the gate output through two inverters gives an ideal transfer characteristic, increases noise immunity and avoids output patterning


Fig. 2.7. Package diagram of the 4016 and 4066 quad analogue switches. Each switch is independent and can transmit signals with a bandwidth of up to 50 MHz , while the on/off control pins can be switched at up to 10 MHz

Other suffixes indicate military-grade devices or more expensive packaging, and need not concern us herê.

Therefore when cmos devices are described and used in circuits in these articles and no prefixes or suffixes are used, it should be understood that reference is being made to the commercial plasticpackaged range, described above, from either Motorola or RCA.

## THE QUAD ANALOGUE SWITCH

cmos technology has produced an excellent means of switching analogue signals. The cmos bilateral switch avoids the drawbacks encountered in bipolar, f.e.t. or single-channel mOS analogue transmission configurations. Also it can be used independently of any other logic or component in many systems that require fully-floating switches.

The 4016 contains four independent bidirectional switches in one 14-pin dual-in-line package (Fig. 2.7). The switches are purely ohmic with an on resistance of about 300 ohms and off resistance in the region of $10^{11}$ ohms. They will transmit frequencies of up to 50 MHz , while the on/off control pin can be switched at up to 10 MHz . This makes it a simple matter to multiplex video signals with low distortion. A simple, high-performance oscilloscope trace doubler/quadrupler using this technique will be described later in this series.

## REMOTE SWITCHING

The matching between four switches on a chip is excellent and crosstalk is low; typically -50 to -80 dB . The control is isolated from the switching circuit and has a very high impedance of $10^{12}$ ohms. Thus the 4016 would be excellent for remote switching over long lines in noisy environments. Another application is in switching electronic organ-stop filters. Pre-arranged mixtures of stops can also be selected by a simple toggle switch.

The only constraint on the switched signal relative to the control signal is that the switched signal must never exceed the levels of the supply lines. Thus if a sine wave of 10 V peak-to-peak with an average value of 0 V is to be switched, $\mathrm{V}_{1 n}$, must be at least +5 V and $\mathrm{V}_{\mathrm{SS}}$ at least -5 V .

The output of cmos devices is generally the inverter. This is a "totem pole" structure whereby the output is always at either $\mathrm{V}_{\text {ID }}$ or $\mathrm{V}_{\mathrm{SS}}$. Certain applications, such as digital filters, require outputs similar to the TTL open-collector output. This is easily implemented with the 4016, as is three-state operation for common bussing of outputs. This will be discussed later.

An alternative pin-compatible device to the 4016 is the 4066. This has a lower $\mathrm{R}_{0 \times}$ of around 80 ohms at 15 V .

The 4016 is one of the few cmos devices where the specification varies considerably between manufacturers. For critical applications the data sheets should be consulted.

Next month: CMOS gates and flip-flops. How gates can be biased in the switching region to give low power, high gain linear amplification. An oscilloscope trace doubler will be described.

## NEWS BRIEFS

## BLINO CALCULATORS

The development of two new calculators from America should be of great help to the blind.
Developed by the American Foundation for the Blind, one calculator has braille output and the other a voice output.

The braille calculator is a modified standard five function type with a floating decimal point and equipped with a braille cell. Within the cell is a two-by-three array of solenoids positioned beneath a similar array of small pins. Energising the solenoids forces the pins above the cell surface in patterns which represent the decimal point and numerals 0 to 9 in braille.

The audible calculator, developed for the Foundation by Telesensory Systems Inc. of California, has a 24word vocabulary built into a speech generating readonly memory i.c. It has six basic functions. including square root, percent, automatic constant, floating decimal and an eight-digit visual display.

The speech key can be depressed repeatedly to announce the display information without initiating further calculations.

The American Foundation for the Blind hope to be able to offer these calculators on a world-wide distribution basis.

## SKY LINK

The world's biggest communications satellite successfully launched from Cape Canaveral in September, is now undergoing tests and will go into service shortly over the busy Atlantic region carrying phone calls to and from the UK, the rest of Europe, North and South America, Africa and the Middle East.

The satellite is capable of carrying more than 6,000 telephone calls and two television channels at once. It is the first of a new generation of high capacity satellites to be launched over the coming years.

Nearly eight feet taller than a London double-decker bus, the satellite is provided by the International Telecommunications Satellite Organisation (Intelsat) in which the UK is the second largest shareholder. United Kingdom calls will be handled by the Post Office's earth satellite station at Goonhilly, Cornwall.


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## AERIAL FIXING

Claimed to take less than two minutes to erect, a new type of TV or radio aerial bracket has just been introduced by Rawlplug.

The adjustable arms of the corner bracket fit into two small holes which have to be chiselled out of the mortar of the brickwork. Once in position, a single locking nut is then tightened to complete installation.

One bracket is reckoned to be sufficient for aerial masts up to 6 ft high. Two brackets are recommended for higher masts.

Ideal for most domestic aerial installations, the Rawlplug SSBI retails at $£ 2.21$ plus VAT $(8 \%)$. For addresses of nearest stockists, readers should write to: The Rawlplug Co. Ltd., Rawlplug House. 147 London Road, Kingston-uponThames, Surrey, KT2 6NH.

## FIRE ALARM

Ideal for garages, lofts and small rooms, the new fire alarm system, type FB-75, from Photain Controls is also suitable for the elderly or infirm.

The unit has been designed so that it is easily installed by just fixing to a wall and connecting to the nearest power point. Once connected to the supply the unit will sound the alarm ( 85 dB ) automatically for any temperature rise, smokc detection or, if operated manually by pulling a cord, emergency.

For smoke detection it is claimed that the alarm will operate within 30 secs of the unit detecting a level of smoke equivalent to 5 per cent obscuration of a light beam over a distance of 1 metre. The heat detection will operate the alarm when

# mariet PLACE 

Items mentioned in this feature are usually available from electronic equipment and component retailers advertising in this magazine. However, where a full address is given, enquiries and orders should then be made direct to the firm concerned. All quoted prices are those at the time of going to press.
the ambient temperature reaches $60^{\circ} \mathrm{C}$ and will reset when the temperature falls to $55^{\circ} \mathrm{C}$.
Once triggered the alarm bell will only cease ringing when smoke or heat are no longer present. In the case of manual operation, pulling the cord for a second time stops the bell ringing.

The recommended retail price for the FB-75 is $£ 20$ plus VAT. Further information is available from Photain Controls, Unit 18, Hangar 3, The Aerodrome, Ford, Sussex.

## POWER UNITS

A range of Selmar compact and reliable mains charging and power units for electronic calculators or low-voltage equipment, such as battery operated recorders, is now being marketed by Stellar Components (Sales).
These power units have been designed to meet the new British Electrical Safety Standards which
become law in April. Suitable for most models of calculators, the units are supplied with a four-pronged universal plug to cover various input sockets found on calculators.

From a safety point of view it would seem to be a bad practice to have three of the four prongs. with a voltage at their tips, bare to the elements-a possible safety risk?

One of the units from the range is a multi-voltage unit which switches between $4.5 \mathrm{~V}, 6 \mathrm{~V}$ and 9 V at 300 mA . The recommended retail price for this unit is $£ 3.95$ plus VAT. Details and prices for the other units can be obtained from Stellar Components (Sales) Ltd., The Causeway, Maldon, Essex.

## DATELINE

To coincide with the announcement of AMI Microsystems settingup a new section to market certain products under the brand name of OM-EX, they have introduced a range of special "notebooks" cum executive desk-top calculators/calendars. They are also marketing a range of l.e.d. and l.c.d. digital watches under the same name.

These include a calendar holder, notebook and 3 -ring binder incorporating a 4 or 5 function calculator with an 8 -digit display, floating decimal and automatic constant.
The highlight of the range is a memo pad holder with a buitt-in solid-state clock as well as a calculator/calendar.

The recommended retait price for the basic calendar/calculator clock set is $£ 32.50$. Complete details of the range of units is available from AMI Microsystems Ltd., 108a Commercial Road, Swindon, Wilts.

SSB1 aerial mast fixing bracket from Rawlplug

The OM-EX executive desk set and leather notebook

Selmar low voltage charger/power unit from Stellar Components


# CAR/ CARAVAN CICCK with humanaderan dumey Iner 

$\star$ Accuracy within a few seconds per week<br>$\star$ Back-up batteries ensure crystal timebase working with ignition off

* Alarm and other optional facilities available


## By M. Fischer*

Adigital clock intended expressly for car use must provide the function of any digital clock to provide an attractive, accurate and reliable display of time. But. additionally, the fact that the clock is in a car demands features not usually found in mains-powered units.

To be legible during the day the display should have the maximum possible brightness, and in order not to distract the driver at night this intensity should be reduced, preferably automatically

A car clock becomes far more attractive if, in addition to showing the time. it can be used as a stop-watch to time journeys. without interfering with the clock function.

The car clock described here includes all these features.

## SUMMARY OF FEATURES

The jumbo-sized l.e.d. displays are driven at the maximum current (and intensity) compatible with a long life-time, giving a readout which is legible under any conditions other than having bright sunlight shining directly onto it. The clock i.c. chosen, the MK50253. has a three-level automatic intensity control, and the display enable input allows an extra MK50253 or MK50250 to be added to serve as a journey timer, sharing the clock display circuitry.

A crystal timebase is used, capable of giving an accuracy of a few seconds a week.

Back-up dry batteries are incorporated to ensure uninterrupted operation if ever the slock is disconnected from the car battery.

## AN EXTREMELY VERSATILE DESIGN

The basic car clock design described in this article is capable of modification or expansion to provide other timing and controlling functions. For such uses, which will generally imply out-of-car installation, the first requirement is conversion for a.c. mains operation.

A mains alarm clock can readily be realised; there is adequate room in the specified case for inclusion of the alternative and additional electronic assemblies.

If the clock is to be used as a controller, a larger size case may be necessary.

Full details of these interesting possibilities will be given in a second article next month.

The MK50253 has an alarm facility and wiring for using this has been included on the clock PCB

The clock is built on four printed circuit boards: the displays (PCB3): the clock (PCB2); the crystal timebase (PCB4) and the power supply and journey timer (PCB1). The clock p.c.b. will also accommodate all components needed to build a mains-only alarm clock: details of using this and other optional functions will be given next month.

## CIRCUIT DESCRIPTION

The main circuit diagram is given in Fig. 1. This shows the clock, display. journey timer and powes supply sections, witheall interconnections. The practical division into three printed circuit board assem. blies is indicated. The crystal timebase circuit is shown in Fig. 2. There are only three connections from this circuit (which occupies PCB4) to the other boards. so little difficulty should arise in correlating Fig. 1 and Fig. 2.

## COMPONENTS

| Resistors |  |
| :---: | :---: |
| R1, R2, R6, R9 120kS (4 off) |  |
| R3-R5 | $3.3 \mathrm{k} \Omega$ (3 off) |
| R7 | ORP12 (Mullard) |
| R8 | $3.3 \mathrm{k} \Omega$ |
| R10 | $12 \mathrm{k} \Omega$ |
| R11 | 1.2 kS |
| R12 | $820 \mathrm{k} \Omega$ |
| R13 | $\dagger .2 \mathrm{k} \Omega$ |
| R14-R28 | $3 \cdot 3 \mathrm{k} \Omega$ (15 off) |
| R29-R35 | $120 \Omega \frac{1}{2} \mathrm{~W}$ (7 off) |
| R36-R47 | $3.3 \mathrm{k} \Omega$ (12 off) |
| R48 | $15 \Omega$ |
| R49 | $3 \cdot 3 \mathrm{k} \Omega$ |
| R50 | $12 \mathrm{k} \Omega$ |
| R51-R53 | $3 \cdot 3 \mathrm{k} \Omega$ (3 off) |
| R54, R55 | $12 \mathrm{k} \Omega$ (2 off) |
| R56 | $1.2 \mathrm{k} \Omega$ |
| R57 | $10 \mathrm{M} \Omega$ |
| R58 | 120ks! |
| R59 | 100s |
| R60 | 3.3 k 2 |
| All $\frac{1}{4}$ W 10 | \% unless otherwi |

## Potentiometers

$\left.\begin{array}{ll}\begin{array}{l}\text { VR1 } \\ \text { VR2 }\end{array} & 22 \mathrm{k} \Omega \\ \text { VR3 } & 50 \mathrm{k} \Omega\end{array}\right\}$ miniature horizontal preset

## Capacitors

| C1 | 10 nF ceramic 63 V |
| :--- | :--- |
| C2 | 10 nF ceramic 63 V |
| C3 | 10 nF ceramic 63 V |
| C4 | 10 nF ceramic 63 V |
| C5 | $100 \mu \mathrm{FF}$ elect. 25 V |
| C6 | $2.2 \mu \mathrm{~F}$ elect. 63 V |
| C7 | $100 \mu \mathrm{~F}$ elect. 25 V |
| C8 | $10 \mu \mathrm{~F}$ elect. 25 V |
| C9 | $5-22 \mathrm{pF}$ trimmer (Mullard $808-00006$ ) |
| C10 | (see text) |
| C11 | 82 pF ceramic |
| C12 | 10 nF ceramic 63 V |
| C13 | 10 nF ceramic 63 V |
| C14 | 10 nF ceramic 63 V |

## Integrated Circuits

| IC1 | CD4011AE | IC4 |
| :--- | :--- | :--- |
| CD4011AE |  |  |
| IC2 MK50253 or MK50250 | IC5 | CD44020AE |
| IC3 MK50253 | IC6 | CD4025AE |

Diodes

| D1-D10 | 1N914 or 1N4148 (10 off) |
| :--- | :--- |
| D11 | T1L209 (L.E.D.) |
| D12 | 1N914 or 1N4148 |
| D13 | TIL209 (L.E.D.) |
| D14 | 1N4001 |
| D15 | 1N4001 |
| D16 | 1N914 or 1N4148 |
| D17 | 1N4001 |
| D18 | 1N914 or 1N4148 |
| D19 | 4V7 400mW Zener |
| D20 | 16V 400mW Zener |
| D21 | 12V 400mW Zener |
| D22 | TIL209 (L.E.D.) |

Transistors

| Transistors |  |  |  |
| :--- | :--- | :--- | :--- |
| TR1 | 2N3703 | TR6 | 2N3704 |
| TR2 | 2N3704 | TR7 | 2N3703 |
| TR3 | 2N3704 | TR8-TR14 | 2N3704 (7 off) |
| TR4 | 2N3703 | TR15 | 2N3704 |
| TR5 | 2N3704 |  |  |

Thyristors
CSR1-6 TIC44 (6 off)

## Switches

S1-S10 Miniature s.p. push-to-make (9 off)
S11-S12 on off switch (2 off)

## Batteries

B1, B2 9 V battery, PP3 (2 off)

## Crystal

XL1 32.768 kHz miniature quartz crystal

## Display

X1-X6 L.e.d. displays FND500 (6 off)

## Miscellaneous

FS1 Line fuse connector with 2A fuse. 20-way flexible flat cable, tinned copper wire, battery clips (2 off), Soldercon i.c. pin sockets, printed circuit boards PCB1, 2, 3 \& 4 ( 4 off), Verocase $75-1410 \mathrm{~J}$, red Perspex front panel. LS1 80S2 miniature loudspeaker.



Fig. 1. Main circuit diagram showing the clock, display, journey timer and p.s.u. (Display driver circuitry is located on PCB2)


Fig. 2. Circuit of the crystal timebase, Crystal frequency is $\mathbf{3 2 . 7 6 8 \mathrm { kHz }}$

One further portion of circuitry remains: though this is optional-and is likely to be of interest only if a mains version of the clock is contemplated. The circuit in question is the alarm. and is given in Fig. 3.
In the following sections, dealing with the circuitry, the major items of interest in the overall system are described. No attempt has been made however to describe the internal functions of the clock integrated circuits.

## ALARM



Fig. 3. Circuit of optional alarm

## DISPLAY DRIVERS

During the time a particular digit is on, the corresponding digit output pin of the IC3 (MK50253) goes to near $V_{\mathrm{ss}}$, firing one of the thyristors CSRI to CSR6. Simultaneously the selected segment outputs also go to near $V_{\mathrm{ss}}$, and as the emitters of the segment driving transistors (TR8TR14) are held at a maximum of +3 V by the selected display l.e.d.s and thyristor, these transistors go into saturation producing arsegment current determined by the value of their collector resistors R29R35.

At the end of a digit pulse there is an inter-digit blanking time during which all the IC3 digit and segment output pins are open-circuited. This turns off all the segment driving transistors, which in turn removes the thyristor anode current, switching the thyristor off.

## AUTOMATIC INTENSITY CONTROL

The cadmium sulphide photoresistor R7, R8 and VR1 form a potential divider between $V+(4)$ and OV which drives an output voltage corresponding to the ambient light level, being high when the latter is high. Through its intensity control input, pin 11, the clock i.c. determines whether the voltage is high, medium or low and varies the percentage of time (duty cycle) each digit is on, and hence the apparent intensity. The three duty cycles are:

| Intensity | Duty Cycle |
| :---: | :---: |
| Bright | $14.3 \%$ |
| Medium | $7.8 \%$ |
| Dim | $2.6 \%$ |

## OTHER FUNCTIONS

The setting switches SI-S8 are simple connections to $V+(4)$. Note that after switching on, the minutes SET button has to be pressed before the clock will start counting and before the other setting switches, will work.

The "P.M." l.e.d. (D|3) brightness is automatically varied to match the rest of the readout. The time is displayed in a 12 -hour format if pin 21 is left open,


Clock Display board (PCB3)
or in a 24 -hour format if the pin is connected (by the jumper provided) to $V+(4)$.

The alarm is not used in the clock described in this article. but the circuit is shown in Fig. 3 for those who want to add it. The alarm output consists of a "bleep" tone of about 500 Hz generated in the clock i.c. Each time the snooze button is pressed the tone is interrupted for 10 minutes.

## JOURNEY TIMER

When wired in the 24 -hour mode IC2 is reset to zero if the power supply is interrupted. This and the count inhibit feature are used to provide a journey timer "stop-watch" with START, STOP and RESET, which reads in hours, minutes and seconds.

When the display enable pin of IC2 is taken to oV, all the digit and segment output pins are opencircuited. By connecting each digit and segment driver pin of the journey timer IC2 to the identical pin on the clock chip IC 3, the readout can be made to show either the time, or the journey timer count. by suitable manipulation of the two display enable inputs.

This display selection and the journey timer StartStop are driven by cmos flip-flops formed by ICIA, b and ICIC. d. These are controlled by pushbutton switches S1, S2. S6 and S7.

The display outputs of both IC2 and IC3 are disabled when the ignition is off, so as to turn off the readout and thereby reduce the current consumption of the whole unit to about 20 mA . The diodes on the various inputs to the journey timer i.c. (IC2) ensure that these inputs are at 0 V when $\mathrm{V}+(3)$ is at 0 V during resetting.

## CRYSTAL TIMEBASE

The circuit diagram for the crystal timebase appears in Fig. 2.

A miniature 32.768 kHz crystal XLI (manufactured for electronic watches) is used with a cmos oscillator IC4 and a divider IC5 to provide outputs at $64 \mathrm{~Hz}, 32 \mathrm{~Hz}, 16 \mathrm{~Hz} .8 \mathrm{~Hz}, 4 \mathrm{~Hz}$ and 2 Hz . The latter five signals are used to gate the 64 Hz square wave so that in every half second only 25 out of 32 pulses are allowed to reach the clock i.c.s, replacing the 50 Hz signal which would normally be derived from the manns.

The gating system is formed by part of IC4 and the whole of IC6 suitably interconnected.

## POWER SUPPLY

TR5 and TR6 form a comparator which switches TR4 on if the voltage of the car battery (B3) falls below a level preset by VR2. This action allows batteries B1 and B2 to supply power to the clock i.c.s. and cmos les.


Journey Timer and PSU board (PCB1)


Clock board (PCB2)


Srystal Timebase board (PCB4)


Fig. 4. Main circuit board PCB1 component layout

As these batteries will conduct for a few seconds a day at the most, their life will only be limited by their shelf life, and this should be over one year.

Without this extra back-up the clock could be reset by a low car battery voltage during starting the engine (as this can fall as low as 5 V ).

Diodes have been included to prevent damage being done by reverse voltage spikes from the car, again occasionally experienced on starting.

A 2A line fuse FSI is fitted in the lead from PCB2 to the car battery.

## CONSTRUCTION

Full size diagrams of the printed circuit boards are given, see Figs. 6, 7, 9 and 11. Component layouts for each board are given, see Figs. 4, 5, 8 and 10 . Inter-wiring is indicated in the PCB diagrams. General assembly can be seen in the photograph of the completed clock.

Although the sequence of assembly of the clock is not critical, the following points should be noted.

Soldercon i.c. sockets should be used to mount the displays and i.c.s. The carrier strips on these should not be broken off until all other soldering has been done.
The light dependent resistor R7 should be mounted after the FND500 digits, with its face just behind the front plane of the digits, pointing a few degrees away from them.

The use of flat cable for as much of the wiring as possible helps to keep the unit tidy. The wires connecting the segment and digit pins of the journey timer i.c. to those of the clock i.c. are soldered to the bottom of PCB1 at the base of the IC3 socket pins.

The display board PCB3 is held in place by two tinned copper wire struts passing through the clock board PCB2.


Fig. 5. Clock circuit board PCB2 component layout. (Alarm circuit components are not annotated -details next month.)


Fig. 6. PCB1 printed circuit master and connecting details. Note the wiring on the left edge is soldered direct to IC3 pins on board PCB2


PCB 2
Fig. 7. PCB2 printed circuit master and wiring connecting details


Fig. 8. Display board PCB3 component layout


## HANDLING THE I.C.s

Standard m.o.s. handling precautions should be used with the MK50253/0 and the cmos i.c.s. An easy procedure is to hold the conductive foam in which the i.c. is shipped with the left hand, with the foam and a finger touching several tracks of the p.c.b. until the i.c. is in its socket: with the right hand take out the i.c. and insert in its socket.

To remove an i.c. from its socket, reverse this procedure, putting the i.c. back into the foam which will have been held with the left hand against tracks of the p.c.b.

This way, the body, the foam, the cmos and the p.c.b. will all be at the same potential, not allowing static to build up.

It is also advisable to be touching some of the pins (of the i.c.) while doing the transferring

Always switch off power before inserting or removing i.c.s: if soldering on the board after i.c.s have been inserted, always wire tip of soldering iron to p.c.b. 0 V , and keep the power off.


Fig. 10. Crystal Timebase board PCB4 component layout. Note that the crystal is fixed to the board by double sided adhesive tape

## CRYSTAL AND DISPLAYS

The crystal leads are fragile-so beware! The crystal is fixed to the p.c.b. with some double-sided adhesive tape, about 5 mm from the holes for the crystal leads, which should have a gentle bend in them.
The FND 500 display has indentations on the top edge (to identify the top of the display). Before switching on double-check that the displays are correctly inserted and that the necessary jumpers are in the p.c.b.
If at any time when the power is on, one segment of a digit is not making proper contact, the full 12 V can appear across two segment diodes of the other digits, exceeding their 3 V maximum reverse voltage and blowing them. For the same reason they must never be inserted or removed while power is on.

## SETTING UP

Power Supply: The car battery voltage at which the back-up batteries B1, B2 cut-in is adjusted by VR2. If the cut-in voltage is too high, say 11 V , the clock may go on to "back-up" at times other than during starting. If the cutin voltage is too low, say 8.5 V , the a.m./p.m. diode D13 may start flashing to indicate a low voltage, or the clock may be reset to zero.
VR2 should be adjusted so that cut-in occurs at a car battery voltage of about 10.5 V . The car battery voltage may be "varied" for this operation by putting diodes (IN4001) in senies with it to drop the voltage in 0.6 V steps, measuring the resultant voltage with a multimeter. The cut-in can be detected by using a 50 mA ammeter or an l.e.d. to indicate that current is being taken from the back-up batteries.
Automatic Intensity Control: Adjust VR1 to obtain the desired set of thresholds.

Crystal Timebase Frequency: This may be adjusted using the trimmer capacitor C9. To increase the frequency, reduce the capacitance of C 9 ; to reduce the frequency, increase the capacitance of C 9 . The


Fig. 11. PCB4 printed circuit master
trimmer is at maximum capacitance when the brass parts cover the nickel parts. If with the trimmer at maximum capacitance, the frequency is still too high, a capacitor must be inserted in parallel with C 9 in the position " C 10 ".

The Speaking Clock is a sufficiently accurate reference for frequency adjusting. The Post Office state that the variation from one day to the next is less than $1 / 20$ second.

## OPERATION

At switch-on the clock may start displaying either the Clock time or the Journey Timer count. The latter will be $00-00-00$; the former will be 12-00-00 if the clock is in the 12 -hour mode, $00-00-00$ if it is in the 24 -hour mode.

## SETTING THE CLOCK

Press the display clock button (S2) to switch the displays to display clock time. Then press the MINS SET (S5), tens mins SEt (S4) and hours SEt (S3) buttons until the correct time is being displayed. The counting is inhibited as long as the count inhibit button (S8) is being pressed. Note: The clock will start counting after, and only after, the miNS SET button has been depressed (even if only momentarily).

## SETTING JOURNEY TIMER

Push the display journey timer button ( S 1), and, if the unit has just been switched on, the RESET button (S9). From now on, the Journey Timer will operate as a stop-watch, controlled by the Start (S7), STOP (S6) and RESET buttons. It can be stopped and started again without resetting. The display can be switched back to display clock without affecting the Journey Timer, and all the control buttons of both functions are functional irrespective of which display mode has been selected.

[^3]
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Harnessing advanced technology for telecommunications expansion
$A^{N}$ explosion in telecommunications is under way. To meet the increasing demands in telephone, telex, computer data, facsimile and other proposed services, a technical revolution will have to take place during the next decade.

Preparations for this revolution are being determinedly and efficiently pursued at the new Post Office Research Centre, Martlesham Heath, Suffolk, recently opened by The Queen. Claimed to be the most advanced centre of its kind in Europe, it will replace Dollis Hill, London, which has been for 54 years the home of Post Office Research.

With a very creditable list of technical achievements in telecommunications behind it, the Post Office is determined to maintain Britain's leading position in world telecommunications in the future. The ultra modern custom designed $£ 11$ million centre will assure the right facilities and environment for the staff of 1,800 . This will include 850 research engineers and scientists, 550 technicians, 80 draughtsmen and 80 trainees.

Of the many important areas of research currently undertaken at Martlesham, mention has to be restricted to but a few. Those mentioned below are vitally important and indicative of the advanced technology soon to be applied in practice.

## TOWARDS THE ALL ELECTRONIC EXCHANGE

Electromagnetic relays are the basic of today's automatic telephone service. In the course of the next ten years these bulky devices will be replaced by semiconductor microprocessors for logical and timing operations. The use of electronic circuits for line signalling will allow existing bulky transformers to be replaced by miniature transformers. Greater reliability, smaller space requirements and lower unit costs will result from this adoption of new technology.

## DIGITAL EXCHANGE SWITCHING

The Post Office Research Department has pioneered work on central digital trunk exchanges. These are now past the research stage. Attention is now being turned to the digital local exchange. Higher speed data and facsimile are the kind of new services that digital techniques will permit. Investigations are being made into systems based upon TTL and cmos.

## COMPONENT RELIABILITY

The supreme importance of semiconductor devices in future developments of the telecommunications network is underlined by the detailed work on component reliability standards being undertaken at Martlesham The task is approached through a blend of basic studies of failure mechanisms and component testing, with the emphasis on accelerated testing to simulate a normal lifespan, often using elevated ambient temperature as the stress factor. Bipolar i.c.s under investigation include
linear, TTL and ECL types. Special procedures are being developed for evaluating complex mos devices, both $p$ - and $n$-channel types.

## TRANSMISSION METHODS

To cope with the rapidly rising demand upon the telecommunications network, new additional transmission methods will have to be brought into use in the next decade. Research and development has been undertaken in two distinct areas: microwaves via waveguides and optical waves along glass fibres.

The use of circular waveguide to carry millimetric digital signals in the frequency band $30-110 \mathrm{GHz}$ has been successfully demonstrated over a 14 km route between Martlesham and Wickham Market in Suffolk. The waveguide consists of a 50 mm diameter helix of fine copper wire set within a glass fibre tube. It is installed into a welded steel duct; the waveguide and the duct are pressurised with dry nitrogen and dry air respectively.

In an operational system repeaters should only be necessary at intervals of 25 to 30 km . The repeaters utilise the latest technical advances in solid state devices and microwave integrated circuits.

The system when exploited up to 90 GHz has an inherent transmission capability of 24 Gbits/s each way, i.e. the equivalent of more than 300,000 two-way telephone circuits. Further capacity is available above 90 GHz if required. This high-capacity waveguide system should have a significant role to play in the trunk network of the future.

## OPTICAL FIBRE COMMUNICATION

For shorter links, as between exchanges in built-up areas, optical fibre cables are expected to offer economic advantages to metal pair and coaxial cables. Lower attenuation at higher message frequencies means fewer repeater amplifiers. The fibre cable will be smaller, lighter and more flexible than existing cables of similar message capacity.

Work in progress at Martlesham covers most aspects of optical cable transmission, including fibre materials and the devices needed to interface between fibres and conventional electronic circuits.

A practical problem for fieldwork has been solvedthis being the jointing of the hair-thin fibres. A special jib has been designed which ensures a perfect cut to the end of the fibre; joints can then be made with the minimum of optical attenuation.

An eight megabit-per-second system is being developed which will be capable of transmitting 120 telephony channels over a 4 to 5 km length of fibre. This system will use a semiconductor (gallium arsenide) light-emitting diode operating at the infra-red wavelength of about 900 nanometres, together with large-core multimode fibre having an attenuation of about $7 \mathrm{~dB} / \mathrm{km}$; a silicon avalanche photodiode detector will be used, with conventional solid-state amplifying circuits.

# GAMEDNUUTIOB UPDAIIE 

## SCHOTTKY FUGITIVES

I always thought that Schottky diodes were destined for perpetual imprisonment inside the faster variety of t.t.I. logic (the 74 S series), but it seems that some of the little blighters have made good their escape and can be obtained individually from Ferranti.
Schottky Barrier diodes have a lot of interesting properties which makes them well worth a second glance. Their forward voltage drop is much smaller than that of a conventional silicon diode when operated at low currents, being typically less than 450 mV at 1 mA .
Their switching performance is nothing short of spectacular due to a complete absence of charge storage, and sub-nanosecond performance is quite possible. If you can think of an application for one of these interesting devices as a fast switch or very high frequency detector or mixer, you will be interested in the ZC2800 and ZC5800 series.

## CLOCK WATCHERS

There are numerous mOS l.s.i. clock clips available to amateurs at the moment, and they all have their particular advantages and disadvantages. Indeed, the most difficult problem seems to be selecting the best device for a particular clock design! You may think that there is not much left to say about these circuits, and that any new design is likely to be just a variation on the theme rather than a fundamental change of direction.

The new Motorola clock chip, MC14440L is, however, a very definite change of direction when compared with run-of-the-mill devices because it utilises cmos technology to give an incredibly low power drain of typically $5 \mu \mathrm{~A}$ (yes, that's right, micro amps!) from a 1.5 V battery supply.

Now you may be thinking that $5 \mu \mathrm{~A}$ for the chip isn't going to put much strain on the battery but 200 mA for the l.e.d.'s certainly will, but the display problem has been taken care of, because the MC14440L teams up with liquid crystal displays to give a clock that will run for a very long time indeed from a single cell!

So, goodbye, mains leads, power packs and the rather doubtful
accuracy of mains frequency (the MC14440L runs from a 32.768 kHz crystal) and hello small, cool, portable clocks which can go anywhere, anytime.
The usefulness of the MC14440L does not stop there however, because it also has a seconds and date capability and a simple time setting circuit to make the whole thing child's play. Apart from a display and a crystal, the only external components are a few R's, C's and diodes, which makes the MC14440L easy to incorporate into the smallest case design. The chip comes in a $40-\mathrm{pin}$ d.i.l. package, but a tiny 10 mm square version (MC14440Z) is available for use in digital watches.

## SAMPLE HOLD

Sample/hold circuits have been used in analogue instrumentation circuits for many years, but very little has been seen of these useful building blocks in any amateur designs because of their high cost.

The idea is quite simple. At a particular point in time an analogue input voltage is sampled, and stored across a capacitor. The capacitor is used as an input to a very high input impedance d.c. amplifier so that the capacitor is not appreciably discharged and an amplifier output voltage is produced proportional to the input voltage at the time the sample was taken. The output voltage from the "hold" amplifier remains steady until another sample is taken.
The basic principles are simple, but in practice if the sample time is short, and the hold time long, the design of such a circuit using readily available components is by no means simple, and a popular solution in the past has been to purchase a ready-made circuit in modular form. This is an expensive way out, and now at last there is a better way with the introduction of a new monolithic i.c. from Harris, the HA2425.
The HA2425 contains a high slew-rate input amplifier, a high performance sampling switch, and an output amplifier with a MOSFET input circuit, connected as a unity gain follower.
A sample/hold circuit can be used to "freeze" a time varying input signal so that an analogue to digital
conversion sequence can be performed, and for this type of application the circuit is often used in the track-and-hold mode, where the output is allowed to follow the input signal between "snap-shots". The HA2425 Is of course suitable for both sample/ hold and track/hold applications, but as we have come to expect from monolithic solutions to old problems, this new i.c. is very versatile, and can be used for a wide variety of other, original applications including analogue multiplexers, de-multiplexers, gated oscillators, phase sensitive detectors and gated opamps.

## GAS DRIVERS

L.e.d. displays are cheap and simple, but if what you really want is an easy-to-read display with style, you would be better off using one of the excellent gas discharge types now available. Now I don't mean the older "Nixie" type of display tubes which were pretty ghastly in some respects, but the new, seven segment devices made by Beckman and Sperry.

These new gas discharge displays look really good, but although the appearance is vastly improved, the old problems associated with driving high voltage cold cathode tubes remain the same, and are likely to send most of us back to the l.e.d.
Thanks to a new family of devices from Motorola, however, the stylish gas displays may be adorning many a clock or counter in the near future. I am referring to the MC3491 cathode driver and the MC3490 and MC3494 anode drivers which take all the high voltage donkey work out of powering a gas discharge display.
The MC3491 contains eight high voltage drivers for connection to the seven cathode segments and decimal point of a display, and each of these drivers acts as a programmed current source to produce optimum display results.
The MC3490 and MC3494 are complementary to the MC3491 and are intended to control the anodes of a time shared display system. The MC3490 accepts a positive going input from the scan circuits while the MC3494 is designed for negative going inputs. All three devices are very versatile in use and can be used in CMOS and MOS systems.


THIS article describes an optically coupled r.p.m. meter designed to measure the propeller speed of model aircraft engines. The circuit which is intended for use with two bladed propellers has two switched ranges of $0-10,000$ r.p.m. and $0-25.000$ r.p.m. in order to cover the normal operational range of these engines

The meter may be used by aeromodellers to pick the optimum diameter and pitch of propeller for a particular engine. This is done by checking if the engine will run with a particular propeller at the r.p.m. where it produces its maximum power output.

Also as many modellers prefer to mix their own fuels, this meter would be extremely useful in evaluating an engine's performance on the fuel. For instance, the performance of common fuel mixes is very dependent on the percentage of nitrate additive used. However, this additive is quite expensive and in excess can reduce the operational life of an engine considerably. So by measuring the r.p.m. of an engine for different fuels a compromise between cost and performance may be found.

## BASIC PRINCIPLES

To measure the speed of an engine an MS4A photodiode is positioned facing the rotating propeller. The output of the photodiode will be a steady d.c. level (due to ambient lighting conditions) added to which is a pulsing voltage caused by the blades of the propeller. These pulses may be of either polarity, for instance if the background
lighting behind the propeller is high then a negative pulse will be given every time a propeller blade interrupts the light falling on the photodiode.

However, if the propeller reflects more light on to the photodiode than it cuts off then the pulses will be positive going. As both these conditions may occur in practice the following stages of the instrument must be able to handle them.

These input pulses are passed through an a.c. amplifier which is followed by a limiting amplifier The object of these stages is to provide the constant amplitude pulses that are required by the type of pulse counter used in this circuit. The limiting amplifier is followed by a pulse counter which drives a 1 mA movement meter. The two r.p.m. ranges of the instrument are obtained by applying different shunt resistors across the meter to vary its sensitivity

## CIRCUIT OPERATION

The input voltage produced by the photodiode D1 (Fig. 1) is coupled via Cl to the base of TR1. TR1 is biased as a normal amplifier and has a voltage gain of approximately 120 . The collector of TR1 is coupled to the base of TR2 via C2. TR2 is biased via R3 so that its collector is normally at zero volts.

For negative going input pulses, the pulses on the collector of TR1 will be positive going. Since TR2 is already saturated a pulse cannot turn it on any harder but it will charge up C2 so that when the pulse finishes and the voltage on the collector of

TR1 falls to its quiescent level, TR2's base will be taken low enough for the transistor to turn off. At high speeds C2 must charge sufficiently' in a short time and so the value of C 2 is kept small.
For positive going pulses the operation of TRI and TR2 is more straightforward. The pulses appearing at the collector of TR1 will now be negative going and so will switch TR2 off and on without substantial charging of C2.

When TR2 turns off, its collector voltage rises up to about 5.6 V where it is restricted from further increase by the Zener diode D2. Thus for normal inputs the pulse height at the collector of TR2 is limited to 5.6 V and is essentially unaffected by supply and input level variations. Now when the voltage on the collector of TR2 is low, the junction of the emitters of TR3 and TR4 is held at about 0.6 V by TR4 and so C5 has a charge on it given by CV.

Since a two bladed propeller at 10,000 r.p.m. gives one pulse every 3 ms ;

$$
I \bumpeq \frac{1 \times 10^{-6} \times 4.4}{3 \times 10^{-3}} \bumpeq 1.47 \mathrm{~mA}
$$

Thus for a 1 mA meter to read full scale at 10,000 r.p.m. the resistor VRI must be adjusted to shunt away 0.47 mA .

For measuring propeller speeds up to 25,000 r.p.m., an extra shunt resistor VR2 is switched across the meter via $\$ 2$.

Although the values of VR1 and VR2 were chosen to facilitate easy calibration of the instrument with the recommended meter, they will allow a wide range of meter movements to be used successfully although they may be a little difficult to set up. A table giving usable movements is given.


Fig. 1. Circuit of the r.p.m. meter

When TR2 collector voltage rises to 5.6 V , TR3 partly discharges C 5 by bringing the junction of emitters up to about 5 V and so the charge on C5 changes by $C\left(V_{2}-V_{1}\right)$ Coulombs. This change of charge is independent of normal input pulse widths and rise times and is only determined by the pulse height which is fixed by D2, and by C5 which should be of paper construction for high stability.

The charge lost by C5 is passed on to C4 via TR3 in the form of high current pulses. The accuracy of the instrument would be impaired if TR3 were allowed to saturate during these pulses and so the internal resistance of C4 must be low enough to eliminate the possibility of this occurring. To this end a high value capacitor was used for C 4 as these tend to have lower internal resistances than their low value counterparts of similar voltage ratings.

The charge built up on C4 is bled away by the 1 mA meter and its shunts at a rate dependent on the charge and since this charge is related to the pulse rate per second the meter reading will indicate the same.
The current fed to the meter circuitry is given by-

$$
\mathrm{I}=\frac{\mathrm{CV}}{\mathrm{~T}}
$$

where CV is the change of charge on C5 per pulse and T is the period between pulses.

Table 1: Usable movements

| Meter Sensitivity | Maximum Meter Resistance |
| :---: | :---: |
| 1 mA | $175 \Omega$ |
| $250 \mu \mathrm{~A}$ | $2 \cdot 2 \mathrm{k} \Omega$ |
| $100 \mu \mathrm{~A}$ | $6 \mathrm{k} \Omega$ |
| $50 \mu \mathrm{~A}$ | $12 \mathrm{k} \Omega$ |

It is possible to use a 1 mA meter movement whose resistance is as high as 820 ohms if the value of VRI is increased to 4.7 kilohms and VR2 to 470 ohms.
The accuracy and high frequency stability of the circuit when operated with poor batteries or in low light levels are ensured by C3 and C6.
The finished circuit draws a supply current of about 10 mA and so will operate for many hours when powered by an Ever Ready PP3 battery. The batteries should be replaced when their output voltage falls below 7 V as the calibration of the instrument may become inaccurate.


Photo showing assembly. The p.c.b. and component arrangement are shown below

## COMPONENTS

Resistors
R1 220ks
$\left.\begin{array}{ll}\text { R2 } & 1 \cdot 2 \mathrm{k} \Omega \\ \text { R3 } & 220 \mathrm{k} \Omega\end{array}\right\} \frac{1}{2} \mathrm{~W} 10 \%$

R4 $2.2 \mathrm{k} \Omega$
VR1 $1 \mathrm{k} \Omega$
VR2 $220 \Omega$
Semiconductors
TR1 ZTX107
TR2 ZTX107
TR3 ZTX107
TR4 ZTX212
D1 MS4A.
D2 KSO56A
Potentiometers
VR1 $1 \mathrm{k} \Omega$
VR2 $220 \Omega$
Capacitors
C1 $22 \mu \mathrm{~F} 10 \mathrm{~V}$ Electrolytic
C2 $1 \mu \mathrm{~F} 10 \mathrm{~V}$ Electrolytic
C3 $0.01 \mu \mathrm{~F} 18 \mathrm{~V}$ Disc
C4 $47 \mu \mathrm{~F} 10 \mathrm{~V}$ Electrolytic
C5 $1 \mu \mathrm{~F} 63 \mathrm{~V}$ Paper
C6 $47 \mu \mathrm{~F}$ 10V Electrolytic

## Miscellaneous

M1 1mA "Sew" Panel Meter Type SD460.
S1 Single Pole Change Over Switch "Subminature Type".
S2 As Above.
B1 Ever Ready Battery Type PP3.
Instrument Case Size $10 \times 10 \times 5 \mathrm{cms}$.
P.C. Copper Laminate Size $4 \times 7 \mathrm{cms}$.



Fig. 2 Component layout and p.c.b. master

## CONSTRUCTION

The prototype meter was constructed using the printed circuit shown in Fig. 2. To make the printed circuit first cut out the correct size of copper laminate board and on the copper side, lightly mark out the desired hole points with a scribe or a centre punch. Then with suitable paint and a fine brush put blobs of paint on the punch marks and join these up to the pattern shown with fine straight lines. This method of painting the printed circuit can give very neat results if done carefully. Allow the paint to dry and then drop the board into a solution of ferric chloride to etch away the unwanted copper. When the etching process is complete, clean off the paint from the remaining copper and drill the holes for the components.

The printed circuit, photodiode, meter and battery were all mounted in an instrument case size $10 \mathrm{~cm} \times$ $10 \mathrm{~cm} \times 5 \mathrm{~cm}$, although if a larger meter is used the case size should be adjusted to suit. With the meter mounted on the top of the case, the photodiode should be positioned on the side of the case above the meter so that when the photodiode is held near a propeller, the meter will be lying face up and easy to read. To mount the photodiode, cut a 1 cm square hole in the desired position on the instrument case, then on the inside of the case glue a perspex window over the hole to seal the case against the ingress of oil and dirt. Then place the photodiode against the inside of the window and glue the wires of the device to the perspex with an impact adhesive to hold the device firm. Cut off the excess wire from the photodiode and connect it to the circuit board via some suitably fine, flexible wires, taking care to get the polarity correct.

## TEST AND CALIBRATION

Once the circuit is complete and connected to its battery, the following voltages may be measured with respect to the negative rail whilst the photodiode is momentarily covered up. TRIc 4.5 V $\pm 2 \mathrm{~V}, ~ T R 2 \mathrm{c}-0.1 \mathrm{~V}$.
Also, shorting the base of TR2 to earth should cause its collector voltage to rise to approximately 5.5 V . If there is any serious discrepancy in any of the above test voltages, check possible faults such as incorrect component location, short circuits, dry joints, etc.

Once the constructor is satisfied with the above checks the instrument may be calibrated. A simple way of calibrating the meter is to make use of the 100 Hz flcker given off by mains fed lamps since, this corresponds to an engine speed of $3,000 \mathrm{r} . \mathrm{p} . \mathrm{m}$. Hold the instrument within a foot or so of a 100 W lamp and adjust VRI until the meter reads 3,000 r.p.m. on the 10,000 r.p.m. range, then after switching S 2 on adjust VR2 until the meter reads correctly on the 25,000 r.p.m. range. The accuracy of this method is limited due to the small meter deflection at the calibration point.

If a signal generator and an l.e.d. are available the instrument may be accurately calibrated. The generator must provide an output greater than 2 V r.m.s. between 100 and $1,000 \mathrm{~Hz}$.

Connect the circuit shown in Fig. 4 to the output of the generator and place the l.e.d. near the photodiode, being careful to shield out any background lighting as this way contain 100 Hz flicker if any mains fed lamps are near.


Fig. 3. Additional components needed for calibration

Set the signal generator to 333 Hz and adjust VRI until the instrument reads full scale on the 10.000 r.p.m. range, then raise the frequency to 833 Hz and adjust VR2 for full scale deflection on the 25,000 r.p.m. range. The accuracy of calibration achieved by this method will depend on the signal generator itself.

## USING THE R.P.M. METER

Once the instrument is calibrated, all that is necessary to determine the r.p.m. of an engine is to hold the meter a few inches away from its propeller and read off the speed. If an erratic reading is obtained, try moving the meter nearer the propeller or more adjacent to the end of the propeller blades.

If the propeller has more than two blades, the true engine speed may be obtained by using the following formula.

True r.p.m. $=\frac{2 \times \text { instrument reading }}{\text { number of blades on propeller }}$



TRANSISTOR ELECTRONIC ORGANS FOR THE AMATEUR
By Alan Douglas and S. Astley Published by Pitman Publishing 119 pages, $215 \mathrm{~mm} \times 140 \mathrm{~mm}$. Price $\mathbf{£ 4 . 5 0}$

THIS is the third and entirely revised edition on the "naming of "parts" and circuitry of small home entertainment organs. In previous editions complete constructional details of a transistorised two manual pedal organ was given. With the appearance of integrated circuits-which has meant integrated oscillators and divider systems, distribution gates, amplifiers and rhythm chips-the discrete transistor divider organ is almost an anachronism and for this reason I think the omission is justified.
Unfortunately there is a dearth of books on topical organ circuitry, unless you are fortunate enough to be on the mailing list of companies such as ITT, G.I. or AMI whose ambit of activities embrace organ electronics. This edition fulfils a need in that it covers most aspects of the modern electronic organ combining circuitry, informed opinion and practical advice which one would expect from the co-author Alan Douglas, doyen of the Electronics Organ Constructors Society.
G.G.

## ELECTRONIC TEST EQUIPMENT

By Harry Kitchen
Published by Argus Books
199 pages, $225 \mathrm{~mm} \times 140 \mathrm{~mm}$. Price $£ 4.50$

TEST equipment has a vital role in electronics. In design, construction, routine maintenance and trouble shooting, one is wholly dependent on the means to measure, as this is the only way to interpret conditions within a circuit.

The range of test gear available is enormous, varying in sophistication with requirement. But even the most well equipped laboratory has a nucleus from which it has grown and the instruments that usually constitute this are basically the content of this book, namely the multimeter, electronics meter, a.f. and r.f. oscillator and the oscilloscope.

As one would expect from an author who has been a regular contributor to both this magazine and to Practical Wireless, his advice on choosing and using these instruments is essentially practical.

In each of the six chapters the principles and common basic circuitry of an instrument is presented. This is distilled with the wisdom of years of experience and many of the personal choices of instrument would prove an excellent basis for a home workshop or laboratory.

## ABC OF HI-FI <br> By John Earl <br> Published by Fountain Press Argus Books <br> 168 pages, $222 \mathrm{~mm} \times 140 \mathrm{~mm}$. Price $£ 3.75$

A Good book for those starting to learn about hi-fi. It contains a wealth of potted information relating to techniques and equipment as well as to the effects they both are employed to produce or to avoid. Most of the vocabulary the budding enthusiast must learn is
to be found within these pages. The contents are subdivided into seven chapters, each dealing with a clearly defined subject area: amplifiers, loudspeakers, programme sources and signals. quadraphony, radio tuners and aerials. recording and replay, sound and room acoustics.

The one comparatively new area is quadraphony. This chapter alone might make the book a useful purchase for followers of the art who while, perhaps, well versed in the more orthodox techniques are yet in some ignorance of the considerable enrichment of the hi-fi language this latest development has been responsible for.

Within each chapter subjects are arranged alphabetically; they range from the mundane ("Hum". "Downlead") to the more exotic ("ambiophony". "Dolby Noise Reduction"); treatment varies from the short sentence to a page or more per item. Diagrams, photographs and oscillograms are used to illustrate the text. which has a down-to-earth and practical flavour.
D.D.R.

## PHYSICAL ELECTRONICS

## By J. Seymour

## Published by Pitman Paperbacks

438 pages, $150 \mathrm{~mm} \times 210 \mathrm{~mm}$. Price $£ 3.25$

AImed at students in electronic engineering and young professional engineers, the book manages to cover this sometimes heavy subject of the physics of electronics without becoming too bogged down-a trap all too many authors often find themselves falling into.

The student will find the book extremely useful as a course book, and the material in it relevant to all three years' work at his university or college. Also, due to its having all the basic information on the functioning of all the popularly used devices, it will provide a good reference for engineers who occasionally need to resort to a textbook, because "they remember doing how an f.e.t. (for instance) works a few years back, but can't quite remember the details".

The book has chapters on microwave devices, junction transistors, field-effect devices, masers and lasers and a comprehensive appendix which expands on some of the theory referred to throughout the rest of the book.
R.W.L.

## I.E.R.E. JOURNAL, GOLDEN JUBILEE ISSUE

Followers of electronics, amateur as well as professional. will find much that is both technically and historically interesting in this Golden Jubilee issue of the I.E.R.E. Journal. The Radio Electronic Enpineer (Vol. 45. No. 10. October 1975).
It contains 19 papers from institute members who recall developments in a variety of fields over the last 50 years. A wide panorama of technological progress and achievement is built up by this collection of authoritative papers.

Some of the areas reviewed are: semiconductor devices; electronic components; lasers and optical electronics; fixed and mobile communications; television; radio navigation aids; computer engineering; electronics and nuclear power: ocean technology; medical electronics; electronics in space.

The Radio and Electronic Engineer is normally available only to members of the I.E.R.E. but copies of this issue can be purchased by interested non-members for $£ 2.50$ per copy. Send remittance to The Institution of Electronic and Radio Engineers, 8-9 Bedford Square. London WICB 3RG.


## HAPPY NEW YEAR

While the great buying public is still tightening its belt in face of economic problems at home, with consequent slackness in consumer electronics business, 1976 looks like being a busy and therefore happy year for manufacturers of professional equipment.

The United States is already past the bottom and what's good for the United States is generally good for the rest of the free world. Recent market surveys forecast accelerating business in the first quarter of 1976 with some market sectors showing gains of as much as 25 per cent.

In Britain, leading capital goods exporters are still doing excellent business with substantial backlogs of order due for delivery. A star export performer is the EMIScanner X-ray equipment but there are plenty of other big sellers. Redifon, for example, recently clinched another flight simulator order, this time for a $£ 3$ million installation in Iraq. Radio communications is still strong with Marconi and Racal leading the field.

Aerospace is doing better than ever with record exports of missiles, principally the Rapier, which in its later version has a strong electronic content, and service companies like International Aeradio are still pulling in extra business like the re-equipment of Sharjah International Airport with navigational aids and other equipment under a $£ 1.2$ million contract. In telecommunications GEC won a South African Post Office contract for stored programme control equipment worth $£ 2.5$ million.

At home there is optimism that recent pronouncements by the Government on giving encouragement to expansion of private industry really mean what they say and are not mere window-dressing.

If the regeneration of British industry means anything at all it means modernisation, including higher levels of automation which, in turn, means more business for electronics.

Consumer electronics may be down in the dumps at the moment but cannot be neglected. An interesting pointer is that Plesseyowned Garrard Engineering Ltd. has been recruiting design engineers for what is described as the "next generation of consumer electronics'. To date, Garrard has been exclusively in the record changer business. There is now a big programme of diversification and expansion into both audio and video cassettes and complete equipments such as home music centres and video recorders.

We also have the example of Sinclair Radionics whose longawaited thrust into the digital watch market is now with us. The "Black Watch" got away to a flying start with a $£ 30,000$ national advertising campaign and production is now running at 5,000 units a week in a bid to capture 30 per cent ( 250,000 units) of the total UK market in 1976.

## BATTERY POWER

The electric car has come back. Yes, back! The first electric horseless carriage ran in Britain long before the turn of the century and there were electric taxi-cabs running in London from 1897 to 1899.

Electric road traction is more common in Britain than most people imagine. Present estimates are that there are some 50,000 electrically driven commercial vehicles on the roads, about half the world's total. In addition there are some 75,000 electrically powered trucks and mobile hoists in use in British industry. So there is already a firm home base on which to build a huge export market.

The problem is the same today as it was at the turn of the cen-tury-the battery. Front runner for the new generation of electric cars is the sodium-sulphur battery which, weight-for-weight, is claimed to store five times more energy than the conventional lead-acid battery and, of course, it is the power/weight ratio which has always been the stumbling block.
Now the U.K. Atomic Energy Authority, the British Railways Board, Chloride Silent Power Ltd., and the Departments of Industry and Environment have come together in a massive assault on the problem. Research will continue separately at the laboratories of UKAEA, British Rail and Chloride, but results will be pooled and coordinated to eliminate duplication of effort.

Similar work is going on in the United States. The Energy Research Laboratories of Gould Inc. are
working on several battery projects. A nickel-zinc battery twice as good as lead-acid is already in use in U.S. Post Office delivery vehicles. A Government-funded project for a battery equivalent in performance to the British sodium-sulphur project is expected to be ready for prototype trials by 1981.

British Rail, however, plan to have a rail car powered by sodiumsulphur batteries running by 1979. It could, on present projection, have a range of 170 miles with a top speed of 75 mph . R\&D time scales tend to be elastic but if all goes well with the British effort it suggests a two-year lead.

If the British project proves successful it will not only generate direct product sales but will also bring in "invisible exports" through world licensing agreements.

The prospect of millions of electric cars on the world's roads is heartening for the electronics industry because of all the electronic control gear which is needed.

## COMPUTERS

The 1975 Marketing Award from the Institute of Marketing went to International Computers Ltd for its strategy in selling the 2903 computer. In the first two years after the launch. ICL sold over a thousand and the 2903 has now established itself as Europe's best selling computer.

Apart from completely new business (i.e. first-time computer buyers), some 200 existing computer users switched to the 2903 with the bulk of the displaced computers, according to ICL, being of IBM manufacture. Total sales of the 2903 are approaching 1,500 with 50 per cent being exported.

As part of a forward research programme, $I C L$ is setting up a pilot plant for the development of advanced technology l.s.i. circuits. The company says it has no intention of going into volume production and the plant will be used mainly for design speed-up and, thus, earlier exploitation of the new and powerful semiconductor technologies now becoming available.

## MPU TAKEOVER IN 1984

By 1984, the year we all love to hate, microcomputer sales in Western Europe will have multiplied 60 -fold according to marketresearchers Frost \& Sullivan. Total sales for 1984 will be $£ 307$ million compared with $£ 5$ million in 1974. Over the decade sales will have topped $£ 1,200$ million with Britain, France and Germany accounting for two-thirds of the European market.

As the MPU will find its main application in "smart" and "intelligent' equipment, it looks as if Orwell's terrifying vision of things to come may well come true if we substitute MPU for Big Brother.


## SURROUND SOUND MATRIX

WITH the increasing popularity of four channel audio systems, one requires also a mode whereby stereo records can be played as well.

The circuit of Fig. 1 is a very simple matrix providing surround sound from any stereo source. The inputs, front-right ( $R$ ) and frontleft ( L ), are derived from the preamplifier of the stereo amplifier (preferably, after the volume and tone controls, so that these act as common controls for the whole of the system, but before the balance control).

The outputs, rear-right ( $R r$ ) and rear-left ( $R 1$ ), are taken through the rear level controls VR2 and VR3 to an additional.stereo power amplifier, and subsequently fed to the rear speakers.
The operation of the circuit is such that when the Depth Control.

VRIa/b, is at minimum, the rear outputs follow the front signals at $180^{\circ}$ phase shift $(R r=-\mathrm{R} ; R 1$ $=-\mathrm{L}$ ), since IC1 and IC2 are working as virtual earth inverting amplifiers. When VRI is at maximum. ICi and IC2 are working as differential amplifiers, and so the rear signals are subtractions of each other $(R)=\mathrm{L}-\mathrm{R} ; R 1=\mathrm{R}-$ L ), thereby reproducing only the out of phase signal of the stereo
information which is rich in ambient content.

In use, the setting of VRI is dependent on personal requirements and the type of material played. The outputs can be expressed generally as: $\operatorname{Rr}=-(\mathrm{R}-\mathrm{XL})$ and $R I=-(\mathrm{L}-\mathrm{xR})$, where x is a fraction dependent on the setting of VRI and can be anything from 0 to 1 .

The maximum setting is not always the best; for example, the out of phase signal of worn records is very noisy and distorted. There should be no output on playing a mono record when VRI is at maximum setting, since $L$ equals $R$, in this case VRI should clearly be set to minimum.

With the resistor values shown. the voltage gain is unity. Should some gain be required, resistors R1, R2, R4 and R5 should be reduced in value according to the formula: gain $=\frac{\mathrm{R} 3}{\mathrm{R} 1}$ or $\frac{\mathrm{R} 6}{\mathrm{R} 4}$. These four input resistors should be all of the same value. The value of the feedback resistors R3 and R6 must be the same as VRla/b; these should be maintained as specified. VRI is a dual-gang linear 100k』 + 100k! potentiometer.
M. Greenfeld,

Leeds.

## RETURN OF POST MAIL ORDER SERVICE

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## SIMPLE TOUCH SWITGH

THIS circuit was developed for switching a bedside light on/ off by touching a plate fixed to bed head.

The plate used is a piece of 0.1 inch Veroboard connected as shown in Fig. 1. When the finger is placed on the Veroboard grid. CSR! fires and pulls in the relay. If the finger is then removed, CSR 1 will remain locked on until touched again.

When in off state all transistors are non-conducting; Cl is charged via D3: On touching the grid, TRI draws base current via TR2, TR3, D1 thus firing CSR 1 , switching on load and making a discharge path for Cl .

When touched to turn off, Cl discharges via D4 and R1, turning on TR4. TR3 draws base currenit via TR4 and D2, turning on TR2 and shorting out CSR1. When the

finger is removed the load is switched off.

If the finger is not removed immediately after the load has
switched on, and Cl has had time to discharge, the load will switch of when the finger is removed.
R. J. Hicks,

Madynlleth, Powys.

## BETTER FIGURES



Fig. 1h


Fig. 2


HE display usually obtained from the numerals 6 and 9 with standard ITL decoders such as the SN7447 is shown in Fig. 1a. This can be modified to a more visual numerical readout (Fig. ib) by using the circuit in Fig. 2. To obtain the improved numerals. segment $A$ must come on when 6 is present in BCD at the decoder input, and segment $D$ must come on when 9 is present.
Transistors TR1 and TR2 are connected between 0 V and the decoder output to form, in effect. "wired-AND" gates with the output. When TR1 or TR2 are on the appropriate segment will light. irrespective of the decoder output. The transistors are fed via TTL AND gates, which detect 6's or 9's at the decoder input.
The AND gates in the prototype were $\frac{2}{3}$ of a 7411. The circuit therefore needs two SN7411's per three displays. TR1 and TR2 can be any silicon $n p n$ transistors with an $\mathrm{I}_{\mathrm{E}} \max >3 \mathrm{~mA}$.

If the digit blanking facilities of the decoder are needed to provide control over the display brightness, the 7411 can be replaced by a 7421. as shown in Fig. 3. This enables the segments turned on by the external transistors to be blanked with the rest of the decoder outputs.

R. Mortimer. Hemel Hempstead.

Fig. 3

NIGHT LIGHT


Fig. Iu


Fig. 2u


Fig. Ib


Fig. 2b

As 1 have a young son who refuses to go to sleep without a light on. I fitted a dimmer to the landing light so that no direct light was thrown into his bedroom.
With the ever increasing cost of electricity, a dimmer seemed to offer a simple compromise. Unfortunately. the landing light is controlled by a two-way switch. one in the hallway and the other on the landing, and 1 wanted to be able to control the light level and also be able to override the dimmer from the hallway irrespective of whether the light was on or off.
To overcome these problems I modified the wiring as shown in Fig. la, $\mathbf{b}$ and modified the dimmer as shown in Figs. 2a. b.
The dimmer is modified to bring both arms of the potentiometer out to separate connections and the value raised so that the light was fully off at the centre of its travel. This was to avoid the light from extinguishing at other levels than full on. when using the hall switch.
T. L. Bunney.

Hadleigh
Essex.

## CAR THEFT ALARM

THe NE556 dual timer (which contains two 555 circuits in a single 14 -pin dual-in-line package) can be used in the simple circuit shown as a car theft alarm.

If any of the car door switches S2 to S 5 is opened, the capacitor C 2 commences to charge through R3. After a time which is approximately $1.1 C 2 R 3$ ( 7.5 seconds with the values shown), the output voltage at pin 9 falls to a value which is only a little above that of the negative line. $A$ current of the order of 30 mA therefore flows through R2 and saturates TR1. If the voltage at pin 5 is low. the relay RLI closes.

The contacts of this relay RLAI short the collector of TRI to its emitter so that the relay "latcheson": that is, it remains energised whilst the potential at pin 5 is low no matter whether the voltage at pin 9 rises again (due to the closing of the car door) or not. The closing of the contacts RLB1 causes the car horn to sound.
The hidden switch $S$ is used to set the alarm and to dis-arm it when one wishes to leave the car doors open. If Sl is closed. Cl is


Fig. 1
discharged and the voltage at the output of pin 5 is kept almost ait the +12 V devel. The relay will therefore not close when Sl is in this closed position. If the alarm is sounding, it may be stopped by closing S 1 so that the voltage at pin 5 rises.
As the owner leaves the vehicle, he can set the alarm by opening Si so that C 1 commences to charge. If the shuts all of the car doors
within a time of $1 \cdot \mid C / R /$ (or about 16 seconds with the component values shown), the relay will not close and the horn will not sound.
Similarly, when the owner returns to the vehicle, he opens a door and C 2 commences to charge. However. if he closes SI within about $7 \cdot 4$ seconds of opening the door, the alarm will not sound.
J. Dance.

Alcester. Warks.

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| 4.0 0 FF 40 V | 70 | $100 \mu \mathrm{~F} 63 \mathrm{~V}$ | $17 p$ |
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| $32 \mu \mathrm{~F} 63 \mathrm{~V}$ | $7 p$ | $1500 \mu \mathrm{~F} 6.4$ | V25p |
| $47 \mu \mathrm{~F}$ 10V | $7 p$ | $1500 \mu \mathrm{~F} 16 \mathrm{~V}$ | 28p |
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## audio signalling

THE need often arises for the audible signalling of a systen) condition such as excessive load current or excess heatsink temperature. A 555 oscillator/timer directly driving a 30 ohm balanced armature earphone makes a simple and convenient tone-generator; the only snag is that the 555 produces a steady tone that does not stand out well from background noise. An intermittent tone of the same loudness is much better in this respect.

An easy way to do this is to use a second 555 to gate the reset input of the first 555 . but the circuit in Fig. I shows how this can be done even more simply with a few extra passive con-ponents.
With the aid of R1. R2 and C1, ICI oscillates at about 2.2 kHz in a conventional manner driving the earphone. Capacitors C2. C4 and germanium diodes D1, D2 form a pump circuit and the lower end of C4 becomes progressively negative


Fig. 1
with respect to the $0 V$ rail. When this voltage reaches a certain value. the 555 is reset via pin 4 of IC1. This state of affairs lasts until the charge on C4 and C5 decays through R4. whereupon the 555 gives another burst of oscillation.

With the component values shown in the circuit, the 2.2 kHz
tone is interrupted about 15 times per second. With a 12 V supply, as in a car environment, this becomes 8 times per second. A wide vatiation of timing is possible by varying the value of C 5 .
D. R. G Self.

Ipswich.
Suffolk.

## CYCLE LIGHTING CONTROL

With the further restrictions recently announced on the consumption of energy and the ever increasing expense of private motoring, many people are reverting to that more efficient. yet slightly less comfortable mode of transport. the bicycle.

On dark nights and stop-start conditions in heavy traffic. efficient lights on a bicycle at all times is vital. Continuous lighting can be obtained using all-battery lamps with the frequent expense of battery renewal. Dynamo lighting obviates this expense but suffers from the disadvantage that the intensity of light varies from full brilliance down to a dim glow at walking pace and none when stopped (Fig. 1).

It was with this in mind that 1 devised the circuit in Fig. 2 to combine the advantages of both systems.

In the modified lighting circuit. the alternator output is rectified by the bridge circuit D1. D2. D3 and D4 and feeds the cycle lighting via switch SI.
With an alternator output of less than the battery voltage of 4.5 V (a 6V supply could be used but would result in shorter bulb life). the battery provides the lighting power via D5, D1-D4 preventing it from shorting through the alternator windings.

When the output rises above 4.5 V this takes over the supply to
the lamps. DS preventing the rectified alternator output from flowing into the battery.

The diodes D1-D5 can be any silicon rectifiers rated at $\frac{1}{2} \mathrm{~A}$ or more at 12 V minimum the off-load alternator output often rises to this value at speed). DI-D4 can be mounted in a plastics tube clipped to the cycle frame. the ends sealed with wax. no significant heat being generated; the total lighting load is only about 5-6W. D5 can be mounted in the lead from the bat-
tery which can be mounted in a weatherproof box behind the seat or attached to the frame.

Only cycles with the integral Dynohub type alternator are suitable for this modification: the rim type friction dynamo is usually earthed to the frame by its fixing bracket and would need to be insulated on its mounting before connecting to the rectifier.
A. R. G. Culder.

Leigh.

Fig. 2


## charger for nickelCADMIUM CELLS

Readers might be interested in this automatic battery charger circuit, for use in tape recorders and other equipment operating from nickel-cadmium accumulators.

Operation of the circuit (Fig. 1) is as follows. The cells charge at a rate set by R1 until, as determined by the setting of VR1, the Zener DI conducts, switching CSR1. This reverse-biases D2, stopping the charge. The l.e.d. also goes out. indicating end of charge.


Fig. 1

The circuit will not attempt to charge dry cells put in the unit due to the higher voltage of these. This unit can be used in addition to any mains supply supplying the tape recorder direct.

The value of Rl depends on rate of charge required.
D. Torry,

Chelmsford, Essex.

## "INSTANT" DIGITAL STOP WATCH CONVERSION

THE following simple modification enables a digital clock to be utilised as a "stop watch" as and when required. and was originally conceived for the purpose of timing international STD ielephone calls. No doubt other uses will suggest themselves to readers and there is scope for variations of the basic idea employed

Fig. I shows a simplified block diagram of a typical 4 -digit clock using integrated circuits of the 74 or similar series. A 5 -pin DIN socket is fitted at the rear of the clock and a miniature 6 V relay with two sets of changeover contacts is incorporated within the clock housing: these being wired up in accordance with Fig. 2. A remote switch is connected by means of a suitable length of 2 -core screened cable to a 5 -pin DIN plug as shown in Fig. 3.

When the "stop watch" facility is required. PLI is inserted with SI open. and the clock is set to zero by switching the mains supply off and on. The shorting link across pins 1 and 4 of PL! causes the relay coil to be energised and the two sets of contacts to change over. One set of contacts prevents the 50 Hz input from reaching IC1 until S 1 is closed. while the other set changes the clock format from hours and minutes to minutes and seconds.
The clock is therefore held at zero until timing is required to commence. SI is then closed and the clock begins counting minutes and seconds. to a maximum of 12 or 24 minutes, depending upon the original design. When the event being timed is completed, SI is opened and the elapsed time will be held on the display until reset to zero by switching the mains supply off
and on. Alternatively, a reset might be incorporated in the clock by connecting a normally closed pushbutton in series with the mains supply.

The relay used in the prototype was found to "pull in" satisfactorily with the coil connected to the +5 V rail, but should any difficulty be encountered in this respect. it could be connected to the unstabilised side of the low voltage supply. through a dropper resistor if necessary.
With regard to S1, a push-on/ push-off mains type pendant switch
was used as this can be held in the same hand as a telephone handset. Very little trouble with contact bounce has been encountered. but "noiseless" type switching circuitry could of course be employed if a higher degree of accuracy is required.

In conclusion it must be emphasised that the remote switch lead be screened as otherwise hum pickup can cause an erratic count to continue despite SI being open.
A. F. Hayden Brighton.


Fig. 2


Fig. 3

# PRTENIE 

## VEHCLE MONTTORING

A system for the remote display of vehicle dashboard information on panels arranged around the rear view mirror is claimed by Regie National Des Usines Renault and Automobiles Peugeot in BP 1401356 . The intention, of course, is to enable a driver to observe readings on speed, fuel level, water temperature and so on, without taking his eyes off the road.

The rear view mirror surround is provided with a l.e.d. or liquid crystal digital display, Fig. 1. A
series of probes convert into variable voltages the measures of all the engine and vehicle functions.

Normally the display shows vehicle speed as sensed by the speed sensor. However, when the driver actuates a pushbutton on the dashboard a master clock starts switching the measure sequences at adjustable time intervals, Fig. 2.

The voltage signals of the successive sequences are fed to a digital voltmeter, the output pulses of which are fed, via the switching system, to a pulse counter which displays them in succession at predetermined time intervals. Simul-
taneously, the display switching system successively illuminates a series of pilot lights around the display to denote the engine function being measured.

After the full sequence of engine functions have been displayed, the sequence switching system again transmits, for display, a measure of the engine's speed as relayed from the speed sensor.

An alarm system continually compares measured voltages with reference voltages and energises an audible or visual signal if a predetermined threshold is exceeded for any measurement.


Fig. 1


## IN BRIEF

BP 1404 007-R. F. Koch, Treating an a.c. signal to produce a modified a.c. signal. A technique for compressing data signals, such as speech signals, and segmenting them to produce an output which is compressed in time but not distorted in pitch. Possibly useful for talking books for the blind.

BP 1404 634-Messerschmitt-Bolkow-Blohm GmbH. Measuring and indicating distance between a motor vehicle and an obstacle. A pulse radar transmitter and receiver system for use on motor vehicles.

BP 1407 761-Burroughs Corporation. Problem oriented language translator and source code generator. Interesting on two counts. Firstly, in that it shows the extent to which ideas in computer programming can now be patented, and secondly, because the patent covers an interesting system for simplifying the production of problem orientated language ( POL ).

BP 1409 343—A. Pirc. Rotary magnetic engine. An engine which relies on the reversible magnetic change which occurs in some materials at the Curie point.

BP 1409 504—A. K. Thatcher. Computer controlled sonic fuel system. Variable fuel pulses are supplied to an air system of an internal combustion engine.

Readers requiring a reply to any letter must include a stamped addressed envelope. We regret that we cannot answer any technical queries on the telephone.

## A Protest

Sir-As a professional in the electronics trade for some 18 years, 1 feel that 1 must finally protest about unnecessary projects appearing in your magazine.

The best example to date appears in the December edition. To replace a simple, efficient, usually reliable and inexpensive bi-metal strip bulb by a sophisticated piece of electronic gadgetry using four transistors, a CMOS integrated circuit four diodes plus other electronic components seems to be the height of lunacy.

As you know, there are people who scoff that many pieces of electronic equipment are unnecessary and their functions could be just as easily performed by their mechanical counterpart more cheaply. It is just this type of project which adds weight to their argument.

B. Timson,

Beds.

## Highly Tuned

Sir-l think 1 can assist you regarding motor cycle electrics and ignition systems in particular (see Readout, December 1975 issue).

The small bikes, i.e. those below about 100 cc use flywheel magnets. Most of these are single cylinder engines ( 2 -stroke or 4 -stroke) and in many cases the timing is adjusted by altering the contact breaker gap. In this case the dwell angle cannot the quoted but a stroboscope is absolutely essential to use with an $F$ mark on the flywheel. Usually no auto advance units are fitted.

Above 100 cc there are the single cylinder and twin cylinder engines. These use a conventional 6 V or 12 V Kettering ignition system. The big difference is that on some engines a double ended coil is used which, with a four stroke engine, produces an idle spark. On these engines it is essential to check that
ignition timing is identical on each cylinder.

With other twin cylinder engines a separate ignition system is used for each cylinder and this also applies to the three cylinder type mentioned in Mr Simpson's letter. Four cylinder engines normally used two double ended coils with, of course. two contact breakers, but there have been a few exceptions which used a distributor.

This by no means exhausts the list as some competition machines use what is called an energy transformer ignition system which relies on an alternator feeding a.c. to a special type of coil. In this system the peak of the a.c. waveform must coincide with the opening of the contacts which in turn must be correct for the engine. Added to this is a system which uses the rectified output from the alternator to charge up a large electrolytic. This capacitor is discharged through the coil by the contact breaker.

In using a large variety of commercial test equipment on these engines I have found the following limitations:

1. Restricted ranges.
2. Some cannot be used when powered from vehicle battery.
3. Cannot be used on 6 volt and 12 volt systems.
4. Lack of application data, etc.

In regard to your "Engine Analyser", specially for motor cycle use, the tacho range is rather restricted. Please bear in mind that the auto-advance does not finish operation until 4,000 r.p.m. is reached, and this applies to some car engines. Obviously this can be overcome using a 2 -stroke/4-stroke switch or relabelling the number of cylinders switch.

In my experience the resistance range would be better if it were $0-10022$ or even $0-1052$. In practice, resistance measurements of alternator stator windings, motor windings. ignition coil primaries, contact resistance are all of low values.

Finally. one minor point is that I have found it preferable to enclose the strobe light in a rubber torch body and fit a simple lens to concentrate the light.

I must congratulate you on producing the analyser which 1 am certain will fulfil a real need. This is even more required in regard to servicing motor cycles than is generally realised as these are expensive ( $£ 1,500$ ) and highly tuned. which until very recently were serviced in the backyard or on the side of the road.

## H. D. Briggs. <br> Telford.

## Short Cut

Sir-Readily replaceable connection of transistors is a frequent requirement when building untested designs and when repairing faulty equipment. This is a time consuming job, and can be done away with by using transistor sockets.

However, as these are relatively expensive and quite scarce, a cheaper and readily available alternative is to use integrated circuits sockets. These can be sawn to the required number of connections with a fine hacksaw; one pair of sockets is lost per cut. For example, by cutting down the centre of the socket, four 3-terminal transistor holders can be obtained from a 14 pin dual-in-line socket.
P. Knight,

Now Malden, Surrey.

## Board Guides

Sir,-Recently 1 had the problem of providing guides for Vero printed circuit boards. This was solved by using strips of plastic channelling, see Fig. 1, normally used for sliding glass cabinet doors.
The guides were held in position with one of the many contact adhesives available on the market.
R. Powell,

Suadi Arabia.


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| 4009 | 1.18 | 4022 | 1.66 | 4 |  |
| 4010 | 1.18 | 4023 | 0.36 | 40 | 2.65 |
| 4011 | 0.36 | 4024 | 1-24 | 4046 | 2. |
| 4012 | 0.38 | 4025 | 0.32 | 4047 | 7.65 |
| 4013 | 0.88 | 4027 | 0.43 | 4049 |  |
| 4014 | 1.72 | 4028 |  |  |  |
| 4015 | 1.72 | 4028 | 1.50 | 4050 |  |

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|  | 0.1 | 0.15 | 0.1 | 0.15 |
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| $2.5 \times 5 \mathrm{in}$ | 40p | 39p | - | 19p |
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| $33 \times 5$ in | 45p | 47p | - | 32p |
| $34 \times 17 \mathrm{in}$ | ¢1.61 | E1.26 | 81.00 | ¢1.92 |
| PINS $\times 36$ | 30p | 30p |  |  |

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(P.E. Feb. 1973 to Feb. 1974)

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Stabilised Power Supply $\quad$ \& 12 Two Linear Voltage Controlled Oscillators and one Inverter-all 3 circuirs:
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Poak Leval Meter
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2 Lot. Voltage Controlled Oscillators f14.55 PCB for both log VCO's Divider, 2 Hold Circuits, 2 Modulation Amplifiers, Mixer and 2 Envelope Shapers PCB' (Holds the first 6 circuits)
PCB for both Envelope Shapers
Keyboard Stabilised

## SYWTHESSEERS AND KEYBOAROS

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Set of 12 PCB's (full requirement)
Voicing and Pre-Amplifier Circuits
PCB for above circuits
Power Amplifier

## KEYBOARDS

Kimber-Allen Keyboards as required for many published circuits, including the P.E. Joanna, P.E. Minisonic and P.E. Synthesiser. The manulacturers claim that these are the finest moulded plastic keyboards made.
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|  |  | 3 Octave | 4 Octave | 5 Oct |
| :---: | :---: | :---: | :---: | :---: |
| Contact | Each | Set | Set |  |
| SP | 20p | 47.40 | 89.80 | $\underline{12}$ |
| 2 P | 24p | 68.88 | $\leqslant 11.76$ | 814.6 |
| PS | 48p | ¢17.76 | ¢23.52 | 629 |

Printed Circuit Boards for use with the above contacts and thus eliminating most of the inter wiring required, are available-details in our lists.

PHONOSONICS

## IIIIIIIIIIIIIII

## P.E. MINISONIC

(P.E. Nov. 1974 zo March 1975)

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$£ 1.66$
$\$ 5.27$
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| :--- |
| $0.5 A$ |
| $+\quad 8 V-5 A$. or $9 V 0.35 A+9 V 0.35 A$, or $12 V 0.25 A$ | +12 V O.25A. or $20 \mathrm{~V} 0-15 \mathrm{~A}+20 \mathrm{~V} 0.15 \mathrm{~A}$, ill at E2 ench.

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44.60
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Component sets include all necessary resistors, capacitors, semiconductors, potentiometers and

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PCB for
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4.58

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Type PC7
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(P.E. NOVEMBER, 1975)

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| $\begin{aligned} & \text { Polyes } \\ & (\mu \mathrm{F}) \end{aligned}$ |  | $\begin{aligned} & \text { Tantalum } \\ & (\mu \mathrm{F} / \mathrm{V}) \end{aligned}$ |
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## INDEX TO ADVERTISERS

| Adam Hall (P.E. Supplies) | 5 |
| :---: | :---: |
| Alben Engineering | 96 |
| Alfa Electronics | 172 |
| Astro Electronics | 102 |
| Automated Homes | 75 |
| Axial Products | 2 |
| Bamber, B., Electronics | 168 |
| Barclay Electronics | 171 |
| Barrie Electronics | 165. 175 |
| Barrie-Smith | 175 |
| Beta Devices | 173 |
| B.H. Components | 156 |
| Bi-Pak | 94,95 |
| Bi-Pre-Pak Lid | 161 |
| J. Birkett | 92 |
| Boffin Projects |  |
| Bridge | 100 |
| British Institute of Engineering Technology | 92. 128, 152 |
| British National Radio \& Electr |  |
| School | 100, 121 |
| Bywood Electronics |  |
| Chiltmead Lid | 108 |
| Chinaglia | 90 |
| Chromasonic Electronics | cover ii |
| C.J.L. | 102 |
| Clef Products | 100 |
| Copper Supolies | 174 |
| Crescent Radio Lid | 106 |
| Crofton Electronics | 92 |
| C. T. Electronics | cover |
| Design Engineering | 152 |
| Dorein ............ | 90 |
| Dziutas, M | 152 |
| Eagle International | 168 |
|  | $162$ |

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