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Our June issue will be published on Friday, May 14, 1976
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The cabinat measures $12^{\prime \prime} \times 9^{\prime \prime} \times 5^{\prime \prime}$ deep approx finished in simulated teak, incorporating a quality 7 " $\times 4$ "elliptical speakér power handling 4 watts, flux density 30.000 maxwells mpedance $8-15$ ohrms nominal, voice coil dia $\frac{3}{4}$ " magnet size $\mathbf{I} \mathbf{0} 00$

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£22.00 pair complete
Complete with crossover $+\mathrm{f} 5.20 \mathrm{p} \mathrm{\& p}$ diagram

EMI 350 KIT $£ 7.25+f 1.20$ p 8 . Complete with crossover Components and circuit diagram

System consists of a $13^{\prime \prime} \times 8$ approx, woofer with a $3^{\prime \prime}$ tweeter, crossover components and circuit diagram. Frequency response: 20 Hz to 20 KHz . Power handling 15 watts RMS inio 8 ohms. (Peak 30 watts.)

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frequancy response); 5.Microphone - milivalts at sok ohms flat frequency responsel.
CONTROLS: Push button ON/OFF, stereo/mono, scratch filter. 6 position rotary selector. Individual rotary controls for treble, bass, balance and volume. Headphone socket, tape out socket. Aux mains output. Frequency response: 25 Hz to 25 kHz at full rated output. Signal to noise ratio: better than -50 dB on all inputs. Tone control range: Bass $\pm 15 \mathrm{~dB}$ at 50 Hz : Complate System Treble $\pm 12 \mathrm{~dB}$ at 10 KHz . Power requirements: '250V A.C. mains at 60 watts. with these speakers
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f6.50 pqp.

SYSTEM 2 £85.00
Viscount IV amplifier (As System la) MP60 type deck (As System 1a)
Two Duo Type III matched speakers - Enciosure size approx $27^{\prime \prime} \times 13^{\prime \prime}$ $\times 11 \frac{1^{\prime \prime}}{2}$. Finished in teak simulate. Drive units $13^{\prime \prime} \times 8^{\prime \prime}$ bass driver, and two 3" (approx.) tweeters. 20 watts RMS. 8 ohms trequency range 20 Hz to $18,000 \mathrm{~Hz}$.
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Speaker including baffle and fixing strip $£ 2.00$ $+45 p p$ \& $p$. Cal Aerial Recommended - fully retractabie $£ 1.60+40 p$ p \& p.
The Tourist I Kit For the experienced canstructor. If you can solder on a printed circuit board you can build this model. Same technical specification as Tourist $\Pi$. Price $\mathbf{f 8 . 2 0}+\mathbf{f 1 . 0 5} \mathrm{p} \& \mathrm{p}$.

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$3^{\prime \prime}$ required to clear
base of mechanism approx.

This is an advanced tat
 nor suitable for those without electrical knowledge and those unable to solder

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Aumble Better than $-35 d B$. Wow Aumble Better than -358 . Wow
Better than $0.2 \%$ Flutter Better than Better than 0.2\%. Flutter Bett
$0.06 \%$ (Gaumont kalee metes).
$0.06 \%$ (Gaumont kalee metee).
Finish -. Satin black mainplate with black turntable mat inlaid with brushad aluminium trim. Tonearm and controls in black and brushed aluminium.

Console size -
 Unit Open $-35 z^{\prime \prime} \times 13 z^{\prime \prime} \times 4$ f", lapp. $^{2}$ This disco console is ideally matched This disco console is ideally matched
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Panal meter ( $\mid \mu \mathrm{A}$ ) (optiona|)
VOICE OPERATED FADER (P.E. Dec. 73)
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Component set incl, PCB
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TAPE.NOISE LIMITER
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Component set (incl, PCB)
Regulated power supply (inel, PCB)
62.60

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Alternative component set with panel
mounting switches
Printed Circuit Board
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Designed for use with reasonable quality tape-decks his high performance pre-amp includes record playback and metering circuits. While stocks last
Stereo component set (excl. panel meter) 624.25 Mono component set (excl. panel meter) $\quad$ fl4.70
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Stereo sub-assembly PCB
12.80
98

## VOLTAGE CONTROLLED FILTER (P.E. Oct. 74)

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Component set
63.41

## ENVELOPE SHAPER AND V.C.A. <br> The ADSR Envelope Shaper published in P.E. April 1976 and having its own voltage controlied Reiease and Sustain functions. <br> Component set incl. PCB

## AUTO WAH-WAH <br> Component Set and P.C.B. ©2.99.

| Transistors |  | BFY51 | 22p | 2N3055 | 48p |
| :---: | :---: | :---: | :---: | :---: | :---: |
| AC128 | 20p | BFY52 | 24p | 2N3702 | 12p |
| ACl76 | 20p | BSY95A |  | 2 N 3703 | 12p |
| BC107 | 13 p | MJE2955 |  | 2N3704 | 12p |
| BCl08 | 13 p | MJE2955 |  | 2 N 3819 | 35p |
| BCl09 | 13p | OC28 |  | 2N3823E | 39p |
| BC147 | 12 p | OC71 |  | 2N4060 | 12p |
| BCl48 | 12 p | OC72 |  | 2N4871 | 36p |
| BC149 | 12 p | OC84 |  | 2N5245 | $51 p$ |
| BC157 | $13 p$ | ORP12 | 66p | 2N5777 | 45p |
| BC158 | 13p | ZTX107 | 12P |  |  |
| BC159 | 13p | ZTX108 | 71 p | Diodes |  |
| BCI82L | 12p | ZTX501 | 13 p | 1 N 914 | 4 p |
| BC184 | 12p | ZTX503 | 13p | IN400\| | 6p |
| BC187 | 25p | ZTX531 | 23P | iN4002 | 7p |
| BC204 | 14p | 2N706 | 13p | IN4004 | 8p |
| BC209C | 14p | 2N914 | 22p | IN4006 | 9 p |
| BC212L | 15p | 2NI304 | 22p | IN4007 | 10p |
| BC213 | 15p | 2N2905 | 27p | OA91 | 7p |
| BC478 | 29p | 2N2907 | 22p | -A202 | $8 \mathrm{8p}$ |
| BCY71 | 22p | $2 N 3053$ | 18p |  | $75 p$ |
| BFY50 | 22p | 2N3054 | 66p | ZS171 | 16 p |

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(P,E. Mar,/Apr. 74)
Programmable for 64,000 rhythm patterns from 8 effects circuits (high and low bongos, bass and snare drums, long and short brushes, blocks and
soft cymbal), and with variable time signacures and rhythm rates. Really fascinating and useful. and rhythm rates. Really fascinating and useful.
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62.76

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41.58

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For use with the above Phasing Unit to automatically control the rate of phasing.
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Pre-amp PCB (P.E. Oct. 1972)
Power Slavas: Power Supply PCB (P.E. Aug. $\quad 55 p$ 1974)

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Component set and PCB
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96p

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| Power supply set incl. PCB | $\$ 6.57$ |}

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Printed circuit board
9 inch spring unit
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| $\}$ W0 0124p |  | $\begin{aligned} & 100 \mathrm{~V} 3 \mathrm{~A} \\ & 200 \mathrm{~V} 3 \mathrm{~A} \end{aligned}$ | 15p | 600V1A | 60 p21.20 | 125 Y 27D |  |  | 7409 | 28p |
| $\} \begin{aligned} & 200 \mathrm{~V} 1 \mathrm{~A} \\ & \mathrm{wo} \mathrm{02} \mathrm{25p}\end{aligned}$ |  |  |  |  |  |  | 7410 | 18 D |  |
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TThe module has a sensitivity of 450 mV and a frequency response extending from 25 Hz to 20 kHz whilst distortion levels are typically below $0 \cdot 1 \%$ ．The use of 4 ， 115 W transistors in the output stage makes the unit extremely rugged while damage resulting from incorrect or short－circuit loads is prevented by a four transistor protection circuit．
The unit is intended for use in many applications such as disco units，sound reinforcement systems，background music players，etc．

## SPECIFICATION：

Output Power： 125 watt RMS Continuous
Operating voltage：50－80
Loads：4－16 ohms
Frequency response： $25 \mathrm{~Hz}-20 \mathrm{kHz}$ Measured at 100 watts
Sensitivity for 100 watts output at $1 \mathrm{kHz}: 450 \mathrm{mV}$
Input impedance：33k ohms

Total harmonic distortion 50 watts into 4 ohms： $0.1 \%$ 50 watts into 8 ohms： $0.06 \%$ S／N ratio：better than 80 dBs Damping factor， 80 hms ： 65
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## UNDER-SOLD?

MANY of the great achievements and commercial successes of electronics are due to the amazing cheapness of semiconductor devices, no less than to their technical versatility. But the first of these attributes could well prove an embarrassment or liability when considering its effect upon the popular image of electronics, and this is especially important now when the whole question of salaries and status of technical employees in the industry has come to the fore, as discussed by Nexus in this month's Industry Notebook.

Cheapness of components has helped reduce certain consumer type products to the expendable grade. Cheap transistor radios provided the first examples of modern electronic products which are sometimes deemed not worth repairing but are discarded when trouble appears. With the arrival of the digital watch, this custom could be carried to its ultimate absurdity. For if prices continue to fall as confidently predicted, it could eventually become more economical and certainly more convenient to throw away the cheaper type digital watch when the batteries expire and buy another.

There is a further interesting aspect well illustrated by this product, since the digital watch is also invading the fine jewellery market. These up-market models owe everything to their external appearance-to the case-since the electronic assembly might be identical to that used in a cheaper version. Cheap, common, and expendable plastics versus expensive,

- exclusive, and indestructible fine metals. Micro-electronics has made it all possible, but the case designers and makers seem likely to skim off the cream.

The digital watch thus offers an illuminating but disturbing example of highly advanced technology selling for a song, and the packaging alone determining whether the final product be a cheap and expendable item or a lasting and valuable possession. Moreover, in more general terms, as the ordinary constructor knows only too well, it is not unusual for the major cost of an electronic project to be taken up by non-active components and also items such as the hardware used in the assembly of the components and to encase the completed project. We all welcome and enjoy low cost active devices, but to the technically-appreciative, values must seem somehow to have become reversed.

Almost too late it seems semiconductor makers sense that they have been blinded by their own technological success and have been unwise in waging price wars on one another in order to acquire a bigger portion of the cake. The short term gains have been considerable, but what of the future? Has then electronics sold itself short? Any cheapening of the technology, any equating of its products with, say, easy-come easy-go plastics commodities must in the long term be detrimental to the status and financial well-being of those employed in electronics. Perhaps the semiconductor industry should recall that charity begins at home, and that cut-price technology is not really in everyone's best interests, and that it often leaves the biggest plums for outsiders to gather.
F.E.B.

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THis article describes the design, construction and testing of a digital frequency meter (D.F.M.). This particular instrument has been designed as a radio society construction project and with this in mind it was necessary to use straightforward, logical design.

Ease of fault-finding has been one of the main considerations and to this end a design has been produced which can be tested and maintained using simple test gear.

The entire logic is contained on a single printed circuit board, and the four-digit display (in this case seven-segment light emitting diodes) is on another board. The power supply and the range switching are the only items that need free wiring.

The basic unit to be described has a 50 ohm input impedance and can measure up to at least 50 MHz . Modifications will be described ta extend

## By A.J.BUXTON

the frequency range to 500 MHz and to give a high input impedance.

A major innovation in this instrument is the use of a large scale integrated (l.s.i.) circuit to replace the majority of the logic. The Ferranti ZN1040E counts, stores, decodes and drives up to four sevensegment displays.

## THE ZN1040E

The ZNIO40E is a large scale integrated circuit fabricated using the "collector diffusion isolation" (c.d.i.) process. Fig. 1 shows the internal functions contained within this device together with the pin connections.

Fig. 1. The internal functions and pin connections of the Ferranti ZN1040E integrated circuit


## SPECIFIGATION . . .

| Frequency Ranges | $10 \cdot 00$ to 99.99 kHz |
| :--- | :--- |
|  | $100 \cdot 0$ to $999 \cdot 9 \mathrm{kHz}$ |
|  | $1 \cdot 000$ to $9 \cdot 999 \mathrm{MHz}$ |
|  | 10.00 to $99 \cdot 99 \mathrm{MHz}$ |
|  | 50 ohm |
| Input Resistance | 40 mV |
| Sensitivity |  |
| Input Voltage d.c. bias | 400 V |
| (max) | 10 Hz on Range 1 |
| Frequency Resolution | 5 MHz |
| Timebase Frequency | Timebase Ageing Rate |
| Timebase Temperature | $\pm 10$ p.p.m. per year |
| Stability | $\pm 10$ p.p.m. $\left(10^{\circ} \mathrm{C}\right.$ to |
|  | $\left.55^{\circ} \mathrm{C}\right)$ |

The device is contained in a 28 -lead plastic encapsulated package. By directly driving from the multiplexed, seven-segment outputs or by decoding the binary coded decimal (b.c.d.) outputs, any contemporary display can be used.

The seven-segment drivers can sink a current of 80 mA , which results in an average current of 20 mA per segment when multiplexed.

When free-running, the internal multiplex clock oscillator gives a frequency of about 500 kHz . This frequency can be lowered by the addition of a capacitor to pin 12. In this instrument a $0.01 \mu \mathrm{~F}$ capacitor is used to give a frequency of 3 kHz .

A direct drive at TTL logic levels will override the multiplexing to the driven frequency.

The ZN 1040 E requires a single 5 V supply and consumes an internal current of 90 mA . It is fully compatible with TTL devices.

## CIRCUIT DESCRIPTION

A circuit diagram of the complete unit minus the power supply is shown in Fig. 2. This circuit shows the basic unit with a 50 ohm input impedance. All the circuitry is contained on two printed circuit boards: one for the logic and input circuits and one for the display. The two boards are shown as dotted lines in Fig. 2.

The circuit is more easily understood if considered in sections.

## THE OSCILLATOR

The oscillator is the single most important function within a digital frequency meter as the accuracy depends almost entirely on the crystal oscillator.

Variations in temperature, crystal age and supply voltage all affect crystal frequency. Adding all the effects together, one can be faced with an error of 30 Hz in 1 MHz or 0.003 per cent.

Initial crystal frequency inaccuracy can be trimmed out using a capacitor ( VCl ). This can also be used to trim out the effects of ageing. There is admittedly room for improvement of the oscillator used in this design but it is simple and in line with the basic design concept.

A 5 MHz crystal ( X 1 ) is used, this frequency being useful as temperature and mechanical stability are optimised at this frequency.

| MAIN UNIT |  |
| :---: | :---: |
| Resistors |  |
| R1-R5 | $270 \Omega$ (5 off) R25 |
| R6-R15 | $100 \Omega$ (10 off) R26 |
| R16 | $2.2 \mathrm{k} \Omega$ R27 |
| R17 | $100 \Omega$ R28 |
| R18 | 150S R29 |
| R19, R20 | $5.6 \mathrm{k} \Omega$ (2 off) R30 |
| R21 | $15 k \Omega$ R31 |
| R22 | $10 \mathrm{k} \Omega$ R32 |
| R23 | $1 \mathrm{k} \Omega$ R33-R |
| R24 | $100 \Omega$ |
| All $\pm 5 \% \frac{3}{4}$ or $\frac{1}{9} \mathrm{~W}$ |  |
| Capacitors |  |
| C1 | 6,800pF |
| C2-C4 | $0.05 \mu \mathrm{~F} 6 \mathrm{~V}$ disc (3 off) |
| C5 | 10pF mica |
| C6 | $0.01 \mu \mathrm{~F}$ disc |
| C7 | $0.05 \mu \mathrm{~F}$ disc |
| C8 | $50 \mu \mathrm{~F} 10 \mathrm{~V}$ elect |
| C9 | $0.05 \mu \mathrm{~F}$ disc |
| C10 | 30 pF mica |
| C11 | $0.05 \mu \mathrm{~F} \mathrm{disc}$ |
| C12 | $0.1 \mu \mathrm{~F} 6 \mathrm{~V}$ disc |
| C13 | $100 \mu \mathrm{~F} 10 \mathrm{~V}$ elect |
| C14 | 30 pF mica |
| C15 | 10pF mica |
| C16, C17 | 330 pF mica (2 off) |
| C18 | $0.05 \mu \mathrm{~F}$ disc |
| C19 | 470 pF disc |
| C20 | $0.05 \mu \mathrm{~F}$ disc |
| C21 | 1,000pF 6V disc |
| C22-C28 | $0.05 \mu \mathrm{~F} \mathrm{disc}$ (7 off) |
| C29 | 2,200 F 25 V elect |
| C30 | $330 \mu \mathrm{~F} 16 \mathrm{~V}$ elect |
| C31 | $0 \cdot 1 \mu \mathrm{~F} 6 \mathrm{~V}$ disc |
| C32 | $100 \mu \mathrm{~F} 6 \mathrm{~V}$ elect |
| VC1 | 3-60pF trimmer |

Transistors and Diodes

| TR1-TR4 | ZTX4403 (4 off) |
| :--- | :--- |
| TR5, TR6 | ZTXX300 (2 off) |
| TR7 | ZTX312 |
| TR8, TR9 | ZTX500 (2 off) |
| TR10-TR12 | ZTX312 (3 off) |
| D1 | $4.7 V 400 \mathrm{~mW}$ Zener |
| D2-D4 | ZS170 (3 off) |

Integrated Circuits

| IC1 | NE592 | IC6 | ZN7400 |
| :--- | :--- | :--- | :--- |
| IC2 | ZN74196 | IC7 | ZN7403 |
| IC3 | ZN1040E | IC8-IC14 | ZN7490 (7 off) |
| IC4 | ZN7474 | IC15 | 78M05 |
| IC5 | ZN74123 |  |  |

Display
LED1-LED4 DL707 (4 off)
Switches

$$
\begin{array}{ll}
\text { S1 } & 2 \text { pole } 4 \text { way rotary } \\
\text { S2 } & \text { Single pole on/off toggle } \\
\text { S3 } & \text { D.p.d.t. mains toggle }
\end{array}
$$

Miscellaneous
T1 Mains primary, $8-0-8 \mathrm{~V} 500 \mathrm{~mA}$ secondary (Douglas MT207CT)
X1 $\quad 5 \mathrm{MHz}$ crystal
SK1 BNC socket
FS1 2 A 20 mm fuse and holder 9 -way tag strip
Filter for display, p.c.b.s, sockets for i.c.s if required, case, nuts and bolts, standoffs, etc.



Fig. 2. Circuit diagram of the Digital Frequency Meter ( 50 ohm version). The dotted lines indicate the boundaries of the two printed circuit boards. The circles with numbers inside refer to the pads on the circuit boards to which wires are connected

Transistor TR10 and associated components form the basic oscillator whose output is fed to the amplifier TR11 and then to TR12 which interfaces the signal to TTL logic levels. As mentioned earlier, the frequency of this clock oscillator is set by trimming the 3 to 60 pF capacitor VCl .


## THE CLOCK DIVIDER CHAIN

The clock oscillator frequency is divided down to generate logic control pulses. These pulses determine the time for which the main signal gate (IC6c) is open. They also control the transfer of information to the displays and the clearing of the counters in the ZN1040E.

This four-range, four-digit counter has four orders of magnitude of full scale display, so four lengths of timing pulse are needed to cover them.

Full scale ranges are 99.99 MHz (in practice limited to 50 MHz by the limitations of the ZN1040E), $9.999 \mathrm{MHz}, 999.9 \mathrm{kHz}$ and 99.99 kHz .


Fig. 6. Circuit diagram of the power supply which produces the 5 V and -9 V lines to the main board


Fig. 3. The timing of the control pulses at various points in the circuit

If the counter is to display, say, 6.800 MHz , then 6,800 pulses must pass to the counters. A decade divider connected to the input (IC2) divides the input frequency by ten, giving a frequency of 680.0 kHz at the signal gate input (IC6c pin 9). This means that to let 6,800 pulses through, gate IC6c must be open for $1 / 100$ th of a second ( 10 ms ). The figure " 6.800 " will then be displayed, the range switch S 1 inserting the decimal point to compensate for the prescaler.

The four ranges thus require the following gate times:

| Frequency Range | Time gate open (secs) |
| :--- | :--- |
| 10.00 to 99.99 kHz | 1 |
| 100.0 to 999.9 k Hz | $0.1(100 \mathrm{~ms})$ |
| 1.000 to 9.999 M Hz | $0.01(10 \mathrm{~ms})$ |
| 10.00 to 99.99 MHz | $0.001(1 \mathrm{~ms})$ |

To obtain these length pulses the 5 MHz clock is divided by six and a half decade counters (IC8 to IC14). The remaining divide-by-two is used to start the transfer, clear and reset logic.

Control pulse selection is effected by selecting one of the open collector NAND gates in IC7 using switch S1b. Each of the four gates is connected to a different point in the divider chain. The nonselected gate inputs are held low by the resistors R33 to R36. The outputs of the four nand gates are WIRED-OR connected via resistor R25.

## THE CONTROL LOGIC

The control logic determines when a transfer or clear pulse is required and when the signal gate can be opened, thus ensuring a correct sequence of events. The timing diagram (Fig. 3) shows the sequence when the fastest range is selected and the circuit of Fig. 4 shows the control logic in more detail.

The output from the end of the divider chain (IC4, pin 12) is a one second high, one second low series of pulses.
Consider the state when the two bistables of IC4 have been set by a pulse from IC7, so that $2 \bar{Q}$ is high and output $1 Q$ is high. The next negative edge from the divider chain will produce a positive edge at the output of IC6b. This will trigger monostable MS2, producing a 120 ns positive "transfer" pulse at

Fig. 4. The control pulse generation circuitry in more detail. The logic shown here produces the clear and transfer pulses required by the ZN1040E and the signal gate control pulses

its 2 Q output. This pulse transfers the information from the counters to the latches and hence to the display in the ZN 1040E.

When the 2Q output of MS2 returns to low, its 2 $\bar{Q}$ output goes high, triggering monostable MS1 which also produces a 120ns "low" pulse. This pulse is used to clear the counters to 0000 . Meanwhile the 1 Q output is high for 120 ns , taking the output of IC6a low which sets BS2's 2Q output high, clearing BS1' whose $1 Q$ output goes low, thus closing the signal gate IC6c. The logic is now set for the timing period.

## TIMING SEQUENCE

The 7474 is a positive edge triggered bistable. On receiving the first positive edge from IC7 after being set, BS 1 is triggered. 1 Q goes high thus opening gate IC6c. I $\bar{Q}$ goes low which has no effect on 2 CLOCK input as a positive edge is required.

The pulse from IC7 goes low, this having no effect on 1 CLOCK. However, when it goes high again (after the required timing period of one second) 1Q wilt go low thus closing the signal gate. At the same time $1 \bar{Q}$ goes high which triggers BS2, setting 2 Q low and clearing $\mathrm{BS} 1.2 \overline{\mathrm{Q}}$ remains high in readiness for the next negative edge which starts the whole sequence.

The action of the bistable BSI divides the pulses from IC7 by two. A 100 Hz frequency, with 2.5 ms high and 7.5 ms low pulses, causes the gate to be open for 10 ms .

## COUNTING AND DISPLAY

After the counters of the ZN1040E are cleared, the signal gate will be opened for a fixed period. The frequency of the pulses entering the count input (pin 22) will be a tenth of the frequency to be displayed. The ZN1040E has a minimum count rate of 5 MHz so the measured frequency can be as high as 50 MHz or greater.

The pulses at the count input are counted on the four cascaded decade counters (Fig. 1). When the signal gate is closed the control logic generates a pulse to transfer the counter information to the latches. The clear pulse then sets the counter to zero. Should there be more than 9,999 pulses, the most significant digits will be lost, only the four least significant digits of the number of pulses being retained.

This feature is most useful when measuring a frequency with more than four significant digits. If, say, 29.215 MHz is to be measured, the instrument can be deliberately over-ranged to read 9.215 MHz , the " 2 " being remembered from the measurement on the next range.

An internal clock generates the multiplexing signals which control the gating that scans the latches, and addresses the digit select output.

When any particular digit is addressed, the latch information relevant to that digit is presented at the seven-segment decoder/driver output. As each display is addressed with a one-in-four time slot, the average power supplied to each segment is 0.25 the peak power.

Resistors R9 to R15 are used to limit the output current to about 25 mA peak. 6 mA per segment average is adequate for most applications.


Fig. 5. Frequency plotted against sensitivity for the NE592 integrated circuit amplifier

The frequency of the internal clock can be lowered by the addition of a capacitor or overriden by driving pin 12 with an external TTL clock. A capacitor of $0.01 \mu \mathrm{~F}$ has been used in this design to give a multiplex frequency of 2.8 kHz .

The displays used in this meter are four DL707 (LEDI to 4). Being common anode l.e.d. displays, they are suitable for driving directly from the ZN1040E, i.e. without interface circuitry. The anode pull-up transistors TR1 to TR4 are used because of the high currents that are required if an " 8 " is to be displayed (all segments used). A current of 200 mA peak can flow in this case. Resistors R2 to R5 limit the current flowing into the bases of these transistors.

Decimal point selection is carried out using the same switch as is used for the gate period selection (S1).

## PRESCALER CIRCUIT

In order to measure frequencies higher than the 5 MHz limit imposed by the ZN 1040 E , it is necessary to pre-divide the input frequency. IC2 is a 74196 decade divider capable of operating at 50 MHz . It is wired as a divide-by-two then a divide-by-five. The 10 pF capacitor C 5 connected to the divide-bytwo output (pins 5 and 6) acts as a load to prevent instability under no-signal conditions.

Though the frequency capability of the ZN1040E is 5 MHz and of the 7419650 MHz , these are minimum figures; a typical pair of i.c.s will operate above this range.

## INPUT AMPLIFIER

The input impedance of the instrument as shown in Fig. 2 is 50 ohm. If a high input impedance is required, then the buffer board to be described next month will be required.

A capacitor at the input ( Cl ) protects the input from d.c. bias potential up to 400 V . Two transistors (TR5, TR6) connected as diodes are used to limit the input voltage to ICI. Transistors are used as a
cheap alternative to high speed switching diodes. If diodes are used here they must have a reverse recovery time of less than 6 ns if the full capabilities of the instrument are to be realised. Resistor R7 limits the current in these two transistors.
The power input to the instrument must not exceed the power handling of R6.
The input amplifier ICl is a wideband video amplifier type NE592 connected in the inverting mode.

The emitter follower TR7 is used to interface ICI to IC2. Under no signal conditions, $2 \cdot 1 \mathrm{~V}$ will be measured at " $B$ ".

The i.c. has four outputs ( $\mathrm{W}, \mathrm{X}, \mathrm{Y}, \mathrm{Z}$ ) used to set the gain. By linking X and Y a gain of 400 at a bandwidth of 40 MHz is obtained. The graph of Fig. 5 shows frequency plotted against sensitivity.
Shorting W and Z gives a gain of 100 with a 90 MHz bandwidth. A $10 \mathrm{k} \Omega$ variable resistor between X and Y allows a variable gain of unity to 400.

The -5 V line required by the NE592 is supplied by the simple stabiliser (TR8 and TR9) fed with -9 V .

## THE POWER SUPPLY

The power requirements of the instrument are 5 V at 500 mA , and -5 V at 30 mA . The power supply and stabiliser for the +5 V line is shown in Fig. 6. The -9 V line is fed to the stabiliser on the main printed circuit board which produces the -5 V for ICl .

The current drain is 350 mA with no-signal input and 500 mA with all eights displayed.

Next month: Constructional details, high impedance buffer and v.h.f. prescaler

NEWS BRIEFS

## Summer School for Teachers

The Department of Electrical Engineering Science at Essex University will be holding its annual Electronics Summer School for teachers during the week July 12-16 and, this year, three courses Linear Circuit Design, Digital Circuit Design and Small Computer Systems will be run simultaneously.

The Linear Circuit Design course is concerned with the use of transistors and operational amplifiers in analogue applications and the basic circuits of a hi-fi amplifier are investigated in detail.

The Digital Circuit Design course concentrates on the use of the transistor as a switch and develops a design using integrated logic circuits; a digital patchboard is used to introduce the concepts of combinational and sequential logic design.

Small Computer Systems is a new course which should be of interest to mathematics teachers as well as those interested in electronics; the aims of the course are to introduce a typical small computer, the PDP-8, to investigate how it is used and to discuss its function in schools.

Further information on the Summer School can be obtained from Mr R. J. Mack at the Department of Electrical Engineering Science, University of Essex, Wivenhóe Park, Colchester CO4 3SQ.

INTRODUCTION TO QUANTUM ELECTRONICS
By P. A. Lindsay

## Published by Pitman

202 pages, $240 \mathrm{~mm} \times 160 \mathrm{~mm}$. Price $£ 6.00$
uantum Electronics is no longer confined to the scientific laboratory. An impórtant and growing technology has emerged based on the practical utilisation of electromagnetic radiation interaction with matter on the atomic scale through the medium of devices such as lasers. The applications of lasers are likely to increase in the future and already they play an important part in industry and in the medical field.

Thus the subject covered by this book could be a very rewarding one for the engineering student to pursue. The term "Introduction" might be misleading. This is an advanced level textbook and it explores the subject in a rigorous analytical manner with extensive use of maths. The author is Professor of Physical Electronics at King's College, University of London.
D.D.K.

## MULLARD DATA BOOK 1976

176 pages, $134 \times 96 \mathrm{~mm}$. Price 50 p.
THIS is a handy pocket reference containing abnidged data on the Mullard range of components for use in consumer applications, including valves, semiconductor devices, TV tubes, capacitors and resistors. Equivalents and comparables are also listed.

For easy reference different coloured pages are used for each of the main sections; blue for semiconductors, yellow for picture tubes and receiving valves and green for capacitors and resistors.

The book is obtainable from specialist components stockists or direct from Technical Press Ltd., Freeland, Oxford, OX7 2AP

## 

HOW INVENTIVE ARE YOU COMPETITION
Full results will appear next month. Unfortunately it was not possible to include them in this issue, as originally hoped.

DIGI-PROBE (April 1976)
In Fig. 7 (p. 292) the resistor on the left-hand edge of the top board should be annotated R16. Also the lead from the junction of R15, R16 and R17 should go to IC2 pin 12 and not as shown to pin " $e$ " on the DL704 display. The lead from pin " $e$ " of the display should be connected to the other side of R16.

OPTO-COUPLED R.P.M. METER (February 1976)
Some constructors have had difficulty in obtaining the MS4A photo-cell specified. This can be obtained from: Davian Electronics, PO Box 38, Oldham, Lancs, OL2 6XJ.

This is an error on the p.c.b. master (page 146). The track in the top left-hand portion, of Fig. 2 which connects the collector of TR1 to the positive supply line (shorting out R2) should be removed.


## SOVIET VENUS PROBES

The Soviet satellites Venera 9 and Venera 10 , continuing their orbiting studies of Venus, have measured the temperature of the clouds near their upper boundary. This was at a level of -35 degrees centigrade. Records of the glow on the night side of the planet indicate that the spectrum differs from that of the Earth glow.

The electron density on the day side is much higher. than the night side and about 90 per cent less than that of the Earth. Another feature is that the ionosphere of Venus appears to be closer to the planet and much thinner than that of the Earth.

The pictures that have been sent back so far have encouraged the Soviet investigators to examine the radio method of exploring through the cloud layer. Although some five years ago Yuri Spiridinov had devised a system of measuring relief using data sent back by Mariner 5, this was not pursued because the general consensus of opinion was that the surface of the planet would be mainly smooth.

The technique involves the critical refraction layer of the Venusian atmosphere about 30 kilometres or so above the surface. The angle of refraction of the radio beam is so great that it must also be reflected by the surface. Using the split beam technique, similar holography in the visible spectrum, one half of the beam passes below the critical level and the other half above the critical level.

A signal sent from Earth will be received by a space probe on the dark side, and will be the sum of the direct and reflected wave. Thus, a picture can be built up from the
recordings and the result is, when computerised, a picture of the surface in the radio frequency spectrum. More than one line scan is needed of course, but since Venus moves very slowly on its axis, 243 terrestrial days, and the probe needs to be in radio shadow, a line by line scan is easily obtained.

Although the original data received from Mariner 5 was insuffcient to produce a full picture, nevertheless the contour was shown at, two frequencies. The indicated variation of height ranged from 0.3 to 2.7 metres. This agreed very well with the pictures that were received from the probes landed on the surface. This technique could be applied in a number of other cases and may prove a very useful tool of the future.

## OUTER BOUNDARY

Spacecraft Pioneer 10 crossed the orbit of Saturn on February 10 and headed out to the boundary of the solar system. On that date it was some 1,000 million miles from the Earth. lts velocity was about $26,000 \mathrm{mph}$.

The equipment has continued to function normally and data continues to be sent back. It is considered that with the sensitivity of the Deep Space Network, Mariner 10 will be in communication until it reaches the orbit of Uranus and maybe further. The orbit of Uranus is about 2,000 million miles from the Earth.

## POWERSAT

The American Congress now has the results of Boeing's power generation satellite proposals. This will require some 30 spacecraft each of which will have a system of converting solar energy to microwaves which are beamed to Earth. The system has been described in detail in an earlier Spacewatch.

The Earth stations will be situated on the equator in desert areas such as Nevada in America. Provision will have to be made for heavy lifting transporters to raise some of the structures. These transporters will be about 90 ft high with a cluster of 21 engines around a 100 ft diameter base. The cost, if the pilot experiments are successful, would be something of the order of 60,000 million dollars over a period of 30 years.

## SET BACKS

A number of casualties have resulted from the new American finance cuts in the space budget. Some of the cuts mean that decades may pass before missions can be set up again.

It is the Space Science area that has suffered most. A Jupiter orbiter probe for launch in 1981 has been deferred. The Moon orbiter and the fly-by for Comet Encke have also been abandoned. Although, here there is a possibility that Helios could be diverted so that the opportunity is not lost.

A tragic loss to astronomy is the withholding of further finance for the 94in orbiting telescope. It will not be possible to advance this project for at least 18 months.

The Venus Pioneer programme which will release probes for different depths of penetration into the Venusian atmosphere will still go ahead.
The mission which was planned for a journey via Jupiter, Saturn Uranus has been postponed and this is the one which means decades in terms of delay because the astronomical positions will not be suitable. This was the "sling-shot" mission where the precise position of the trajectories would have enabled the gravitational effects to help the vehicle on its way.

## FUTURE PLANS

However, some good news is available and the studies planned for the next 11-year period of solar activity will go ahead. The mission will cost 85 million dollars using a 3,000lb satellite which will be known as the Solar Maximum Mission. This will be the first modular design to carry instrumentation retrievable by Shuttle.

At the time of going to press, four launchings have taken place this year. They are: Helios $B$, the second of the German Solar probes; Communications Technology Satellite, a joint effort of NASA and Canada; Intelstat IV-A-B, owned by International Telecommunications Organisation and Marisat. launched for Comsat General Corporation. Marisat- $B$ will follow in May and Marisat- $C$ later in the year.

The RCA Satcom, second of the domestic communications satellites, was due for launch in March. A NATO satellite $3-A$ will be in geostationary orbit in April for North Atlantic Treaty Organisation Relay. LAGEOS, which is Laser Geodynamic Satellite for predicting ocean surface conditions and circulating patterns. It will alṣo be concerned with earthquake hazards and is due for launch in April. Comstar $I-A$ and Comstar $I-B$ will be launched in May and August.

Finally, the Tiros Operational Satellite for the National Oceanic and Atmospheric Administration will be placed in orbit in September

# Usimg <br> Mos digtatal.C. 

By D.B. JOHNSON-DAVIES \& A.M. MARSHALL a.a.

## PART 5

THIS part concerns electronic switches and oscillators with practical circuit examples.

## MONOSTABLES

The monostable is basically a single-shot oscillator. It produces an output pulse of constant width independent of the duration of the input pulse, thus curtailing long pulses and extending short ones. The simple differentiating circuit of Fig. 5.1 performs the first function, as shown by the waveforms in (a). The period of the output pulse depends on the transfer voltage $\mathrm{V}_{\mathrm{T}}$ of the inverter as the time constant $R C$ charges from $V_{S S}$ to $V_{D D}$, and will vary between devices from about $0 \cdot 4 \mathrm{RC}$ seconds with $\mathrm{V}_{\mathrm{T}}$ $=30$ per cent of $\mathrm{V}_{\mathrm{DD}}$ to 1.2 RC seconds with $\mathrm{V}_{\mathrm{T}}=$ 70 per cent of $\mathrm{V}_{\mathrm{DD}}$. If, however, the input pulse is shorter than this period, as in the waveforms (b), the capacitor does not charge fully to $\mathrm{V}_{\mathrm{T}}$ and the output pulse will be constrained to the length of the input pulse. In other words this circuit will only act as a pulse shortener.

This differentiating circuit can be used as a simple delay unit, and Fig. 5.4 shows a frequency doubler using two such circuits, one triggering on each edge of the clock pulse.

For the circuit of Fig. 5.1 to function as a monostable, it must be made to latch on until the full output pulse has been delivered. This is achieved in Fig. 5.2 by using a Nor gate to hold the input to the differentiating circuit "high" until the capacitor has charged to $\mathrm{V}_{\mathrm{T}}$. This excellent monostable does however suffer from the variation in period between devices, mentioned above.

The circuit of Fig. 5.3 can be used if a more predictable period is needed. Two gates fabricated on the same chip will have closely matched transfer voltages, and by using two identical RC time constants, the between device variations are effectively cancelled out.

## SCHMITT TRIGGERS

At the interface between analogue and digital circuits comes the Schmitt trigger, which gives a snappy "yes" or "no" for an undecided analogue input signal. The perfect Schmitt has a characteristic as represented in Fig. 5.5. The Schmitt, if presented with a slowly rising input voltage, will switch sharply
off at $\mathrm{V}_{\mathrm{UT}}$ (the upper threshold voltage) and will not switch back on again until the voltage has fallen below $\mathrm{V}_{\mathrm{LT}}$ (the lower threshold voltage). This difference $\mathrm{V}_{\mathrm{UT}}-\mathrm{V}_{\mathrm{LT}}$, is called the hysteresis, and it prevents the circuit from going into oscillation when the input is held at one of the thresholds.

One way of obtaining hysteresis is to make the transfer point unstable by applying positive feedback, as in Fig. 5.6. The feedback is equal to R1/R2, and this should be less than the combined gain of the inverters for switching to occur. The average thréshold voltage $V_{T}$ can be varied, if required, by connecting a resistor R 3 to $\mathrm{V}_{\mathrm{DD}}$ for $\mathrm{V}_{\mathrm{T}}$ of greater than $\mathrm{V}_{\mathrm{DD}} / 2$, and to $\mathrm{V}_{\mathrm{SS}}$ for $\mathrm{V}_{\mathrm{T}}$ less than $\mathrm{V}_{\mathrm{DD}} / 2$.

Another type of Schmitt trigger, unique to cmos, makes use of the variation of the transfer characteristic of multi-input gates described earlier. Fig. 5.7 shows a circuit with a hysteresis of about 30 per cent of $\mathrm{V}_{\mathrm{DD}}$. This can be reduced to 15 per cent of $V_{D D}$ if required by taking one of the three inputs of gate $A$ to $V_{D D}$. The other two gates are arranged as a familiar R-S latch.

## TRANSFER CURVE

If one of the inputs of a two-input gate is held at somewhere between the two logic levels (i.e. between 3 and 7 volts with a 10 volt supply) the transfer curve for the other input is altered as shown in Fig. 5.8. The Schmitt triggers of Fig. 5.9 make use of this property of cmos gates. The circuit in (a) using NAND gates triggers at above $\mathrm{V}_{\mathrm{DD}} / 2$, whereas using Nor gates as in (b) gives trigger levels below $\mathrm{V}_{\mathrm{DD}} / 2$. In both circuits the hysteresis can be varied from 0 to 40 per cent of $V_{D D}$ by altering $V_{x}$.

## SCHMITT PACKAGES.

As an alternative to constructing Schmitt triggers from discrete gates, the cmos family contains a few ready-made Schmitt packages. The 4093 contains four 2 -input Nand gates each with Schmitt circuits on both inputs. The 40106 is a hex-inverter with Schmitt inputs, and the package outline is the same as the 4069 . In both these devices $V_{T}$ is approximately $\mathrm{V}_{\mathrm{DD}} / 2$, and the hysteresis is 2 V with a 10 V supply.


Fig. 5.1. Simple differentiating circuit. The period of the output pulse depends on the transfer voltage $\mathrm{V}_{\mathrm{T}}$ of the inverter. For explanation of waveshapes below, see text
(3) $\qquad$ (3)



Fig. 5.4. Frequency doubler using two differentiating circuits, one triggering on each edge of the clock pulse, as can be seen from the waveshapes


Fig. 5.3. With gates fabricated on the same chip and identical RC time constants output pulse periods can be more accurately predicted


WAVEFORMS


(2)

(3)

(4)


Fig. 5.2. Monostable circuit achieved by connecting a NOR gate to hold "up" the input to the differentiating circuit until the capacitor has charged to $\mathbf{V}_{\mathrm{T}}$

(4)

(5)


Fig. 5.5. The ideal Schmitt trigger characteristic. It can be defined either in terms of $V_{\text {LT }}$ and $V_{\text {UT }}$ (the lower and upper thresholds) or in terms of $V_{T}$ and $H$ (the average threshold voltage and hysteresis)


Fig. 5.6. Schmitt trigger formed from two inverters with positive feedback. R1 can be from 1 to 100 times R2. With the values shown (R3 not connected) and a supply of $10 \mathrm{~V}, \mathrm{~V}_{\mathrm{LT}}=5.0 \mathrm{~V}$ and $\mathrm{V}_{\mathrm{UT}}=5.8 \mathrm{~V}$ approximately


Fig. 5.7. Schmitt trigger using threeinput gates


Fig. 5.9. Schmitt trigger circuits with hysteresis determined by the voltage $V_{\mathrm{X}}$. (a) With NAND gates the circuit triggers at a voltage greater than $\mathrm{V}_{\mathrm{DD}} / 2$.
(b) Equivalent circuit with NOR gates triggers at below $V_{D D} / 2$
$v_{x}$


Fig. 5.10. Crystal oscillator

Fig. 5.8: Transfer curve variations for non-logic levels $V_{x}$ at one input of a two-input NAND gate. The supply is 10 V


Fig. 5.11. Showing an L/C oscillator the frequency of which being stable to within about 0.001 per cent for a 2 V change in the supply voltage


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Fig. 5.12. Ring oscillator which uses only one capacitor which is charged and discharged through the inverters


Fig. 5.14. Low-power astable with current limiting resistors $R 1$ and $R 2$. These can be any value up to about $470 \mathrm{k} \Omega$. With the values given the frequency is about 450 kHz

## STABLE OSCILLATORS

For more critical applications, stable oscillators can be made by connecting a crystal or L-C network as a feedback network across an inverter. A crystal oscillator circuit is shown in Fig. 5.10, and crystals resonating at up to several megahertz can be accommodated by altering the values of the two capacitors. Fig. 5.11 shows an L-C oscillator, the frequency being stable to within about 0.001 per cent for a 2 V change in the supply voltage.

The ring oscillator of Fig. 5.12 uses only one external capacitor which is charged and discharged through the mosfets of the inverters. It will oscillate at between 1 kHz and 10 MHz for values of C from $1 \mu \mathrm{~F}$ to 1 pF .

The Schmitt trigger will operate as an oscillator giving a range of 1 Hz to 1 MHz with suitable values of $R$ and $C$ (Fig. 5.13). Six discrete oscillators can be built with one 40106 package.

## LOW POWER

Where power consumption is critical, as in battery: powered circuits, it may be worth modifying these simple oscillator circuits given in the previous part in order to conserve a few milliwatts.


Fig. 5.13. Schmitt used as an oscillator


Fig. 5.15. Low-power crystal oscillator. Frequencies of up to 10 MHz are possible with a supply of 15 V

The quiescent current drawn by a gate is negligible; of the order of nanoamps. The major part of the dissipation of a cmos oscillator occurs during the transition between states, due to the charging and discharging of circuit capacitances, and therefore increases with frequency. This dissipation can be reduced at the expense of decreased output drive capability by the addition of resistors between the source and $V_{S S}$, and the drain and $V_{D D}$ of the mosfet pair, thus decreasing the current that flows during conduction. The 4007 dual complementary pair plus inverter provides access to the individual mOSFETS and so can be used in such circuits.

Fig. 5.14 shows a low-power oscilla tor constructed from a 4007 package, with a frequency of about $1 / 2 \cdot 2 \mathrm{RC}$. The power consumption at 10 volts falls from about 5 mW with R1 and R2 shorted as in the simple oscillator circuit, to about $200 \mu \mathrm{~W}$ with R1 $=\mathrm{R} 2=100 \mathrm{k} \Omega$. Due to the increased output impedance the oscillator is very sensitive to loading, and so an inverter is added as an output stage.

The crystal oscillator of Fig. 5.15 requires only about $30 \mu \mathrm{~W}$ with a supply of 5 volts, and the presence of R1 and R2 has the added effect of stabilising the frequency against variations in supply voltage.


THE measurement of capacitance has always been more difficult than the measurement of resistance, voltage, etc.

The traditional method of performing the operation is with some form of impedance bridge, but this can involve a lengthy ritual of balancing and adjustment to obtain a final reliable reading.

The circuit to be described will allow instant measurement of capacitance from less than 1 pF to greater than $10 \mu \mathrm{~F}$, displaying the result on an ordinary multimeter.

## THEORY OF OPERATION

The add-on capacitance unit uses simple, conventional techniques and relatively few components. Referring to the block diagram (Fig. 1) it will be seen that there are three basic sections: an oscillator, a virtual earth amplifier, and a precision rectifier arrangement whose output feeds a voltmeter (a multimeter set to a range whose f.s.d. dies between $1-3 \mathrm{~V}$ ).


Fig. 1. Block diagram of the Add-on capacitance unit

If we assume the sinewave oscillator is set to a frequency $f_{0}$, its output voltage is $V_{0}$, and the unknown capacitance value is C , then simple theory yields the current flowing into the virtual earth as being:

$$
i=\frac{V}{Z}=\frac{V_{0}}{1 / 2 \pi f_{0}} \overline{\mathrm{C}}
$$

This current is directly proportional to the admittance (the reciprocal of impedance) of the capacitor which, in turn, is proportional to the value of the capacitance and the frequency.

## VIRTUAL EARTH

Since the current cannot flow into the inverting input of the operational amplifier, an equal and opposite current from the output will flow via the feedback resistor $\mathrm{R}_{\mathrm{a}}$ such that the two cancel out at the inverting input providing the so-called "virtual earth".

The voltage appearing at the output of the op. amp. will thus be $i \times \mathrm{R}_{\mathrm{a}}$. However, since $i$ itself is proportional to capacitance and frequency (of the oscillator) and the output of the virtual earth amplifier is proportional to $i$, it follows that this output voltage will also be proportional to the feedback resistor $R_{a}$ as well as the capacitance and frequency.

In practice $R_{a}$ and the frequency $f_{0}$ are switched to allow a very wide range of capacitors to be measured (less than lpF to well over $10 \mu \mathrm{~F}$ ) with good accuracy (dependent on the quality of the meter and components, but can be as good as 1-2 per cent).
The output from the virtual earth amplifier is then rectified with a precision rectifier and presented to the voltmeter (multimeter) for display. The precision


Fig. 2. Full circuit diagram
rectifier merely uses a further op. amp. to overcome diode forward voltage drops and thus obtain accurate rectification down to low output levels.

The circuit diagram is shown in Fig. 2.

## INTEGRATION

The output from the unit consists of half sinewaves, and it is left to the meter to integrate these and provide a continuous reading. No problems were encountered with the larger type of meters (AVOs, etc.), but with smaller ones needle "judder" may cause annoyance. If this arises the effect can be alleviated with the simple addition of two extra components.

If a $100 \mu \mathrm{~F}$ capacitor is connected across the output of the unit via a $330 \Omega$ resistor and the meter output is taken across the capacitor, the integration process is greatly improved and the judder cut down considerably. The addition of these components increases the meter reading a certain amount and therefore the gain of the rectifier stage must be reduced to maintain accurate callibration.

## COMPONENTS

The only components that are required to be of any appreciable accuracy or stability are the range resistors R8 to R11. If possible these should be 1 per cent types, otherwise 5 per cent types can be used if they are "hand-picked" with an accurate ohmmeter.

The integrated circuits are all 741 s . Although it was initially suspected that bandwidth/slew rate limitations would prohibit their use this was not found to be the case in practice, and high accuracy was maintained throughout all ranges. This obviously keeps the price down and maintains appeal to the economy-minded constructor.

Resistors mounted on the Veroboard have to be small if they are to be mounted horizontally. There is no reason why slightly larger resistors should not be used provided they are end-mounted.


## STABILISATION

The thermistor specified is the popular R53 type. When used in Wien bridge oscillators of conventional design these have the property of stabilising the output at a little less than 1 V r.m.s. This was found to be rather low for this application and hence a potential divider has been inserted between the output of the 741 and the thermistor.

This causes the thermistor to think that the output level is lower than it is and the circuit stabidises at an output level of around 2 V r.m.s. as opposed to the original $700-800 \mathrm{mV}$.

One of the unfortunate side effects of using some types of thermistors as amplitude stabilising elements is the time required for the output to stabilise after range switching. This is particularly so in this case where a large frequency change is performed. If it is felt that the settling time is too long (it in fact
then be checked to be at approximately at earth potential this is a good test as it is a quick method of checking all is well throughout the complete circuit). Solder bridging, missing Vero breaks and components not properly soldered in are the sort of faults usually found to be at the root of any problems encountered.

The oscillator section should begin to oscillate within a few seconds of switching on; checking and re-checking once again being necessary if no output is obtained.

Calibration should commence with the rectifier gain potentiometer VR3 being set to minimum gain (slider nearest to D2 cathode). The two operating frequencies 20 Hz and 20 kHz should then be set as accurately as possible with a scope. If one is not available then VR1 and VR2 should be adjusted to their mid-positions.


Fig. 3. Component layout and board-cutting details
amounts to about 3 or 4 seconds) án alternative f.e.t. stabilised arrangement can be substituted (see "Modifications").

## CONSTRUCTION

A certain amount of dexterity is required in the construction of the Veroboard as space is very much at a premium. If the specified components are used no great problems should be encountered, and the unit can easily be built on the free board provided.
Construction should commence by cutting the breaks on the Veroboard with either a proper Vero spot-face cutter or a drill of correct size. Components should then be soldered in with care to avoid bridging of parallel tracks. Wire links can be made from stripping ordinary single cored connecting wire or using greater than 22 s.w.g. tinned copper wire from other sources.

C1, R1, C2, R2 and R5-9 are mounted on their respective switches to ilessen the number of components on the Veroboard. Obviously, if a larger piece of board is available they can be mounted adjacent to the other components.

## CALIBRȦTION AND TESTING

Firstly, it is worthwhile mentioning that initial setting up and testing of the device is made easier by having an oscilloscope at hand.

As a precautionary measure the current supplied to the unit should be measured when the unit is first tested. This should be in the region of $5-10 \mathrm{~mA}$ using 9 V supplies. The outputs of the 741 s (pin 6) should

## COMPONENTS .

Resistors


All resistors $\frac{1}{10}$ W 5\% unless otherwise specified

## Capacitors

C1, 3 1nF plastic or ceramic
C2, $4 \quad 0 \cdot 47 \mu \mathrm{~F}$ plastic or ceramic (pref. type C280)
C5 $\quad 2.2 \mu \mathrm{~F}$ tantalum 10 V

* C6 2.2 $\mu \mathrm{F}$ elect. 10 V

Potentiometers
VR1-3 $10 \mathrm{k} \Omega \mathrm{min}$. vertical
Semiconductors

| D1-3 | 1N4148 |
| :--- | :--- |
| *D4 | 1N4148 |
| *TR1 | 2N3819 |
| IC1-3 | 741 |

## Miscellaneous

R.S. Components "midget" wafer switches, 3 -pole 4 -way, and 4 -pole 3 -way. Case: Vero plastic box $120 \times 65 \times 40 \mathrm{~mm}$ code no. 65-2518. Knobs and hardware, crocodile clips, two PP3 batteries

* Components marked with an asterisk required for optional modification only-see text


Internal layout of the add-on unit. The batteries are held in place with an aluminium bracket and a 30 mm CSK 6BA bolt. There is enough room either side of the free board to allow it to be held in place with the 6BA bolt/nut arrangement as shown


Fig. 4. Alternative f.e.t. stabilising network around oscillator section

## PRECISION CAPACITORS

At some stage in the calibration procedure it is necessary to refer to either one, or (preferably), a range of accurate capacitors. These can often be found on ex-equipment circuit boards which are generally on sale at many electronics stores. An ideal range would contain such values as $10 \mathrm{pF}, 100 \mathrm{pF}$, $1 \mathrm{nF}, 1 \mu \mathrm{~F}, 10 \mu \mathrm{~F}$, all within 20 per cent tolerance. Obviously it may prove difficult for some constructors to obtain such capacitors, in which case less accurate ones will have to be resorted to. The majority of the above mentioned capacitors are in fact used for checking and it is possible to calibrate the capacitance unit successfully with only one $10,000 \mathrm{pF}$, exploiting the fact that two ranges overlap $(10,000 \mathrm{pF}$ and 10 nF ). It may therefore be considered worthwhile to invest in one precision $10,000 \mathrm{pF}$ capacitor and use standard types for spot checks throughout the other ranges.
Set the $\mathrm{nF} / \mathrm{pF}$ switch to nF and check that the oscillator output is about $1.5-2 \mathrm{~V}$ r.m.s. Set the multiplier switch to $\times 10$. Connect the $10,000 \mathrm{pF}$ capacitor to the test leads and the "meter output" to
an appropriately adjusted multimeter (a low voltage d.c. range with an f.s.d. between 1 and 3 V ). The capacitance unit is designed to give 1 V output for "full-scale" reading on each range; hence, if a 1 V range is available this would do best.

The rectifier gain control should now be increased with VR3, and if all is well it should be possible to adjust the output level such that the meter is reading 1V. The nF/pF switch should now be set to the pF position and the multiplier switch to 10,000 (corresponding to $10,000 \mathrm{pF}$ f.s.d.). The meter should read in the region of 1 V and any discrepancy adjusted out with the 20 kHz trimmer VR1.

If this cannot be done, and no scope was initially available to set the two operating frequencies reasonably accurately, VR1 should be adjusted to give as near 1 V output as possible and VR3 then used to set it exactly. In doing this the nF range will be misaligned, and therefore, with the $10,000 \mathrm{pF}$ capacitor still connected, and the range and multiplier switches once again set to read 10 nF, VR 2 should be used to adjust the output to read 1 V once more. It is a question of juggling with the three calibration potentiometers (VR1-3) as outlined above until the unit is satisfactorily calibrated, although it is worth stressing that an oscilloscope can save quite a lot of time in this process.

## MODIFICATIONS

Earlier it was mentionted that the response time of the thermistor was rather long and that it could be reduced by inserting an f.e.t. stabilising arrangement in place of it. The modified section is shown in Fig. 4. The output of the oscillator is rectified and then used to bias the gate of an $n$-channel f.e.t. If the output level reduces the f.e.t. is turned on which increases the gain of the amplifier and stabilises the level. In practice the value of the feedback resistor R14 is a little critical; if it is too large the output level will not be held stable, and if too small, no output will result. A value of between $270 \Omega$ and $1 \mathrm{k} \Omega$ was found to be suitable.


Some time ago we were asked to design and construct an inexpensive unit to enable a blind man to steer a yacht on a straight course. The design, which is a direct result of that request, is fully described in this article.
In addition to being of immense help to blind sailors, the design will also assist those sailors who use wind vane steering, as it can provide an off course alarm, and also power boat enthusiasts, as it can be used to steer a straight course without the need to look away from the water ahead. It could also prove very useful on a long passage where a compass course must be sailed, particularly at night, as it allows the use of ears rather than eyes, which can become tired.
The audible output from the unit is provided through a crystal earpiece and can be a high frequency, no output or a low frequency. The no output indicates that the yacht is on course and the high or low frequency that the yacht has gone off course in one direction or the other. The width of the no output (or dead) band can be varied from about 5 degrees ( 2.5 degrees off course on either side) up to about 50 degrees ( 25 degrees off course either side) this is to suit the conditions and the response of the boat/helmsman and is adjusted by the helmsman with a sensitivity control.

In use the boat is put on course, the compass is revolved until the unit gives no output at maximum sensitivity and the helmsman then steers to keep no output, adjusting the volume and sensitivity to suit himself. When used as an of course alarm a relay switches on a loud alarm to indicate that the boat has gone off course by more than the amount previously set.

## CIRCUIT OPERATION

Two Hall effect probes are mounted on a suitable compass to detect the position of the magnet inside that compass. The Hall effect probes (H1 and H2) are fed with a constant voltage by TRI (Fig. 1), the voltage being derived from the forward voltage drop of D1 and D2 in series (about $1-4 \mathrm{~V}$ ). The output from each probe is fed to an input of the 741 op . amp. ICl, one to the inverting input and one to the non-inverting input Provided both inputs are at the same level the output at pin 6 will be zero (comparator arrangement) this can be corrected by adjustment of the offset control VR1.

If the pole of the compass magnet is between the two probes the output from them will be similar. If the pole moves towards one probe the output from that probe will increase (see section on Hall effect later) and from the other decrease-this will cause the output of ICI at pin 6 to rise or fall at a rate dependent on the setting of the sensitivity control VR2 (this provides a variable degree of negative feedback to IC1).

The output from pin 6 is fed directly to the bases of TR2 and TR3 which act simply as switches to prevent the relays from loading the output of ICI. If the output goes high, TR2 is turned on, thus connecting the two relays across the 0 V and +9 V lines. Relay RLA will be turned on, thus connecting the supply to the unijunction oscillator formed by TR4 and its associated components and producing an audible output in the crystal earpiece connected to SK1. Relay RLB will not turn on due to the presence of D3.

If however the output at pin 6 goes low TR3 will be turned on thus operating RLA and RLB, this


The complete Audio Compass also showing the audible warning device used as the off course alarm
not only connects the oscillator but also shorts out R5, thus greatly increasing the frequency of the oscillator and hence of the output at SK1. The audible note thus indicates towards which probe the compass magnetic pole has moved.

The sockets SK2 and SK3 across RLA1 may be used to trigger an audible alarm, powered by the yacht's or other supply, to give an audible off course alarm. This facility can be set to trigger from about 2.5 degrees off, to about 25 degrees off by means of VR2 but does not indicate which way off course the yacht has gone (more about this later), and when in operation prevents the use of the normal audible output. The facility is added for those who use some form of wind vane self steering. It will provide an audible alarm if the yacht has been steered off its original course by the preset amount. The alarm recommended is the RS type audible warning device 12 V or 24 V , as required.

## COMPONENT NOTES

One or two further points concerning the circuit operation should be made clear before we proceed, these concern the components used and their siting in the unit. It was found necessary to provide a stabilised supply for the two probes as their output varies considerably with the current flowing through them. The simple supply formed by TR1, R1, D1


Fig. 1. The complete circuit diagram of the Audio Compass

and D2 was found to be perfectly adequate, provided internal cells are used and the unit is not connected to the boats supply. The components forming this stabiliser are sited as near the two probes as possible in order to negate any lead resistance which may prove troublesome.

The values of resistors R2 and R3 set the limits to the sensitivity of the unit and those shown were found to be most suitable. Reduction of R3 will decrease the minimum dead band, but if this is taken too far the unit will be difficult to set up and it would become impossible to keep within the dead band when in use.
Similarly, reduction of R2 will increase the dead band, but if this is taken too far, the limits of the sensing probes will be reached and either no output will result or only one note may sound. Experimentation with these values, either up or down, will not harm the unit and some constructors may find it helpful to do this. It is not recommended that preset resistors are permanently employed as their value could easily be altered with a knock.

It should be fairly obvious from the circuit description above that relay RLB must operate at the same instant or preferably before RLA. If this is not the case the output will always start at a low frequency and then go high if the voltage at TR2/TR3 emitters goes low.
To prevent this, the working voltage of both relays must be checked and that which operates at the lowest voltage used for RLB; the operating voltage will probably be around 3 V . For this same reason a germanium diode must be used for D3 since the voltage drop across this will only be about 0.2 V , instead of $0: 7 \mathrm{~V}$ for a silicon diode.

The two diodes used in the stabiliser circuit must be silicon diodes and are used to provide a "Zener" voltage of about 1.4 V .

## hall effect

The Hall Effect was discovered by E. H. Hall about 90 years ago and the principle involved accounts for the deflection of cathode ray beams in magnetically deflected tubes, so it has been employed for some considerable time, although many readers may not be aware of it.

Basically the effect causes a voltage (the Hall voltage) to be set up in a conductor or semiconductor in the presence of a magnetic field when an excitation current flows through the conductor or semiconductor. The effect is illustrated by Fig. 2. The
current $I_{e}$ is the excitation current flowing in the material which is in the presence of a magnetic field $\beta$ this causes the Hall voltage $V_{\mathrm{h}}$ to be set up.
The Hall voltage is actually caused by the effect of the field on the electrons flowing in the conductor. The electron flow is illustrated in Fig. 3a and the distorted flow caused by a magnetic field in Fig. 3 b .

The electrons tend to build up along the edge of the conductor and, since they are negatively charged give rise to the Hall voltage as indicated. The maximum Hall voltage is limited because the abundance of negatively charged electrons tends to repel further electronis (like charges repel), hence a state of equilibrium is reached when the magnetic force equals the repulsion of further electrons. This state is reached in much less than a microsecond and the Hall voltage will therefore quickly follow any variations in magnetic field. If the excitation current is varied, a greater number of electrons are introduced and hence $V_{\mathrm{h}}$ increases.
It is thus easy to see why $V_{11}$ is directly proportional to both $I_{\mathrm{e}}$ and $\beta$.

## PRACTICAL CONSIDERATION

In the application described the Hall voltage is minute since the field around the compass magnet is also very small. In addition to this an offset voltage is set up in the device which causes a continuous voltage of about 1.5 V per ampere (excitation current) to appear in addition to the Hall voltage. This offset voltage can be greatly reduced by the design and material of the probe-of the order of 2 mV or less per ampere-but such probes are expensive (about $£ 30$ each-as opposed to about $£ 1$ each for those used).

The effect of the offiset voltage is taken care of in the circuit of Fig. 1 by using the 741 a comparator so that it only senses the difference in the input voltage and not its level. Since two similar probes are used the offset voltage on each will be similar and any slight variations can be cancelled by the offset control VR1 (a multiturn preset) which varies the bias on the two input circuits of ICI.

We have shown that variations in the excitation current will provide variations in the Hall voltage. To provide stability both probes are fed by the stabiliser. Although this does not hold the current through each probe constant it has been found to be perfectly adequate since the probes are similar and therefore have a similar resistance/temperature
characteristic. They are also mounted relatively close to each other.

The current through each probe has been set to about 15 mA to provide enough sensitivity, consistent with reasonable battery life when using a PP9 battery. This current could, if required, be increased-by increasing the Zener voltage set by D1-D2-up to a maximum of 75 mA for each device. If this maximum is approached, steps must be taken to ensure that no one probe is exceeding that current.

## HOUSING

The construction of the unit has been kept as simple and straightforward as possible. It was decided that to be of use to the blind yachtsman it

## COMPONENTS . . .

## Resistors

| Resistors |  |  |
| :--- | :--- | :--- |
| R1 $22 \mathrm{k} \Omega$ | R4 | $22 \mathrm{k} \Omega$ |
| R2 $150 \mathrm{k} \Omega$ | R5 | $120 \mathrm{k} \Omega$ |
| R3 $5.6 \mathrm{k} \Omega$ | R6 | $1 \mathrm{k} \Omega$ |
| All $\pm 10 \% \frac{1}{4} \mathrm{~W}$ carbon |  |  |

## Capacitor

C1 $0.039 \mu \mathrm{~F}$
Potentiometers
VR1 $10 \mathrm{k} \Omega$ multiturn preset
VR2 $100 \mathrm{k} \Omega \mathrm{lin}$. carbon
VR3 $2 \cdot 2 \mathrm{k} \Omega \log$ carbon
Semiconductors
H1, H2 SE3V 566 Hall effect probes ( 2 off-Electrovalue)
D1, 2 any small silicon diodes ( 2 off )
D3 any small germanium diode
TR1, 2 2N2926
TR3 2N3702
TR4 TIS43
IC1 741 op amp

## Miscellaneous

RLA1, 2 6-9V d.c. 700 coil reed relay (348-970 Doram 2 off)
SK1 Line jack socket and plug (PL1) to suit X1
SK2, 3 Plastic encapsulated banana sockets and plugs (PL2, 3) to suit (2 off each)
S1 D.p.d.t. switch with thread to match dolly cover-see miscellaneous
B1 PP9 9V battery and clips
B2 PP39V battery and clips
X1 Crystal earpiece with plug and lead at least 1 metre long.
WD1 Audible warning device 12 V or 24 V as required (Doram). Only required for off course alarm function.
Cases $188 \times 110 \times 60 \mathrm{~mm}$ and $100 \times 50 \times 25 \mathrm{~mm}$ Vero or Bocon (West Hyde Developments) plastic boxes with interlocking lid; cable gland ENCGQ (West Hyde Developments); plastic dolly cover for S1 (WS234 Home Radio Components); knobs pointer (2 off-see text); Veroboard 0.1 in matrix approx. $100 \times 100 \mathrm{~mm}$; connecting wire; 4B.A. fixings; 4 way cable at least 2 inetres (see text); three suction pads, two terry clips for 25 mm diameter tube (stern pulpit).

## Compass

Sestrel Junior dingy compass with gimbálled mount (see text).


One way of mounting the compass unit on the rail. It could also be inverted so that the compass is more easily accessable
would not only need to be portable but also easily able to be fixed and used on any yacht, as few blind people own their own boats.

It is necessary to mount the compass where it can be adjusted to set the course and where it is free from knocks. To this end the compass unit has been equipped with two Terry clips so that it can be fitted to the stern pulpit of most yachts.

Similarly, the control unit which measures about $190 \times 110 \times 65 \mathrm{~mm}$ is fitted with rubber suction pads so that it can be attached to any smooth fiat surface near the helmsman. The two units are linked by a single four-way lead. This lead must be long enough to cover most situations (about 2 metres)

The control unit carries the batteries and has an output for the earpiece as well as sockets for an audible alarm. Both boxes carrying circuitry should be fairly splash proof and all metal work must be able to stand up to salt water type environment.

The control box of the Audio Compass, this is fitted with three suction pads for mounting purposes


Obviously if the unit is to be used only on one craft or if it is to be used only as an off course alarm, it could be permanently fixed and in the latter case could be completely housed below deck. The off course alarm only application allows certain sections of the circuitry to be omitted, more about this later, however, since the cost of these sections is relatively low we would advise the constructor to build the complete unit so that the audio compass can be used should the need arise. This also means that the owner would be able to offer a blind crew a useful position and, from the author's own experience, this could prove to be valuable and interesting for both parties.

## CONSTRUCTION

Construction of the two units is shown in Figs. 4 and 5 . There are no special precautions other than saying it is probably safer to use a holder for ICI. This is useful when changing i.c.s as we have found that there are a number of duff ones about. It is also necessary to take the usual precautions when soldering D3, since this is a germanium device and thus casily damaged by excess heat.

The output socket for the crystal earpiece is mounted on a 300 mm length of twin-cable which passes out of the case through the cable gland. This adds extra length to the lead and allows the box to remain sealed (it is difficult to get a sealed jack

## CONTROL UNIT



Fig. 4. Construction and wiring of the control unit. Veroboard layout is shown above with connecting points indicated by numbers which tie up with the lower diagram and Figs. 5 and 6

socket). The socket can be a "line" type or can be mounted in any suitable small container and may be easily changed if it becomes badly corroded.
The fitting and wiring of the two probes is shown in Fig. 6. These probes are very small and must be handled and soldered with great care in order not to damage the leads. They do not seem to be particularly heat sensitive but excess heat should be avoided. The probes are eventually covered with Araldite to protect them and fitted--square marked side inwards-against the plastic ring which is fitted over the compass, in line vertically with the magnet.
Construction of the compass unit is shown in Fig. 6. The materials used should be plastic or brass as
indicated, because these are non magnetic and corrosion resistant. The revolving ring is made of Contiboard white iron-on edging which is used glue side inwards with the ends overlapped and "ironed" together to form a ring.

## INITIAL TESTING

Before testing the unit the supply current on the positive line should be checked to ensure the probes are not consuming too much current. Supply current should be about 30 to 50 mA (depending on the output of ICl ) and definitely not more than 60 mA . If all is in order the unit can be tested for correct operation.

## COMPASS UNIT



Fig. 5. Layout and wiring of the compass unit. The lead outlets can be sealed with silicon compound


Fig. 6. Fitting and wiring of the two Hall effect probes. Basic arrangement of the compass unit is also shown, the wires to the probes should be extra-flexible to allow the compass to swing freely


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Internal view of the control box showing general layout. Foam rubber can be used to hold the batteries in place

To do this switch on and, with the sensitivity control turned fully up and the probes away from the compass, adjust the offset control VRI until no output results or until the output frequency is just on the point of changing. Next introduce a metal screwdriver between the two probes and move it setween the probes. The magnetism usually contained in the screwdriver should be enough to cause the two output signals to be produced, depending on which probe is approached.

If the unit does not function correctly the output of the i.c. can be checked, it should vary from +7 to - -7 volts with variation of magnetic field at the probes. Once this is established a similar voltage should appear at the emitters of TR2 and TR3 and this should cause RLA and RLB to operate. Check this by measuring the resistance across RLAI and RLBI. If all is well but no output results the fault must be in the unijunction oscillator, check the wiring and test for fatulty components.

Voltages on ICl pins 2 and 3 -the Hall voltage
Construction of the compass unit. Use of a connecting block allows the two units to be separated

plus offset-should be about 0.3 V and it may be possible to see slight variation of this on a high impedance voltmeter ( 20.000$) \Omega / \mathrm{V}$ plus) if one pole of a magnet is brought close to the relevant probe.

ALARM ONLY
Should it be decided to construct only an off course alarm the unijunction oscillator, relay RLB and D3 may be omitted. The contacts of RLAI then go only to SK2 and SK3 and are wired to the alarm and external supply as shown in Fig. 1.

The omission of these parts will save very little current and make little difference to the duration of the battery supply. With the above omissions the alarm will sound whichever way off course the craft has gone and thus no indication of direction is provided. If direction indication is deemed necessary for the alarm function, two different alarms should be employed e.g. that specified and a bell. The second alarm being wired to RLBI-which, together with D3 must be retained-in the same way as indicated for WD! and RLAI. In addition to this a second germanium diode must be inserted in series with RLA, in the opposite polarity to D3. Thus when the output voltage of TR2/3 goes positive RLA will operate and when negative RLB will operate.

If as suggested earlier, the complete unit is constructed although only to be used as an alarm, the output carpiece can be left disconnected while in use-this will not affect operation of the unit or do any harm.

FINAL ASSEMBLY
The compass is gimballed in one direction only to take care of the heel of the craft, the floating card and magnet can move to overcome any pitching. The probes must be covered in Araldite and fixed to the ring on the compass housing in line with the magnet. A course setting line can be drawn between the probes.

It has been found that Araldite will not produce a good bond on the plastic ring and this is all to the good as it means that the probes and their Araldite covering may be prised off if necessary.

The possibility of using compasses other than that specified can be investigated simply by affixing the probes in the most suitable position (as near to, and on the same plane as the magnet) with insulation lape and trying the unit. The compass should be of good enough quality to function correctly without violent swings when in use.

Once in place the probes and their wiring can be protected by a couple of layers of plastic insulation tape. It must be noted that the ring should not be continually rotated on one direction as this will eventually break the leads. If this is thought to be a problem a slop should be fitted to the rotating section to prevent it going past 360 degrees.

When complete the unit can be set up by arranging the compass so that the north pole lines up with the line between the probes. Preset VRI should then be adjusted with VR2 at maximum sensitivity, until no output results or the frequency just changes. The unit is then ready for use.

The complete unit is intended to be reasonably water resistant and to this end a cable gland is used where the wires enter the main case. Also a flexible plastic dolly cover is used to protect SI from the influx of water.


Another method of mounting the compass unit. It should be remembered that the gimbal takes care of yacht heel

The simplest method of sealing the two potentiometers is to employ knobs with a fairly large diameter and flat underside. Two cut-down babies" teats are then glued to the case so that when the knobs are fitted they push down on the rubber ring forming a reasonable seal. The teats can be easily and cheaply replaced when worn.

The cable entries to the compass unit can be sealed with silicon bath sealing compound and this could also be used around the joint on the smaller case once final testing is complete. The larger case must be easily taken apart to facilitate battery charging.

## IN USE

Due to the design of the unit similar output notes and dead band are produced with respect to both the north and south poles of the compass. This is not a problem once the boat is on course but should none the less be noted.

The off course signal will continue to sound, should a correction not be made, until the craft has passed through 180 degrees. Because of this action it is always best to set the probes to sense one pole,
say north, as this then provides a standard output with variation in course, e.g. if going off course in an easterly direction a high note would sound if in a westerly direction a low note. Should the other pole be sensed, these notes would reverse.

Due to the fact that the compass specified cannot be corrected and that its environment will change, it should not be used as an accurate course indicator. It is better to put the craft on the correct heading and set the audible compass (at maximum sensitivity) to suit. It is possible to make up a normal deviation chart for the compass, if it is mounted in a fixed position, to enable accurate setting should this be required.

The prototype compass has been successfully tested in various yachts and it has been found that in most sea conditions the movement of the craft gives rise to bleeps from the unit before a continuous note sounds, these bleeps gradually increasing in length as the craft goes off course until a continuous note sounds. This provides a good indication of the rate of change of heading, of how far off course one has gone, and therefore of the amount of correction required. This also provides good indication when coming back on course, since the reverse then happens with the bleeps getting shorter until silence prevails.

A similar output will result when an off course alarm is employed-if the first few bleeps do not arouse the crew the continuous note soon would.

## COST

The complete unit can be constructed for approximately $£ 20$, about half of this being the cost of the compass specified. The audible alarm, if required, will add approximately $£ 1.50$ to the overall cost. Please note that the above prices are only estimates and do not include V.A.T.

## ACKNOWLEDGEMENT

The author wishes to thank Des Sleightholme, editor of Yachting Monthly, who put forward the original idea, for his assistance in testing the unit and Jeff Bull who acted as a "guinea pig" and provided valuable criticism from the blind helmsman's point of view.

## A happy man. The Audio Compass

 allows Jeff Bull, who is totally blind, to take full control of the helm without any directional assistance from other crew members

# STM RDIUUTIT: UPDAIIE 

## V-F-V BREAKTHROUGH

There has been available for a number of years, an extremely useful and versatile class of circuit modules known as $V$ to F 's or F to V 's, to those professional engineers fortunate enough to be able to justify their expense. Yes, you guessed it, the reason why $V$ to F 's and F to V 's have not been seen in these pages before is because they have been much too expensive for amateur use. I am very pleased to report that this situation has now changed with the introduction by Teknis Electronics of a monolithic V-F-V which knocks spots off the expensive hand-made modules on the grounds of cost, size and performance!
The abbreviation $V$ to $F$ stands for "voltage-to-frequency" and F to V stands for "frequency to voltage" and the new device from Teknis, the A8400, will do both, unlike some of its more expensive predecessors which were often just single-function devices.
The usefulness of a device which can convert a d.c. input voltage into a directly related output frequency of between 0 and 100 kHz , and vice versa, is really quite mind boggling. Fancy turning your frequency counter into a digital voltmeter with 0.05 per cent linearity and 5 digit resolution? Just hook up an A8400 as a $V$ to $F$ and feed the output into your counter and you've done it-just like that ...:
Or, do you want to record slowly changing d.c. signals on a tape recorder with simple replay? Use an A8400 connected as a $V$ to $F$ to turn those d.c. signals into audio tones, then replay them later through an A8400 connected as an F to V-it's that easy. The possible applications go on and on, and are limited only by your imagination, the availability of a cheap monolithic device brings to amateurs the advantages enjoyed by instrumentation engineers for years.
The A8400 does cost rather more thian a 741 , in fact about $£ 12$ in small quantities, but this is about a third of the cost of its nearest rival, and a bargain in my book!

## PSEUDO-SINE

Consumer Microcircuits Ltd. are a British firm who make a very usetul range of m.o.s. integrated circuits
intended for use in audio-tone signalling and control system applications. Their range already includes tone transceivers, frequency sensitive switches and tone triggered timers, and has recently been extended with a fascinating little device known as the FX205 sinewave oscillator. As expected the FX205 is as unique as the other circuits in the Consumer Microcircuits range, and could well be useful for use in a wide range of amateur projects, from radio control systems to intercoms.

The FX205 generates a stable audio tone of between 25 Hz and 5 kHz using only a single external resistor and capacitor, the output signal being of a "pseudo-sinewave" shape generated entirely by the digital circuitry of the chip. Internal circuitry includes an astable oscillator, a monostable, a digital to analogue converter and a four bit binary counter.
In operation the astable is timed by the external RC network and the resulting output is divided down by the counter, the outputs of which drive the $D$ to $A$ converter which is weighted so as to produce the "pseudo-sine" output signal which is sufficiently pure for use as a signailing tone. The internal monostable can be used, if desired, to produce "tone bursts" up to ten seconds in length under the control of an external trigger signal, which could be just a push switch closure.
A "tone enable" input is also provided for use when gated operation is required, and the option is available of using an external synchronising signal instead of the internal oscillator. Where multiple-tone signals are required, the outputs of several FX205s can be "WIRE-OR'd" together.


Fig. 1. A8400 as a V to $F$ converter

## PAINLESS POWER

Monolithic audio power amplifiers are limited in power dissipation due to chip size constraints, so if you want to make those woofers throb with a bit more than the paltry 10 watts afforded by even the sturdiest monolithic devices, you'll have to either use a discrete design or go in for a pre-packaged hybrid.

A new series of hybrid amplifiers with output powers of $10,20,30$ and 50 watts is now available from Rastra Electronics Ltd., and you may find that one of these is more costeffective than a conventional discrete design, especially if you are suffering from the dreaded "wiring-up-itis" (wiring-up-itis, has of course been known to make expensive woofers disappear up their own infinite baffles, to make 2N3055's glow like beam tetrodes, and make grown men cry!)

The new hybrids from Rastra the S1-1010G, S1-1020G, S1-1030G and S1-1050G are made in Japan by Sanken and are complete power amplifiers suitable tor Hi-Fi, musical instruments and public address applications. The output stage is a quasi-complementary class $B$ type using passivated power transistors with good "second-breakdown" resistance, and built-in current limiting on the S1-1030G and S1-1050G. The oerformance specification seems quite yood, for the S1-1050G for example, full power bandwidth is from 20 Hz to 20 kHz while delivering 20 V r.m.s. to an 8 ohm load with a 66 volt supply. Full power t.h.d. is 0.5 per cent maximum, and the signal to noise ratio is typically 90 dB .


Fig. 2. FX205 as a tone-burst generator. The burst length is determined by $R_{T}$ and $C_{T}$

# NEXT момти! OUT AND ABOUT <br> <br> with PE 

 <br> <br> with PE} of models, this system features 7 channels with full proportional control and 2 channels which provide basic "on-off" control. The system operates on a time-division multiplex principle, and compares very favourably with the more

## PLUS

 expensive commercial units on the market at the moment
## for the MOTORING DIGITAL MILOMETER

Designed for use on car rallies or for applications where accurate map reading and navigation from a car are essential, or merely as an accurate elapsed distance indicator.
The use of $\mathbf{7}$-segment displays allows easy reading Day or Night

## AUDIO MILLIVOLTMETER

With a frequency response extending from below $\mathbf{2 0 H z}$ to above 200 kHz and a sensitivity which allows a f.s.d. of 1 mV to be obtained, the Audio Millivoltmeter will no doubt find a ready home on many an audiophile's test bench

# PRACTICAL <br> ELECTRONICS 

Our June issue will be published on Friday, May 14, 1976
PLEASE NOTE
It is In your Interest to piace a firm order with your newsagent-in advance. Back numbers are not avallable, so make sure of your copy now!

# Simple EMMPHItin 

ORiginally intended to generate a computer sounding voice for an amateur dramatic society, this device can be used to make "Dalek" type voices, and as such can provide hours of entertainment for children.'

The circuit is simple, easy to set up, uses little current, can be battery operated, and is suitable for fitting into a child's space suit or Dalek outfit, as well as its original application.

The output level is 500 mV , hence it is compatible with both the AUX input on most amplifiers (for stage use) and the many available i.c. power amplifiers for battery operation in a child's toy.

## CIRCUIT DESCRIPTION

The usual way of producing a mechanical voice is by synthesiser techniques such as ring modulation. An oscillator giving a sine wave output is used to amplitude imodulate the audio signal.

A circuit similar to this was tried, but whilst it worked and gave good results, it was somewhat tricky to set up, and there were doubts about its long term stability.

modulation input


Fig. 1. Voice input before and after modulation

The final circuit behaves in a similar way to a conventional amplitude modulator, but the modulating waveform is a square wave (Fig. 1).

The circuit diagram of the final circuit is shown in Fig. 2. ICl is a 741 operational amplifier arranged as an inverting amplifier. The circuit is designed for a single power supply, hence. R3, R4 provide a mid rail voltage at their junction. The gain of the amplifier is determined by the ratio R5/R2. The microphone used has an output of 15 mV so the gain is set to 30 to give the required 500 mV output.

COMPONENTS

Resistors
R1 $10 \mathrm{k} \Omega$ ( $100 \mathrm{k} \Omega$ for high output microphone)

R2 $1 \mathrm{k} \Omega$ ( $39 \mathrm{k} \Omega$ for high output microphone)
R3 $1 \mathrm{k} \Omega$
R4 $1 \mathrm{k} \Omega$
R5 $39 \mathrm{k} \Omega$
R6 $1 \mathrm{k} \Omega$
R7 $100 \mathrm{k} \Omega$
R8 $22 \mathrm{k} \Omega$
All resistors $10 \% \frac{1}{2}$ W carbon
Potentiometers
VR1 $50 \mathrm{k} \Omega \mathrm{tin}$.
VR2 $50 \mathrm{k} \Omega \mathrm{lin}$.
VR3 $500 \mathrm{k} \Omega \mathrm{lin}$.
All horiz. min. presets

## Capacitors

| C1 | $0.1 \mu \mathrm{~F}$ plastic or ceramic |
| :--- | :--- |
| C2 | $25 \mu \mathrm{~F} 16 \mathrm{~V}$ elect. |
| C3 | $1 \mu \mathrm{~F} 16 \mathrm{~V}$ elect. |
| C 4 | $0.1 \mu \mathrm{~F}$ plastic or ceramic |

Integrated circuits, diodes
IC1 741
D1 Any general purpose silicon diode (1N914, 1N4148, etc.)
Miscellaneous
Relay RLA R.S. Components (access through Doram) type: D.l.L. reed relay, Form A (for 6-12V supplies)
Veroboard $2 \frac{1}{2}$ in $\times$. 3 in $(65 \mathrm{~mm} \times 80 \mathrm{~mm}$ ), case and hardware to suit


Fig. 2. Circuit diagram of the modulator


Fig. 3. Details of the modulation oscillator


Fig. 4. Component layout and Vero cutting details

When relay RLA1 contact closes, the gain is reduced and is given by the ratio (VR1 + R6 in parallel with R5)/R2.

RV1 therefore controls the "depth" of the modu-- lation and thus the amount of distortion.

## MICROPHONE

The moving coil microphone used was somewhat bassy, so capacitor Cl was included to give a certain amount of bass cut. As the gain of the amplifier is determined by negative feedback, it is very easy to add shaping should such features as bandpass filtering be required.

If a ceramic or other high output microphone is used, resistors R1 and R2 should be increased in value to reduce the gain of the amplifier.

## CONSTRUCTIONAL DETAILS

The circuit is constructed on $0 \cdot 1$ in Veroboard, and layout and track cutting diagrams are given in Figs. 3 and 4. These are straightforward and should present no problems.

Trim pots are used on the circuit although there is no reason why the pots should not be mounted remotely.

To sum up: VRI controls the depth of the distortion, VR2 the volume, VR3 the rate of distortion. In theory VR1 should affect the volume, but in practice at the levels of distortion necessary, the effect is not unduly noticeable.

## MODULATION OSCILLATOR

The relay is "buzzed" by an oscillator (shown in Fig. 3) constructed from the ubiquitous 555 timer. The frequency of oscillation is controlled by R7, R8, C4 and VR3.
VR3 controls the "rate" of the modulation. The relay can be driven up to 250 Hz but it was found that the best results were given in the range $20-60 \mathrm{~Hz}$.
Diode DI clips any inductive spikes generated as the relay coil de-energises. This is included in the reed relay used in the prototype.

It might be thought that the life of the relay would be very short being maltreated in this manner, but as the relay used (and most reeds) have a mechanical life in excess of 10 million operations the author did not feel this posed any problem.

Contact life is not so easy to assess as it is determined by two conflicting factors. A large switched current causes contact burn, conversely a small arc helps to clean oxidation off the contacts. The current being switched in this circuit is infinitesimal, so the contact life will be determined by the dirt on the contacts. It is impossible to say when failure will occur, but the prototype has been working for several months without showing any signs of iminent death.

The supply can be anywhere in the range 6 to 15 volts (with suitable choice of reed relay). The prototype was built for 9 volts operation.

With a 9 volt supply, the current consumption is about 12 mA .

CMOS DIGITAL I.C.s
continued from page 389


Fig. 5.16. The bilateral switch as a voltage-controlled variable resistor. The curve shows the variation in resistance with control voltage. The measuring arrangement is also shown.


Fig. 5.17. Voltage-controlled oscillator. The voltage $V_{C}$ determines the frequency, and with a 10 V supply a range of between 10 and 15 kHz is obtained with the values shown

## VOLTAGE CONTROLLED CIRCUITS

The bilateral switch has so far been considered as an almost perfect switch; its resistance changes from about 300 ohms with the control pin at $V_{D D}$ to several megohms with the control grounded. However, it can also operate as a voltage-controlled variable resistor (VVR) if non logic-level voltages are applied to the control input (Fig. 5.16).

The resistance depends somewhat on the voltage levels at the terminals of the bilateral switch, and this causes slight distortion of the transmitted signal. The voltage at either terminal should not go above $\mathrm{V}_{\mathrm{DF}}$ or below $\mathrm{V}_{\mathrm{ss}}$. This simple $\mathrm{VVR}_{\mathrm{R}}$ features extremely good isolation between the control input and the terminals, the resistance being greater than $10^{12}$ ohms, and can be used as the basis for voltage controlled filters, amplifiers and oscillators. A single $n$-channel device can also act as a $\mathrm{VVr}_{\mathrm{R}}$, and a similar curve is obtained although the minimum "on" resistance may be as high as 1,000 ohms. A simple vco (voltage controlled oscillator) based on this and built from a single 4007 package (one device is not used) is shown in Fig. 5.17. The paralleled resistance of the $n$-channel mosfet and R1 varies between approximately 1,000 ohms with the mOSFET "on", and R1 with mosfet "off". A range of between 10 and 15 kHz is obtained with the values shown for $\mathrm{V}_{\mathrm{c}}$ of between 0 and 10 volts.

## Next month : Retriggerable monostable and digital low pass filters

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Items mentioned in this feature are usually available from electronic equipment and component retailers advertising in this magazine. However, where a full address is given, enquiries and orders should then be made direct to the firm concerned. All quoted prices are chose at the time of going to press.

## CASES

To complement their range of Minicases, Olson Electronics have just introduced a new range of robust, sloping front instrument cases.

Ideal for housing many of the constructional projects published in this magazine, particularly test gear, the cases are made from 20 g mild steel and the 45 degree sloping front panel from 20 g aluminium. The cases are only 95 mm high by 95 mm deep and supplied in three width sizes: $150 \mathrm{~mm}, 200 \mathrm{~mm}$ and 250 mm . The front panel is 100 mm by the required width.

Supplied with four rubber feet they are sprayed in light brown hammer finish and the front panels are finished in light grey semi-gloss enamel.

Full particulars of these excellent cases can be obtained from Olson Electronics Ltd., Factory No. 8, 5-7 Long Street, London, E2 8HJ.

## WORK HOLDER

A unit which allows the constructor a free hand when soldering components into small circuit boards is the latest product from. Special Product Distributors.
Called the JA500 Reversible Assembly Frame, a small board is clamped in position and a foam pad. in a covering lid, holds the components to be soldered firmly in position which allows the work piece to be turned over for soldening.

The frame will handle circuit boards up to $220 \mathrm{~mm} \times 170 \mathrm{~mm}$ and the overall dimensions of the assembly is $250 \mathrm{~mm} \times 280 \mathrm{~mm}$.

Further details and price of the Reversible Assembly Frame can be obtained from Special Product Distributors Ltd., 81 Piccadilly, London, W1V 0HL.

## CERAMIC SOUNDERS

There are very many practical applications for audible warning or indicating devices, some of which include paging, systems failure, etc. where the sound output requirement, whilst being less than, say, an intruder alarm, has the composite advantages of reliability and efficiency.

ITT are marketing a range of five piezo ceramic sounders of sound outputs varying below 93 dB . The tones available are continuous or pulsed:

Details are available from ITT Components Group Europe, Materials Division, Edinburgh Way, Harlow, Essex.

## NEW TOOLS

A new range of high quality American tools for electronics are being offered by West Hyde Developments. These consist of $4 \frac{1}{2}$ in curved needle pliers, $5 \frac{3}{4}$ in long: nosed pliers, 44 in side-cutters and 4in face-cutters.

Made from finest alloy steels, they have p.v.c. handles and are supplied in strong p.v.c. pockets which can be easily hung up.

As well as the tools, there are two mirrors with useful features; one has a magnetic base, the other a variable angle head that can be remotely controlled.

Further details are available from West Hyde Developments Ltd., Ryefield Crescent, Northwood, Middlesex.

## CATALOGUE

The new 100 -page Electronics Catalogue from Tandy now lists a very large range of components from light emitting diodes and integrated circuits to calculator keyboard and printed circuit etohant kits.

Although the semiconductors seem rather expensive they are all guaranteed to be first quality and not rejects or "fall-outs".

The catalogue is devoted mainly to their vast range of audio hi-fi equipment and includes complete systems. Test gear, car radios and aerials are also included.

Copies of the 1975/76 Electronics Catalogue can be obtained from Tandy Corporation (Branch UK), Bilston Road, Holyhead Road, Wednesbury, Staffs.


## Ceramic sounders from ITT



## TERTRELS

ONE of the things which usually causes students problems is the use of the decibel. In specifications it is quite common to find it used as the unit for gain. Somel mes the decibel is used for the power gain of an amplitier and sometimes for the voltage gain. Often the unit is used erroneously, and we must be sure of the meaning presented to us.

## THE DECIBEL DEFINED

First, let us took at the definition of the decibel. The "deci" means one tenth of" and the bel is a logarithmic snit of the ratio of two powers. Normally we use the Lnit when measuring gains or losses. Consider Fig. I which shows an anplifier of input impedance $Z_{1}$ with a load impedance $Z_{2} I_{1}$ and $I_{2}$ represent input and output currents, white $V_{1}$ and $V_{2}$ are input and output voltages respectively.


Fig. 1 Loaded power block from which equations are derived

Let us assume that $V_{1}$ is $10 \mathrm{mV} . Z_{1}$ is $10 \mathrm{k} \Omega, V_{2}$ is 10 volts and $Z_{2}$ is 100 . Then by Ohm's Law:

$$
I_{1}=\frac{V_{1}}{Z_{1}}=0 \cdot 1,1 / \mathrm{A}
$$

and

$$
I_{2}=\frac{V_{2}}{Z_{2}}=1 \mathrm{~A}
$$

Voltages and currents are all r.m.s. Numerically, the power gain ( G ) is therefore:

$$
G=\frac{v_{1} \cdot 1 .}{v_{1} \cdot I_{1}}-\frac{10.1}{10^{-2} \cdot 10^{-i}}=10^{10}
$$

Note that this is a number. It means that the output power is ten thousand million times greater than the inpui power. The voltage and current gains are respectively:

$$
\frac{V_{2}}{V_{1}}=10^{3} \text { and } \frac{I_{2}}{T_{1}}=10^{7}
$$

Obviously the voltage gain multiplied ty the current gain is equal to the power gain.
So far we have made no mention in our calculations of decibels. If the input power is $P_{1}$, and the output power $P_{2}$, then the gain in bels is:

$$
G=\log _{10} \frac{P_{2}}{P_{1}}=10 \text { bels }
$$

The decibel is only one tenth of a hel and therefore there are ten times more decibels than bels for a given gain.

$$
G=10 \log _{114} \frac{P_{2}}{P_{1}}=00 \text { decibels }
$$

## VOLTAGE GAIN IN DECIBELS

We sometimes find voltage gains expressed in decibels. and this is where the error and confusion arise. The definition we used said that the decibel contains a power ratio. We can rewrite our expression for gain as follows:

$$
G=10 \log \frac{V_{8^{2}}}{Z_{2} \cdot Z_{1}} \cdot \frac{V_{1}{ }^{2}}{V^{2}} d B
$$

When, and only when, $Z_{1}=Z_{2}$ we can say that:

$$
\begin{aligned}
& G=10 \log \left[\frac{V_{2}}{V_{1}}\right]^{2} d b \text { which in turn gives us: } \\
& G=20 \log \frac{V_{2}}{V_{1}} d B
\end{aligned}
$$

Students find difficuliy in remembering when to use $10 \log$ and when to use 20 log. If in doubt, always use $10 \log$ and always consider power Having said that we are then faced with information that gives voltage gains in decibels whe the input and output impedances of the device are differe it. Theoretically, the figures are meaningless but in practice the person specifying the gain has used $20 \log V_{2} v_{1}$, regardless of the fact that it does not apply. In our example, the voltage gain would be incorrectly stated as 60 dB .

Finally. may I appeal to uriters of specifications to either stick to power gain figures or else to quote the maximum sensitivity of the amplifier. We would then have the figures we are really interested $n$. That is the input voltage (and impedance) required to produce the quoted maximum output power.


## SALARY AND STATUS

Looking through the situations vacant columns in the professional electronics press it is clear that experienced engineers are still in demand both at home and overseas. But it is equally clear that the salaries offered to professional engineers have increased only relatively marginally during the past three years while unqualified people have enjoyed unprecedented increases in income.

Registered dock workers, for example, now enjoy a guaranteed minimum of $£ 3,000$ a year whether they work or not. Coal face workers are in sight of $£ 5,000$ a year. Shift workers on London Transport are in the $£ 3,000$ to $£ 4,000$ bracket, and complaining assembly-line workers in automobile plants are not nearly as bankrupt as their employers. There are even well-authenicated examples of unemployed people drawing up to $£ 5,000$ a year from the State, providing they have enough children and hire-purchase commitments.

Now and again one spots what looks, from the salary point of view, a winner. Up to $£ 8,000$, for example, was recently offered for a product planning manager in communications technology which, in this instance, meant satellite communications. Candidates were required to have "the maturity, standing and personality to nego tiate at all levels from Director downwards and the necessary drive to lead a team in expanding the company's capability in the microwave field and particularly space'.

But that was only the beginning. A candidate also needed to have had practical field experience,
have developed hardware, and preferably have commercial experience in the preparation of tenders. He must have knowledge of the national and international agencies concerned with space communications and, of course, all the technical standards in force. It would also be helpful if the candidate had some knowledge of line transmission and analogue and digital modems, multiplexing and switching equipment, data programs and video circuits.

The job entails forecasting the forthcoming market and defining the hardware needed, and producing in collaboration with development engineers, a programme to ensure that hardware is available at the right time at the right price. Quite rightly, the advertisement states that "This is possibly one of the most important appointments to be made in the Company for some time."

Clearly then, this is a key job which was no doubt keenly contested and ably filled. But it seems strange that such an important position, central to the company's future prosperity and that of possibly hundreds of work people should, in gross money terms, be worth no more than two bus drivers or one and a half hewers of coal, however worthy the drivers and hewers may be. After tax, of course, the differential becomes even less attractive.

## MAINSTREAM

If we now drop our sights a little and look at the mainstream of engineers we find experienced chartered engineers attracting salaries in the range $£ 3,000$ to $£ 4,000$ and technician engineers from $£ 2,000$ to a little over $£ 3,000$ for the best people.

Generally, the best payers are the Civil Service and the nationalised corporations. But, even here, salaries tend to be low. A communications technician for a gas board with at least five years' experience of u.h.f./v.h.f. mobile radio and a sound knowledge of the principles of microwave/multiplex links and digital systems can start with as little as $£ 2,361$.

At an armed services resettlement briefing for electronics tradesmen earlier this year, people soon to leave the services were surprised and dismayed to learn that their service pay and allowances were superior to the salaries they could command in "civvy street". They were mostly mature people, many with family responsibilities, to whom a salary of $£ 2,500$ and the need to find accommodation represented a major fall in standard of living and probably quality of life as well. They'd be better off to stay on.

## WORK OF LOVE

From a strictly financial point of view electronic engineers, be they of chartered or technician status or even totally unqualified academically, are their own worst enemies. They tend to love their work and regard it as a vocation more than a job. If they need to have employment to live, then there is nothing they'd rather be in than electronics with its everchanging technology and novelty, and its intellectual challenge. Provided they were receiving a reasonable reward they were contented.

Unhappily, 25 per cent per annum inflation has overtaken the professional electronics engineer, and those in the lower pay brackets are now barely above the poverty line. With inflation currently at 15 per cent and scheduled not to drop to single figures before the end of the year, the more poorly paid will soon be in distress and the better-off still steadily sinking in real terms.

## POINT OF CONFLICT

The Council of Engineering Institutes has now come out firmly with the proposal that professional engineers should join a trade union. By the Government sanctioning and even encouraging the widespread introduction of the closed shop, this move was perhaps inevitable. A great number of engineers will discover that if they are not union members they will have no job. A second compelling reason for the recommendation is that union muscle, ruthlessly applied, always achieves its objective. This is an established fact of which there are many recent examples.

The professional engineers are now in a dilemma. Professionally they have one code of conduct and as union members they will have another and these will often be in conflict. In a universally closed shop, to defy union instruction can mean expulsion from the union and thus expulsion from employment for the remainder of a working life. But equally, to comply with union instruction may involve both agonies of conscience and breaches of professional codes of conduct.

How this muddle of loyalties will be resolved remains to be seen. Perhaps there is no solution except to emigrate to the United States or West Germany where industrial affairs are conducted more logically and where real merit is rewarded realistically.

## MULTIPLE OCTAVE ORGAN

THIS is my original circuit for a simple organ, using the NE555V, which can play through several musical octaves on a limited keyboard of eight keys in two different ways.
The circuit (Fig. 1a) uses a NE555V operated in its astable mode, frequency of oscillation being given
as $f_{1}=\frac{1}{t}=\frac{1.44}{\left(R_{\text {in }} 8+2 R_{\mathrm{B}}\right) \mathrm{C}}$.
Thus $\frac{f_{2}}{f_{1}}=\frac{R_{\mathrm{A}_{1}}+2 R_{\mathrm{B} 1}}{R_{\mathrm{A}_{2}}+2 R_{\mathrm{B} 2}}$
The musical notes are in the ratio of $9 / 8: 10 / 9: 16 / 15: 9 / 8:$ 10/9:9/8:16/15.

This also shows that the musical ratio depends only on the ratio of the resistors R1 to $\mathrm{R} 8, \mathrm{R}_{\mathrm{B}}$ being kept constant; also the capacitor between pin 6 and pin 1 of ICl kept constant.

From eqn 1, with the note C at $262 \mathrm{~Hz}, \mathrm{~S} 1$ at 2:
$R 1=53 \mathrm{k} \Omega$ (i.e. $33 \mathrm{k} \Omega+20 \mathrm{k} \Omega$ )
$R 2=47 \mathrm{k} \Omega$
$\mathrm{R} 3=42 \mathrm{k} \Omega$ (i.e. $20 \mathrm{k} \Omega+22 \mathrm{k} \Omega$ )
$\mathrm{R} 4=39 \mathrm{k} \Omega$
$R 5=34 \cdot 5 \mathrm{k} \Omega$ (i.e. $33 \mathrm{k} \Omega+1.5 \mathrm{k} \Omega$
$\mathrm{R} 6=30 \mathrm{k} \Omega$
$\mathrm{R} 7=27 \mathrm{k} \Omega$
$\mathrm{R} 8=25.8 \mathrm{k} \Omega$ (i.e. $24 \mathrm{k} \Omega+1.8 \mathrm{k} \Omega$ ).
S 1 is a single pole, 3-way switch used for octave selection. At position 1 the notes range from $\mathrm{C}_{1}$ to $C^{\prime \prime \prime}$, i.e. 131 Hz to 262 Hz . Similarly at position 2 , the range is $\mathrm{C}^{\prime}$ to $\mathrm{C}^{\prime \prime}$ ( $262-524 \mathrm{~Hz}$ ) and position 3 covers $\mathrm{C}^{\prime \prime}-\mathrm{C}^{\prime \prime \prime}(524 \mathrm{~Hz}-1,024 \mathrm{~Hz})$.


As the organ notes consist of a pulse of fixed duration (determined by $\mathrm{R}_{\mathrm{B}}$ and capacitor selected by Sl ) repeated at musical frequencies, it is desirable to clamp the output pulses to a fixed duration to produce a "smooth" octave selection. A suitable monostable circuit using a NE555V is shown in Fig. 1 lb .

One of the inherent disadvantages of a simple organ is that the pressing of two or more keys will give only a note. However, this is exploited here to play two musical octaves on the eight keys without using the octave selection switch S1. It can be shown from eqn 1 that pressing key $c^{\prime}$ and key $d^{\prime}$ will produce note of $C^{\prime \prime}$ and that this occurs systematically as shown in Fig. 1c below.
This method will enable one to play notes in the upper octave without bothering to operate Sl .

The output is melodious as it consists of a pulse of short duration repeated at musical frequencies; a tweeter is recommended at the amplifier output to reproduce these short pulses more clearly. For simplicity the semitone keys are omitted but can easily be added by calculating the required resistor from eqn 1. With the semitone keys added, there will be 13 keys and the corresponding ratio of each frequency to the one before it is 1.0595 . Since the musical ratio is virtually dependent on R1 to R8, they should be preferably better than $\pm 5$ per cent. Close tolerance capacitors should be used for S1 for octave selection as they maintain musical ratios of the musical octaves.

Pek Yaw Kee,
Sarawak, E. Malaysia.

Fig. 10


EXAMPLE WITH S 1 AT 2
KEYS OF ORGAN


Fig. Ic


Fig. 1

## automatic time switch

This automatic time switch was designed to switch a radio and itself off after about half-an-hour, so that I could leave the radio on and go to sleep. The tuner has a start/reset switch which is illuminated by an l.e.d., see Fig. 1.

The circiut uses a NE555 timer as a monostable. The delay period is adjusted by VRI which compensates for the tolerances of R2 and Cl . The relay is a 6 V 2 -pole make type, with as high a coil resistance as possible. SI is a 2 -pole make switch and K3 is selected to suit the l.e.d. The relay contacts RLA1 interrupt the positive supply line to the radio.

> P. Levey,

London, S.E. 26.

## FURTHER USES FOR UNIJUNCTIONS



Fig. 1. Burglar Alarm

Rarely does one see the Unijunction transistor being put to any use other than as a relaxation oscillator. It can of course be put to many diverse uses.
In Fig. 1 it is used as a Burglar Alarm. Even if the microswitoh circuit is remade after it is broken the bell, buzzer, etc. will stay on. Most common unijunction transistors will sink up to one amp, and so relatively heavy loads can be used without resorting to a relay. To reset the alarm, one of the load leads is broken.
In Fig. 2 the Unijunction transistor is used as a Frequency Divider.

If half-wave rectified a.c. from the mains is put in the input, the first stage divides by five, as with the second, and the third divides by two. Surplus unijunctions and transistors can be used, but the capacitor charging resistors may have to be changed. Generally if each stage is disconnected from the previous one, it should run at a slightly lower frequency than its expected working frequency. The circuit can be adapted readily for other applications as it is cheaper than TTL dividers.
A. F. Rabagliati,

Oundle, Northants.


Fig. 2. Frequency Divider


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| :---: | :---: |
| MULTIMETER U4324 <br> 34 Ranges. High sensitivity. <br> 20.000 న/Volt. <br> Vdc-0. 6-1200V in 9 renges. <br> Vac-3- 900 V in 8 ranges. <br> ido- $0.06-3$ A in 8 ranges. <br> lac-0.3-3A in 5 ranges. <br> Resiatance:- $25 \Omega-5 \mathrm{M} \Omega$ in 5 ranges. <br> Accuracy-dc and R- $2 \downarrow \%$ of F.S.D <br> Size $-167 \times 98 \times 63 \mathrm{~mm}$. <br> ac and $\mathrm{db}-4 \%$ of F.S.D. <br> Supplied complete with storage case, <br> teat leads. spare diode and battery <br> PRICE \&11. 72 not P. \& P. 75 p | U4324 |
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| (P.E,), LEIGHTON ELECTRONICS 59 NORTH STREET, LEIOHTON BUZZARD Tel.: Leighton Buzzard 2316 (Std. Co | NTRE, 7 7EO 05253) |

Readers who run mobile discotheques, like myself, may be interested in this simple yet effective visual cueing device to be used in place of headphones on existing equipment with PFL (Pre-fadelisten) facilities. For those readers not familiar with PFL, it is a system which allows the operator or DJ to locate the start of a second record whilst a first is playing, and so eliminate the time gap between one record and the next.

This device described, gives the operator a visual indication of the start of a record by means of an l.e.d. modulated in brightness by the music signal.

The circuit, see Fig. 1, consists of an REC70 bridge rectifier DI and a small red l.e.d. type TlL 209, D2, connected as shown. The music signal is rectified by the bridge and produces a varying d.c. voltage across the l.e.d.

The prototype was constructed inside a standard jack plug, of the type with a long plastic barrel, see Fig. 2.

The system has been tested with several makes of disc mixer, and found very successful. In most cases a gain control for the PFL is incor-

porated in the mixer, and when using the visual cueing light, this invariably needs to be set at maximum for highest sensitivity. In this case the d.c. peak voltage is unlikely to exceed 3 V and result in any damage to the l.e.d. With this setting, it may be found that once
the record is playing the l.e.d. appears continuously on, but this is no disadvantage as the cueing light has only to indicate the start of the record.

S. E. Grist, Guildford,

"LIGHTS ON" INDIGATOR

THIS is a useful aid for motorists in that it gives a warning that the car lights are still on when attempting to get out of it.

The circuit (Fig. 1) is a simple unijunction transistor oscillator which gives a continuous note for about five seconds when the door is opened, that is, when the door switch is closed. The period selting can be changed by altering the value of C ?

When power is removed from, the circuit it will reset in five minutes.

As the audible warning is for negative earth vehicles, for positive earth interchange connections at $A$ and $B$.

$$
\text { R. A. Sudron, } \begin{aligned}
& \text { Shadwell. }
\end{aligned}
$$



## courtesy light timer

THE circuit in Fig. 1 is designed to extinguish a car interior light approximately 20 seconds after the car door is closed, allowing time to fasten seat belts, etc. The timer starls when the switch contacts open, and the light is extinguished automatically after the delay period has elapsed.
TR3 and Darlington pair TR4 and TR5 form a complementary bistable which is triggered on when TR5 collector is earthed by the switch S1, whose contacts carry the current for the interior lamp, LP1
When SI contacts open, the lamp current flows through TR5. About 1.5 V is dropped across this. turning on TRI via R3; C1 then charges through TRI and R1. When C1 has charged to about $5 \mathrm{~V}, \mathrm{TR} 2$ conducts and diverts base current from TR3. The bistable then turns rapidly into the off state, discharging C1 via D1 and R2 ready for the next operation. Battery drain in the off state is about 1 mA . which is taken by R3.


## Fig. 1

For loads of up to 6 W no heatsink is needed for TR5 and a very compact unit can be built, possibly in the lamp housing itself. Loads of up to 36 W can be switched with a heatsink. If the load is shorted the bistable should switch off without damage.

The circuit shown is for negative earth vehicles. A positive earth version can be made using a BD132 for TR5, transposing ZTX300 and ZTX500 transistors and reversing the polarities of C1, D1 and D2.
P. Albericci,

Stockport

THE following might be of interest to some of your photographically minded readers.

The latest fashion for Dual Fade Slide Presentation seemed a natural subject for solid state control, so triac circuitry came to mind. Most modern slide projectors use a low voltage halogen lamp and the low voltages necessitated a slightly different approach to circuitry.

Fig. 1 shows the basic interface circuitry which was built into a projector. This had spare pins available on its standard 6-pin DIN socket (pin 3 to earth, pin 2 is the slide change solenoid). The triac is simply wired in parallel with the existing lamp on/oft switch.

Fig. 2 shows the simple manual control used to fade the projector lamp. Zeners DI. D2 take the place of the diac used in mains voltage circuits. The high value of capacitance is required to provide the relatively large gate current to the triac which is sluggish in the low voltage conditions. VRI is a slider potentiometer for ease of use during a show, and VR2 is a preset which enables the full scale of the slider to be used.

A pair of projectors using this circuitry has been used for several successful slide-tape shows and the

## dUAL FADE FOR SLIDE presentation



Fig. 1


Fig. 2

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| 2．2 $\mu \mathrm{F}$ | 25 V | 7p | 470 $\mu \mathrm{F}$ | 25V $12 p$ |
| 4．7MF | 25 V | 7p | 470山F | 35 V 14 p |
| 10ヶF | 25 V | 7p | 470 $\mu \mathrm{F}$ | 83 V 20 p |
| $22 \mu \mathrm{~F}$ | 25 V | 7p | 1000， F | 90V 12p |
| 47 $\mu \mathrm{F}$ | 25 V | 7p | $1000 \mu \mathrm{~F}$ | 18V 17p |
| 47 $\mu \mathrm{F}$ | 40 V | \％p | $1000 \mu \mathrm{~F}$ | 25 V 200 |
| 47 $\mu \mathrm{F}$ | 63 V | 4 | $1000 \mu \mathrm{~F}$ | 50 V 38 p |
| 100 $\mu \mathrm{F}$ | 10V | 7p | $1000 \mu \mathrm{~F}$ | 83V 44 p |
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| 200uF | 10 V | 7p | $2200 \mu \mathrm{~F}$ | 25V 30p |
| 200 ${ }^{\text {F }}$ F | 50 V | 14 p | 2200， 1 F | 40V 53p |
| 220） F | 25 V | 10p | 4700～F | 18V 44p |
| $220 \mu \mathrm{~F}$ | 30 V | 11p |  |  |

POLYESTER C200 250 Y
$0.01,0.015,0.022,0.033,0.047,0.068$. all ip each． $0.1,0.15 .5 p ; 0.22 .7 p$ ； －33．8p；0．47，9p；0．68，11p； $1 \mu F, 14 p$ ； $2 \cdot 2 \mu \mathrm{~F}, 1 \mathrm{p}$ ； $3 \cdot 3 \mu \mathrm{~F} 63 \mathrm{~V}, 24 \mathrm{p}$ ．

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Case type
$1410,205 \times 140 \times 40 \mathrm{~mm} \quad \mathrm{E} 2.90$

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| :--- |
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| AB12 $76 \times 51 \times 25 \mathrm{~mm}$ | 48 |
| AB13 $152 \times 102 \times 51 \mathrm{~mm}$ | 30p |
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| AB15 $203 \times 1152 \times 76 \mathrm{~mm}$ | L1．42 |
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| AB17 $254 \times 114 \times 78 \mathrm{~mm}$ | E1．22 |
| $A B 19305 \times 203 \times 76 \mathrm{~mm}$ | E2．00 |

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$500 \mu \mathrm{~A}, 80 \mathrm{p} .10 \mathrm{p}$ P．\＆P．


Model UD．130．Fre－ quency response 50－ 15，000c／s．Impedance Dual 50 K and 600 ohms， 87．50．26p P．\＆ P ．
$42 \times 42 \mathrm{~mm}$ meters $1 \mathrm{~mA}, 10 \mathrm{~mA}$ ， $100 \mathrm{~mA}, 500 \mathrm{~mA}, \mathbf{2 2} 76.16 \mathrm{p}$ P．\＆P． $60 \times 45 \mathrm{~mm}$ meters $50 \mu \mathrm{~A}, 100 \mu \mathrm{~A}$ ， $500 \mu \mathrm{~A}$ and 1 mA VU meter， $62 \cdot 92$ ． llp P．\＆P．

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THis circuit (Fig. 1) was designed for use with an electronic voltmeter to indicate when the battery voltage falls below 7.5 V where the accuracy of the meter begins to deteriorate.

The circuit can be made to switch at any voltage by altering R1, R2, D1, and by swopping pins 2 and 3 there is a choice of either normally on or normally off operation. The normally off arrangement is shown as in this state the circuit only takes 1.4 mA .

\section*{P. Boscott, Banbury}
low voltage indicator


Fig. 1

CAR ALARM


THE requirements to be met for the car alarm were that it should give an audible/visual alarm for a preset interval and then reset to a "ready" state, the car meanwhile being immobilised. The quiescent power consumption must also be low.

The following circuit was devised which gives an alarm lasting about 20 seconds (bleeps horn and/or flashes headlamps) and breaks the ignition circuit, after which it resets ready to be triggered again. The methods used to trigger the alarm are door courtesy switches and/or a trembler switch. This alarm has the advantage over other types that it attracts attention and immobilises the car, but does so without being a public nuisance and without draining the battery (the alarm might be set off by an innocent party accidentally).

With power applied, in the quiescent state, the CSR1 is switched off and current is only drawn through

TR2, this being about 20 microamps.

If any of the alarm switches are earthed TR1 conducts, both causing TR2 to conduct and switching on the CSR. TR2 thus shorts the timing capacitor each time a switch is made and the timing period starts from this point. The CSR supplies power to the Schmitt trigger and, as TR6 is conducting initially, to the multivibrator. RLA contacts then open and close periodically to give the alarm. These contacts can be connected to the horn or headlamps but must therefore be of suitable rating. The writer used a miniature 700 ohm relay which closed at 20 milliamps and fitted heavier 5 amp contacts salvaged from defunct microswitches so obtaining a 10 amp capacity relay at low cost.

As the timing capacitor charges up, TRS base voltage is raised until the Schmitt switches, TR6 being cut off and TRS conducting. TR5's current is less than the hold-
ing current of the CSR which switches off, leaving the circuit in its quiescent state again.

Relay RLB is enabled when the CSR is on, and must be a latching type, either electrically or mechanically. The latter is probably preferable as the ignition reset switch can be hidden away.
If trouble is experienced by spurious triggering of the alarm, this should be cured by connecting a capacitor across TR1 base as shown and decoupling the supply if required.

Layout is completely non-critical, as are components. BC108's were used because they were to hand, but lower gain types are suitable. Note that TR1 is \(p n p\), the rest are \(n p n\). The CSR was a sunplus item. The time delay can be altered by varying the values of R6 and C2.
D. W. Bickley, Wolverhampton.

\section*{recording level indicator}

ORDINARY tape recorder VU meters cannot respond quickly enough to sudden loud peaks. One can therefore record at too high a level without knowing, resulting in distortion. Peak-reading meters solve this problem to some degree, but are complex and not always totally successful.
The circuit (Fig. 1) eliminates this problem, and can easily be added to almost any transistor tape recorder. If the peak level of the input a.c. signal exceeds a certain level,
the 555 timer wired as a monostable is triggered, illuminating the l.c.d. for about 0.4 sec , as a warning that the recording level is too high. VR1 sets the trigger level, low resistance corresponding to a high trigger voltage, between 0 and 2.5 V .

Input impedance is over \(10 \mathrm{k} \Omega\) for most settings of VR1, and the unit can be connected direct to the output of the record amplifier. It should however be connected in front of the tape head driver, as this is a constant-current driver, not constant-voltage.

D1 may be any l.e.d. D2, C3 and R4 stabilise the trigger level against
changes of \(\mathrm{V}_{\mathrm{cc}}\). If \(\mathrm{V}_{\mathrm{cc}}\) is stabilised, these components can be omitted, connecting point \(X\) to \(V_{c c}\) and leaving pin 5 of the NE555 unconnected, VR1 should now be 100 k 12 . This circuit will work for any \(\mathrm{V}_{\mathrm{cc}}\) from 6 to 15 V , although R2 may need reducing to \(680 \Omega 2\) at low voltages. The maximum current demand with l.e.d. on, is 20 mA , and less than 5 mA with it off. In use, it is best to set it to trigger at a level corresponding to \(+2-3 \mathrm{~dB}\) record level.
N. R. Arnot,

Welwyn Garden City, Herts.

\section*{TOUCH KEYBOARD}

For those not wishing to spend
t20 on keyboard inechanics for their synthesiser, 1 have devised an electronic alternative.

The complete circuit is shown in Fig. 1. The contacts are made by etching a keyboard on fibreglass p.c.b. The circuit marked \(A\) is dupli-
cated for as many keys as required. A finger pressed on the contacts switches the transistor pair, producing about 5.3 V across the resistor. The diodes "code" the key position into a 6-bit binary signal passing. on to circuit \(B\). (The key shown is the 13th.)

This circuit is a sampling digitalanalogue convertor, which takes an input from the keyboard when a key is pressed.

The output may be taken to a
buffer stage as shown, allowing the keyboard to be compressed, expanded or offset up and down the spectrum. An integrator would produce a portamento effect.

A typical key design is shown in Fig. 2. If single plate operation is required the circuit of Fig. 3 could replace the two transistor circuit of Fig. 1, but it may be found unreliable.
N. B. Sargeant,

Fleet.


Fig. 1


Pig. 1


Fig. 2


Fig. 3

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350 & 247 & 10.93 & 1.41 \\
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\section*{PATENT INCREASE}

On January 1, 1976, the price of a printed copy of a British Patent rose, by more than 100 per cent, to 75 p . This is still cheap for a fact-packed specification of a hundred-or-so pages, accompanied by a dozen-or-so sheets of drawings, and is tolerable for the more average length of a dozen pages. But it makes any patent of brief content rather expensive.

For this reason, readers should remember that most of the official publications of the British Patents Office can be consulted free of charge at over two dozen libraries dotted around the United Kingdom. Fortunately, this situation is unlikely to change, because it would undermine the basic principle of patenting, whereby an inventor is awarded a monopoly in return for disclosing details of his invention to the public in a Patent Office publication.

Free access to any patent mentioned in this column should currently be available through the public, commercial or central library in the following towns:

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\section*{LICENSING GUIDE}

The British Library recently pub. lished another of its extremely useful "Guides to Literature". These are available free by post from the Bayswater Branch of the Science Reference Library (10 Porchester Gardens, Queensway, London W2 4DE), or on personal request from the SRL attached to. the Patent Office at Southampton Buildings, Chancery Lane, London WC2.

Each guide has the same general format; a background to the subject and clear references to all the most useful literature that is available to the public in the SRL.

So far there have been no publications specifically on electronics topics (subjects covered have been artificial polymers, automotive fuels and odd protein sources), but news of any guideline on a selected area of electronics will be reported if and when it is published.

The latest guideline relates to "Patent Licensing Opportunities", and could be of considerable interest to both inventors and manufacturers working in the electronics field. Some of the source references given relate to regular publications which publicise both inventions available for licence and potential licensees seeking inventions; other references relate to the legal aspects of licensing, both here and abroad.
In the latter context, it is important to bear in mind that since we have joined the EEC the situation in Europe has become somewhat confused. Briefly, the Treaty of Rome forbids any restriction or distortion of competition within the EEC and thus would appear to ban any exclusive licence, i.e. any licence that gives any one manufacturer in a territory the right to corner a market without fear of competition from other manufacturers.

In 1962 the Common Market Commission issued its now famous, so-called "Christmas message" which appeared to condone exclusive licenses if tied to a patent. But this 1962 exemption has been steadily confused and eroded. Currently, to the publicly admitted dismay of the CBI, inventors and manufacturers must realistically regard any straightforward exclusive licence in Europe as void under the Treaty of Rome.
There is legal machinery for asking the Commission in Brussels to give its advance opinion on a draft licence, but this is a lengthy procedure, riddled with red tape. Thus anyone with an electronic invention to license and hopes of profit without problems, is best advised to steer clear of licensing any one manufacturer to the
exclusion of others. Likewise firms are best advised to avoid entering into exclusive licences if humanly and commercially possible.

\section*{REMOVING DENTS}

A simple but allegedly previously ignored approach to straightening out dents in car bodywork is claimed by Erwin Schill, of Switzerland, in BP 1403164.

According to the inventor, it is well known that to use a welding tool to soften the damaged metal leaves stresses in the sheet after removal of the dent. The proposed solution is to use a welding tool in the manner of a hammer, so that a multitude of tiny spots are heated briefly to a high temperature, rather than a whole area.

The necessary tool is shown in Fig. 1; the handle incorporates a hammer head with a central copper electrode which is surrounded by a safety sheath. The dent is flattened using short tapping movements so that the electrode briefly contacts the metal and heats spots of around 1 mm square to \(1,000^{\circ} \mathrm{C}\) for a fraction of a second at a time. The material surrounding each contact point draws in and if the spots are peppered over the surface to be treated (Fig. 2) the result is an overall flattening without undesirable stressing of the metal.

BP 1403164


Fig. 1
Fig. 2

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－Normal distribution function \((\operatorname{Pr}(x))\)
－Gamma function（ \(\Gamma(x)\) ）
－Group operationa（ \(= \pm\), O．\(^{\text {．}}\) ．II x \(\|\) ）

－Power function（y＇）
－Reciprocal（ \(1 / x\) ）
－Square toot \((\sqrt{x})\)
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\multicolumn{1}{|c}{1} & \multicolumn{1}{c}{2} \\
\cline { 1 - 2 } 52 & \(4-8\) \\
58 & \(5-9\) \\
185 & \(8-12\) \\
230 & \(9-18\) \\
430 & \(15-2\) \\
600 & \(10-2\) \\
700 & \(12-2\) \\
700 & \(16-2\) \\
700 & \(16-2\) \\
1,250 & \(18-36\) \\
2,500 & \(31-4\) \\
2,500 & \(36-4\) \\
\(15 k\) & \(85-1\)
\end{tabular}
\begin{tabular}{|c|c|}
\hline 3 & 4 \\
\hline \(2 \mathrm{c} / \mathrm{o}\) & 75p* \\
\hline \(6 \mathrm{c} / \mathrm{o}\) & 85p \\
\hline 6M & 65p* \\
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\hline \(4 \mathrm{c} / \mathrm{o}\) & \(85{ }^{\text {8 }}\) \\
\hline 6M & 85p" \\
\hline \(2 \mathrm{c} / \mathrm{o}\) & 65p* \\
\hline \(4 \mathrm{c} / \mathrm{o}\) & \(85 p^{*}\) \\
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\begin{tabular}{l|ll|ll|ll|l} 
CMOS ICS & CD 4029 & 0.94 & CD4059 & 3.64 & CD4510 & 1.12 & Displays
\end{tabular} \(\begin{array}{ll}\text { CO4000 } & 0.17\end{array}\)

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CO4007 & 0.17 & \(C 04036\) & 1.81 & \(C D 4067\) & 2.95 & \(C D 4520\) & 1.03 \\
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\hline
\end{tabular}
 \begin{tabular}{ll|ll|ll|ll|l} 
CO4009 & 0.46 & CD4038 & 0.88 & CO4070 & 0.18 & CO4555 & 0.74 & Flat Cable \\
CD4010 & 0.48 & C04039 & 2.86 & CD4071 & 0.18 & CO4556 & 0.74 & 20WAY 1M
\end{tabular} \begin{tabular}{ll|llll|lll|l} 
CO4011 & 0.17 & CD4040 & 0.88 & CD4072 & 0.18 & MC14528 & 0.86 & 20WAY 1M
\end{tabular}
 \begin{tabular}{ll|ll|ll|l|l} 
CD4014 & 0.83 & CD4043 & 0.83 & CO4076 & 1.27 & MC14566 1.21 & 1OWAY 1M \\
CD4015 & 0.33 & CD4044 & 0.77 & CD4077 & 0.10 & MCM14552 & 0.80
\end{tabular}
 \begin{tabular}{ll|ll|ll|l|l} 
CD4017 & 0.83 & CO4046 & 1.10 & CD4081 & 0.18 & Clock ChIps & IC Socket \\
CD4018 & 0.83 & CO4047 & 0.74 & CD4082 & 0.18 & MK50253 5.60 & PIns
\end{tabular} \begin{tabular}{ll|ll|ll|ll} 
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CD4019 & 0.46 & CD4048 & 0.46 & CD4085 & 0.59 & MM5314 & 4.44
\end{tabular} \begin{tabular}{ll|ll|ll|ll} 
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C04021 & 0.83 & CO4050 & 0.46 & CD4089 & 1.27 & AY51202 & 4.76 \\
C04022 & 0.79 & CD4051 & 0.77 & CD4093 & 0.68 & MK50250 & 5.00
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0.4 & SN7494 & 0.75
0.88 & SN74150 & 1.20
0.68 & SN74175
SN74176 & O. \begin{tabular}{l}
0.8 \\
1. \\
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\end{tabular} \\
\hline SN7401 & 0.16 & SN7411 & 0.20 & SN7437 & 0.28 & SN7450 & 0.90
0.16 & SN7481 & 1.0 & SN7495 & \begin{tabular}{l}
0.88 \\
0.68 \\
\hline
\end{tabular} & SN7/151 & O.68 & SN74176
SN74180 & 1. \\
\hline SN7401AN & 0.29 & SN7412 & 0.22 & SN7438 & 0.28 & SN7451 & 0.16 & SN7482 & 0.6 & SN74100 & 1.10 & SN74154 & 1.20 & SN74181 & 1. \\
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\hline SN7404 & 0.19
0.19 & SN7417 & 0.28
0.15 & SN7442 & 0.65 & SN7460 & 0.16 & SN7485 & 1.0 & SN74119 & 1.75 & SN74160 & 0.88 & SN74192 & 1. \\
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\hline SN7406 & 0.36 & SN7423 & 0.23 & SN7446 & 0.84 & SN7472 & 0.21 & SN7490 & 0.4 & SN74122 & 0.42 & SN74162 & 0.88 & SN74196 & 1. \\
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0.19 & SN7425
SN7427 & 0.23
0.23 & SN7447 & 0.80 & SN7473 & 0.31 & SN7491 & 0.7 & SN74123 & 0.65 & SNT4183 & 0.88
1.80 & - \({ }^{\text {SN74197 }}\) & 1.5 \\
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0.15 & 15 & \% & DIS & UN & FO & & MIX & & SN74164
SN74165 & 1.60
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\(\mathrm{BC1} 60\) & 0.68 \\
\hline 18
\end{tabular} & GET882 & 0.85 & \({ }^{\text {OC56 }}\) & 0.80 & 7430 & 0.16 \\
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\hline 2 N 2219 & \(0 \cdot 25\) & \(\begin{array}{ll}\text { BCY } 91 & 0.45 \\ \text { BCY } 22\end{array}\) & GEX 44 & 0.08 & \({ }^{0088}\) & 0.60 & \({ }_{7437}^{7483}\) & 0.87
0.87 \\
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0.10 & 7451 & 0.16 \\
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\hline 2N3706 & 0.11 & \(\begin{array}{ll}\text { BF115 } & 0.80 \\ \text { BF167 } & 0.85\end{array}\) & \({ }_{\text {MJ E2965 }}\) & 1-27 & \({ }_{0}^{0} 0 \mathrm{C812}\) & 0.45 & 7475 & 0.45 \\
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\hline 2N3819 & 0.85 & \begin{tabular}{ll} 
BF185 & 0.89 \\
Br194 & 0.10 \\
\hline
\end{tabular} & MPF105 & 0.88 & \({ }^{\text {OC114 }}\) & \(0-88\) & \({ }^{7484}\) & 7 \\
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1.15 & BFA61 0.85 & NKT214 & 0.81 & OC14 & 0.80 & 7493 & 0.70 \\
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0.30 & \({ }^{74965}\) & \\
\hline 40250 & 0.54
0.81 & \({ }_{\text {BFX } 29}{ }^{\text {Bra }}\) & NET219 & 0.23 & OC171
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0.20 & BFX30 0.88 & NET222 & 0.80 & OC201 & 1.50 & 74100 & 1.89 \\
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\hline AAZ18 & 0.18 & \(\begin{array}{ll}\text { BFX83 } \\ \text { BFX84 } & 0.65 \\ 0.85\end{array}\) & NET231 & 0.84 & OC203 & 0.76. & 74111 & 8 \\
\hline AAZ17 & \({ }^{0.18}\) & BFX85 0.28 & NET272 & 0.80 & OC204 & 1.50 & 74118 & 0.90 \\
\hline AC107 & 0.05 & BFX86 0.\% & NET278 & 0.20 & 00205 & 1.75 & 7418 & 1.68 \\
\hline \({ }_{4 \mathrm{Cl27}}\) & 0.85 & BFX87 0.96 & NET275 & 0.25 & 00208 & 1.10 & 71121 & \% \\
\hline \({ }_{4}{ }^{\text {c128 }}\) & 0.15 & \({ }_{\text {BFY } 10}{ }^{\text {Bra }}\) & NET277 & 0.20 & OC207
OC460 & 1.00
0.20 & 74128 & 1.00 \\
\hline \({ }^{\text {ACl }}\) (187 & 00.81 & BFY11 0.50 & NKT301 & 1.00 & OC470 & 02008 & 74141 & 0.80 \\
\hline \({ }_{\text {ACYI7 }}\) & \({ }_{0}^{0.20}\) & \({ }^{\text {BFY }} 170\) & NET304 & 1.00 & OCP71 & 1.80 & 74145 & 1.90 \\
\hline ACY18 & 0.85 & \(\begin{array}{ll}\text { BFY18 } & 0.45 \\ \text { BFY18 } & 0.55\end{array}\) & NKT408 & 1.00 & ORP12 & 0.60 & 74180 & 00 \\
\hline ACY 19 & 0.95 & \(\begin{array}{ll}\text { BFY19 } & 0.65 \\ \text { BFY } 24 & 0.45\end{array}\) & NET404 & \({ }_{0}^{1.0}\) & \({ }_{\text {ORP6 }}\) & 0.65
0.48 & 74154 & \\
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74170 & 5 \\
\hline \({ }_{4} \mathrm{CrY}^{28}\) & 0.86 & \(\begin{array}{ll}\text { BFY58 } & 0.20 \\ \text { BFY }\end{array}\) & \({ }_{04} 0\) & 0 & GX640 & 0.75 & 74174 & 1.67 \\
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ACY 40 & 0.78 & BFY84 0.86 &  & 0.08 & \(8 \times 641\) & 0.75 & 74178 & \\
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\hline ACY44 & 0.38 & \(\begin{array}{ll}\text { BR100 } & 0.40 \\ \text { B8X27 } & 0.50\end{array}\) & 0471 & 0.80 & 8X644 & 0.85 & 74190 & 8.000 \\
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B8x & \\
\hline 0.90
\end{tabular} & OA78 & 0.15 & 8X 845 & 0.85 & 71192 & 8.00 \\
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\hline AD161 & 0.041 & \begin{tabular}{ll} 
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BGY26 & 0.17 \\
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\end{tabular} & 0A81 & 0.10 & V15/30? & 0.75 & 74194 & 1.80 \\
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74196 & \\
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\hline AF115 & & & OAB6 & 0.16
0.07 & ve0/201P & 0.75 & 74198 & \(8 \cdot 7\) \\
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0.24 & \({ }_{\text {BT }}\) BYY \({ }^{\text {a }} / 500 \mathrm{R}\) & OA90
OA91 & \({ }_{0}^{0.07}\) & XA101 & 0.10 & 74189 & \%.58 \\
\hline AFl18 & 0.57 & 0.75 & 0a9b & 0.07 & XA102 & 0.18 & \multicolumn{2}{|l|}{} \\
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ELECTRONIC CONSTRUCTION KIT
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 4 Transig - 312 -way Connect Capacitury 10 1ransiaturs 3, Loudspeaker Earpicce Mica Baseboard Wonn 3w/LW/aW Coils - Ferrite Complete kit of parts including construction plan Total building costs \(\mathbf{8 9 . 9} 9\) P.P. and Ins. 65p
P. \& P. \(£ 3 \cdot 50\)
- hatteryless Cry hal Rad
- One Transistor Radio
- Transis
- 3 Transistor
- Transistor Regenera-
- Andible Continuity
- Sensitive Pre-Amplifier

\section*{V.H.F. AIR CONVERTER KIT}

Build this converter kit and receive the aircraft band by placing it by the side of a radio tuned to inediuns wave or the long wave band and operatiag as shown in the instructions supplied free with all parta.
Uses a retractable chrome platerl telescopic aerial, kain control, r.IT.J. tuning capacitor, fransistor, etc. All parts including case



\section*{POCKET FIVE}

Now with \(3 \frac{1}{t}\) in Louds
3 tunable Wave-
bands. MW, LW
bands. MW, LW
7 stages, 5 transis-
supersensitive ferrite rod
serial, attractive black and
gold case. Size 5 主in \(\times 1 \frac{1}{2}\) in
\(3 \frac{1}{4}\) in approx.
Complete kit of parts including construction plans.
\(\left.\begin{array}{l}\text { Total } \\ \text { Building Costs }\end{array}\right\}\)

\section*{NEW}

Everyday Series
Build this exciting
new series of

designs.
E.V.5. 5 Transistors and
diodes. MW/LW. Powered by 41
battery. Ferrite rod aerial. tuning condenser, volume control, and now with 3 in . loudspeaker. Attractive case with red speaker grille. Size \(9 \mathrm{in} . \times 5 \mathrm{ith} \times 23 \mathrm{in}\) approx. All partsincluding Case and Plans
Total Building costs 34075 P. \& P. +1 Ins. 50 p
E.V.6. Case and looks as above. if Transistors 3 diodes. Powered by \(9 V\) battery. Ferrite rod aerial, 3 in . Ioudspeaker, etc. \(\mathrm{MW} / \mathrm{LW}\) coverage. Push/Pul output.
All partsinchuding Case and Plans
\(\mathbf{f 5} \mathbf{5 0}_{\text {P. \& P. }+ \text { tho. } 5 \mathrm{sp}}\)
E.v.7. Cuse and lookta a a above, 7 Transitotors and 3 diodes. Six wavebands, MW/LW, Trawler Band SW1, SW' 2 , sw 3, powered by 9 V battery. Push pull output Telescopic aerial for short waves. 3in. Loudspeaker All parts ineluding Case and Plans. Total 1 nimidid

\section*{ROAMER TEN MARK 2}

\section*{WITH VHF INCLUDING AIRCRAFT}

Nou with free earpiece and switched socket. 10 tran
 VHF and local stations, also aireralt hanil. Built is ferrite rod aerial for MW/LW. Chrome plated 6 section telescope aerial, cant be angled and rotated for peak hort wave and virf listening. Push puli output using Cansistors. Car aerial pocket. 10 transistora plus 3 diodes. Ganged tuning condenser with VHF section. Separate
Folume aircraft bani


Volume on/oft. Wave
change and tone contro
Attractive Cane in rich gold blocking. win. \(x\) in. \(\times\) \$n.



\section*{EIECTRONIC CONSTRUCTION KIIS}
 8 transistors and 3 diodes. Push pull out 3 in loudspeaker, gain control, usipert, 9 geetion swivel ratcliet and retractable chrome plated tele-
 capacitors, transistors, etc. will receive T. \(V\). sound, public service bannl, aircraft. V.H.F. Iocal battery (not supplied with kit).
Complete kit of parts \(\$ 7.95\) P.P. and ins. 55p

E.C.K. 4

7 Trandistorn, 8 tuneable wavebands. MW, LW , Trawler Band. 3 Short Wave Bands. Receiver Eit With Uin \(X\) in Ioudspeaker. Push inull ontput stage, gain control, and rotary switch. 7 transistors and 4 diodes.
\(f\) sition chrome-plated telescopic aerial. 8 in sensitive ready pound ferrite rod aerial, tuning capacitor, resistors. capacitors, etc. Operates from a 9 volt P.P. 7 battery (not supplied with kit)
Complete kit of parts \(£ \mathbf{7 - 2 5}\) P.P. and Ins. 55p


\section*{EDU-KIT JUNIOR}
orojects withouerless Electromt Construction Kil. Build these projects without Soldering I ron or Soldes.
\(\star\) Crystal Radio Mediun Wase Coverage-No Battery One Transistor Kadio
* Trangistor Regenerative Radio * 3 Transistur Earpiece Radio Medium Wave Coverage * Transistor Medium Wave Loudspeaker Radio * Electronic Noise Genera
* 4 Transistor Puil/Pu

Al parts including loudspeaker, earpjece, MW ierrite rud aerial, canacitors, resist ors, transistors, etc
Complete kit of paris
including construction plans
\(\mathbf{I}\)
\(\mathbf{N}\)


Tuning condenser, 2 bolume controls, 2 slider awitches, ferite errite rod aerial. hattery clips, tag boards, 10 Units once constructed are detachable irom master nit enabling them to be stored for future use. Iusal or schools, educational authorities and all those interested in radio construction All parte including case and plans.
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