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ELECTRONICS

VOLUME 13 No. 5 MAY 1977

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CONSTRUCTIONAL PROJECTS

ORGAN TREMOLO UNIT by L. F. Reeve A solid state phase modulator for electronic organs	338
BURGLAR ALARM by P. J. Fasoli A sensitive amplified bridge design	342
ONE ARM BANDIT by K. Amor A scaled down version of the popular gaming machine	359
DARKROOM TIMER by A. Koltai & G. S. Brimble An accurate, reliable timer with digital readout	366

GENERAL FEATURES

MICROPROCESSORS EXPLAINED—3 by R. W. Coles The instruction set and programming techniques	346
SEMICONDUCTOR UPDATE by R. W. Coles A look at some recently released devices	355
MOBILE DISCO TECHNIQUES —2 by N. McLeod Equipment and operating techniques for the disco enthusiast	372
INGENUITY UNLIMITED TV Tennis Score—Motor Driver—Programmable Melody Generator— Joanna Modification	376

NEWS AND COMMENT

EDITORIAL—New Voice for Components Industry	337
NEWS BRIEFS DIY Computing Conference—All Electronics Show—Sound '77 Exhibition— Microprocessor Forum	345
SPACEWATCH by Frank W. Hyde Venus—Infra Red Telescope—Landsat	352
MICROPROCESSOR REPORT Products discussed at a Cambridge Seminar	356
MARKET PLACE Interesting new products	370
STRICTLY INSTRUMENTAL by K. Lenton-Smith Electronic music matters	371
POINTS ARISING Digital Voltmeter—Putting It Together—Solar Heating Controller— Hazard Warning Flasher—pH Meter	382
INDUSTRY NOTEBOOK by Nexus What's happening inside industry	385
PATENTS REVIEW Thought provoking ideas on file at the British Patents Office	386

Our June issue will be on sale Friday, May 13, 1977 (for details of contents see page 365)

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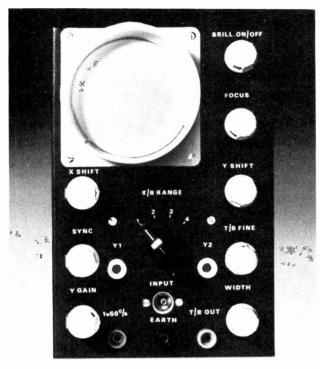
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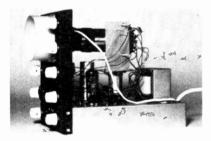


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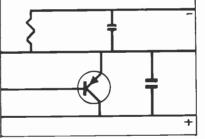
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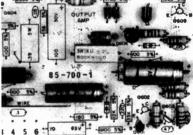




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2N708 2N709	0.15	BC116A BC118	0-28 0-20	GD12 GET102	0·10 0·50	OC26 OC28	0.60	7408 7409	0.28
2N1091	0-55	BC121 BC122	0.20	GET103 GET113	0.85 0.85	OC29 OC30	0.75	7410 7411	0.18
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AD140 AD149	0.65	BFY53 BFY64	0.23	0A71 0A73	0.20	SX 644	0.85	74190 74191	2.00 2.00
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AF118 AF119	0.75	BSY27 BSY51	0-23 0-50	0A91 0A95	0.07	XA101 XA102	0·10 0·18	74199 Plugin so	
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COMPONENTS SETS include all necessary resistors, capacitors, sericonductors, potenti-meters and transformers. Hardware such as cases sockets, knobs, etc. are not included but most of these may be bought separately. Fuller details of kits. PCBs and parts are shown in our

CIRCUIT AND LAYOUT DIAGRAMS are supplied free with all PCBs designed by Phonosonics

PHOTOCOPIES of the P E texts for most of the kits are available-prices in our lists

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Set of basic component kits for above	
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9in spring unit	£6 · 50
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Based upon P.E. Sound Design circuit Component set (incl. PCB) \$3.64 TREBLE BOOST UNIT (P.E. Apr 76)

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.1	VOICE OPERATED FADER (P.E. Dec. 73) For automatically reducing music volume talk-over —particularly useful for Disco work home-movie shows	during or foi
7	Component set (Incl. PCB)	£3 · 97
9	VOLTAGE CONTROLLED FILTER (P.E. Oct. 74) An independently designed VCF that can be used P.E. Synthesiser	with the
r t	Component set Printed circuit board	£3-80 £1-38
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	Basic component set (excl. thyristors) Printed circuit board for above Power supply PCB for power supply	£15-92 £3-90 £5-78 £1-79
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	P.E. TUNING FORK (P E Nov. 75) Produces 84 switch-selected frequency-accurate to LED monitor clearly displays all beat note adjus Ideal for tuning acoustic and electronic in instruments alike	nes. An tments musical
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Ś	A twin-channel visual display unit for monitoring th level of audio signals. Well suited for use inter-coupling our many sound producing kits t avoid signal over-loading Component set (incle PCB) (as published)	o help
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Vu METER Size 40 × 40 × 29mm Sensitivity 130µA No. Price 1321 £2-00	CD4001 CD4002 CD4006 CD4007 CD4008 CD4009 CD4010	Price £0-14 £0-18 £0-18 £0-98 £0-98 £0-95 £0-55 £0-55	Type CD4012 CD4013 CD4015 CD4016 CD4017 CD4018 CD4019 CD4020	Price £0 · 18 £0 · 50 £0 · 90 £0 · 50 £0 · 90 £1 · 00 £0 · 52 £1 · 10	Type CD4022 CD4023 CD4024 CD4025 CD4025 CD4026 CD4027 CD4028 CD4029	Price £0.95 £0.18 £0.72 £0.18 £0.98 £0.60 £0.85 £1.15	Type CD4031 CD4035 CD4037 CD4040 CD4041 CD4042 CD4043 CD4044	Price £2-20 £1-05 £0-95 £0-95 £0-82 £0-82 £0-98 £0-94	Type CD4046 CD4047 CD4049 CD4050 CD4054 CD4055 CD4056 CD4069	Price £1-30 £1-10 £0-55 £0-55 £1-20 £1-20 £1-20 £1-40 £0-40	Type CD4071 CD4072 CD4081 CD4082 CD4510 CD4511 CD4516 CD4518	Price £0 - 28 £0 - 28 £0 - 18 £0 - 28 £1 - 30 £1 - 60 £1 - 60 £1 - 08	No. 242 Electronics Pocket Book Price £3-75† No. 239, 30 Photo- electric Circuits
MINI	CD4011	£0 · 18	CD4021	£0·98	CD4030	£0·55	CD4045	£1-40	ČD4070	EO · 40	CD4520	£1-25	and Systems Price £1·80†
MULTI-METER Size 60 × 24 × 90mm	Туре	Price a	Туре	Price				CS Price	Tupo	Deine	Turr		Just a selection from our huge stocks!
Sensitivity 1000 ohms/V A.C. VOLTS 0-10, 50, 250, 1000 D.C. VOLTS 0-10, 50, 250, 1000 D.C. CURRENT 0-1 100mA Resistance 0-150kΩ No. Price 1322 £5.95	CA3018 CA3020 CA3028A CA3035 CA3036 CA3042 CA3043 CA3043 CA3046 CA3052	*£0.80 *£1.37 *£0.70 *£1.40 *£1.30 *£1.35 *£1.35 *£1.55 *£1.55 *£0.50 *£1.60 *£1.94	Type LM309K LM320-5V LM320-12V LM320-12V LM320-15V LM320-24V LM380N LM381AN LM3900N MC724P MC1303L MC1304P MC1310P	£1-75 £2-00 £2-00 £2-00	Type MC1469R NE536 NE536 NE540 NE555 NE555 NE566 NE562B NE565A NE566 NE567	12-50 10-50 12-00 12-00 12-10 1-	Type UA711C 72711 UA723C 72723 UA741C 72741 741P UA747C 72747 UA747B	*£0 · 32 *£0 · 32 £0 · 50 £0 · 50 *£0 · 20 *£0 · 20 *£0 · 20 *£0 · 70 *£0 · 79 *£0 · 35	Type 72748 72748P SN76013N SN76023N SN76110 SN76660 SL403D SL414A TAA550B	Price *C0-35 *C0-35 *C1-40 *C1-40 *C1-50 *C1-75 *C1-75 *C1-75 C0-35	Туре ТААб21А ТААб61А ТАО100 ТВА540Q ТВА6418 ТВА6418 ТВА800 ТВА810S ТВА820 ТВА920Q ТСА270S	Price *£2:00 *£1:50 *£1:30 *£2:50 *£2:55 *£0:80 *£0:95 £0:90 £3:40 *£3:90	SEE OUR 1977 CATALOGUE 126 pages packed with valuable information ORDER NOW ONLY 30p <i>plus</i> 15p p. & p.
P&P Postage and Packing add 25p unless otherwise shown. Add extra for airmail. Minimum order £1.	CA3075 CA3081 CA3089E CA3090AQ CA3123E LM301AH LM304	*£1-50 *£1-50 *£4-25 *£4-25 *£1-40 *£0-47 £3-00 *£0-95	MC1312PQ MC1330P MC1339 MC1350 MC1351P MC1352P MC1456G MC1456L	*£1-50 *£1-35 *£1-50 *£0-75 *£0-85 *£0-85 *£0-85 £3-95	UA702C 72702 UA703A UA709C 72709 72709 UA710C 72710	12 - 50 * £0 - 46 * £0 - 25 * £0 - 25 * £0 - 25 * £0 - 46 * £0 - 25 * £0 - 40 * £0 - 30					P (6,	2 WA	AK RE, HERTS

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TRANSFORMERS

MINITURE MAINS Primary 200 Type Price No. Type Price 2024 MT280 0-62V, 0-62V RMS E1 30* 2025 MT1500 D-12V, 0-12V RMS E1 30* 2026 Secondary Price 2021 6V-0-6V 100mA E1 30* 2023 12V-0-6V 100mA E1 30* 2024 Secondary Price 2023 12V-0-12V 10mA E1 30* 2024 Secondary P 8 P 30p 2025 SY-0-5V 10mA E1 30* 2026 SY-0-5V 10mA E2 30* P 8 P 30p 2028 12V-0-12V 1 amp E2 60* P 8 P 30p 2029 STANDARD MAINS Primary 20V P 8 P 30p 2031 13mp E3 64* P 8 P 30p 2031 13mp E3 64* P 8 P 50p 2032 13mp E3 42* P 8 P 50p 2033 2 amp E3 45* P 8 P 50p 2034 13mp E3 45* D 85* 200MV Dimensions 20 × 16 × 15mm	TRANSFOR	MERS	
NINIATURE MAINS Primary 240V Price No Secondary Price 2021 69-0-94 100mA 50 85 2023 124-0-124 100mA 50 85 2026 64-0-64 1 amp 12-70* P & P 30p 2026 64-0-64 1 amp 12-70* P & P 30p 2027 94-0-94 1 amp 12-30* P & P 30p 2028 124-0-124 1 amp 12-60* P & P 30p 2028 1524-0-154 1 amp 12-30* P & P 30p 2031 304-0-304 1 amp 12-30* P & P 30p 2031 124-0-124 1 amp 12-80* P & P 30p 2031 12-0-154 1 amp 12-80* P & P 30p 2031 12-0-154 1 amp 12-84* P & P 30p 2031 12-71 12-51 31, 40, 50, 5025 V No Rating Price P & P 30p 2031 12-71 12-51 31, 40, 50 12-25* No Gatal 12-64* P & 80p 20023 2000 DUP	with two independent secondary y	vindings	
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2021 6V-0-6V 100mA C1 30* 2022 9V-0-9V 100mA C0 85* 2023 7 2V-0-12V 100mA C1 30* 1 AMP MAINS Primary 240 V No. Secondary Price 2026 6V-0-6V 1 amp C2 20* P & P 30p 2028 12V-0-12V 1 amp C2 30* P & P 30p 2028 12V-0-12V 1 amp C2 30* P & P 30p 2033 30V-0-30V 1 amp C2 30* P & P 30p 2034 15V-0-15V 1 amp C2 30* P & P 30p 2034 15V-0-15V 1 amp C2 30* P & P 30p 2034 12V-0-12V 1 amp C2 40* P & A P 30p 2034 14 15 7. 8. 14. 15 7. 19. 25. 31. 33. 40. 50. 25-0-25 V No. Raimp C3 42* P & A P 30p 2032 1 amp C3 42* P & A P 30p 2033 2 amp C5 45* P & A P 30p 200mV Dimensions 20 × 16 × 15mm C9 25* MINIATURE INTER/DRIVER Primary 200.1, Secondary 1k.0., 25*.45* D 62* 35* C2 3*			Price
No. Secondary Price Price P & P 30p P 302 2026 6V-0-6V 1 amp P 22: 20° P & P 30p P 302 2028 15V-0-15V 1 amp P 22: 20° P & P 30p P 30p P & P & P 30p P & P & P 30p P & P & P & P 30p P & P & P & P & P & P & P & P & P & P &	No. Sectorary 2021 6V-0-6V 100n 2022 9V-0-9V 100n 2023 12V-0-12V 100n	1Α 1Α πΑ	£1-30* £0-85*
2026 6V6V 1 amp 12:20 P. & P. 30p 2027 39V9V 1 amp 12:80 P. & P. 30p 2028 12V012V 1 amp 12:80 P. & P. 30p 2028 12V-0-13V 1 amp 12:80 P. & P. 30p 2030 30V93V 1 amp 12:80 P. & P. 30p 2031 jamp, 1 amp and 2 amp current rating. Secondary taps are 0-19:25-33-40-50V Yoltages available by use of taps: 4, 7. 8. 10.14, 15, 17. 19:25, 31, 33, 40, 50, 25-0-25 V. No. Rating P. 30p 2031 jamp 12:42 P. 30p 2032 2032 1 amp 12:42 P. 30p 2032 2032 1 amp 12:42 P. 30p 2032 2017 MIN-14, 15, 17. 19:25, 31, 33, 40, 50, 25-0-25 V. No. Rating 2033 2 amp 12:42 P. 4 P. 30p 2032 2 amp 12:42 P. 30p 2033 2 amp 12:41 Stamp 20:23 20100 10:41 15:150 10:23* 10:23*	1 AMP MAINS Primary 240 V	Deine	
2020 15V-0-15V 1 amp 22:90* P & P & P & 30p STANDARD MAINS Primary 240V Multi-tapped secondary mains transformers available in j amp, 1 amp and 2 amp current rating. Secondary taps are 0-19:25-33-40-50V Yoltages available by use of taps: Voltages available by use of taps: 4, 7, 8, 10, 14, 15, 17, 19, 25, 31, 33, 40, 50, 25-0-25 V. No. No. Rating Price 2032 2 amp 2031 j amp, 1 23:42* P & P & P 30p 2032 2032 1 amp C3:42* P & P & P 30p 2032 2033 2 amp C3:42* P & P & P 30p 2032 2032 1 amp C3:42* P & P & P 30p 2032 2033 2 amp C3:42* P & P & P 30p 2032 2031 j amp C3:42* P & P & S P 30p 2032 2031 j amp C3:42* P & P & S P 30p 2032 2031 j amp C3:42* P & S 50p 2032 200mV Dumensions 20 × 15 (K), Ratio 5 10:23* 20:35* 20:32* Primary 10k0, Secondary 2k0, C T. 15 × 13 × 13mm </td <td>No. Secondary 2026 6V-0-6V 1 amp</td> <td>£2.70* P</td> <td>& P. 30p</td>	No. Secondary 2026 6V-0-6V 1 amp	£2.70* P	& P. 30p
2030 30V-0-30V 1 amp C3-80 ⁺ P. 8. P. 30p STANDARD MAINS Primary 240V Multi-tapped secondary mains transformers available in j amp. 1 amp and 2 amp current rating. Secondary taps are 0-19:2-53-40-50V Yes and 2 amp current rating. Secondary taps are 0-19:2-53-40-50V No. Rating Price 2031 j amp C3-42 P. 8 P. 30p 2031 j amp C3-42 P. 8 P. 30p 2032 j amp C3-42 P. 8 P. 30p 2032 j amp C4-49 P. 8 P. 30p 2032 j amp C3-42 P. 8 P. 30p 2033 2 amp C4-40 P. 8 P. 85p 2032 j amp C3-42 Secondary 5 ohms 200mw Dimensions 20 × 16 × 15mm Order No. 2037 E0-23* C3-23* Primary 20kΩ, Secondary 1kΩ, 15 × 13 × 13mm. Order No. 2040 E0-33* C3-42* Primary 10kΩ, Secondary 1kΩ, 20 × 15 × 15mm. Order No. 2041 E0-32* C3-24* Primary 500 ohm, Secondary 1kΩ, 20 × 15 × 15mm. Order No. 2043 E0-32* C3-24* Order No. 2041 E0-32* C3-24* E0-32* C3-24* Primary 10kΩ, Secondary 1kΩ, 20 × 15 × 15mm. Order No. 2043 E0-32*	2027 9V-0-9V Tamp 2028 12V-0-12V 1 amp	£2.60* P	& P. 30p
Multi-tapped secondary mains transformers available in jamp, 1 amp and 2 amp current rating. Secondary taps are 0-19:25-33-40-50V Voltages available by use of taps: 4, 7, 8, 10, 14, 15, 17, 19, 25, 31, 33, 40, 50, 25-0-25 V No. Rating Price 2034 jamp C3-42* P. 8 P. 30p 2034 21 amp C3-42* P. 8 P. 30p 2034 21 amp C3-42* P. 8 P. 30p 2033 2 amp C3-43* P. 8 P. 30p 2034 21 amp C3-44* P. 8 P. 80p 200mW Dimensions 20 × 16 × 15mm Order No. 2037 00 × 16 × 15mm Order No. 2038 00 -23* LT710 MIN. INPUT Primary 20k0, Secondary 1k0, Ratio 5 1. Order No. 2038 00 -23* LT711 MIN. DRIVER Primary 10k0, Secondary 1k0, 15 × 13 × 13mm. Order No. 2040 00 + 20 × 15 × 13 × 13mm. Order No. 2041 00 + 20 × 15 × 13 × 13mm. Order No. 2041 00 + 20 × 15 × 13 × 13mm. Order No. 2041 00 + 20 × 15 × 15mm. Order No. 2042 00 + 20 × 15 × 15mm. Order No. 2042 00 + 20 × 15 × 15mm. Order No. 2043 00 + 15 × 13 × 13mm. Order No. 2043 00 + 15 × 15mm. Order No. 2043 00 + 15 × 15mm. Order No. 2043 00 + 15 × 15mm. Order No. 2045 00 + 15 × 15mm. Order No. 2046 00 + 20 × 32 × 15 × 15mm. Order No. 2047 00 + 20 × 32 × 15 × 15mm. Order No. 2048 00 + 32 × 20 × 20mm. Order No. 2049 00 + 20 × 32 × 30 × 20mm. Order No. 2049 00 + 20 × 32 × 20 × 20mm. Order No. 2049 00 + 20 × 32	2029 15V-0-15V 1 amp 2030 30V-0-30V 1 amp	£2-90* P £3-80* P	& P. 30p & P. 30p
2031 1 amp L1 · 42 F & B p 500 2032 1 amp L1 · 42 F & B p 500 2032 1 amp L2 · 42 F & B p 500 2032 1 amp L2 · 42 F & B p 500 2032 1 amp L3 · 42 F & B p 500 2032 1 amp L3 · 42 F & B p 500 2032 1 amp L3 · 42 F & B p 500 2032 1 amp L3 · 42 F & B p 500 Order No 2037 E0 · 23* E0 · 23* E0 · 23* Primary 10k0, Secondary 1k0, 15 × 13 × 13mm E0 · 42* E0 · 42* E0 · 42* Primary 10k0, Secondary 1k0, 00 · 15 × 13 × 13mm E0 · 42* E0 · 42* E0 · 52* Primary 10k0, Secondary 1k0, 00 · 15 × 15 × 15mm E0 · 32* E1 · 717 MIN. INPUT Primary 500 ohm, Secondary 2k0, C T. 20 × 15 × 15mm E0 · 32* E0 · 32* E0 · 32* Order No. 2043 E1 · 722 MIN. OUTPUT E0 · 32* E0 · 32* Primary 1 2k0, C T. Se	Multi-tapped secondary mains tr amp, 1 amp and 2 amp current	1, 33, 40, 50. 2	25-0-25 V.
AUDIO OUTPUT Primary 1:2k0, Secondary 5 ohms 200mW Dimensions 20 × 16 × 15mm 00-25* MINIATURE INTER/DRIVER Primary 20k0, Secondary 1k0, Ratio 5 1. 00-23* Order No. 2036 E0-23* LT710 MIN. INPUT Primary 10k0, Secondary 1k0, 15 × 13 × 13mm. 00-42* Drimary 10k0, Secondary 2k0, C.T. 15 × 13 × 13mm. E0-42* Primary 10k0, Secondary 2k0, C.T. 15 × 13 × 13mm. E0-35* Order No. 2040 E1712 MIN. OUTPUT Primary 500 ohm. Secondary 1k0, 20 × 15 × 15mm. E0-52* Order No. 2041 E0-28* Primary 150k0, Secondary 1k0, 20 × 15 × 15mm. E0-52* Order No. 2042 E1713 MIN. INPUT Primary 20k0, Secondary 2k0, C.T. 20 × 15 × 15mm. E0-32* Order No. 2043 E1722 MIN. OUTPUT Primary 10k0, Secondary 2k0, C.T. 20 × 15 × 15mm. G0-32* Order No. 2045 E0-33* Corder No. 2045 E0-33* Drimary 10k0, Secondary 3 2 and 8 ohm. 200mW. Dimensions 20 × 15 × 15mm. G0-der No. 2049 Corder No. 2045 E0-33* Corder No. 2045 E0-33* Corder No. 2	2031 ± amp 2032 1 amp	£3-42* P £4-40* P	8 P 30 p 8 P 50 p
Order No. 2037 D0 23 MINIATURE INTER/DRIVER Primary 20KG. Secondary 1KG, Ratio 5 1. Order No. 2038 E0 - 23* Draft No. 2030 ET710 MIN. INPUT Primary 100KG. Secondary 1KG, 15 × 13 × 13mm. Order No. 2030 E0 - 42* Drany 100KG. Secondary 2KG, C.T. 15 × 13 × 13mm. Order No. 2040 E0 - 42* Primary 100KG. Secondary 2KG, C.T. 15 × 13 × 13mm. Order No. 2040 E0 - 28* ET712 MIN. OUTPUT Primary 500 ohm. Secondary 8 ohms. 100mW. 15 × 13 × 13mm. Order No. 2041 E0 - 28* ET717 MIN. INPUT Primary 500 chm. Secondary 1KG, 20 × 15 × 15mm. Order No. 2042 E0 - 52* ET712 MIN. INPUT Primary 20KG. Secondary 1KG, 20 × 15 × 15mm. Order No. 2043 E0 - 32* ET722 MIN. DRIVER Primary 10KG. Secondary 2KG, C.T. 20 × 15 × 15mm. Order No. 2044 E0 - 32* Primary 12KG. C.T. Secondary 3.2 and 8 ohm. 200m.W. Dimensions 20 × 15 × 15mm. Order No. 2046 E0 - 32* Primary 12KG. C.T. Secondary 3.2 and 8 ohm. 200m.W. Dimensions 20 × 15 × 15mm. Order No. 2047 E0 - 43* ET728 MIN. DRIVER Primary 10KG. C.T. Secondary 3.2 and 8 ohm. 400m.W. Dimensions 25 × 20 × 20mm. Order No. 2047 E0 - 43* Dimensions 25 × 20 × 20mm. Order No. 2049 E0 - 43* Dimensions 25 × 20 × 20mm. Order No. 2049 E0 - 42*	2033 2 amp		
Primary 20kΩ, Secondary 1kΩ, Ratio 5 1. C0-23* Order No. 2038 E0-23* Primary 100kΩ, Secondary 1kΩ, 15 × 13 × 13mm. Order No. 3051 E0-42* Primary 10kΩ, Secondary 2kΩ, C.T., 15 × 13 × 13mm. Order No. 2040 E0-35* LT712 MIN. DUTPUT Firmary 10kΩ, Secondary 2kΩ, C.T., 15 × 13 × 13mm. Order No. 2040 E0-32* Primary 10kΩ, Secondary 8 ohms. 100mW, 15 × 13 × 13mm E0-28* Order No. 2041 E0-28* Primary 50kΩ, Secondary 1kΩ, 20 × 15 × 15mm. Order No. 2042 E0-52* Primary 50kΩ, Secondary 1kΩ, 20 × 15 × 15mm. Order No. 2043 E0-32* Primary 0kΩ, Secondary 2kΩ, C.T., 20 × 15 × 15mm. Order No. 2043 E0-32* Primary 12kΩ, C.T., Secondary 3.2 and 8 ohm. 200mW. Dimensions 20 × 15 × 15mm. Order No. 2045 E0-38* CT726 MIN. OUTPUT Primary 12kΩ, C.T., Secondary 3.2 and 8 ohm. 200mW. Dimensions 20 × 15 × 15mm. Order No. 2045 E0-38* LT726 MIN. DRIVER Primary 1kΩ, C.T. Secondary 3.2 and 8 ohm. 400mW. Dimensions 25 × 20 × 20mm. Order No. 2049 E0-43* Primary 1kΩ, C.T. Secondary 3.2 and 8 ohm. 400mW. Dimensions 25 × 20 × 20mm. Order No. 2049 E0-42* Primary 500 ohm C.T. Secondary 3.2 and 8 ohm. 400mW. Dimensions 25 × 20 × 20mm. Order No. 2049 E0-42* Primary 500 ohm C.T. Secondary 3.2 and 8 ohm. 400mW. Dimensions 2	AUDIO OUTPUT Primary 1-2kΩ, 5 200mW Dimensions 20 × 16 × 15m Order No. 2037	econdary 5 of m	£0 · 25*
Primary 100k0, Secondary 1k0, 15 × 13 × 13mm. 0rder No. 3051 00 × 240 LT711 MIN. DRIVER Primary 10k0, Secondary 2k0, C.T. 15 × 13 × 13mm. 0rder No. 2040 ET712 MIN. OUTPUT Primary 500 ohm, Secondary 8 ohms. 100mW, 15 × 13 × 13mm. 0rder No. 2041 00 × 28 × 13 × 13mm. 0rder No. 2041 00 × 28 × 10 × 10 × 10 × 10 × 10 × 10 × 10 × 1	Primary 20kΩ, Secondary 1kΩ, Re Order No. 2038		£0 · 23 °
Primary 10k.Π. Secondary 2k.Π. 15 × 13 × 13mm Order No. 2040 LT712 MIN. OUTPUT Primary 500 ohm. Secondary 8 ohms. 100mW. 15 × 13 × 13mm Order No. 2041 £0.32* LT717 MIN. INPUT Primary 500 ohm. Secondary 8 ohms. 100mW. 15 × 13 × 13mm Order No. 2041 £0.32* £0.52* E1717 MIN. INPUT Primary 10k.Π. Secondary 1k.Π. 20 × 15 × 15mm. C0.42* £0.32* Order No. 2043 £0.32* £0.32* Order No. 2043 £0.32* £0.32* Order No. 2045 £0.32* £0.32* Primary 12k.Π. C.T. Secondary 3.2 and 8 ohm. 200mW. Dimensions 20 × 15 × 15mm Order No. 2045 £0.38* Order No. 2045 £0.38* £0.38* Order No. 2045 £0.38* £0.43* Dremary 1k.Π. C.T. Secondary 3.2 and 8 ohm. 20mW. Dimensions 25 × 20 × 20mm £0.43* £0.43* Dimensions 25 × 20 × 20mm £0.43* £0.43* Dimensions 25 × 20 × 20mm £0.42* £0.42* Dimensions 2	Primary 100kΩ, Secondary 1kΩ, 1 Order No. 3051	5 × 13 × 13mm	1. £0+42*
Primary 500 ohm. Secondary 8 ohms. 100mW. 15 × 13 × 13mm Construction Order No. 2041 E0:28* LT717 MIN. INPUT Primary 150kΩ, Secondary 1kΩ, 20 × 15 × 15mm. Order No. 2043 E0:52* Primary 20kΩ, Secondary 1kΩ, 20 × 15 × 15mm. Order No. 2043 E0:52* LT712 MIN. INPUT Primary 20kΩ, Secondary 1kΩ, 20 × 15 × 15mm. Order No. 2043 E0:32* LT722 MIN. DRIVER Primary 10kΩ, Secondary 2kΩ, C T., 20 × 15 × 15mm. Order No. 2044 E0:32* LT724 MIN. OUTPUT Primary 12kΩ C.T., Secondary 3.2 and 8 ohm. 200mW. Dimensions 20 × 15 × 15mm Order No. 2045 E0:38* Primary 500 ohm. Secondary 3.2 and 8 ohm. 200mW. Dimensions 20 × 15 × 15mm Order No. 2046 E0:38* LT726 MIN. OUTPUT Primary 1kΩ C.T. Secondary 3.2 and 8 ohm. 400mW. Dimensions 25 × 20 × 20mm Order No. 2047 E0:43* Primary 200 ohm C.T. Secondary 3.2 and 8 ohm. 500 mW. Dimensions 25 × 20 × 20mm Order No. 2049 E0:43* Primary 500 ohm C.T. Secondary 3.2 and 8 ohm. 500 mW. Dimensions 25 × 20 × 20mm. Order No. 2049 E0:42* LT730 MIN. OUTPUT Primary 500 ohm C.T. Secondary 3.2 and 8 ohm. 500 mW. Dimensions 25 × 20 × 20mm. Order No. 2049 E0:42* LT730 MIN. OUTPUT Primary 500 ohm C.T. Secondary 3.2 and 8 ohm. 500 mW. E0:42* LT730 MIN. OUTPUT Primary 500 ohm C.T. Secon	Primary 10kΩ, Secondary 2kΩ, Order No. 2040	C.T., 15 × 1	3 × 13mm. £0 : 35*
13mm Order No 2041 £0-28* LT717 MIN. INPUT Primary 150kfl, Secondary 1kfl, 20 × 15 × 15mm. Order No £0-52* LT719 MIN. INPUT Primary 20kfl, Secondary 1kfl, 20 × 15 × 15mm. Order No £0-32* LT722 MIN. DRIVER Primary 10kfl, Secondary 2kfl, C T., 20 × 15 × 15mm. Order No £0-32* LT724 MIN. OUTPUT Primary 12 kfl, C T., Secondary 3.2 and 8 ohm. 200mW. Dimensions 20 × 15 × 15mm £0-38* Order No. 2045 £0-38* LT726 MIN. OUTPUT Primary 500 ohm. Secondary 3.2 and 8 ohm. 200mW. Dimensions 20 × 15 × 15mm £0-38* Order No. 2045 £0-38* LT726 MIN. OUTPUT Primary 1kfl, C T., Secondary 3.2 and 8 ohm. 400mW. Dimensions 25 × 20 × 20mm £0-43* Order No. 2047 £0-43* Drimers 25 × 20 × 20mm £0-39* Order No. 2049 £0-39* LT728 MIN. OUTPUT Primary 200 ohm C.T. Secondary 3.2 and 8 ohm. 400mW. Dimensions 25 × 20 × 20mm. Order No. 2049 £0-42* Primary 500 ohm C.T. Secondary 3.2 and 8 ohm. 500 mW. Dimensions 25 × 20 × 20mm. Order No. 2049 £0-42* LT730 MIN. OUTPUT Primary 500 ohm C.T. Secondary 3.2 and 8 ohm. 500 mW. Dimensions 25 × 20 × 20mm. Order No. 2049 £0-42* LT730 MIN. OUTPUT Primary 500 ohm C.T. Secondary 3.2 and 8 ohm. 500 mW. Dimensions 25 × 20 × 20mm. Order No. 2049 <td>LT712 MIN. OL Primary 500 ohm. Secondary 8 (</td> <td>phms. 100mW.</td> <td>15 × 13 ×</td>	LT712 MIN. OL Primary 500 ohm. Secondary 8 (phms. 100mW.	15 × 13 ×
Primary 150kΩ, Secondary 1kΩ, 20 × 15 × 15mm £0 - 52* Order No. 2042 £1719 MIN. INPUT Primary 20kΩ, Secondary 1kΩ, 20 × 15 × 15mm. £0 - 32* Order No. 2043 £0 - 32* Primary 20kΩ, Secondary 2kΩ, C. T., 20 × 15 × 15mm. £0 - 32* Order No. 2043 £0 - 32* Primary 10kΩ, Secondary 2kΩ, C. T., 20 × 15 × 15mm. £0 - 32* Order No. 2044 £0 - 32* Primary 1 2kΩ C. T., Secondary 3 2 and 8 ohm, 200mW. Dimensions 20 × 15 × 15mm. Order No. 2045 £0 - 38* C1725 MIN. OUTPUT Primary 500 ohm. Secondary 3 2 and 8 ohm. 200mW. Dimensions 20 × 15 × 15mm. C0 - 38* C1726 MIN. DRIVER £0 - 38* Primary 1kΩ C. T. Secondary 3 2 and 8 ohm. 400mW. Dimensions 25 × 20 × 20mm. Order No. 2046 £0 - 39* L1729 MIN. OUTPUT Primary 200 ohm C. T. Secondary 3 2 and 8 ohm. 400mW. Dimensions 25 × 20 × 20mm. Corder No. 2049 Order No. 2049 £0 - 42* L1730 MIN. OUTPUT Primary 500 ohm C. T. Secondary 3 2 and 8 ohm. 500 mW. Dimensions 25 × 20 × 20mm. Order No. 2049 Corder No. 2049 £0 - 4	13mm Order No 2041		
Primary 20k0, Secondary 1k0, 20 × 15 × 15mm. £0-32* Order No. 2043 £0-32* Primary 10k0, Secondary 2k0, C T. 20 × 15 × 15mm. Order No. 2044 Primary 12k0, C T. Secondary 3 2 and 8 ohm. 200mW. £0-32* Dimensions 20 × 15 × 15mm. CT. Secondary 3 2 and 8 ohm. 200mW. Dimensions 20 × 15 × 15mm. £0-33* Order No. 2045 £0-33* Primary 1 2k0, C T. Secondary 3 2 and 8 ohm. 200mW. Dimensions 20 × 15 × 15mm. Order No. 2045 £0-38* Primary 500 ohm. Secondary 3 2 and 8 ohm. 200mW. Dimensions 20 × 15 × 15mm. Order No. 2046 £0-38* Primary 1k0, C T. Secondary 3-2 and 8 ohm. 400mW. Dimensions 25 × 20 × 20mm. Order No. 2047 £0-43* LT728 MIN. OUTPUT Primary 200 ohm C.T. Secondary 3-2 and 8 ohm. 400mW. Dimensions 25 × 20 × 20mm. C0-42* Vitra Vitra MIN. OUTPUT Primary 500 ohm C.T. Secondary 3-2 and 8 ohm. 500 mW. Dimensions 25 × 20 × 20mm. C0-42* Vitra Vitra Vitra MIN. OUTPUT Primary 500 ohm C.T. Secondary 3-2 and 8 ohm. 500 mW. Dimensions 25 × 20 × 20mm. C0-42* Viter No. 2049 £0-42* Viter No. 2049 £0-42*	Primary 150kΩ, Secondary 1kΩ, 2 Order No. 2042	20 × 15 × 15mm	£0 · \$2*
LT724 MIN. OUTPUT Primary 1 2K1 C T. Secondary 3 2 and 8 ohm. 200mW. Dimensions 20 × 15 × 15mm. Order No. 2045 LT726 MIN. OUTPUT Primary 500 ohm. Secondary 3 2 and 8 ohm. 200mW. Dimensions 20 × 15 × 15mm. Order No. 2046 LT726 MIN. OUTPUT Primary 500 ohm. Secondary 3 2 and 8 ohm. 200mW. Dimensions 20 × 15 × 15mm Order No. 2046 LT728 MIN. DRIVER Primary 1k1 C T. Secondary 500 ohm C T. Dimensions 25 × 20 × 20mm Order No. 2047 Primary 200 ohm C.T. Secondary 3 2 and 8 ohm. 400mW. Dimensions 25 × 20 × 20mm Order No. 2048 LT730 MIN. OUTPUT Primary 500 ohm C.T. Secondary 3 2 and 8 ohm. 500 mW. Dimensions 25 × 20 × 20mm. Order No. 2049 LT30 MIN. OUTPUT Primary 500 ohm C.T. Secondary 3 2 and 8 ohm. 500 mW. Dimensions 25 × 20 × 20mm. Order No. 2049 ELE.D.S Type Size Order No. Colour PriL213 0 125in Diso 1504 FLV115	Primary 20kΩ, Secondary 1kΩ, 20) × 15 × 15mm	£0·32*
Primary 1 2kG C.T. Secondary 3.2 and 8 ohm. 200mW. Dimensions 20 × 15 × 15mm Order No. 2045 Primary 500 ohm. Secondary 3.2 and 8 ohm. 200mW. Dimensions 20 × 15 × 15mm Order No. 2046 LT726 MIN. OUTPUT Primary 500 ohm. Secondary 3.2 and 8 ohm. 200mW. Dimensions 20 × 15 × 15mm Order No. 2046 LT728 MIN. DRIVER Primary 1kC C.T. Secondary 3:2 and 8 ohm. 400mW. Dimensions 25 × 20 × 20mm Order No. 2047 LT729 MIN. OUTPUT Primary 200 ohm C.T. Secondary 3:2 and 8 ohm. 400mW. Dimensions 25 × 20 × 20mm Order No. 2048 LT730 MIN. OUTPUT Primary 500 ohm C.T. Secondary 3 2 and 8 ohm. 500 mW. Dimensions 25 × 20 × 20mm. Order No. 2049 LT50 IIS01 Order No. 2049 C0-42* LLE.D.S Type Size Order No. 2049 259 FLV115 21n Dizensions 25 × 20 × 20mm. 259 FLV115 21n 1502 GREEN 259 FLV115 21n	LT722 MIN. Df Primary 10kΩ, Secondary 2kΩ, C Order No 2044	T., 20 × 15 × 1	15mm. £0-32*
Primary 500 ohm. Secondary 3 2 and 8 ohm. 200mW. Dimensions 20 × 15 × 15mm C0-38* Order No. 2046 C0-38* Primary 1kf1 C.T. Secondary 500 ohm C.T. Dimensions 25 × 20 × 20mm C0-43* Order No. 2047 E0-43* Primary 1kf1 C.T. Secondary 3.2 and 8 ohm. 400mW. Dimensions 25 × 20 × 20mm Order No. 2048 E0-33* Primary 200 ohm C.T. Secondary 3.2 and 8 ohm. 400mW. Dimensions 25 × 20 × 20mm Order No. 2048 E0-39* LT729 MIN. OUTPUT Primary 500 ohm C.T. Secondary 3.2 and 8 ohm. 500 mW. Dimensions 25 × 20 × 20mm. Order No. 2049 Order No. 2049 E0-42* L.E.D.S Type Type Size Order No. Colour Price TIL209 1 25in 1501 RED 12p TIL211 0 1 25in 1503 YELLOW 25p FLV410 0 2in 1505 GREEN 25p 2nd Grade L.E.D.s A pack of standard sizes and colours which fail to perform to their very ridged specification. but which are ideal for experiments. Order No. Price Order No 1507<	Primary 1-2kΩ C.T., Secondary Dimensions 20 × 15 × 15mm.	JTPUT 3-2 and 8 ohr	m, 200mW. £0-38*
Primary 1k1 C. T. Secondary 500 ohm C. T. Dimensions 25 × 20 × 20mm Order No. 2047 Primary 200 ohm C. T. Secondary 3.2 and 8 ohm. 400mW. Dimensions 25 × 20 × 20mm Order No. 2048 LT30 MIN. OUTPUT Primary 500 ohm C. T. Secondary 3.2 and 8 ohm. 500 mW. Dimensions 25 × 20 × 20mm Order No. 2049 C. Size Crear No. Colour Type TiL210 0 125in 1502 Crear No. Colour Price TiL209 0 125in 1502 CREEN 25 FLV410 0 2in 1504 FLU0W 25 PLV410 0 2in 1505 CREEN A pack of standard sizes and colours which fail to perform to their very ridged specification. but which are ideal for experiments. Order No. 1507 Colour Dimensions Size Crear No. Price Pack of 5 0 125in 1508 Crear No. Price Pack of 5 0 125in 1508 Crear No. Price Pack of 5 0 125in Crear No. Price Crear No. Pri	Primary 500 ohm, Secondary 3 Dimensions 20 × 15 × 15mm	JTPUT 2 and 8 ohr	
LT729 MIN. OUTPUT Primary 200 onm C.T. Secondary 3.2 and 8 ohm. 400mW. Dimensions 25 × 20 × 20mm Order No. 2048 LT730 MIN. OUTPUT Primary 500 ohm C.T. Secondary 3.2 and 8 ohm. 500 mW. Dimensions 25 × 20 × 20mm. Order No. 2049 LT30 MIN. OUTPUT Primary 500 ohm C.T. Secondary 3.2 and 8 ohm. 500 mW. Dimensions 25 × 20 × 20mm. Order No. 2049 LE.E.D.S Type Size Order No. Colour Pril.213 0.125in 1501 RED 12p TIL210 0.125in 1503 PLV310 2 in 1504 PLV410 2 in 1505 GRAde L.E.D.S 25p LOUG GRAde L.E.D.S A pack of standard sizes and colours which fail to perform to their very ridged specification. but which are ideal for experiments. Order No 1507 20-90 L.E.D. CLIPS Size Order No. Price Pack of 5 0 125in 150 150	I Primary 1kO C T Secondary 5	RIVER 30 ohm C.T. D	
LT30 MIN. OUTPUT Primary 500 ohm C.T. Secondary 3 2 and 8 ohm, 500 mW. Dimensions 25 × 20 × 20mm. Order No. Colour Price L.E.D.S Type Size Order No. Colour Price TiL210 1501 RED 120 TiL210 1250 GREEN 25p TIL213 0 1251 1502 GREEN 25p TIL213 0 1251 1502 GREEN 25p TIL213 0 1251 1502 GREEN 25p TIL213 0 1251 150 FLV410 0 2 in 1505 GHEEN 25p 2 and Grade L.E.D.S A pack of standard sizes and colours which fail to perform to their very ridged specification. but which are ideal for experiments. Order No 15040 ELE.D. CLIPS	LT729 MIN. O	JTPUT	
L.E.D.s Type Size Order No. Colour Price TiL209 0 125in 1501 RED 12p TiL211 0 125in 1502 GREEN 25p FLV115 0 12in 1503 YELLOW 25p FLV115 0 1504 RED 12p FLV410 0 2in 1505 GREEN 25p PLV410 0 2in 1505 GREEN 25p 2nd Grade L.E.D.s A A pack of standard sizes and colours which fail to perform to their very ridged specification. but which are ideal for experiments. 0'rder No 1507 £0'-90 L.E.D. CLIPS Size Order No. Price Pack of 5 0 12p			
L.E.D.s Type Size Order No. Colour Price TiL209 0 125in 1501 RED 12p TiL211 0 125in 1502 GREEN 25p FLV115 0 12in 1503 YELLOW 25p FLV115 0 1504 RED 12p FLV410 0 2in 1505 GREEN 25p PLV410 0 2in 1505 GREEN 25p 2nd Grade L.E.D.s A A pack of standard sizes and colours which fail to perform to their very ridged specification. but which are ideal for experiments. 0'rder No 1507 £0'-90 L.E.D. CLIPS Size Order No. Price Pack of 5 0 12p	LT730 MIN. OF	UTPUT	m 500 mW
Type Size Order No. Colour Price TIL210 0 125in 1501 RED 12p TIL211 0 125in 1503 GREEN 25p TIL213 0 125in 1503 YELLOW 25p FLV115 0 2in 1505 GREEN 25p FLV310 0 2in 1505 GREEN 25p PLV410 0 2in 1506 YELLOW 25p 2 nd Grade L.E.D.s A pack of standard sizes and colours which fail to perform to their very ridged specification. but which are ideal for experiments. 0rder No. 1507 £0.90 L.E.D. CLIPS Size Order No. Price Pack of 5 0 12bin 1508/0-125 15p	Dimensions 25 × 20 × 20mm. Order No. 2049	y 3 2 and 0 on	£0 · 42*
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TiL209 0 125in 1501 HED 126 TiL211 0 125in 1503 YELLOW 25p TiL213 0 125in 1503 YELLOW 25p FLV115 0 2in 1505 GHEEN 25p FLV310 0 2in 1505 GHEEN 25p Qad Grade L.E.D.s A pack of standard sizes and colours which fail to perform to their very ridged specification. but which are ideal for experiments. 0 1507 £0.90 L.E.D. CLIPS Size Order No. Price Pack of 5 0 126in	Type Size Order I	No. Colour	Price
FLV115 0.2 in 1504 RED 12p FLV310 0.2 in 1505 GREEN 25p FLV410 0.2 in 1506 YELLOW 25p 2nd Grade L.E.D.s A pack of standard sizes and colours which fall to perform to their very ridged specification. but which are ideal for experiments. Order No. 1507 £0.90 L.E.D. CLIPS Size Order No. Price Pack of 5 0.12 jun. 1508/0-125 15p	TIL209 0 125in 1501 TIL211 0 125in 1502	GREEN	250
FLV410 0 2in 1506 YELLOW 259 2nd Grade L.E.D.s A pack of standard sizes and colours which fail to perform to their very ridged specification. but which are ideal for experiments. Order No. 1507 50.90 L.E.D. CLIPS Size Order No. Price Pack of 5 0.125 159	ELV115 0-2in 1504	RED	12p
A pack of standard sizes and colours which fail to perform to their very ridged specification, but which are ideal for experiments. Order No. 1507 £0.90 L.E.D. CLIPS Size Order No. Price Pack of 5 0.1251 159	FLV310 0 2in 1505 FLV410 0 2in 1506		
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L.E.D. CLIPS Size Order No. Price Pack of 5 0 125in 1508/0-125 15p	to their very ridged specification	n, but which a	LE IGATION
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Pack of 5 0 125in 1508/0-125 15p Pack of 5 0-2in 1508/0-2 30p	Size	Order No.	Price
	Pack of 5 0 125in Pack of 5 0 2in	1508/0 · 125 1508 0 · 2	15p 30p

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SCREWS. S	lotted cf	ks of BA	threaded	cadmium	-plated
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1in 0BA ≩in 0BA	839 840	£1 12 £0 83	1in 4BA	845	£0·51 £0·38
1in 2BA ∔in 2BA	842 843	£0 · 69 £0 · 54	Tin 4BA	847	£0-33 £0-50
lin 2BA	844	£0·63	jin 6BA	849	06 - 03
BA NUTS	-packs	lin 6BA	850 £0 n-plated ful	∙33 Inutsin mu	Itiples
of 100	No	Price	Type	No.	Price
Type 0BA	855	04 03 08 03	4BA 6BA	857 858	£0 42 £0 36
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supplied	in multij No.	ples of 100 Price		No.	Price
Type 0BA	859	£0·19	Type 4BA	861	£0 · 15
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and side	is, alumi	nium botto	m, front an	vinyl cove d back.	Biles
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158	9in	5	in	2‡in	£1·60*
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and scre	ews. Len		Width	Height	Price
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160 161	4in 4in		lin }in	1≨in 1∔in	62p* 62p*
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100V RM 200V RM	IS	E	3R1/200		£0 · 30 £0 · 35
400V RM	IS	E	3R1/400		£0 · 35
SILICON 50V R	MS		3R2/50		£0 · 40
100V R 200V R	MS	E	3R2 100 3R2 200		£0-45 £0-50
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	ounting			510	10.14
Туре	BLOW 20 No.	Туре	No	Туре	No.
150mA 250mA	611 612	1A 1-5A	615 616	3A 4A	619 620
550m A	613	2A 2 5A	617 618	5A All 5	621 peach
	614				
800mA	614				
800mA	JRGE 20	Туре	No.	Туре	No.
800mA ANTI-SU Type 100mA 250mA	URGE 20 No. 622 623	Туре 1А 2 А	625 626	2 · 5A 3 · 15A	628 629
800mA ANTI-SU Type 100mA	No. 622	Type 1A 2A 1.6A	625 626 627	2 · 5A	628
800mA ANTI-SU Type 100mA 250mA 500mA	URGE 201 No. 622 623 624	Type 1A 2A 1∵6A All 7	625 626 627 peach	2-5A 3-15A 5A	628 629 630
800mA ANTI-SU Type 100mA 250mA 500mA QUICK Type	URGE 201 No. 622 623 624 BLOW 1 No.	Type 1A 2A 1-6A All 7 -In Type	625 626 627 peach	2-5A 3-15A 5A	628 629 630
800mA ANTI-SU Type 100mA 250mA 500mA QUICK Type 250mA	URGE 200 No. 622 623 624 BLOW 1 No. 631	Type 1A 2A 16A AII 7 ∹In Type 500m AII 7	625 626 627 peach No. 1A 632 peach	2:5A 3:15A 5A Type 800mA	628 629 630 No. 634
800mA ANTI-SU Type 100mA 250mA 500mA QUICK Type 250mA Type	JRGE 200 No. 622 623 624 BLOW 1 No. 631 No.	Type 1A 2A 16A AII 7 -in 500m AII 7 Type 25A	625 626 627 peach No. A 632 peach No 638	2:5A 3:15A 5A Type 800mA Type 4A	628 629 630 No. 634 No 641
800mA ANTI-SU Type 100mA 250mA 500mA QUICK Type 250mA	URGE 200 No. 622 623 624 BLOW 1 No. 631	Type 1A 2A 1.6A All 7 All 7 Type 2 5A 3A	625 626 627 peach No. 1A 632 peach	2-5A 3-15A 5A Type 800mA Type	628 629 630 No. 634 No
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800mA ANTI-SL Type 100mA 250mA 500mA CUICK Type 250mA Type 1A 1.6A 2A JU 19777 126 20	JRGE 20 No. 622 623 624 BLOW 11 No. 631 No. 635 636 637 Ust a sel our hug SEE CA	Type 14 2A 16A All 7 500m All 7 500m All 7 25A A All 6 ection from te stocks ¹ OUR TALOO packed win formation	c25 626 627 p each No 14 632 p each No 639 p each	2:5A 3:15A 5A Type 800mA Type 4A	628 629 630 No. 634 No 641
800mA ANTI-SL Type 100mA 250mA 500mA CUICK Type 250mA Type 1A 1.6A 2A JU 19777 126 20	JRGE 20 No. 622 623 624 BLOW 11 No. 631 No. 635 636 637 Ust a sel our hug SEE CA	Type 14 2A 16A All 7 500m All 7 500m All 7 25A A All 6 ection from te stocks ¹ OUR TALOO packed win formation	c25 626 627 p each No 14 632 p each No 639 p each	2.5A 3.15A 5A Type 800m <i>A</i> Type 4A 5A	628 629 630 No 634 No 641 642
800mA ANTI-SL Type 100mA 250mA 500mA OUICK Type 250mA Type 1A 1-6A 2A JU 1977 126 20	BLOW 1; No. 623 624 BLOW 1; No. 635 636 637 SEE SEE SEE SEE CONL	Type 14 2A 16A All 7 500r All 7 500r All 6 25A All 6 ection from te stocks ¹ OUR TALOO packed winformation R NOV Y 50p	c25 626 627 p each No 14 632 p each No 639 p each	2.5A 3.15A 5A Type 800m <i>A</i> Type 4A 5A	628 629 630 No 634 No 641 642
800mA ANTI-SL Type 100mA 250mA 500mA CUICK Type 250mA Type 1A 1.6A 2A JU 19777 126 20	BLOW 1; No. 623 624 BLOW 1; No. 635 636 637 SEE SEE SEE SEE CONL	Type 14 2A 1.6A 3007 3007 307 255A 3A 3A 3A 3A 3A 3A 3A 3A 3A 3A 3A 3A 3A	c25 626 627 p each No 14 632 p each No 639 p each	2:5A 3:15A 5A Type 800mA Type 4A 5A Dep	628 629 630 No. 634 No 641

	SWIT	CHES		
	Description DPDT ministure slide DPDT standard slide	No. 1973 1974		Price £0 · 10* £0 · 12*
	Toggle switch SPST 1} amp 250V a.c.	1975		£0·33*
	DPDT standard side Toggle switch SPST 1) amp 250V a c Toggle switch DPDT 1 amp 250V a c Rotary on-off mains switch Push switch—Push to make Push switch—Push to break	1976 1977 1978		£0 - 36* £0 - 42* £0 - 13*
	Push switch-Push to break	1979		£0 · 18*
	ROCKER SWITCH A range of rocker switches SPST moulded in high insula- tion. Material available in a choice of colours ideal for small apparatus Description	Colour RED BLACK WHITE BLUE YELLOW LUMINOUS No.	No. 1980 1981 1982 1983 1984 1985	Price £0 · 22* £0 · 22*
l	Miniature SPST toggle, 2 an 250V a.c.			£0 - 50*
l	Miniature SPST toggle, 2 an 250V a.c.			£0·55*
	Miniature DPDT toggle, 2 an 250V a.c.			£0 · 65*
ł	Miniature DPDT tongle, cent	1960 1961		£0 · 85*
1	off, 2 amp 250V a.c. Push button SPST, 2 an 250V a.c.			£0 · 65*
	Push button SPST, 2 an 250V a.c. Push button SPST, 2 an 250V a.c. Push button DPDT, 2 an 250V a.c.			£0·68*
	Push button DPDT. 2 an 250V a.c.			*0.80
	MIDGET WAFER SWITCHES Single-bank wafer typesui 100mA or 150V d.c. in nor break contacts. These switc	6	hing at 2 ds make indle 0-2	50V a.c. before- 5in dia.
	Description	Orde	er No.	Price
l	1 pole 12 way	19(19 (66	£0 48* £0 48*
	2 pole 6 way 3 pole 4 way 4 pole 3 way	191		£0-48* £0-48*
	MICRO SWITCHES Plastic button gives simi	ple	er No.	Price
	on-on action Rating 10 amp 250V a.c. Button gives 1 pole chan over action Rating 10 amp 250V a.c.	194 194	6 9	£0 · 20
	Rating 10 amp 250V a.c.	19	70	£0·25
ł	DISI	PLAYS		
		Ord	er No.	Price
	Type BDL707 0-3in single BDL747 0-6in single BDL727 0-5in double	15 15	10	£0-80 £1-50
1			12	£1-80
-	COLD CATHODE ITT 5087 side viewing indicator tub points. Wide viewing angle series anode resistors			
		Order no	roia Pri	Ce 10.00
	VOLTAGE I	REGULA	TOR	5
	Positive Regulators TO220 MVR 7805 5V £1-35 MVR 7812 12V £1-35	Case	R 7815 15 R 7824 24	
l	Negative Regulators TO22	0.0454		
3	MVR 7905 5V £2-00 MVR 7912 12V £2-00	MVF MVF	7915 15 7924 24	V £2.00 V £2.00
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NEW VOICE FOR COMPONENT INDUSTRY

THE formation of the Electronic Components Industry Federation (ECIF) by a marriage between the Radio and Electronic Component Manufacturers' Federation and the Electronic Components Board provides the UK components industry with a single and powerful voice to represent its interests.

The creation of this new body coincides with the recognition by the Government of the component industry's importance to our economic affairs. For the electronic component industry is one of five selected industries from an original list of 40, that have been deemed of strategic importance for the future. With this recognition comes the establishment of a Department of Industry support scheme for British-based component makers, with an initial sum of £20m to be made available. The new streamlined association ECIF will be working in very close cooperation with the NEDO Sector Working Party on components.

The ECIF is important in that it represents about 90 per cent of the entire component manufacturing business in the United Kingdom. The list of 145 member companies embraces the smaller firms, many of these producing the less exalted components without which the glamorous components like semiconductor devices would be deprived of their chance to shine, as well as the big companies with household names. Included amongst the latter are well-known American semiconductor makers who have established factories in the UK.

But the exclusion of other well-known names in the semiconductor field is equally notable. General Instrument Microelectronics, for one, has a manufacturing establishment in Scotland and many of their current i.c. devices have been designed and developed here. Yet this multi-national company has declined the invitation to join ECIF. Apparently GIM are rather sore at being treated as "foreigners" whenever there is any Government hand-out to help finance research and development work by UK firms. So they feel reluctant to chip-in for the common cause by contributing to an organisation such as ECIF.

Wholly British owned firms such as Ferranti, GEC and Plessey have, from time to time, received financial backing from the Government. This is part of a determined "effort to strengthen the indigenous semiconductor industry. But facts of life concerning the semiconductor industry in general suggest that those US firms that have set up development and manufacturing facilities in the UK should be equally encouraged in their efforts, which do of course contribute directly to our economy. On the very important technological plane, these companies keep the UK in the main stream of big developments originating in their home bases across the Atlantic. How better to encourage them develop their roots over here than by making them eligible for consideration for financial backing? There are signs that the Government is beginning to think this way; if so, any coolness towards the ECIF will no doubt disappear and that body will become all the more relevant to the component situation in the UK and thus be better able to fulfil its intended purpose: to help maintain a thriving and forward looking components industry in this country.

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11

ELECTRONIC organ fans will be familiar with the pleasant sound produced by a Leslie rotating type loudspeaker unit, and most would probably agree that what gives rise to this pleasing effect, is the mechanically induced phase modulation of the sound.

By L. F. Reeve

It may seem sacrilegious to suggest that there are other, more flexible and less clumsy ways of achieving phase modulation of audio signals, but an organ which has enjoyed almost legendary popularity over the years, does produce its modulation at the electrical signal stage. The Hammond A100 employs an electromechanical device consisting of a short period delay line, which is mechanically scanned in such a way as to induce undulating phase changes in the signal, controllable at the keyboard.

The circuit described in this article goes one stage further, producing an aural appeal virtually equal to the "true Leslie effect", by a completely electronic process.

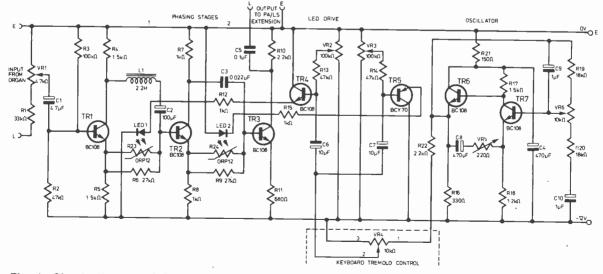


Fig. 1. Circuit diagram of Organ Tremolo Unit. LED1 is optically coupled to R23, and likewise LED2 with R24

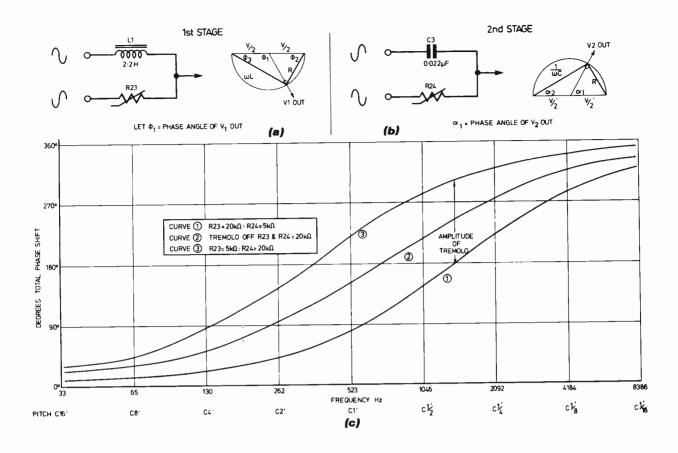


Fig. 2. (a) Vector diagram of inductive stage. (b) Capacitive stage. (c) Phase shift versus frequency

TREMOLO EFFECTS

The unit was specifically designed for injecting phase modulation into the electrical signal available at the organ's auxiliary output socket; this signal then being fed to a conventional power amplifier loudspeaker extension. The tremolo speed is approximately 5Hz, which will blend acoustically with the tremolo from the organ's Leslie speaker system, regardless of the latter's precise speed. With both tremolos on, the effect is similar to theatre organ tremolo, and a modified effect can also be obtained by running the organ's unit at "chorus" speed (about one cycle in 1.5 seconds), with full external tremolo. If the control of the external tremolo is now eased back to a low level, a very reasonable "straight" organ sound is obtained.

Although the author disliked the idea of having both the internal and external units running at chorus speed, there is no reason why an experimental oscillator, running at the slower speed, should not be added to this unit in conjunction with suitable changeover switching.

Obviously the electronic tremolo unit can be used on its own, with an organ not fitted with its own system. In this case the tremolo unit and the PA/LS extension can be regarded as a relatively inexpensive alternative to a rotation loudspeaker type extension, with considerable improvement in tone ambience as a whole.

Practical Electronics May 1977

CIRCUIT DESCRIPTION

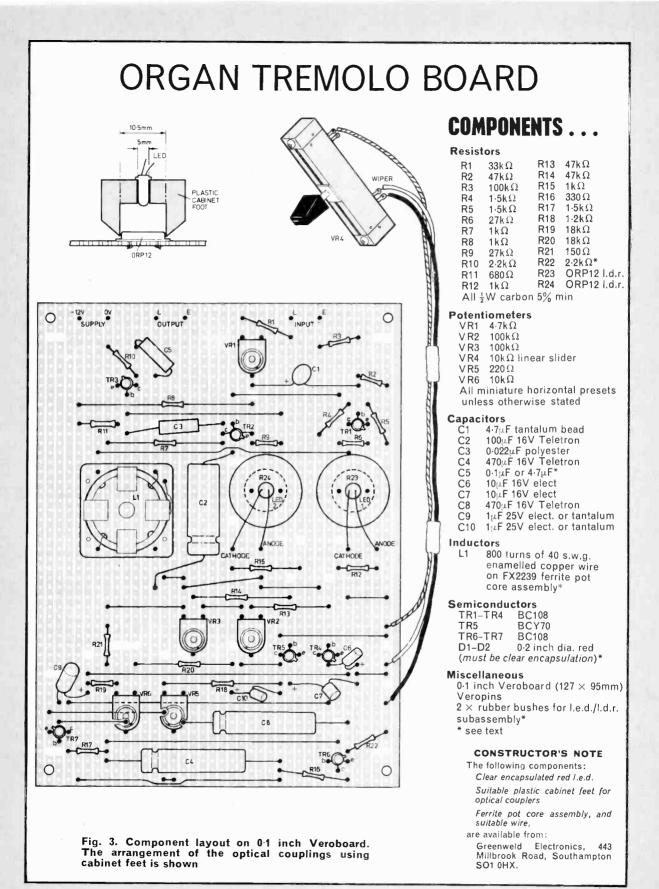
Referring to the diagram in Fig. 1, the organ signal passes through two phasing stages, and then a conventional output stage. The first stage contains an inductance/resistance combination and the second stage, a capacitance/resistance combination, the latter being subjected to a 180 degree phase inversion by the second transistor.

The resistance element in each case is a light dependent resistor coupled to a light emitting diode, and mounted in a light-proof container.

The oscillator section (TR6 and TR7) drives the two transistors TR4 and TR5 through the tremolo control potentiometer VR4. Because TR4 is *npn* and TR5 is *pnp* the two l.e.d.s are driven in antiphase. This causes the respective resistances of R23 and R24 to swing alternately between the arbitrary limits of $5k\Omega$ and $20k\Omega$.

The phase changes imposed on the signal by the two stages are shown in Figs. 2a and 2b, and as can be seen, the inductive stage creates the phase lag θ_1 , and the capacitive stage produces a phase lead of α_1 . The maximum total phase variation for any given frequency can be read from the graph in Fig. 2c, and this is $\theta_1 + \alpha_1$ degrees. The plots were produced mathematically with formulae derived from the vector diagrams.

1



Practical Electronics May 1977

From the trigonometrical ratios applicable to a right-angle triangle:

Tan $\theta_2 = \frac{\omega L}{R}$ equation (1) It can be shown that $\theta_1 = 2\theta_2$ degrees. Since $\theta_3 = 90 - \theta_2$ degrees and also $\theta_3 = \frac{180 - \theta_1}{2}$ degrees. Therefore $90 - \theta_2 = \frac{180 - \theta_1}{2}$ Multiplying by 2: $180 - 2\theta_2 = 180 - \theta_1$ $\therefore \theta_1 = 2\theta_2 \dots$ equation (2) Combining equations (1) and (2) we get $\theta_1 = 2 \tan^{-1} \left(\frac{\omega L}{R}\right)$ or $\theta_1 = 2 \tan^{-1} \left(\frac{2\pi f L}{R}\right)$

Similar rules define α_1 as being 2 tan⁻¹ (2π fCR).

The graphical plots of these formulae correspond closely to measurements taken with the unit in operation.

Most electronic organs have a high signal level at their extension output sockets, and for this reason the value of $33k\Omega$ has been chosen for R1, to give an audio output signal which is about -10dB with respect to the input level. This attenuation can be reduced if required, by lowering the value of R1.

The purpose of R22 is to regulate the maximum depth of tremolo available at VR4. The value of $2 \cdot 2k \Omega$ found suitable in the prototype, can be changed up or down in value, to cover tolerances in l.e.d. performance.

Capacitor C5 should be 0.1μ F for a 200k Ω output, or 4.7μ F for a 10k Ω impedance output.

If the second oscillator is required for the chorus effect, it can be constructed using the same circuit as the oscillator section of Fig. 1, and making $R19-22k\Omega$, $R20-12k\Omega$, $C9-10\mu F||4\cdot7\mu F$ (14·7 μF), and C10 $-50\mu F$. The decoupling components R21 and C4 may be omitted in this instance. The period of oscillation is about 1 to 1.5 seconds.

CONSTRUCTION

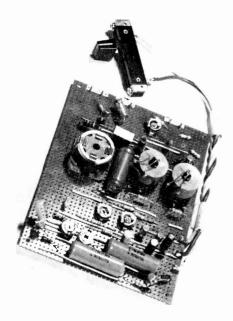
The two l.e.d./l.d.r. sub assemblies need no explanation, being put together as indicated in Fig. 3. It should be borne in mind that the l.e.d.s are required to be withdrawn from their housings, to carry out the adjustments detailed later.

The winding of the inductor coil can be done using a handbrace held in a bench vice. The bobbin is lightly clamped between two oversize washers on a 50mm long 2BA screw, and secured with a nut, the end of the screw being held in the handbrace chuck. The nut is lightly tightened, after the bobbin has been centred to rotate concentrically. It is advisable to clean the end of the wire and anchor it to the chuck with adhesive tape, before starting to wind.

Have a piece of sticky tape cut to size and ready to wrap around the coil after the 800 turns of 40 s.w.g. enamelled copper wire have been wound on. There is plenty of room on the bobbin, and ordinary "pile" winding is satisfactory.

The coil ends should be brought out of diametrically opposite slots, which after assembly of the inductor,

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are soldered to the pins as shown on the circuit board layout. An carth connection is soldered between the fifth pin, and the adjacent clip.

Components are mounted on Veroboard, the layout of which is shown in Fig. 3. It is recommended that the complete panel should be mounted in a vacant space inside the organ's housing, to allow the audio and tremolo control connections to be made more readily. The only outgoing signal will then be the phase processed output lead.

POWER SUPPLY

The total current consumption of this 12 volt positive earth system is 25mA, and may be available from the organ's supply. If this is not possible, and a mains power pack is not desired, then two PJ996 or similar batteries would provide a generous source of supply.

ADJUSTMENTS

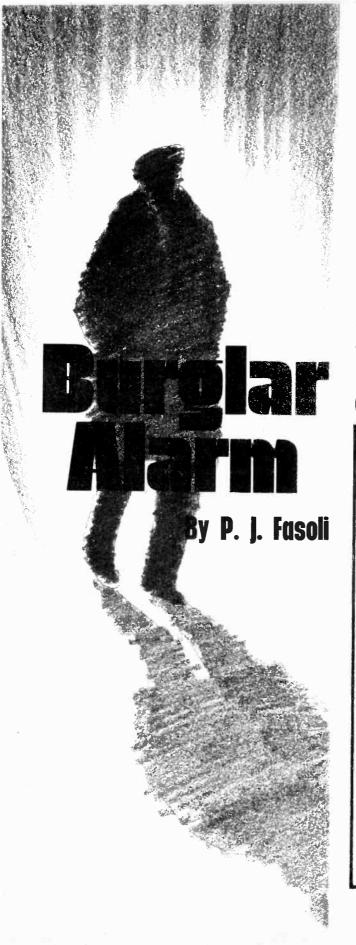
(1) Before switching on, VR2 and VR3 should both be turned to an almost fully clockwise position, thus cutting off TR4 and TR5.

(2) After switching on, VR5 and VR6 should be adjusted to obtain a sinusoidal waveform from the oscillator. This is best done by connecting an oscillo-scope across VR4, but it can also be done by ear, as a non-sinusoidal waveform will produce audible harmonics if connected to the P.A./L.S. extension. This of course would be done after installation.

(3) With zero drive from the oscillator, and with the l.e.d.s removed from their housings, turn VR2 anticlockwise until LED1 begins to glow. Similarly turn VR3 anticlockwise until LED2 begins to glow. Replace the two l.e.d.s in their housings.

(4) Adjust VR1 to the point where the unit accepts full organ signal without overloading.

This completes the adjustments, and the unit should now be ready for use.



THIS alarm is suitable for all types of security application, in a house, shop or any other place that needs to be protected from an intruder.

It can be used with open and closed circuit sensors, and is of bridge design to give the maximum protection. Entry and exit delays are provided, allowing one to enter and leave the building without need for a key switch at the door.

The alarm is run from internal batteries, an HP1 was used in the prototype. This battery will give up to six amperes. Since the unit consumes only 10mA in the Guard condition, the cost of providing a mains power supply unit was hardly felt to be justified.

THE CIRCUIT

As will be seen from Fig. 1, the alarm is built around a 741 operational amplifier, operating in the open loop mode as a voltage comparator. The alarm voltage V_1 is set by the divider chain comprising resistors R_x and R_y . The voltage at the junction is applied to the inverting input (pin 2) of IC1. A similar voltage V_2 is set on the non-inverting input (pin 3) by the potential divider formed by R1, VR1 and R2.

Providing that V_1 remains more positive than V_2 , the output of the 741 (pin 6) will stay at about two volts.

COMPONENTS ...

Resistors R1, R2 3-9kΩ R3, R4 39k Ω **R**5 $12k\Omega$ **R**6 $22k\Omega$ **R**7 560 Ω R8 47Ω R9 220Ω1W All unspecified resistors ½W Potentiometers VR2 1M Ω min. preset VR1 50kΩ lin. Capacitor 470µF 16V electrolytic C1 Semiconductors BFY51 TR1 TR2 TIS43 IC1 741 D1 1N4148 BTY79-400R (400V 6A thyristor) CSR1 Miscellaneous **S**1 Push-on, delay switch (see text) S2 3-pole, 4-way key-operated switch* S3 S.P.C.O. microswitch RLA D.P.C.O. relay, 185 Ω 12V coil, continental type WD1 Audible warning device, 12V (RS Componets 248-808) 12V lamp with holder, green lens LP1 LP2 12V lamp with holder, red lens

- B1 HP1 battery
- Plug for B1; heat sink for CSR1 (see Fig. 2); socket for IC1 (if required); 12-way terminal strip; aluminium box (see text); alarm bell, 12V; R_x, R_y and sensors (see text)

*The key-operated switch is available from F & G Electronics (Manchester), 28 Middleham Street, Manchester M14 7NG In this condition, the forward bias on TR1 is not sufficient for RLA to be energised. If, however, the resistance of line A increases or line B decreases, the balance will be upset, changing the state of the input stage and making the output rise to about 10 volts. As soon as this happens, transistor TR1 will saturate and energise relay RLA/2. When the unit is in the Guard condition, the relay will latch when set off, thus keeping the bell powered. The only way to stop it is by means of the key-operated switch S2.

When the alarm is triggered, power will be applied via RLA2 and S2c to the timer, based on the unijunction TR2. The delay, set by the charging rate of C1 via R6/VR2, will allow up to eight minutes to enter and switch off the alarm. Power will also be applied to an audible warning device WD1, to indicate that the timer has started. The bell will not ring until the timer has completed its cycle and fired thyristor CSR1.

A microswitch S3 fitted to the case operates if the lid is opened. The switch contacts place a short circuit across the gate and anode of the thyristor, causing the bell to ring if the unit is tampered with.

EXIT DELAY

The exit delay is simply a push-to-make, delay switch S1 connected across the base emitter junction of TR1 to hold off the alarm until the exit door has been closed. This type of switch is intended for use on the stairways of blocks of flats, etc. turning the lights on when pushed, but turning them off again after a period of a couple of minutes or so, depending upon its setting, to save power. Such switches are available from most good electrical shops.

If an intruder enters he will not be able to silence the alarm by pushing S1, as the relay will have latched on via its contact RLA1. Lamps LP1 and LP2 are for setting the unit, and for indicating the state of the alarm loops when switching the unit on. If the alarm loops are upset, RLA will be energised, illuminating the red lamp instead of the green when S2 is turned to the Set Balance position. The doors and windows should then be checked before turning S2 to Guard, otherwise the bell will ring.

Resistor R9 maintains a holding current of about 30mA through the thyristor, which would otherwise switch off when the bell trembler contacts open.

CONSTRUCTION

The prototype unit was built in an aluminium box $180 \times 205 \times 75$ mm, large enough to accommodate the battery and all the electronics. Most of the components are mounted on a printed board, as shown in Fig. 2. Of the remainder, VR1, LP1, LP2, S1 and S2 are mounted on the lid, and the audible warning device is mounted at the top of the box, with a hole to let out the sound. All external connections are made via a 12-way terminal strip.

SETTING UP

To set up the alarm, fit R_x and R_y , which can be any value from $10k\Omega$ to $56k\Omega$. In the prototype system, $33k\Omega$ and $27k\Omega$ were used. Place the key in S2 and

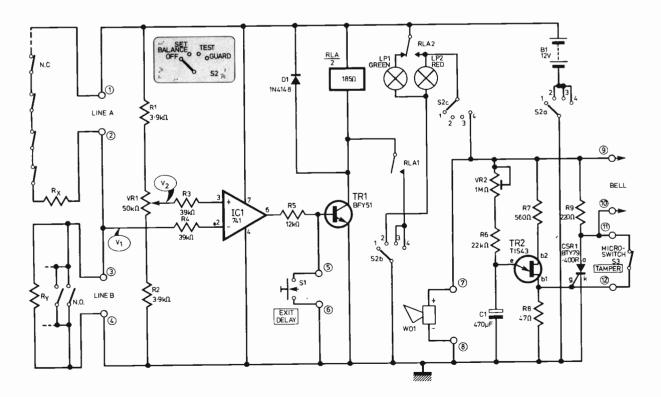
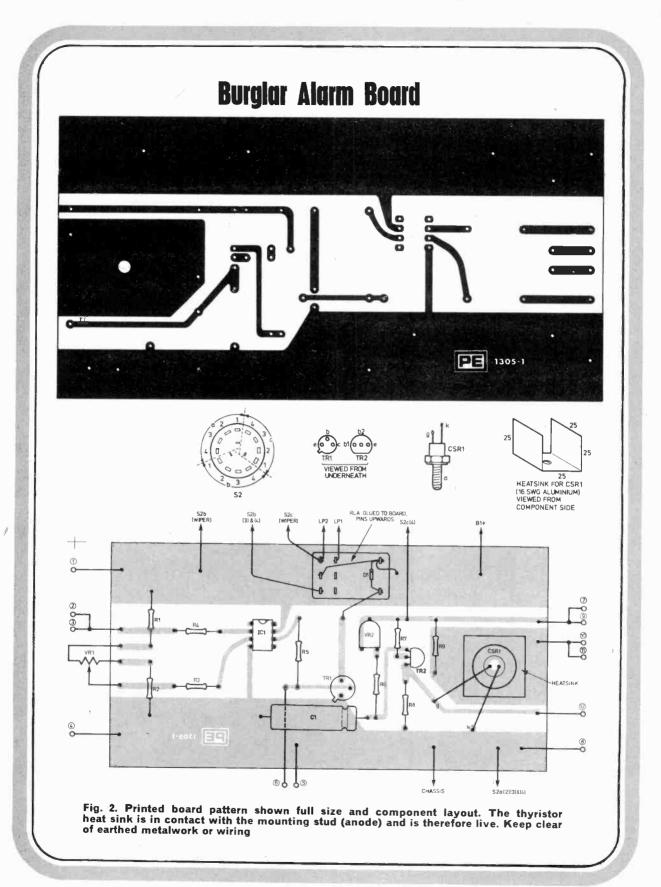


Fig. 1. Circuit diagram of the complete burglar alarm. The values of $R_{\rm X}$ and $R_{\rm y}$ are discussed in the text. Circled numbers identify connections to the 12-way terminal block

343



turn to Set Balance. Adjust the balance potentiometer VRI to the point where the green lamp just comes on, then remove R_x and short out R_y in turn. In each case the green should go out and the red come on. This indicates that all is well.

Next turn S2 to Test, and as before open and short circuit the resistors. The relay should latch and not reset until S2 is returned to Set Balance.

To set the timer, connect two links, one from No. 7 on the terminal strip to battery positive, and one from No. 8 to battery negative. Disconnect the audible warning device to stop the noise while setting up. Connect a 12 volt lamp to terminals 9 and 10 to indicate when the thyristor has fired, and adjust preset VR2 to give the required delay. The only remaining item to be tested is the tamper switch. To do this remove all the links and reconnect the audible warning device. Remove the temporary lamp from terminals 9 and 10 and wire in the bell. Turn S2 to Guard, i.e. alarm on, and remove the lid, whereupon the bell should ring.

The setting of VRI should be checked when the unit is installed with the external sensors connected to lines A and B. These sensors may be reed switches, microswitches, pressure mats, etc. as required.

To make the alarm more difficult for an intruder to bypass, line A and line B can be run in one four-core cable. The intruder will then not know the correct ones to cut or join.



First UK Conference on DIY Computing

A ONE-DAY conference entitled "Build Your Own Computer" is being staged by Online Conferences at the Institution of Electrical Engineers, Savoy Place, London WC2, on Saturday, 14 May.

Designed to cater for the home computer builder and for companies considering building their own microcomputer system from scratch, the conference will deal with the principles of computer construction, availability of components, costs, use of domestic TV and audio equipment as peripherals, programming, and the effect of microelectronics on consumer goods.

Tickets are £8 in advance, or £9 at the door. Further details from Online, Cleveland Road, Uxbridge, Middlesex.

See Us at the All Electronics Show

THE All Electronics Show is being held from 19-21 April at Grosvenor House, Park Lane, London W1. Among the 200 or so exhibitors will be PRACTICAL ELEC-TRONICS (Stand 127, Great Room) showing a selection of recent and future projects.

The show is open from 10am to 6pm daily, admission 50p.

Sound '77 Exhibition

THE Sound '77 Exhibition, organised by the Association of Sound and Communication Engineers, will take place at the new Wembley Conference Centre from April 19-21.

On display will be a wide range of alarm systems, amplifiers, microphones, loudspeakers, sport event timing equipment, music systems and public address and audio equipment. The exhibition is open daily from 10am to 6pm (5pm on the last day) and admission is free.

The organisers were formerly the Association of Public Address Engineers. Their new name is intended to acknowledge the ever-widening scope of sound communications, in industry, commerce, public services, and entertainment.

London Meeting a Great Success

PRACTICAL ELECTRONICS joined forces with a large semiconductor manufacturer and a component retailer to present a Microprocessor Forum for Constructors.

The first date, Saturday, 26 February, was soon oversubscribed, and a repeat of the Forum was organised for the following Saturday, 6 March. PE's associates in this unique presentation were National Semiconductor (UK) Ltd., one of the world's foremost microelectronics manu-



Competition winners A. Mackintosh and A. Challinor receiving their Introkit prizes from Mrs. J. L. Marshall

facturers, and A. Marshall (London) Ltd., one of the largest distributors of electronic components. Audiences of about 200 attended each staging of the

Audiences of about 200 attended each staging of the Forum at Berners Hotel, London WI, listening attentively to two National Semiconductor engineers, Dave Brown and Stewart May, who spoke on different aspects of microprocessors and answered questions from the audience.

A feature of the first Forum was the presentation by Mrs J. L. Marshall of prizes to the first two winners of the PE Microprocessor Competition, A. Mackintosh of Brighton, and A Challinor of Stoke-on-Trent.

An eliminati. competition is under way to find the third main prize winner, and we hope to publish a full list of winners in our June issue.



Part 3 - THE INSTRUCTION SET AND PROGRAMMING TECHNIQUES INCREDITION SET AND PROGRAMMING TECHNIQUES INTERPORT OF THE INSTRUCTION SET AND PROGRAMMING TECHNIQUES INTERPORT OF THE INSTRUCTION SET AND PROGRAMMING TECHNIQUES

A "Miss World" contest for microprocessor chips would be difficult to judge. There would be those who liked their MPUs well stacked (see Part 2), the bipolar brigade who preferred speed to comfort, and of course the "leg men" hooked on long graceful instruction sets! At the end of the day, though, the vital statistic which would receive the **most** scrutiny, and receive the **most** admiration and wolf-whistles, would undoubtedly be the instruction set. It is the very existence of the instruction set which sets MPU chips apart from standard LSI, and as far as we are concerned, "Vive la difference!"

THE instruction set is, in effect, a rudimentary "language" through which we earthlings can communicate our desires to the inner recesses of the semiconductor chip, a language which is designed by the chip makers in most cases. Of course the ideal language for microprocessor programming would be English, or if you happen to be French, French, but microprocessors are far removed from our intellectual level (thank goodness!) and require us to "talk down" to them using a basic language with a very limited vocabulary, namely, the instruction set.

With larger computer systems much effort has been expended in writing special programs called COMPILERS which translate instructions written in "almost English" into the currency of the computer instruction set, but while this is a universally practised technique for large systems, it is a newcomer to microprocessors and can only be used on MPU systems with a lot of memory space available.

It seems unlikely that we will see microprocessor systems in our price range which are capable of running compilers for some time yet, which means that when we do use microprocessors we have to think in terms of the basic instruction set that the MPU is born with, and be familiar with the way the MPU chip operates in a hardware sense. This fact of life is not unpalatable since it gives the whole subject a distinct "hardware" or "gates and wire" flavour which should make using microprocessors a natural and exciting experience for most electronics enthusiasts.

INSTRUCTION CODING

Fig. 3.1 gives some examples from an MPU instruction set, and as you can see, there is an English sentence or two to describe the effect of the instruction to us mortals, and a binary code word which specifies the instruction to the MPU. Now the MPU can't read English, and we find it very difficult to remember and use patterns of l's and 0's, and so to write a program we would use a list of instructions like the one in Fig. 3.1 to "look-up" the binary code for a specific operation so that we can use it in our program.

To make it a little easier for us to remember the binary patterns of MACHINE CODE as it is called, most people find it best to use a notation called HEXADECIMAL which represents groups of four binary bits as a single character from the set 0 to 9 and A to F which provide the necessary 16 possibilities.

Using hexadecimal the eight-bit instruction 11101000 becomes E8, which is both easier for us to use and straightforward for a program to convert into binary if required.

An alternative to hexadecimal is OCTAL code where the binary is split into groups of three bits, each represented by a decimal digit from the set 0-7, so that the binary instruction above would be coded as 350 in this notation.

Octal is losing a lot of ground these days to hexadecimal which is undoubtedly a more powerful and easy-to-use technique for microprocessor use, especially since it has the added protection that in a list of

4040 MICROPROCESSOR

INSTRUCTION SET

Summary of Processor Instructions

*Two Cycle Instructions

			Instruction Code									
Mnemonic Description			D	PR			OP.	A				
		03	D2	01	00	03	02	01	00			
		-										
	MACHINE GR	UUP										
NDP	No Operation	0	0	0	0	0	0	0	0			
HLT	Halt	0	0	0	0	0	0	0	1			
BBS	Branch Back and SRC	0	0	0	0	0	0	1	0			
LCR	Command Register to Accumulator	0	0	0	0	0	0	1	1			
OR4	Logical DR, Index Register 4 and Accumulator	0	0	0	0	0	1	0	0			
OR5	Logical O.R. Index Register 5 and Accumulator	0	0	0	0	0	1	0	1			
AN6	Logical AND, Index Register 6 and Accumulator	0	0	0	0	0	1	1	0			
AN7	Logical AND, Index Register 7 and Accumulator	0	0	0	0	0	1	1	1			
DBO	Designate RDM Bank 0	0	0	0	0	1	0	0	0			
081	Designate RDM Bank 1	0	0	0	0	1	0	0	1			
SB0	Select Index Register Bank 0	0	0	0	0	1	0	1	0			
SB1	Select Index Register Bank 1	0	0	0	0	1	0	1	1			
EIN	Enable Interrupt	0	0	0	0	1	1	0	0			
DIN	Disable Interrupt	0	0	0	0	1	1	0	1			
RPM	Read Program Memory,	0	0	0	0	1	1	1	0			
	Half-Byte per Instruction						-	-				
* JCN	Jump Conditional to Address	0	0	0	1	Cı	C2	C3	C4			
	A ₂ A ₂ A ₂ A ₂ A ₁ A ₁ A ₁ A ₁ A ₁ Condition Code, C ₁ C ₂ C ₃ C ₄	Α2	A2	A2	Å2	Α1	Α1	Α1	Α1			
* FIM	Fetch Immediate, ROM Data D2D1	0	0	1	0	R	R	R	0			
	to Index Register Pair RRR	02	D_2	02	D_2	D 1	D1	D1	D ₁			
SRC	Send Register Control	0	0	1	0	R	R	R	1			
FIN	Fetch Indirect, Data from RDM to Index Register Pair RRR	0	0	1	1	R	R	R	0			
JIN	Jump Indirect to Address in Register Pair RRR	0	0	1	1	R	R	R	1			
*JUN	Jump Unconditional to Address	0	1	0	0	A ₃	Α3	A3	Α3			
	A3A2A1	A2	Α2	Α2	A ₂	Α1	Α1	A۱	Α1			
" JMS	Jump to Subroutine at Address	0	1	0	1	Α3		A3	A3			
	A3A2A1	A2	Α2	A2	A ₂	Α1	Α1	Α,	Α,			
INC	Increment Register RRRR	0	1	1	0	R	R	R	R			
* ISZ	Increment Register RRRR. Go 10 Address A2A1 if result is not zero,	0 A ₂	1 A ₂	1 A ₂	1 A ₂	R A 1	R Aı	R Aı	R A ₁			
ADD	otherwise go to next instruction Add Register RRRR to Accumulator	1	0	0	0	R	R	R	R			
SUB	with Carry Subtract Register RRRR from	1	0	0	1	R	R	R	R			
LO	Accumulator with Borrow Load Contents of Register RRRR to	1	0	1	0	R	R	R	R			
хсн	Accumulator Exchange Contents of Register	1	0	1	1	R	R	R	R			
BBL	RRRR and Accumulator Branch Back and Load Data DDDD	1	1	0	0	D	D	D	D			
LDM	to Accumulator Load Data DDDD to Accumulator	t	1	0	1	D	0	D	D			

untion Code

Fig. 3.1. The complete instruction set of the Intel 4040. Note that although the 4040 is a 4 bit MPU, it uses an 8 bit instruction code

"hex" code there are always a few letters to remind you that this *is* "hex" whereas a list of "octal" can look very like a list of decimal numbers which of course it is not, since, for example, 77 octal equals 63 decimal when interpreted as a numerical value. See Fig. 3.2 and Fig. 3.3.

INSTRUCTIONS AND NUMBERS

This last point concerning octal code raises the interesting question of just how the MPU differentiates between 11101000 the instruction which means (say) "subtract from memory", and 11101000 the number

Mnemonic	Description		0	lr PR	structi	ion Cod	ie DP.	A		
Muemonic	Uschpton	03	02		Do	03	D ₂	D1	00	
	I/O and RAM GF	ROUP								
WRM	Accumulator to Selected RAM Main Memory Character	1	1	1	0	0	0	0	0	
WMP	Accumulator to Selected RAM Dutput Port	1	1	1	0	0	0	0	1	
WRR	Accumulator to Selected RDM Dutput Port	1	1	1	0	0	0	1	0	
WPM	Accumulator to Selected Half-Byte in Read/Write Program Memory	1	1	1	0	0	0	1	1	
WRO	Accumulator to Selected RAM Status Character 0	1	1	1	0	0	1	0	0	
WR1	Accumulator to Selected RAM	1	1	1	0	0	1	0	1	
WR2	Status Character 1 Accumulator to Selected RAM	1	1	1	0	0	1	1	0	
WR3	Status Character 2 Accumulator to Selected RAM	1	1	1	0	0	1	t	t	
SBM	Status Character 3 Subtract Selected RAM Main Memory Character from	1	1	1	0	1	0	0	0	
RDM	Accumulator with Borrow Selected RAM Main Memory	1	1	1	0	1	0	0	1	
RDR	Character to Accumulator Selected ROM Input Port	1	1	1	0	1	0	1	0	
ADM	to Accumulator Add Selected RAM Main Memory	1	1	1	0	1	0	1	1	
RDO	Character to Accumulator with Carry Selected RAM Status Character D	1	1	1	0	1	1	0	0	
RDI	to Accumulator Selected RAM Status Character 1 to Accumulator	1	1	1	0	1	1	0	1	
RD2	Selected RAM Status Character 2 to Accumulator	1	1	1	0	۱	1	1	0	
RD3	Selected RAM Status Character 3 to Accumulator	1	1	1	0	1	1	1	1	
	ACCUMULATOR	GRO	UP							
CLB	Clear Accumulator and Carry	1	1	1	1	0	0	0	0	
CLC	Clear Carry	1	1	1	1	0	0	0	1	
IAC	Increment Accumulator	1	1	1	1	0	0	1	0	
CMC	Complement Carry	1	1	1	1	0	0	1	1	
CMA	Complement Accumulator	1	1	1	1	0	1	0	0	
RAL	Rotate Left, Accumulator and Carry	1	1	1	1	0	1	0	1	
RAR	Rotate Right, Accumulator and Carry	1	1	1	1	0	1	1	0	
TCC	Transmit Carry to Accumulator, Clear Carry	1	1	1	1	0	1	1	1	
DAC	Decrement Accumulator	1	1	1	1	1	0	0	0	
TCS	Transfer Carry Subtract and Clear Carry	1	1	1	1	1	Ō	Ō	1	
STC	Set Carry	1	1	1	1	1	0	1	0	
DAA	Decimal Adjust Accumulator	i	i		1	1	Ō	1	1	
КВР	Keyboard Process	i	i	í i	1	1	ī	Ó	0	
DCL	Designal Command Line	i	i	i	i	1	1	Ō	1	
	NOTES:									
	(1) The condition code is assigned									
	C ₄ = 1 Invertiumo	cond	stio	n						

C1 = 1 Invert jump condition

C1 = 0 Not invert jump condition

C₂ = 1 Jump if accumulator is zero

- C₃ = 1 Jump if carry/link is a 1
- $C_4 = 1$ Jump if test signal is a D

(2) RRR is the address of 1 of 8 index register pairs in the CPU.

(3) RRRR is the address of 1 of 16 index registers in the CPU.

(4) Each RAM chip has 4 registers, each with twenty 4-bit characters subdivided into 16 main memory characters and 4 status characters. Chip number, RAM register and main memory character are addressed by an SRC instruction. For the selected chip and register, however, status character locations are selected by the instruction code (OPA).

which has a value of 232 in decimal.

The answer is that the MPU cannot differentiate between these two possibilities at all; it interprets anything that is placed in its instruction register as an instruction, and it will interpret any part of the store content (including instructions) as data if told to do so by the program.

This means in practice that if, for example, a programmer inadvertently jumped the program counter into a table of data in store, the MPU would endeavour to use the data as a program sequence, probably with bizarre results!

INSTRUCTION TYPES

Even small, four-bit MPU chips have a repertoire of about fifty distinct instructions, and powerful eightand sixteen-bit devices may boast one hundred or more. At first reading, the instruction repertoire of a microprocessor can be a bit bewildering, and it's not easy to see the potential usefulness of such instruction names as RAR, POP, PSW, JCN, or even BRA!

Rather than examine each instruction in isolation it is better to group instructions which perform similar operations under common headings, and very often the manufacturers do this for us in their handbooks.

Unfortunately, no two manufacturers have the same ideas as to what the group titles should be, and so we have picked four general headings into which it should be possible to place any of the instructions of any microprocessor currently available. (You might like to examine the instruction set of Fig. 3.1 and decide which group each instruction belongs to.)

(a) DATA TRANSFER INSTRUCTIONS

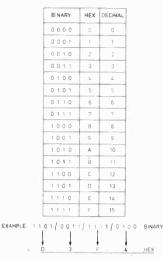
Data transfer instructions are used to move data about in the microprocessor system, either word by word (parallel transfers) or bit by bit (serial transfers). Data is the raw material upon which the MPU chip operates, and it is important that the MPU chip should be able to fetch data from input ports, store data in RAM, move data from register to register and manipulate the position of individual bits in the accumulator, and so on.

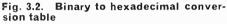
Examples are:

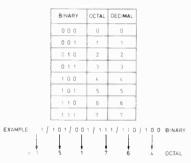
- sta Store accumulator in memory (Intel 8080)
- TSX Transfer stack pointer to index register (Motorola MC6800)
- LD Load data from memory into the accumulator (National SC/MP)
- RAR Rotate accumulator and carry to the right (Intel 4040)

<u>Glossary of Terms</u>

- ASSEMBLERS—Software programs which translate instructions in Assembly Code (e.g. mnemonics) into Machine Code instructions which can be recognised by an MPU chip.
- COMPILERS—Software programs which are used to translate instructions written in a *High Level Language* into *Machine Code* instructions which can be recognised by an MPU chip
- **DIRECT ADDRESSING**—An addressing mode where the address of the *operand* is contained in the instruction.
- HEXADECIMAL—A base-16 number system using the character set 0 to 9 and A to F which can be used interchangeably with binary. This coding system is easier to use and remember than binary, and is widely used in microprocessor literature.
- HIGH LEVEL LANGUAGE—A computer language which is easy to use and understand but which requires extensive translation (compiling) into Machine Code before it can be used to control an MPU chip.
- IMMEDIATE ADDRESSING—An addressing mode which uses part of the instruction itself as the operand data.









- **INDEXED** ADDRESSING—A form of *Indirect* Addressing which uses a special location known as an Index register to hold the address of the operand. Index registers can be incremented or decremented under program control.
- **INDIRECT** ADDRESSING—An addressing mode where the address of the location where the address of the *operand* is located is contained in the instruction.
- LANGUAGE—A systematic means of communicating instructions and data to a microprocessor (or computer) system.
- MACHINE CODE—A "low-Level" language understood directly by a microprocessor chip and using binary notation.
- MULTI-WORD (BYTE) INSTRUCTIONS—Instructions which require more than one line or location, in program memory.
- OCTAL—A base-8 number system using the character set 0 to 7 which can be used interchangeably with binary. Not as popular as *Hexadecimal*.
- **OPERANDS**—Data used in machine operations (e.g. Addends, Subtrahends, Dividends, etc.).
- **RELATIVE ADDRESSING**—An addressing mode where the address of the *operand* is built up by combining the current program count with a displacement value which is part of the instruction.

(b) ARITHMETIC AND LOGICAL INSTRUCTIONS

Arithmetic and logical instructions generally operate on a pair of data words, one of which is resident in the accumulator. The result of the instruction is generally a single word which is stored back into the accumulator. The execution of this group of instructions requires the arithmetic and logic unit (ALU) described last month.

Examples are:

- OR 4 Logical OR the accumulator with index register 4 (Intel 4040)
- ADC Add with carry (Motorola MC6800)
- SUBT Subtract register from accumulator (Intel 8080)
- DAD Decimal add to accumulator (National SC/MP)

(c) BRANCH INSTRUCTIONS

Branch instructions are used to modify the sequence in which the instructions in a program are carried out. Without these instructions program operation would start at address zero and continue by incrementing the address counter after each instruction until address N was reached and the counter recycled to zero.

Branch instructions allow the contents of the program counter to be replaced by some address which may be unrelated to its previous contents, so that program flow continues by incrementing from the new start address.

This group contains a sub-group which may be termed the "conditional-branch" group, which is especially important because it allows a microprocessor to make decisions on the basis of the nature of data with which it is provided. Conditional branch instructions replace the program counter content *only* if some specified conditions are true; if these conditions are false, program flow continues unchanged, by counter increment.

Examples are:

- JNZ Jump if accumulator content is not zero (National SC/MP)
- JUN Jump unconditionally (Intel 4040)
- JSR Jump to subroutine (Motorola MC 6800)
- RET Return (e.g. from subroutine) (Intel 8080)

(d) CONTROL AND MISCELLANEOUS

The group control and miscellaneous contains a bit of a hotch-potch of instruction types, and is included really to provide a home for those instructions which do not fit into the other three groups!

Examples are:

- HALT Halt processor and do not carry out any further instructions (National SC/MP)
- NOP No operation. Do nothing but move on to next instruction (Intel 8080)
- EIN Enable interrupt detection logic (Intel 4040)
- CLR Clear (Motorola MC 6800)

INSTRUCTION FORMAT

By now you should be getting a feel for the sort of things we can tell the MPU chip to do when we write an instruction in a program, but you may be wondering how the microprocessor knows which memory locations it must use as OPERANDS when carrying out instructions such as LOAD, or ADD. Taking an ADD instruction as an example, just what does the microprocessor need to be told, or to assume, in order to carry out the addition in the required way? Well, it needs to know . . .

- (i) The operation to be performed (In this case ADD),
- (ii) Where the first operand is
- (iii) Where the second operand is
- (iv) Where it must put the result
- (v) Where the next instruction is

An instruction word could be imagined where all these facts are specified by certain sections of the instruction code, but a moment's thought reveals that this would demand a very long word length, say four bits to specify ADD, eight bits for the first operand address, eight bits for the second, and so on.

An MPU chip using explicit instructions like this would be extremely versatile and powerful, but impossibly clumsy and difficult to work with, and so unsuited to the low cost applications microprocessors are intended for. A less versatile but much more compact instruction word format is generally used for MPU chips where most of the required information is *assumed* by microprocessor rather than being explicitly stated when the instruction is written into the program.

- (i) The operation to be performed remains explicit (e.g. ADD)
- (ii) The first operand remains explicit and is coded as a memory or register address
- (iii) The second operand address is assumed to be the accumulator
- (iv) The result is automatically stored back into the accumulator
- (v) The next instruction address is assumed to be the next in numerical sequence (unless a branch instruction is involved) and so is found by incrementing the program counter.

Even with this more compact instruction format it is often necessary to use more than one instruction word to store the necessary code, giving rise to what are termed "multi-word" or "multi-byte" instructions, which of course carry with them the necessity to increment the program counter by more than 1 to find the next separate instruction in sequence.

ADDRESSING MODES

We saw above that most microprocessor instruction words contain a single address, the address of the first operand, but we now have to examine the *way* in which the address is specified because there are a variety of addressing modes which can be employed, and using them wisely can make our programs more efficient. As an example, the Motorola MC 6800 has no fewer than seven addressing modes, which puts it ahead of the SC/MP and 8080 with four, and the 4040 with three, unless you happen to be of the opinion that seven modes are excessive and tend to leave you "spoilt for choice".

MPU chip manufacturers all have their own ideas about what their addressing modes should be called, and how they should operate, and it will be necessary to study these in the handbooks when the use of a particular chip is contemplated.

For the purposes of this introductory series we have chosen to describe four modes which can be considered fundamental, and from which the others have evolved. You may find it useful to compare the addressing modes of a particular MPU chip with the modes described here, trying to spot the similarities and the differences, where there are any.

IMMEDIATE ADDRESSING

The most straightforward mode of the lot is IMME-DIATE ADDRESSING because the instruction does not actually contain an address: it contains the data itself which may be part of the first instruction word or contained in a subsequent word.

This mode is useful for fetching constants which do not change once the program has been written; this "unchanging" quality is vital because instructions, and therefore immediate data, are often stored in ROM.

DIRECT ADDRESSING

DIRECT ADDRESSING is the mode which one would instantly recognise as being a necessary part of any processing system. In this mode the instruction contains the address in an explicit form, so that for an ADD instruction for example, the MPU chip is told precisely where to go in store to find the number to be added to the accumulator.

INDIRECT ADDRESSING

INDIRECT ADDRESSING is a very useful but at first rather tricky-to-understand mode where the instruction contains the address of the address of the data (now read that again!). That is to say that the address contained in the instruction does not point to the data itself but to a location (which may be a location in store or a register) which contains the address where the data is to be found. A particularly common form of indirect addressing is called INDEXED ADDRESSING where the instruction points to a special register called the index register which contains the required address.

The strength of indirect addressing lies in the fact that it is not necessary to know precisely what the final address is when the program is being written, it can be computed in, say, the index register so that it is in effect data dependent rather than program dependent.

RELATIVE ADDRESSING

In RELATIVE ADDRESSING the instruction contains not the address of the data, but a displacement value which is added to the program counter contents to make the effective address. This addressing mode is used only with branch type instructions where it allows looping backwards or forwards through a program without the need to specify absolute addresses. Looping backwards is possible because the displacement can be a negative number in "twos-complement" binary notation.

PROGRAMMING TECHNIQUES

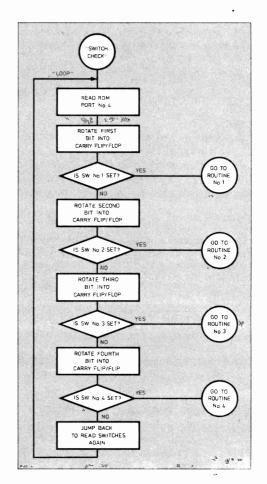
Programming a microprocessor is a fascinating and rewarding experience and well worth the investment in time required to learn the necessary fundamentals. The best way to learn is by actually doing, and to get to the stage where you are ready to try your hand at simple programs, you should start by studying examples of the type which MPU manufacturers often provide in their literature. The Intel "MCS-40 USERS MANUAL" is particularly rich in programming tips and examples, although of course these are restricted to the particular case of the 4040 chip.

Even if you are quite used to writing programs for a large computer in a high level language like Fortran, you will find the strict format of microprocessor machine code a challenge at first, but by becoming familiar with the hardware and instruction set of your choice of MPU you will soon become adept at the art!

HARDWARE/SOFTWARE TRADE OFF

The success of the microprocessor is due to the fact that it enables a large amount of hardware to be replaced by changeable software in the form of programs. When an MPU is applied to a particular application, however, the designer has to decide just how to create a balance between the amount of hardware and the amount of software employed, so as to suit his particular circumstances.

Take the case of a decimal keyboard connected to an MPU system. Should the designer provide a series of gating functions to turn each of the "one out of ten" .key closures into a corresponding four bit BCD code, or should he read all ten lines into the MPU and provide a program which does the job? Should he provide a TTL monostable key-debounce circuit, or should he incorporate some timing routines into his program which achieve the same result?





These are questions which can only be answered in context, depending as they do on how much program space is available and how much it costs, and how much room there is for extra hardware, and how much it, in turn, costs.

A PROGRAMMING EXAMPLE

We do not have sufficient space here for a comprehensive programming course, but in order to at least set the scene, we can trace through the evolution of a particular program example.

The job is to examine four toggle switches and depending on which one of them is set (if any) to jump to a particular segment of a program. The microprocessor to be used is the Intel 4040, and the switches are connected to ROM input port number four.

The first step in writing the program is to draw up a flow chart which describes in an easy to understand, graphical form the operations necessary to complete

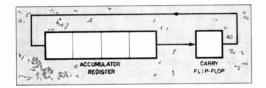


Fig. 3.5. Action of RAR instruction

the task. Flow charts can be of a very general form which can be understood by everyone and could be adapted for use with any microprocessor, or they can be detailed, and suited only to a particular chip architecture and instruction set. We find it helpful to let flow charts evolve from the general to the detailed form as ideas are added and the constraints of the chip are taken into account. Our final flow charts usually detail the actual instructions we intend to use.

SWITCH CHECK

Fig. 3.4 shows the flow chart for our Switch Check program in an "Inbetween" stage which is flavoured by the 4040 but nevertheless easy to follow for those new to the concept.

The flow chart already shows that a strategy has been chosen to achieve the goals set down, but it should be remembered that there are several other ways to achieve the same ends, and this one is not necessarily the best. The strategy used is that of a "skip chain", a simple but effective technique for examining the state of input lines, which in this case also incorporates a "wait loop".

When the Switch Check routine is entered (from the larger program of which it is a part) it begins by reading the state of the four switches into the accumulator by first addressing port 4 (FIM, SRC) and then reading it (RDR). The switch states can be examined by shifting each bit in turn into the carry flip-flop and using the JCN (jump on condition) instruction to either vector the program to the appropriate routine if a switch is SET, or to continue round the loop if it is not.

Once "Switch Check" has been entered there is no way of leaving it until a switch is closed, hence the "wait loop" tag, although this feature could be dispensed with, if appropriate, by leaving out the JUN instruction and allowing a further exit from the bottom of the routine if no switches are set.

Table 3.1 "SWITCH CHECK" PROGRAM SEGMENT

LABEL	MNEMONIC	OPERAND	COMMENT
LOOP	FIM	RP0, 40 Hex	Load port ad- dress code to
	SRC RDR	RP0	register pair 0. Select port. Read port into accumulator.
	RAR		Rotate switch 1 into CARRY.
	JCN	CARRY SET, "ROUTINE 1"	Jump to ad- dress ''Routine '1'' if CARRY is
	RAR		set. Switch 2 to CARRY.
	JCN	CARRY SET, "ROUTINE 2"	Test switch 2.
	RAR		Switch 3 to CARRY.
	JCN	CARRY SET, "ROUTINE 3"	Test switch 3.
	RAR		Switch 4 to CARRY.
	JCN	CARRY SET, "ROUTINE 4"	Test switch 4.
	JUN	"LOOP"	Jump to LOOP since no switches set.

RP=Register Pair Hex=Hexadecimal

Notice that the switches are ranked into a priority order by the fact that switch one is checked first and so on. If the routine is entered with more than one switch set, the lowest number switch will be the only one recognised.

The JCN instruction can be used to test for a variety of different conditions such as jump if carry is 0, jump if carry is 1, jump if accumulator contains all zeros, etc. In our case we wish to test bits individually and so we rotate them into the carry flip-flop one at a time using RAR which rotates the accumulator and the carry data one bit at a time as shown in Fig. 3.5.

The complete program, using mnemonic instruction codes is shown in Table 3.1. Before this could be entered into a PROM and used, the mnemonics would have to be replaced by their hex and binary equivalents and the addresses which here are represented by names such as LOOP or ROUTINE NO 1 would have to be specified absolutely.

ASSEMBLERS

If an ASSEMBLER facility was available on a larger computer or a development system it would be possible to enter the mnemonic codes as shown in Fig. 3.6, complete with address names, or labels as they are called. In this case the assembler program would convert the mnemonics into the required machine code and insert absolute addresses where necessary, a facility which helps the programmer a great deal but which of course is expensive in storage space and not available for use with the cheaper prototyping and development systems.

NEXT MONTH: Peripheral Chips and Hardware.



VENUS

The Russians have tackled the problem of the brilliant but hostile planet Venus with great vigour.

Mythology would have us believe that Venus represented love and happiness in the form of a goddess and that Mars was the warlord and the mark of aggression. In the event it is clear that the roles are reversed. However, in spite of the millenia of observations it is only in the last few years, the space years, that knowledge of the planet and its puzzles has been brought to the stage of acceptance in some detail.

Before the advent of the Mariner and Venera spacecraft the cloud cover was all but completely opaque. Many enterprising amateurs spent countless hours observing the planet. The members of a section of the British Astronomical Association devoted to observations of Venus produced drawings which were built up into a conjectured picture of the surface features. As it turned out these pictures and the drawings were remarkable for their anticipation of the real surface. These features were first confirmed by radar measurements.

CHANGES

The rotation period of the planet had undergone many changes and the figures varied from a few days to hundreds of days. Finally it was found to be 243 days, and that its direction was opposite to that of the Earth. Thus the Sun would rise in the west and set in the east. Its year is shorter than that of the Earth, just over 224 days. The poles are only tilted by 3 degrees and therefore the planet does not experience seasonal changes like the Earth.

CLOUDS

The cloud cover rotates very rapidly and the tops of these clouds have a rotation period of about four days. Their velocity is therefore very high reaching as much as 100 metres a second.

The white clouds of the Earth consist of drops of water but Venus is very different. The quantity of water vapour is very small being only 5 parts in 10⁴. This was established spectrographically. The nature of the spectogram showed that there were liquid drops in the clouds. Such a condition at the temperature of 233K indicated that the liquid could not be water.

The surprise that awaited the investigators was that it could only be due to sulphuric acid. It was found that these liquid drops were 75 per cent concentrated sulphuric acid. This was finally resolved in 1973.

Between the years 1967 and 1975 the Russians launched seven vehicles of the Venera class and in addition there were probes which entered the atmosphere. In 1967 Venera 4 indicated that there was at least 97 per cent of carbon dioxide in the atmosphere.

Though liquid water cannot exist on the surface of Venus, a reaction between the surface and the carbon dioxide of the atmosphere takes place. On Earth this reaction is a slow one. On Venus, with a temperature of the order of 750K, such a reaction is very rapid. The other similar reactions that take place include hydrochloric and other acids. The whole chemical effect in the atmosphere contrasts with that of the Earth.

There is little or no magnetic field on Venus but an ionosphere does exist. This does fit in with current thinking, that the intensity of a dipole field depends on the angular velocity. A great deal of data is now available about the Venusian atmosphere and the ionosphere. One interesting fact that emerges is that short wave radio signals would be limited to the daylight hours for global communication.

There are few signs of mountains though some radio-astronomy telescopes have shown 2km high hills. It is possible that as a result of the high surface temperature, the crust is more malleable and that the possibility of mountain formations is remote. There is no sign of impact modification of the surface and this could mean that almost all the effects will have been by volcanic action.

Much still remains to be done before a conclusive picture of Venus can be drawn. The effect of the solar wind shows quite a different picture as compared with the Earth. The density of the corona is directly dependent on solar activity.

HAWAIIAN INFRA-RED TELE-SCOPE

A new development is possible in connection with the Hawaiian telescope. The $\pounds 2.5$ million telescope is being built by Grubb Parsons and is to be put into service next year. The mirror is 3.8 metres in diameter and is the largest of its kind in the world.

Designed for infra-red operation the tolerances can be much wider than for optical work. This is reflected also in the guidance systems. The original tolerance was set at 2–3 arc seconds, but Grubb Parsons have indicated that the mirror now in the polishing stage is so good that a little extra polishing could bring the tolerance to 1-0 arc second.

This would effectively halve the time for observations. This will not be to the standard for optical work except for very short exposures. In spite of this there would be a gain for optical astronomers. The improvement for the infra-red application is very great.

The cost of doing this extra polishing is £12,000. Surely this is a small price to pay for such an improvement. Since this means that the instrument will carry out a programme in half the time originally scheduled it is the same as saying that for an extra £12,000 the astronomers will have the equivalent of two telescopes at a cost of £2.5 million each. This is a bargain too tempting to be ignored. No doubt the SRC will see it this way.

LANDSAT

The value of the services that can be offered by *Landsat* has been so much in evidence that the Soviet Union is pursuing plans to build a terminal for themselves. So far applications have come from a number of countries. These include Australia, Norway and Sweden, India, Japan and at least one Arab State.

For over a year a new 13-channel scanning system has been the subject of a cleaning up process. Interference has been very considerably reduced and the false colour reproduction developed to a specialised degree. This enables colour to indicate differing conditions of surfaces, leaves and plants as well as tree growth.

Extensive research has shown that water, because of its changed state when taken up in plants, can be an indicator of great value. It is certainly the case that the value of the pictures for countries which have plans for long term development in agriculture and forestry will be considerable.



Still soldering on?

You may be that rare person who gets his circuit designs right first time, everytime.

But it's much more likely that you experiment to see what works, and what doesn't.

In which case you ought to know about Bandridge Decs.

Bandridge Decs enable you to try almost any number of possible circuits, without having to use your soldering iron.

You simply push the wires of your circuit components into the holes in the Dec to make a perfect solderless contact.

Which means that you can use the components over and over again.

And, of course, we don't have to tell you how much time it will save you. There are four Decs available to suit every possible circuitry requirement. From simple discrete work to 2xDIL or 4xTO5 Station work.

And for larger or more complex circuits you can use any number of Decs linked together.

Eventually of course, you'll need that soldering iron to make up the permanent version of your circuit design.

And when you do you'll probably want to use one of the matching Bandridge Blob boards, to make your job that much easier.

But until then we suggest you put your soldering iron aside and get yourself a Bandridge Dec.

> Bandridge Decs – Available at all good component stockists, where you see the Bandridge sign.

S-DEC: for use with discrete components only. (70 contacts)

T-DEC: for discrete work, or linear ICs. (208 contacts) U-DEC 'B': As DEC A but includes two 16 lead IC sockets. (208 contacts)

U-DEC 'A': allows 2xDIL or 4xTO5 Stations but also takes discrete components. (208 contacts)

353

A free circuit design booklet is supplied with every Dec.

For your nearest stockist contact Bandridge Ltd., 80a Battersea Rise, London SW11 1EH. Tel: 01-228 9227.

Bandrida

The Blob Story.

Yes, they have got a funny name: Blob Boards.

And if you've never heard of them, you might wonder what on earth they're for.

After all they sound more like sci fi than practical electronics.

But in fact there is a good reason for the name.

It actually describes the way these printed circuit boards work. You just put a tiny blob of solder onto circuit board and component and you've made a perfect contact.

Every time.

There are of course a few other printed circuit boards around.

But we think the prices are a bit shocking. Our prices, we think you'll agree, are more down to earth.

These Blob Boards are about half the price of the few comparable alternatives.

And unlike those alternatives, on most Bandridge Blob Boards you won't have to break the contact rails to make your circuit. So you'll be able to use them again and again.

The roller tinned copper on Blob Board makes soldering easy, and it won't corrode, so

they'll work for as long as you want them to. You'll find a Bandridge Blob Board for every circuit you'll ever want to make, from the simplest to the most complex.

And if you're using Bandridge solderless DEC's for your prototypes you'll be pleased to learn that there's a Blob Board that exactly matches every DEC.

So when you're looking for a circuit board it'll be worth your while remembering Blob Boards.

As if you'd ever forget a name like that.



integrated circuit work

but will take IC package.



T100 T300 TL340 F100L

HI-Z PRE. AMP

Are you bored with analogue integrated circuits which do everything but polish your shoes, and digital integrated circuits which boast so many gates and functions that it sometimes seems that their I.Q. is higher than yours? Do you yearn for the simple life again, a return to the days when an individual transistor was treated as something to be revered, rather than as a sort of plastic giveaway? You do? Well then perhaps I can interest you in the T100 and T300 devices from Siliconix, which do not boast kilo-transistors or mega-gates, but simply a well designed single j.f.e.t. stage which, with the aid of a simple four-pin package, forms a useful and capable high-Z preamplifier stage for use with microphones and other transducers.

Inside the TO72 can there lives just the j.f.e.t., a source load resistor, and back-to-back Schottky diodes which form the high impedance gate bias resistance. The devices feature an input impedance of at least 200 megohms, and an output resistance of between 500 and 1,300 ohms, together with a very low noise voltage output of less than 4.0 microvolts in a 10kHz audio bandwidth.

These integrated circuits are, of course, high-to-low impedance converters, and can be used wherever such a function is required. The T100 is intended specifically for low power applications such as hearing aid input stages, where its 50 microamp supply drain is important, while the T300 is a general purpose device drawing 350 microamps. Both can operate from supplies of from 1.3 volts to 30 volts, and are ideal for matching to ceramic, electret-capacitor, air-capacitor, and piezo-electric microphones.

GOODBYE ZENERS

If you could design your own ideal Zener regulator diode what would you list as desirable characteristics? / would want low impedance, a sharp knee, temperature compensation, low noise, high dissipation, and a really easy to use package. If you added to that list a programmable voltage rating, so that only one type of component has to be purchased to provide any voltage between three and thirty volts, you would have designed yourself a Texas Instruments' **TL430** which must surely replace standard Zeners in most applications.

The manufacturers call it a "precision three terminal shunt regulator" but it is almost as easy to use as a standard Zener, and nowhere near as pricey as it sounds. "Zener" voltage is normally programmed with the aid of two fixed resistors, but if you like, you could use a skeleton pot, and hence build yourself a "Variable-Zener" which could be very useful in a wide variety of applications which at present require numerous resistors and an op. amp., in addition to a conventional reference diode.

Performance of the TL430 is better than its predecessor in every respect, slope impedance is only 15 ohms. over the current range 600 microamps to 100 milliamps, as compared with 5 ohms to 100 ohms for standard Zeners over a more restricted current range. Temperature stability is 0.005 per cent per degree Celcius, which is as good as the most expensive Zener devices, which are themselves restricted to a voltage of around 5 to 6 volts to achieve that sort of performance. Noise output is low, and the three-lead TO92 or eight-pin Minidip package will dissipate a creditable 775 milliwatts to make the TL430 almost twice the regulator that the standard 400 milliwatt BZY88 is!

I sometimes get a bit demoralised when I realise that most of the devices I talk about in this column actually originate in that former colony of ours, where they eat hamburgers and chew gum!

Wouldn't it be nice, I muse, if just for once, a British firm could summon the courage, the know-how and the money, to produce a real worldbeater of a product which would give us something to be really proud of, something which would demonstrate that we really do have a part to play in advanced semiconductor technology.

Well, I can stop musing, because Ferranti have actually gone out and doneit, by producing a super, wonderful, powerful, 16-bit, and above all, BRITISH microprocessor which really promises to deliver the goods in this, the most challenging semiconductor technology of them all!

The name of this patriotic new chip is the F100L, and it has the distinction of being the first microprocessor which has been wholly designed, developed and manufactured in Europe, without any American connection at all. Development was sponsored by the Ministry of Defence, God Bless them, and this will no doubt guarantee a market in a host of advanced defence projects and give a hefty boost towards profitability. The F100L can stand on its own, though, and should provide some healthy competition for other 16-bit micros such as the Texas 9900 and the National Pace, which it can outperform in many respects.

The chip is produced in the homegrown Ferranti CDI process which is a bipolar technology providing LSI circuit density and low power (375mW) along with typical instruction execution times of only 3 to 4 microseconds. The chip is just 0.23 inches square and uses no less than six feet of aluminium track interconnections to hook up about 7,000 separate components into a microprocessor which is not far short of a full sized computer in the performance that it offers.

As far as we, as amateurs, are concerned, the F100L will enable us to put together a home computer of formidable power and ability when the chips become freely available during the next year or two. The single phase TTL clock and the simple 5 volt supply requirements should make the assembly of an F100L computer fairly straightforward, and Ferranti are going to back up the basic MPU chip with a range of CDI interface chips in the near future to replace the standard TTL components presently necessary.

This is a chip we can all be proud of (after all we do have an investment in it) so let us wish it every success for the future.



MICRO '77 is the name of Cramer's current series of microprocessor seminars being given at various points in the country, culminating with one in London on April 25. Cramer call themselves the "Heavyweight Microcomputer Team", perhaps because for each of their three main franchises, Motorola, Texas Instruments, and Zilog, they have two specialists available, one with hardware and one with software knowledge.

We went along to the Cambridge seminar held on February 8.

THE PAST AND THE FUTURE

The first part of the programme was an extremely lively introduction to microprocessors presented by Ian Perry. It is almost impossible to talk about the history of microprocessors without mentioning Intel; the first microprocessor was the 4004, designed in 1971 by them for Decimo, the calculator manufacturer, who saw that the way their small firm could compete in the mass calculator market would be to divide the single calculator chip into its four parts: the CPU, RAM, ROM and 1/0. A whole range of calculators could then all use the same CPU and RAM, differing only in the ROM programme and in the 1/0 and keyboard.

Today about 50 per cent of CPUs produced go into data terminals. Most second-generation CPUs, such as Motorola's 6800 and the Intel 8080, have followed along similar lines but now the trend seems to be going full circle with the re-integration of all the parts onto one chip. Again the innovator is Intel, and their 8748, to be released later this year, will combine the CPU, a PROM and programmable I/O onto one chip.

In a glance into the future Ian Perry predicted some interesting advances. Memory is to grow ever cheaper, and 16K dynamic RAMS should soon be available at a competitive price. The first low-power CMOS PROM is now available from Harris, though the price is high. The greatest possibilities lie in the area of subfunctions: chips which work alongside a CPU to extend its capabilities.

For example, Advanced Micro Devices have a numbercruncher chip on its way which will give multiply and divide functions; also in the pipeline are floppy-disc controllers, c.r.t. controllers, keyboard interfaces and sophisticated 1/0 packages, all of which leave the CPU free to do greater things.

Low power systems are still a year away; although TI make an I²L version of their 9900, the price is prohibitive. The present CMOS CPUS from Intersil and RCA have reduced instruction sets due to the unsolved problems of mass integration, but silicon on sapphire CMOS may provide the answer.

MOTOROLA

The M6800 from Motorola is now the most popular CPU in Europe, and perhaps because of its familiarity, this part of the programme was less interesting. This popularity may in part be due to Motorola's sensible policy of providing a wide spread of development products, and the whole range was on show at the seminar. Although Cramer will be glad to sell you an Exorciser plus TI Silent 700 terminal for around \pounds 4,000, for the impecunious among us there is now the self-contained D2 kit which, like the SC/MP kit with keyboard, eliminates the need for a terminal by enabling you to enter programs directly in machine code from a hex keyboard, with readout of the address and data on six seven-segment displays.

The problem of how to store programs is overcome by the provision of an audio cassette interface, and the system includes 256 words of RAM and a PIA. At £188 it seems good value. (A review of this kit will appear in a subsequent issue.)

In the mid-price range there is the Polyvalent Development System, comprising an ASCII keyboard, a tiny vDU with interface board, and various computer boards which can be bought separately, or together for £771. For example, the Display Interface board will convert any TV into a vDU with the addition of a keyboard, and costs £200.

ZILOG

In presenting the Zilog Z80, Roger Phebey was understandably enthusiastic as the Zilog story seems to be one of uninterrupted success. Zilog Corp. was founded in 1974 by two Intel executives, who have since been joined by top men from Intel, Motorola and Fairchild. In 1975 M. Shima came from Intel, where he had designed the masks for the 8080, and developed the Z80 CPU. The first units were manufactured in mid-1976 by Mostek for Zilog while their factory was being completed, and now Mostek second-source it.

From the start Zilog decided to make a souped-up 8080, and in fact the Z80 contains the actual 8080 codes as a subset. But the likeness ends there; the Z80 has twice as many registers, twice the number of instructions including additional 16-bit operations, block transfer and search which in one instruction operate on blocks of up to 256 words of memory, and there are two modes of interrupt besides the 8080 mode. The chip uses a single 5V power rail, and a single-phase clock. The Z80 is not very accessible to the amateur as yet; the cheapest complete system includes two floppy discs and sells for £4,200, and the only board available as yet is the MCB at £347; for this you get 4K of RAM though.

TEXAS

The Texas Instruments' TMS9900 is one of the few 16-bit CPUS currently available, and with its multiply and divide instructions it seems very minicomputer-like. It has none of the usual on-chip registers; instead a block of 16 contiguous RAM locations pointed to by the workspace pointer provides 16 "primary" registers, making interrupt and subroutine handling especially efficient. The chip has 16 data lines and so needs an impressive and expensive 64-pin package; furthermore it requires a four-phase clock.

Prospective computer builders may prefer to wait for the TMS9800 due later this year, which provides an onchip oscillator and uses multiplexing to fit the same functions in a 40-pin package. At present there is only the 990/4 development system available which with a Silent 700 terminal costs about £4,000, but Cramer is developing in collaboration with TI a Micro-99 microcomputer board which, when it is available, will be £310.

Finally a point stressed at the seminar was: do not feel obliged to buy the whole system from one manufacturer. It is perfectly good practice to link up one CPU with a different memory and a third 1/0 device. Also once the application is decided upon it is probably cheaper to replace general purpose 1/0 devices by custom wired latches. Amateurs now have an extremely wide range of devices to choose from, and can construct a complete microcomputer for well below the cost of comparable ready-built development systems.

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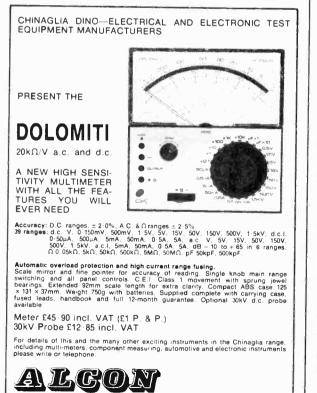
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19 MULBERRY WALK, LONDON SW3 6DZ TEL: 01-352 1897





By K. Amor

This unit was designed with the younger generation in mind. However, it has certain differences with its popular electromechanical counterpart. First, instead of the usual apples, plums and cherries forming combinations, numbers from 0–9 are displayed in the form of three seven segment displays. Second, the machine will only indicate when a player has won, but will not actually pay out. However, it is possible to bring a signal out to operate an electromechanical device if a prize giving feature is required.

FEATURES

Six winning numbers are internally set-up and when displayed the "Win" l.e.d. will light. As can be seen from the prototype these selectable numbers must be carried on the panel for the player's reference when making a decision for "holding" one or two numbers.

It is possible to change the six winning combinations from time to time as desired.

74 series TTL logic is used in the main which allows a compact unit to be made-up.

TIMING CIRCUITRY

When the start lever is operated a positive going pulse is produced as a result of the microswitch opening (Fig. 1). This pulse triggers monostable IC2 into operation so that pin 6 goes high (+5V) for approximately five seconds. The pulse period is not critical, hence the use of the electrolytic in the timing circuit.

As pin 6 is high the three gates that form IC3 are all enabled so that pulses from the three unijunction oscillators can pass through and be counted by the three 7490 divide-by-ten counters.

Practical Electronics May 1977

COMPONENTS

Resisto	'S		
R1	33kΩ	R9	180Ω
R2	82 Ω	R10	47kΩ
R3	180 Ω	R11	$15k\Omega$
R4	33kΩ	R12	1kΩ
R5	82 Ω	R13-34	220 Ω
R6	180 Ω	R35	1kΩ
R7	33 kΩ	R36	22 0 Ω
R8	82 Ω		
AIL+N	20% tolerance		

Capacitors

C1	4,700pF	C4	100μF elect. 25V
C2	5,700pF	C5	2,200µF elect. 25V
C3	9,400pF	C6	0-33µF
Note	e: C2 and	C3 are	made up

Semiconductors

TR1	BC184
TR2-4	2N4853 (3 off) or TIS43
TR5	BC184
IC1	7490
IC2	74121
1C3	7410
IC4-6	7490 (3 off)
IC7-9	7447AN (3 off)
IC10	7400
IC11-13	7442 (3 off)
C14-16	7404 (3 off)
IC17	7430
IC18–19	7410
LED1-3	DL747 (3 off)
LED4–5	TIL209 (2 off)
IC20	MVR-5V 5V 1A regulator (RS) or
	LM309K

Transformer

T1 6.5V 1A mains transformer

Switches

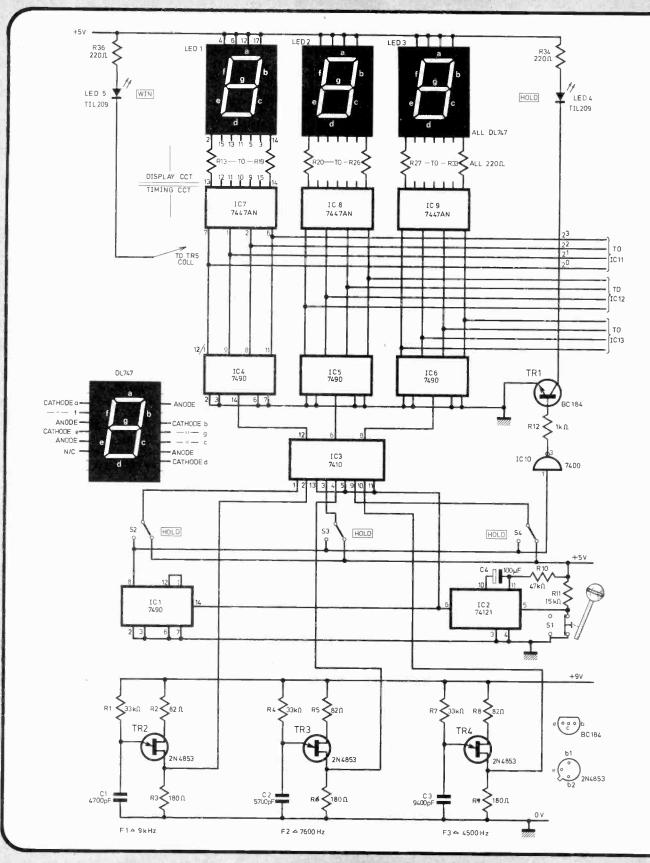
SI Lever microswitch	S1	Lever	microswitch
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S2-4 Spdt switches (3 off)

S5 Double-pole mains on/off

Miscellaneous

D1-4 1A full wave bridge rectifier A suitable case is the Type "U" $8in \times 6in \times 6in$ (H. L. Smith, 287 Edgware Road, London W2)



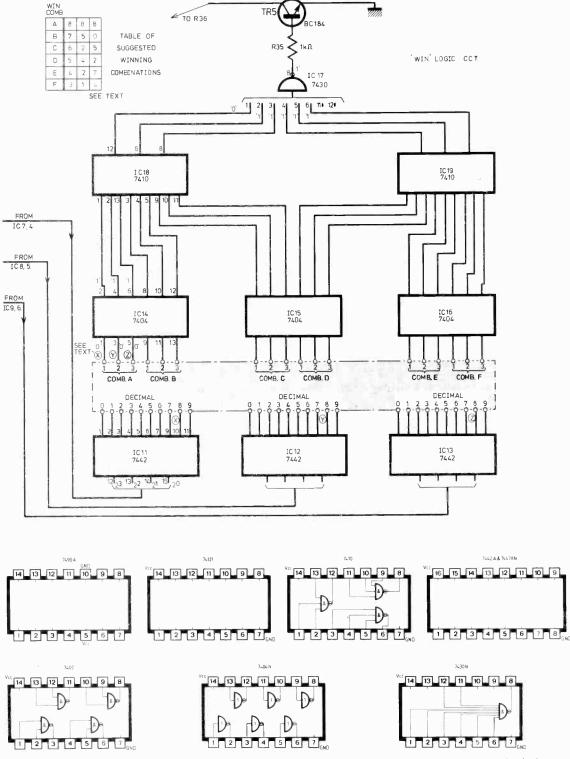
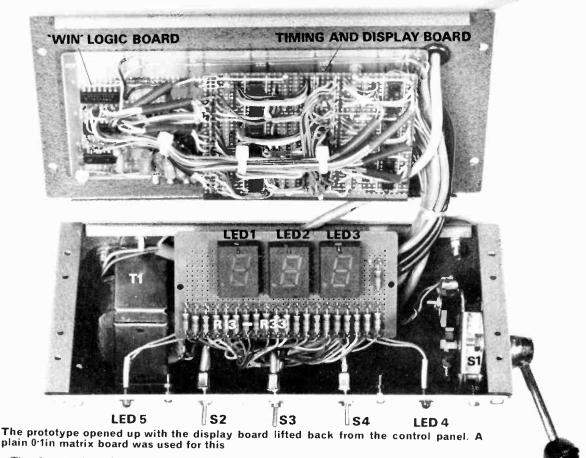


Fig. 1. The timing, display and win logic circuitry for the One Armed Bandit. The shaded panel embraces the Veropins used for patching arbitrary winning combinations. The six combinations used in the prototype are in the top table with patching for combination "A" indicated by linking the Xs, Ys and Zs. Pin diagrams for all semiconductors are given



The frequencies of the unijunction oscillators are determined by the value of the resistor and capacitor connected to the emitter and the actual running frequency value is 1/0.7 CR Hz.

As can be seen from Fig. 1 the oscillators run at three different frequencies of approximately 4.5, 7.6 and 9kHz.

These frequencies are sufficiently high and different so as to reduce the possibility of the same number appearing in successive operations of the machine. Also, since they are free running, random number selection is better.

NUMBER DISPLAY

The gated serial pulses from the unijunction oscillators are converted to parallel binary form at the counter outputs. When the monostable pulse finishes, the gates of IC3 are cut-off inhibiting the drive to the counters. Since these will have rippled through many times during the five second period, random binary numbers will be stored at their outputs.

To convert these to decimal at the l.e.d. displays they are fed to 7447 decoder/drivers. Resistors R13-R33 are for limiting current to the segments. With the values given the current is approximately 10mA/segment which is an economical figure but provides adequate display brilliance.

"HOLD" CIRCUITRY

The negative going edges of the five second start pulses are counted by IC1 which forms the basis of the "Hold" circuitry. Every four start pulses will cause IC1 pin 8 to go low. This stays low for a further six operations of the machine.

During this time a "Hold" will be available since the 7410 gates can be inhibited by the operation of the switches S2–S4. When any one of these are thrown the associated gates are connected to the "Hold" counter which means that the number in this arm is retained.

To show "Hold Available" the output from IC1 is inverted by IC10 to drive TR1 so lighting the l.e.d.

At the end of a "Hold Available" period the circuit will automatically release the display irrespective of switches in the "Hold" position. However, it is advisable to clear switches prior to the "Hold Available" light coming on again.

"WINNING" LOGIC

Six winning number combinations in groups are available but the selection of groups is arbitrary. In the prototype these were 888, 750, 625, 542, 427 and 314.

To implement the "Win" light signal Veropins are arranged as in Fig. 1. Three groups of ten take the 0-9 outputs from the decoders and six groups of three provide the inputs to inverter.

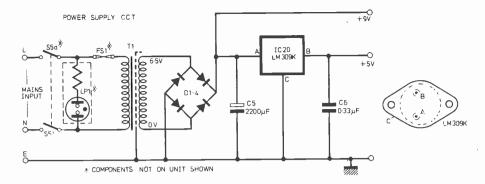


Fig. 2. The power supply circuit

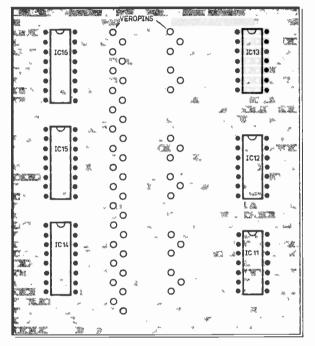


Fig. 3. Suggested i.c layout for the 0·1in matrix win logic Veroboard. A Veropin arrangement for patching is shown

To wire a "Win" combination, say 888, leads are connected from the 8s at the decoder outputs and then to a group of three pins (the order of wiring is shown as X, Y, Z in the figure).

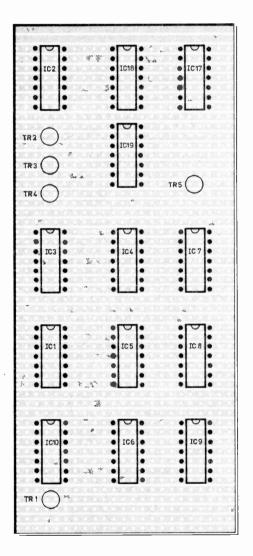
The inverters and gates are used to get the logic levels right.

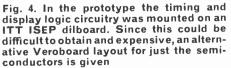
An eight input NAND gate receives the six levels from IC18 and IC19. When any one of these is low a "Win" condition will be indicated due to base current flow. For illustration, the various required logic levels to achieve an 888 win are shown.

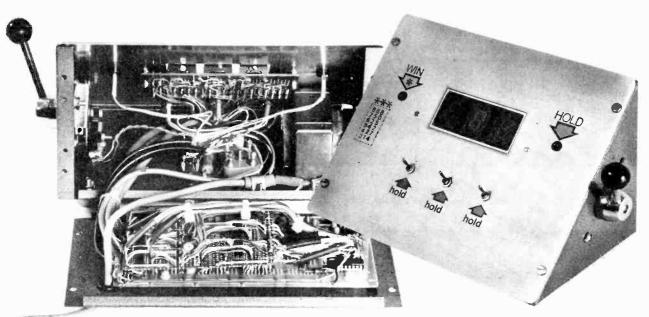
POWER SUPPLY

The power unit is shown in Fig. 2. Here a mains transformer supplies 6.5V r.m.s. to a full wave rectifier. The rectified output appears at C5 as +9V for supplying the u.j.t. circuits.

For the logic and display a 5V 1A regulator is used. This is short circuit protected and will allow almost an amp to be drawn before limiting.







Showing the control panel and internal assembly of the prototype. The p.s.u. is mounted on the base panel. The timing and win circuit boards are shown sandwich mounted on the rear panel using nylon nuts and screws. Cable ties should be used with looms

CONSTRUCTION DETAILS

A suitable case is the "U" type from H. L. Smith Ltd. With all the holes drilled and display cutout completed, the panel is ready for lettering. Before this is carried out the aluminium front panel should be rubbed with steel wool. This produces a very pleasing brushed surface.

The front panel lettering can now be carried out having decided what the winning sets of numbers are. This type of dry transfer lettering can be purchased at a good stationers.

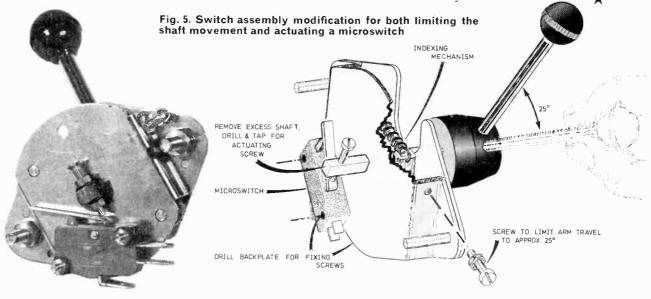
Numbers, letters and lines come in sheet form and are best applied by rubbing over the top of the transfer with a soft lead pencil, taking care that no grease is allowed on the surface below. It is advisable to spray the whole panel with a protective lacquer to prevent lettering from being removed and the aluminium discolouring. Leave this to harden for about three hours before mounting components. The back, bottom, and sides may be finished off by spraying with a cellulose aerosol paint.

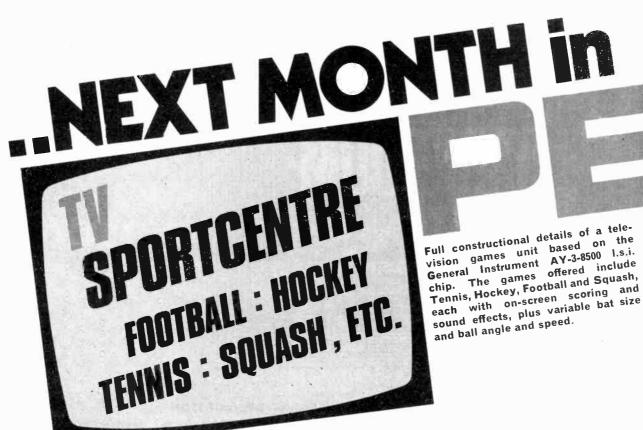
A dark colour is suggested so as to contrast with the front panel.

LEVER ASSEMBLY

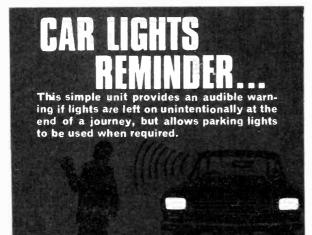
The mechanism in the prototype for the microswitch S1 is made-up of an Arrow-Hart rotary switch shaft unit. The modification to the assembly for limiting the rotary switch action and operating S1 is shown in Fig. 5.

It should be possible for constructors to modify some \pm in shaft multiway switch, making up a handle and so achieve this assembly cheaply. Others will probably opt for a simple press switch rather than this elaboration for authenticity.



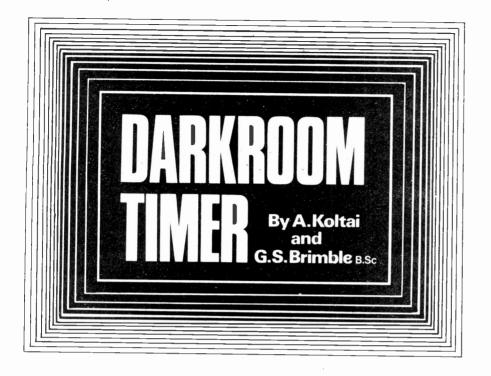


...also in this issue



tion of mains interference.





AMATEUR photographers often express a need for an accurate, reliable and repeatable timer for enlarger control or for timing various darkroom jobs; such a need also occurs in other hobbies. This article describes an accurate, repeatable solid state countertimer. By using digital techniques, an accuracy of about ± 0.5 seconds is achieved with no need for calibration, and the circuit has been designed to be as simple and cheap as possible to construct.

THE DESIGN

Analogue circuits which provide these facilities have been described in the past, but these always depend ultimately on CR networks for timing accuracy. As home constructors have found, it is neither cheap nor easy to obtain components, especially capacitors, of sufficient accuracy and stability to produce useful results. Furthermore, accurate calibration of the control element (usually a variable resistor) implies access to an accurate timer, which is rarely the case.

This circuit overcomes both these problems by using the mains power frequency as a standard frequency source. The electricity generating boards maintain this frequency to better than one per cent; since the frequency and thus the period of the mains is well known (50Hz, 20ms), and the division factor in counting this frequency is known, the timer is self-calibrating.

Another requirement in darkroom timers is a visible indication of elapsing time. In an analogue circuit this can only be provided by a meter, with attendant problems of lighting and reading accuracy. It is often difficult for the home constructor to provide safe-light illumination of the meter movement. The present design takes advantage of light-emitting diode sevensegment displays as numeric digit indicators. The great majority of l.e.d.s emit in a narrow band of wavelengths in the red part of the spectrum, and consequently provide self-illuminating digits which are inherently "safe".

CIRCUIT DESCRIPTION

Mains power is transformed down to 9V r.m.s.which is rectified by D2 and regulated by IC3 to provide +5V d.c. (see Fig. 2). This regulator is more expensive than a Zener diode or simple series-pass transistor regulator, but it is overload- and overheatprotected and virtually impossible to destroy. It automatically limits current to about one ampere and this feature provides excellent protection of expensive components during testing.

The 9V a.c. is also passed through a current limiter to a Zener diode D1 which half-wave rectifies and clips the top off the remaining half cycle to provide an approximately square 50Hz waveform, as shown in Fig. 1. Note that the low level of the waveform is not 0V due to the diode forward drop (about 0.6V). However, TTL manufacturers guarantee that any voltage below 0.8V will be accepted as a low so this 50Hz is fed directly into a scaler or divider. This consists of two chips, type 7490, which each contain a divide-byfive scaler and a flip-flop to divide by two. The first chip IC1 divides the 50Hz by 5 to give 10Hz, then the next, IC2, divides by 10 to produce a 1Hz signal, i.e. a square wave of period exactly one second. This constitutes the standard frequency which is counted in the timer section.

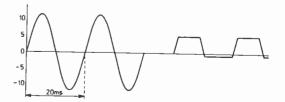


Fig. 1. The a.c. output of T1 is rectified and clipped by Zener D1 to provide a 50Hz square wave drive to the divider chain

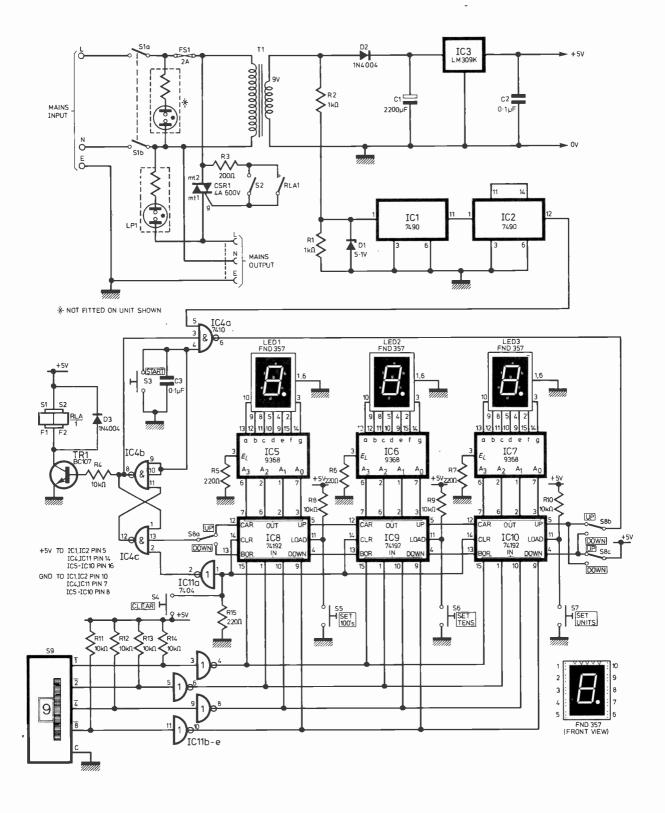


Fig. 2. Circuit diagram of the complete darkroom timer. Relay RLA is a twincoil reed type with the two coils connected in parallel for 5V operation (see Components list)

367

TIMING

The actual timing is done by a chain of up-down decade counters type 74192. These devices count up from zero to nine, then provide a carry out to the next stage, or count down from nine to zero, then provide a borrow signal to the next stage. Control of direction of counting is simply a matter of routing pulses to either count up or count down inputs. As well as this serial mode (one bit at a time), the counters can be preset by a parallel load (all four bits which define a decimal digit are used at the same time). This facility is the key to the timer mode of operation. The contents of the counter appear as a parallel 4-bit binary-coded decimal (BCD) digit on the four output lines, which can be displayed on a l.e.d. digit by using a BCD decoder chip.

In this design, three such counters are connected in series; that is, the carry and borrow outputs of the low digit IC10 (units) connect to the up and down inputs respectively of the higher digit IC9 (tens), and similarly for the tens digit to hundreds digit (IC8) connections. The BCD output from each stage is taken to a Fairchild 9368 decoder-driver which converts BCD to 7-segment code suitable for driving common-cathode l.e.d. digits, which in this case are Fairchild FND 357 0-375in digits. If needed, more digits can be included by simply adding identical stages.

PARALLEL LOAD

The 74192 chips are provided with a "clear" input which when pulsed high sets the contents to zero. These pins are connected together and taken to a pushbutton S4 to provide zeroing of the counter. Each chip also has its "parallel load enable" input taken to an individual button S5–S7, which when pressed,

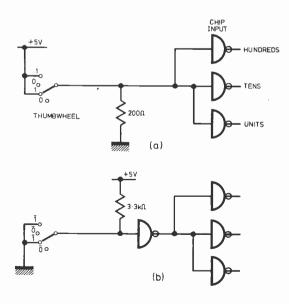
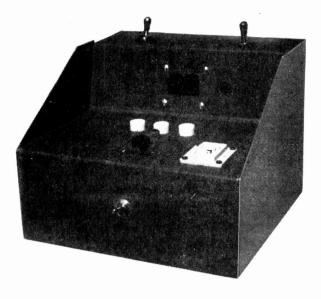


Fig. 3. (a) A heavy current is drawn from the 5V supply via the low value resistor if a BCD thumbwheel switch is used to drive the counter chip direct

(b) Using a BCD complement switch and one inverter considerably reduces the load on the 5V supply as a higher value resistor can be used



The prototype timer was built in a specially made aluminium case providing separate compartments for the logic and mains wiring for improved interference rejection

causes the contents of that counter to be set to the BCD digit coded on its input lines. Since both these functions set a determined state in the counter, switch bounce cannot alter the contents, so simple, inexpensive switches can be used.

THUMBWHEEL SWITCH

The preset input digit is set on a BCD-coded thumbwheel switch. The switch is wired up to produce complement or inverted code, that is, logic one is low and logic zero is high. The code is then inverted to the correct levels by part of a hex inverter type 7404 (IC10). The reason for this is that if the code is generated directly by the switch, each bit in the digit must be connected to 0V by a low resistance to provide current sinking for the inputs of the 74192 chips when logic zero is selected; so when logic one is selected, this resistor draws heavy current from the +5V supply (see Fig. 3). The inverter chip is needed anyway to provide the alarm clear function, to be described shortly.

Two gates of a triple three-input NAND chip are used to make a set-reset flip-flop to detect and operate the alarm function which indicates timeout in timer mode; this flip-flop is set by the START switch and reset by either the CLEAR switch or a carry output from the top digit of the counter chain. The outputs of this flip-flop and of the start switch are ored with the 1Hz pulse input in 1C4a to inhibit this pulse from reaching the counter if the flip-flop is reset or the START button is being held down.

The output of the flip-flop drives a reed relay RLA via a switching transistor TR1. This arrangement isolates the logic and mains portions of the circuit. The relay switches a triggering signal to a triac to connect and disconnect mains power to an output socket SKA. A s.p.s.t. switch in parallel with the relay allows the socket to be permanently powered independent of the timer for focusing, etc.

OPERATION

To see how the unit operates, first consider the counter mode of operation. When the CLEAR button is pressed, the flip-flop is reset which means the output which feeds back to the start gate IC4a is low, forcing the output of that gate to be high. This line is switched to the count up input of the counter chain, which contains all zeros. Thus the digits displayed are all "0", and the carry output of each chip is high, and in particular, the top digit whose carry is connected to the flip-flop.

COMPONENTS . . .

Resistors

R1, R2	1kΩ	
R3	200 Ω	1W
R4, R8-R14	$10 k \Omega$	(8 off)
R5-R7, R15	220 Ω	(4 off)
½W unless o	otherwi	se specified

Capacitors

C1	2,200µF 16V electrolytic
C2, C3	0-1µF ceramic disc

Semiconductors

IC1, IC2	7490
IC3	LM309K 5V regulator
IC4	7410
IC5-IC7	9368PC Fairchild (3 off)
IC8-IC10	74192 (3 off)
IC11	7404
TR1	BC107
LED1-LED3	FND357 Fairchild (3 off)
D1	BZY88 C5V1 5·1V 400mW Zener
D2, D3	1 N4004
CSR1	6A 400V Triac

Switches

- S1 D.P.S.T. mains toggle
- S2 S.P.S.T. mains toggle
- S3-S7 S.P. push-to-make push buttons, see text (5 off)
- S8 3-pole 2-way rotary or toggle (e.g. RS Components 339-471)
- S9 Thumbwheel edge switch, BCD complement output (e.g. RS Components 338– 181, plus mounting cheeks 338–197)

Miscellaneous

T1 '9V 2A secondary

RLA Reed relay, 5V coil, 240V a.c. contacts (e.g. RS Components coil 349–030, windings paralleled, plus reed switch 338–147)

LP1 240V neon indicator; F1 2A fuse with holder; Heat-sinks for IC3 and CSR1

Note:

The 9368PC and FND357 are available from S.C. European Components Ltd., Unit 9, M40 Industrial Centre, Blenheim Road, High Wycombe, Bucks. Prices are £2.07 and £1.29 each respectively, plus 50p post and packing per order, plus 8% VAT When the START switch is pressed, it holds the start gate IC4a off but sets the flip-flop so that input to the start gate goes high. Releasing the START switch then allows the 1Hz signal through the gate, where it is inverted, to the count up input of the counter chain. Thus seconds are counted and the current count is displayed. Note that up to one second error can occur here because the counter will increment when the 1Hz signal goes high regardless of when in the previous second the button was released.

TIMER

Operation in the timer mode is similar, except that the up-down switch S8 connects the output of the start gate to the count down input of the counter chain, and the borrow output of the chain to the flipflop. The CLEAR button initialises the logic as before. Then each digit of the time interval to be measured is set in the counter by dialling it on the thumbwheel switch S9 and latching it into the counter by the appropriate parallel load switch S5–S7.

Thus to time 54 seconds the numbers 0, 5 and 4 would be set into the hundreds, tens, and units digits respectively. When the START switch is pressed and released RLA is energised via TR1, and the 1Hz pulses from the standard frequency source cause the counter to subtract seconds until zero is reached. When the digits are all zero, and the clock signal into the count down input goes low, the borrow output of each counter and thus the input to the flip-flop goes low which resets the flip-flop. This switches off the transistor and thus the relay which turns the triac off and interrupts the power to the output socket.

Note, however, that the clock signal goes low half a second after the high transition which caused the zero to appear. Thus the alarm operates half a second late; this effect couples with the ± 0 , ± 1 second error in starting to yield ± 0.5 second accuracy. This is a fraction of a stop at normal processing times.

CONSTRUCTION

The prototype unit was housed in a specially-made folded aluminium box, as shown in the photograph. With this arrangement it is possible to keep all mains voltages in the rear compartment which is shielded from the TTL circuit to avoid interference problems. The rectifier and regulator circuit is constructed on a piece of stripboard and mounted in this compartment. The regulator itself requires a $4^{\circ}C/W$ heat-sink.

The counter circuit is easily constructed on i.c. stripboard. This assembly bolts beneath the panel carrying the pushbuttons. In the prototype these buttons were home-made to provide large size (for finding in the dark) without being expensive.

DISPLAYS

The seven-segment displays and decoder-drivers are mounted on a further piece of i.c. stripboard, forming a single module requiring only power and data bits to be connected. This module bolts behind the top panel which has a cutout to let the displays show through.

Layout is not critical in this circuit but it is best to keep all wires as short as possible and to shield the logic from mains, because in the high state logic gates have high input impedance.



Items mentioned in this feature are usually available from electronic equipment and component retailers advertising in this magazine. However, where a full address is given, enquiries and orders should then be made direct to the firm concerned. All quoted prices are those at the time of going to press.

INFRA-RED LISTENING

Complete freedom of movement as well as uninterrupted listening to your favourite hi fi record or radio and television programme is possible with the new range of infra-red transmitters and headphone receivers manufactured by Sennheiser and being marketed by Hayden Laboratories.

For home use there are mono and stereo systems available. The prices are not cheap but for the housewife she can keep an eye on her children or carry on with the housework while listening to a programme. Using infra-red techniques means that there are no wires to the headphones for people to trip over. This makes it ideal for business and educational lectures.

The two channel headphone receiver, type HDI 434, is claimed to be a completely new development. A feature of the headphones is the three position channel selection. The centre gives stereo reception. In position 1 only the sound of the 95kHz channel is fed to both headphone capsules. In position 2 the signal of the 250kHz channel is received. This allows true two-channel operation even of completely different audio signals.

The volume and also the balance for stereo operation can be easily set by two slider controls. The receiving lens of the infra-red diodes is mounted in the edge of the earpiece.

The companion two-channel transmitter, type SI 434, is specially designed for covering an area the size of most large living rooms. The electronics for both transmitting channels and the 12 necessary infrared luminescent diodes are contained in a small case measuring only 200 \times 80 \times 17mm. The transmitter is powered from an external supply.

The transmitter audio connection cable is plugged in to a DIN headphone output socket of the selected equipment, i.e. radio, television or hi fi, and the transmitter is then modulated with the available signal. These modulated signals are picked up by the receiving lens in the headphones. The cost of the two channel HDI 434 headphones is £86.50 and the SI 434 transmitter is £72.50, excluding VAT. A lightweight mono stethoset type headphone type HDI 40 is available from £44.66 and a mono transmitter, type SI 406, from £44.10, excluding VAT.

Further information and technical details of the Sennheiser range of domestic and professional infra-red equipment is available from Hayden Laboratories Ltd., Hayden House, Church Road, Chalfont St. Peters, Bucks, SL9 9EW.

CLOCK/CALCULATOR

"The all action time computer" is the simplest way of describing the new Casio CQ-1 Computer Quartz available from Tempus.

Ideal for the executive to salesman and the sportsmen/women to the student, the CQ-1 is a timepiece (including day/date), stopwatch, alarm and calculator all in one very small case.

The clock function has a useful four alarm feature which can be used for numerous applications. The alarm can be set in four time positions and at intervals of one minute or more. The command for a.m. and p.m. setting is also possible. The alarm output is generated by a miniature buzzer and emits a different tone for each alarm setting.

For the sports enthusiasts the stopwatch facility has a stop/start key and a lap key. The stopwatch will read hours, minutes, seconds andlapsed time up to 9 hours, 59min 59 9secs. The lap key enables the timing display to be "frozen" for lap times to be recorded, the timing sequence continuing until the lap key is again pressed and the readout reverting to a total time elapsed.

The calculator is a basic four function $(+, -, \times, -)$ type including constant. Calculations up to 8 digits are possible and by operating



the keys, time and date calculations are possible. Also, as the calendar is programmed from 1901 to 2099 inclusive, any day of the week or the number of days for a certain period of time can be calculated.

With suggested functions such as setting for the time to leave for school, office, shopping, taking medicine, as well as a reminder for important business appointments, telephone calls and meetings. Not to mention the usefulness of the calculator for homework, household and business accounts, the Casio CQ-1 Computer Quartz would seem a reasonable family investment at £29.75; particularly for late risers and for keeping parking meter fines to a minimum.

Whilst still on time, Tempus have an excellent range of digital watches, including attractive ladies' slim-line types and a range of World time watches.

For full details of Casio CQ-1 Computer Quartz and the complete range of digital watches readers should contact Tempus, 19-21 Fitzroy St., Cambridge (0223-312866)

BREAKDOWN TESTER

Some time ago now we published a design for a "Breakdown Tester". An instrument using similar principles is now being manufactured by Stoneleigh Electronics.

The type 3C15 Tester determines the breakdown voltage of transistors and diodes (including Zener diodes) at the current you select. The device to be tested is connected across the test terminals of the instrument and when the "test" button is pressed, a constant current source is applied to the device. The constant current source can be set within the range 1-15mA and has an output potential of up to 300V.

The breakdown voltage is displayed on a voltmeter which has switched ranges of 10, 30, 100 and 300 f.s.d. A polarity switch allows rapid checking of forward and reverse characteristics. There is visual indication when the device is either open circuit or the breakdown voltage is in excess of the 300 volt capability of the instrument.

Details of price and further information on the 3C15 Breakdown Tester can be obtained from Stoneleigh Electronics, Ltd., Mawney Road, Romford, RM7 7SE.

BOUNCING CHEQUES

We have been informed by A. Marshall (London) Ltd., that because so many cheques have not been honoured by several banks they are forced to announce that in future most cheques will be cleared before any goods are despatched.

Postal orders, cash and approved accounts will be dealt with in the normal way.

Old habits die hard, we are told, and this is particularly true when playing piano or organ. With the organ especially it is necessary to extemporise to a large degree and thus easy to develop *bad* habits such as chord clichés, the same old key changes and registrations. However hard the grey matter is working, one of the most difficult habits to break is playing "chords" on a monophonic keyboard. A number of synthesisers will give a rude response to this oversight.

The Wurlitzer organs were among the first to be fitted with a small synthesiser but the shape of its manual was a reminder in itself: white keys were only as long as the index finger (black keys being about an inch in length), so that it was almost impossible to play chords—let alone forget!

Strictly

by K. Lenton ~ Smith

ONE MAN BAND

Most commercial organs include a Rhythm Unit these days which, used in moderation, is a useful device. Adding an R.U. is not difficult at present as chips which will generate the various patterns are freely available; several pulse-operated "voices" and the switching are the only additional requirements. Like TV sets, the essential control is the off switch! Even so, with the tempo control set to enable him to count the beats, the beginner will be helped to struggle through new music. The more expert performer is spurred on by his "sideman", especially in the Latin-American vein.

Something else to play as well? My choice would be Electric Piano, rather than synthesiser, because of its polyphonic nature. Faced with pedals, three keyboards and their controls, there is plenty to think about but the technique is the same for all.

For some weeks past I have experimented with organ/piano versus organ/synthesiser and there is no doubt in my mind which is the better combination. Although most organs are fitted with percussion, the piano envelope complements the organ admirably. The tonal capability of the piano is purposely limited to perhaps The E.O.C.S. was founded some twenty years ago by a small group or enthusiasts headed by Arthur le Boutillier. In those days there was very little technical information available and the idea of the Society then as now, was pooling of data anc experience. Publications sent to members eventually developed into the Society's own "organ", the *Electronic Organ* magazine. Membership now extends across the world and regular meetings are held in London, Coventry and Manchester.

three voices, but is sufficient to pro-

vide a most desirable extra keyboard

Needless to say, it is essential to

place the additional keyboard on top

of the organ so that the keys line up

up at 8' pitch! The only real dis-

advantage is its short compass but

extending this to 88 notes would rule

out the advantage of portability. The

classicist would, in any case, prefer

to play the acoustic parent instru-

Mention of the Electronic Organ

Constructor's Society may seem a

little out of place at this point. My

first reason is because a number of

readers have enquired about this

Society following my note in last

December's edition on the demon-

stration of MES 53 at their London

4

ment

meeting.

ORGANISATION

for the one-man performance.

The Society's brief does not cover electronic organs alone: it is interested in all forms of electronic music and pipe organs. Membership is mixed, from church organists to pop group performers, but most are simply concerned with building an organ to their own specification. Readers interested in this Society should write to Ralph Purdy (Membership Secretary, E.O.C.S.), 11 The Avenue, Station Road, Billericay, Essex.

TECHNICAL ENQUIRY

While on the subject of E.O.C.S., Douglas Shaw has suggested in Sound Design (page 32) that enguiries on adding a "Minisonic II" to an existing organ might best be answered by joining that Society. Under the heading "Technical Enquiries" he also mentioned the different playing technique required and the possibility of using a gating system to ensure response from one key only.

In case my previous remarks on combining electric piano (such as "Joanna", also in *Sound Design*), rather than synthesiser, with an organ sound biased, perhaps I should pursue my theme. For "live" playing, there is not enough time to do justice to a synthesiser; minute adjustments to its many controls are not possible, except perhaps for solo performance. Attempting to play it whilst also manipulating two other manuals, pedals and *their* controls must relegate the synthesiser to a "Univox" type of keyboard (which had a shortlived existence three decades ago).

Listening to the expert synthesiser player on disc, and reading the record sleeve notes, brings one message home forcibly. A few minutes of a L.P. recording probably involved many hours work. It needs time to obtain the precise effect you are looking for, so multi-recording at leisure is the only respectful way to treat one of Moog's progeny. My experience shows that the "Minisonic II'' sounds impressive through a reverberated system and Leslie speaker but its resources can only be used in a cursory fashion when playing an organ with it.

HOOK-UP

Notwithstanding these comments, there should be no problem in adding a "Minisonic" to an existing organ or including it in a constructional project. If space is at a premium, the synthesiser could be placed on top of the organ console: the music stand has to be raised in this case, bifocal-wearing readers should be warned!

The low-level outputs should couple into the organ's main amplifier quite satisfactorily, though it should be noted that d.c. levels are present. For this reason it is best to use 0.1μ F coupling capacitors, between synthesiser and organ as a permanent feature, otherwise there is the possibility of upsetting the amplifier by directly connecting the output of IC18 to a d.c. carrying point on the organ. In the case of a single-channel organ, these coupling capacitors can be joined at the organ end to produce a monophonic input signal.

Signals from the "Minisonic^{*} can be inserted before or after the swell pedal according to taste. On the whole, I would suggest using the synthesiser unenclosed, i.e. after the swell control, if signal levels allow.



HIS month we deal with monitoring systems, speakers and ancillaries.

VU METER

By far the most logical place to put any VU meters in your system is right at the end, so that they measure, albeit roughly, the actual power supplied to the loudspeakers. They should be calibrated so that 0VU (the transition point from black to red) occurs comfortably before the amplifier starts to clip. This can be done with a test tone and an oscilloscope, or failing that, a sensitive pair of ears.

A suitable circuit for driving a VU meter direct from the output of a power amplifier is shown in Fig. 6a. Note that it draws all its power from the signal itself.

If you wish to include a meter prior to the main volume control, it should be calibrated so that with the main output control at maximum, 0VU on the console meter corresponds to 0VU on the power amplifier(s). When the limiter is in circuit, 0VU on the console should correspond to the maximum output from the limiter, and a preset volume control, connected in series with the main output control, will probably be needed to match the level from the limiter to the sensitivity of the power amplifier. A circuit for driving VU meters from signals in the range 200mV upwards is shown in Fig. 6b.

PRE-FADE

All disco equipment ought to have some form of prefade listening. This is a means by which the DJ can listen to any of his inputs on a pair of headphones, regardless of whether they are being used to play through the mixer or not. By this means he can cue up records and tapes while a record is playing, and with an effective system this can be done very efficiently and simply. All that is basically required is a switch which will select any of the inputs, or the main output, and present them, via a low-power amplifier, to his headphones.

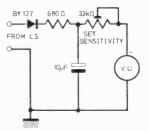


Fig. 6a. How to drive a VU or similar meter directly from the loudspeaker terminals of an amplifier

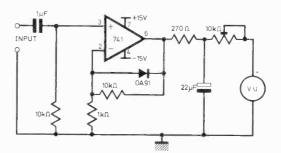


Fig. 6b. This circuit will drive VU meters from ordinary line-level sources

A simple rotary switch can be used, but more elegantly, PO keyswitches, of the type used on telephone switchboards, can be used in a series arrangement which supplies the main output to the headphones except when one switch is operated, whereupon the output of that particular channel will be heard instead. Each channel has a switch of its own, which can be arranged in a logical position on the control panel.

The wiring of a four-channel bank of pre-fade switches is shown in Fig. 7, with all the switches in their "normal" position. As you can see, operating any one of the switches causes the series line carrying the main output to be interrupted, and the channel output (taken from the top of the channel fader) is substituted for it.

It will be necessary to attenuate the main output in order to avoid deafening the operator when switching from the relatively quiet output of a record deck to the amplified main output. The preset shown in the diagram could be set so that with the VU meter reading normally, that is, peaking around -3, the output from the record decks whether heard directly through prefade only, or after having passed through the disco mixer, is more or less the same.

HEADPHONES

When choosing a pair of headphones, bear in mind that you could be wearing them for up to five or six hours at a stretch. Above all, therefore, they must be light and comfortable. Personal favourites are the AKG K140, a fairly new model available at around £13 from the discount stores, with the lightweight Sennheisers, just as comfortable but a little shrill, a close second.

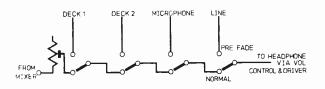


Fig. 7. How a bank of PO keyswitches may be used to select pre-fade inputs or the main output from the console mixer

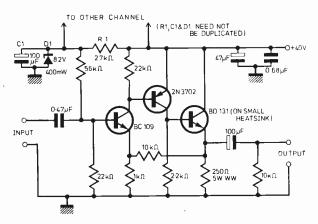


Fig. 8. High quality Class A headphone driver circuit for high impedance headphones (600 Ω and above)

Table 1: POWER OUTPUT FOR VARIOUS SUPPLIES AND LOUDSPEAKER IMPEDANCES

Supply		Load Impe	dance
Voltage	4Ω	8 Ω	16 Ω
80	178W	89W	45 W
70	134W	67W	34W
60	96W	48W	24 W
55	80W	40W	20 W
50	64 W	32W	16W
45	50W	25W	13W
40	38W	19W	10W
35	28W	14W	7W
30	20W	10W	5W

Notes:

1. The supply voltages refer to *total* supply volts e.g. 60V or $\pm 30V$.

2. Output figures are for typical amplifier modules using direct-coupled output stages, and assume unlimited current capability.

My preference is for high impedance headphones, as they can be driven by a simple but very high-quality Class A driver circuit (Fig. 8). Lower impedance phones, below about 300 ohms, will require a Class AB driver—there are a multitude of integrated circuits which will do this job adequately.

STEREO

Four years ago I would have said that stereo was a waste of time for a mobile discotheque. Nowadays, with nearly all new singles recorded in stereo, and having tried it in practice, I am not so sure. The problem with stereo is that people have to be roughly equidistant from the two loudspeakers in order to appreciate it, and with a mobile set-up this is difficult to achieve. Of course there is great benefit to be drawn from having all your circuitry duplicated, since any failure can, with the addition of a stereo/mono switch at input and output, be quickly by-passed, and the equipment run in mono.

THE POWER AMPLIFIER

The power amplifier is the link between the duly processed low level signal from the control circuits of your console and the loudspeakers. Its purpose is to take a signal of a few hundred millivolts and amplify it to a power of tens or even hundreds of watts at an impedance level suitable for driving loudspeakers. It is better to have a power output *less than the maximum your loudspeakers can handle so that a fault*, such as an open-circuit earth connection on the input, can never cause enough power to be delivered to the speakers to damage them.

Kits for this application are legion and represent a cheap and effective way of obtaining the necessary power. Bear in mind, though, that the use of units originally designed for domestic applications may cause problems when they are subjected to the gruelling conditions at a disco. In short, the poor little things may overheat and expire, much to your dismay and embarrassment. So use one capable of more power than you actually require, and then limit its power output by using the minimum supply voltage which will deliver the necessary swing. A table of supply voltages for particular outputs is given in Table 1. The figures are approximate but should prove useful.

LOUDSPEAKERS

Avoid using 12 or 15in bass units as your only link with the outside world. All you will get is a murky, muddy sound, and nobody will be able to hear the words of the songs, or anything you try to say to them. That many disco devotees do not seem to mind this is no excuse for sloppiness in this department!

Do not expect to right matters with the inclusion of a couple of tweeters from domestic hi-fi units. Either they will be too quiet to be effective, or you will blow them up.

Consider a domestic loudspeaker system, containing, say, one 8in bass/midrange unit and a 3in tweeter. This achieves a reasonable "balance of power" over the whole frequency range. But a 15in bass unit, with four times as much cone area, will require four such tweeters to achieve the same balance. Plus, in all probability, some midrange units to fill in the gap between the two widely different systems. So unless you use tweeters specifically designed for high power use (and they should be labelled as such), or your tweeters fall off the back of lorries, you will have an expensive time making up multi-way systems for your disco.

A good idea is to use twin-cone units, where one large assembly handles most of the frequency range fairly comfortably, or, if you can afford them, the excellent Tannoy "dual concentric" speakers where the bass unit and matched tweeter are mounted concentrically together with a crossover all in a single chassis.

When making cabinets for your speakers, preferably stick to one of the manufacturers' recommended designs. Although the larger the cabinet the more efficient the system and the deeper the bass (as a rough rule), do not go berserk; you will, after all, have to cart them about. Castors, too, are one of man's greater inventions.

PLACEMENT OF SPEAKERS

The absorption of sound by a thick layer of human flesh is quite rapid at frequencies above about 200Hz and it simply does not make sense to put your speakers on the floor unless they are about 7ft high and with the business end at the top! They must be placed so that the axes of the speakers are at least 6ft from the ground so that people can hear them with as few obstructions as possible between speaker and ear.

If this is impossible, they should at least be tilted upwards so that sound may be reflected back over the audience from the ceiling. Having them tilted has the additional advantage that people will usually then refrain from putting their half-consumed drinks on them. Drinks at parties have a habit of getting spilt, and brown ale does little to improve the accoustic properties of a sound cabinet!

With your speakers up high you will be able to cover a greater area at less power than you would were your speakers below shoulder height. This technique is just as valid for small parties, where the music is provided by a normal domestic stereo system, usually pushed to the limits of its endurance. Placing the speakers on a high shelf or on top of a wardrobe will enable your guests to hear the music much more clearly when the room is full.

THE LIGHT SHOW

Once upon a time, dances and discos frequented by young people took place in near darkness, and lots of unkind jokes were told about discoveries made by eager young men on bringing their chosen partner into better lighting conditions. Nowadays, however, matters are somewhat different, and many operators take an almost savage delight in exposing their audience to varying coloured lights.

The heart of many a lighting system is the miniature spotlight bulb, usually rated around 75 to 100 watts and driven by a thyristor or triac to flash either sequentially with its neighbours or in time to a selected part of the audio spectrum. These bulbs are available in two formats, BC, which is a bayonet fitting like an ordinary light bulb, and ES (Edison Screw) which, as its name suggests, screws into the holder like a torch bulb. BC is to be recommended for lower power lamps, up to 150 watts, as it is cheaper, and screw-in types tend to unscrew themselves. Higher power bulbs are almost universally screw fitting.

There is a large number of commercially-made units, as well as a host of constructional designs for flashing spotlights on and off. A well designed one should take an utterly minimal amount of current from the audio power amplifier but unfortunately this is not always the case. It is a good idea to use a rough old amplifier solely to drive your light show if you have any doubts on this score. The spikes produced by the thyristors turning on and off can play havoc with your sound system if the light cables come near any sensitive part of the mixer circuitry, so do keep them well out of the way.

EFFECTS PROJECTORS

Effects projectors work on two principles; first there is the wheel, which is spun round like a gramophone record between the bulb and the lens, heating up and causing the coloured oils inside to drift in and out of each other.

Then there is the cartridge, which is driven from its edge and produces moiré patterns as its two sides interact with each other. Both can be very pretty, though you may have to look around before you see one you really like.

STROBES

Stroboscopic lighting has been used for a long time in industry to observe moving pieces of machinery as if they were stationary by arranging that the moving part is always in the same place when the flash occurs. Recently, though, it has found a less precise application in conjunction with discotheques. You take a strobe light, set it at a few cycles per second, and shine it on the dancers. Hopefully you stop before they start falling over or being sick.

People with epileptic tendencies of which they may themselves be unaware, may even throw fits when exposed to lighting of a particular strobe frequency. That apart, it can still be fun but do not use the strobe for more than five minutes in any hour and if anyone complains or looks disturbed by the flashing light, stop using it immediately.

SAFETY PRECAUTIONS

Your equipment should be earthed at all times to the mains earth and if you are using an extension lead it should be checked regularly for continuity along the

I

earth lead. All mains connections should be made with proper plugs and sockets and kept well out of any areas where drink may be spilled or where people may trip over them.

Connections to spotlights should be of stout cable and all the holders should be securely earthed. The greatest care should be exercised when operating on spotlights with the power on, since the output from sound/light or sequencer units is usually not isolated from the mains, as all audio equipment would be.

Always string your cables well out of harm's way; if you have them up along the ceiling they will not get dirty or wet but be sure they will not fall down, either.

INTERCONNECTIONS

By far the easiest way of interconnecting your audio department is to use standard jack sockets, available everywhere. They are robust, positive and reliable, which is more than can be said for most of the miniature connectors that adorn hi-fi systems. Of course, they do not have to stand up to such rigorous use. Have a set of leads exclusively for use with your disco. If you use jacks throughout, all you will need are a series of jack-to-jack leads of various lengths. It is useful to have a short and a long set of loudspeaker leads, so that you need not have an excessive amount of cable trailing around.

All the pluggery needed with sound to light units usually comes with the unit, or can be bought from the same shop. This should not present any problems if the plugs are wired securely.

STORAGE

On an average night you will get through roughly a hundred singles. To ensure an adequate selection, you will need to take perhaps three to five times as many as this. Any more is really a bit excessive. Whatever sort of order you decide to keep them in, you will soon learn where to find everything provided nobody messes about with your collection.

Cardboard boxes are useful for keeping singles in, but have a rather limited life. Smart record boxes made out of half-inch chipboard can be fitted with a lid and a padlock to keep marauding fingers out while you go and fetch your car at the end of the evening.

A travelling bag can be filled up with leads, notepaper for requests, your advertising material, a soldering iron and all the other odds and ends that come in useful.

MUSIC

It is outside the scope of this article to go deeply into the fraught waters of musical taste. However, the music papers feel free to pontificate on electronic equipment so perhaps a word or two on their subject might not be amiss.

Lately the disco audience has been polarising swiftly into two camps; broadly, soul and rock. There has always been a split of this kind but in the last couple of years it has become much worse. The problem now is that if you play two soul records in a row someone with more persistence than imagination will insist on the Rolling Stones or Status Quo.

Conversely, play too much rock and the soul freaks will descend on you breathing fire—their requirements are usually more diverse but no less enthusiastic. Of course, if the event is specifically a soul or rock or reggae disco this problem will not arise, but you may still find yourselves bombarded with requests for things you have never heard of. Do not fret, but do take a note of what you are asked for and attempt to hear it on your next trip to the record shop.

Do not waste too much time buying up the latest releases; many people, quite understandably, will not dance to anything they have not heard at least a dozen times and by then the record in question is probably available from one of the second-hand shops at half the original price. Always note requests on a piece of paper, whether or not you have the records asked for; not only can you use this to guide your choice of material but it is also much easier to read a dedication from paper than it is to try and memorise it.

COPYRIGHT

Round every record label is the legend "Unauthorised Copying, Public Performance and Broadcasting of this record prohibited", or words to that effect. Most DJs ignore it completely. The organisation concerned with administering the public performance of gramophone records is the Performing Rights Society Ltd., 33 Berners St., London, W1P 4AA, who will send you on request details of the legal position regarding the public performance of gramophone records, the gist of which is that you can please yourself what you do at Johnny's birthday party, and if you are playing somewhere where there is a fee for admission then the onus for obtaining alicence falls on the person hiring you.

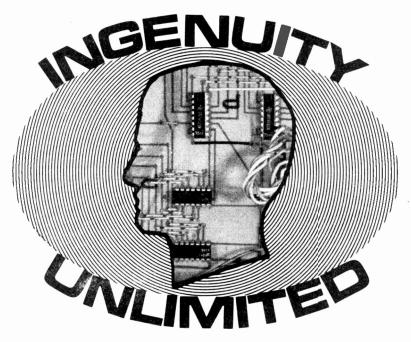
CONCLUSION

Don't kid yourself: despite the claims of the advertisers, "easy money earned with our £350 disco console!" (for whom?)—operating a mobile discotheque is really hard work—the public only sees the easy bit. You may not get home till 2 or 3 o'clock in the morning and you'll be worn out when you do.

You will have to put up with requests for records you have played so often you wish they had never been recorded, and you may have to cart your equipment up and down stairs cluttered with people who display as much reluctance to move out of your way as they did to dance to your music. Don't underprice yourself; better to do two discos for £40 than three!

All the same, it can still be a rewarding pastime, and a great deal of enjoyment can be had from your mobile disco, especially when you've built some or all of it yourself. I hope this article has made clear to you something of the art of running a mobile discotheque. Good Luck!





A selection of readers' original circuit ideas. It should be emphasised that these designs have not been proven by us. They will at any rate stimulate further thought.

Why not submit your idea? Any idea published will be awarded payment according to its merits.

Articles submitted for publication should conform to the usual practices of this journal, e.g., with regard to abbreviations and circuit symbols. Diagrams should be on separate sheets, not inserted in the text.

Each idea submitted must be accompanied by a declaration to the effect that it is the original work of the undersigned, and that it has not been accepted for publication elsewhere.

THE TV tennis Score Unit is designed to attach to the Videomaster TV tennis game, to display the score on the television screen immediately after each goal or point is scored. The score is displayed in the seven segment format, with each score being shown on its own side of the screen, as shown in Fig. 2b. The game progresses until one or other player reaches a score of nine, at which point that player is the winner. This condition is detected by G5, which prohibits the game from proceeding any further. A new game is started by pressing the reset button.

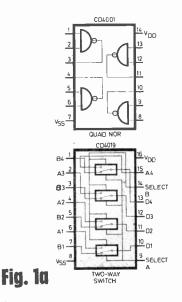
TV TENNIS SCORE UNIT

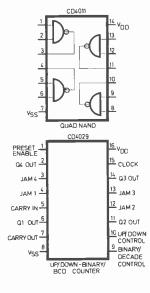
The four input gates (G1 to G4), update each player's score counter (CTR A and CTR B). Switch A is controlled by the Line Sync timing circuits, and switches at the correct time during the sweep, to display each counter score on its side of the screen. The timing circuits consist of two Line "single shots" and three Frame "single shots", and these define the character position and size. The gating at the Video Output (G9 to G18), suitably blanks the seven segment decoded counters to give the correct television screen representation of the score, and also inhibits the display during play.

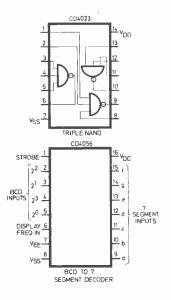
Fig. 3 shows how the third Line "single shot" (LS3) produces double score, by pulling up LS1, and switches the two-way switch giving "ghost" figures which do not mask the bats or ball.

The prototype was constructed on two d.i.l. circuit board offcuts, which fitted within the lid of the Videomaster unit. The logic is COSMOS throughout, which consumes very little current from the Videomaster batteries.

D. E. Launchbury, Shirley, Solihull.







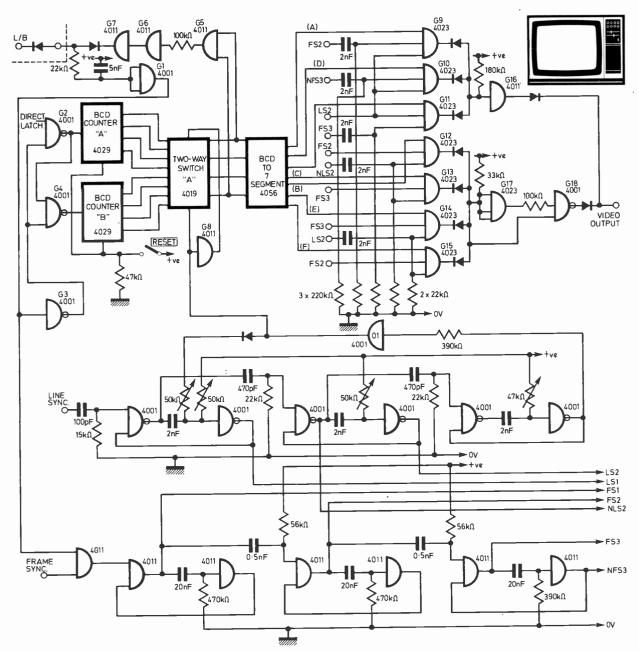
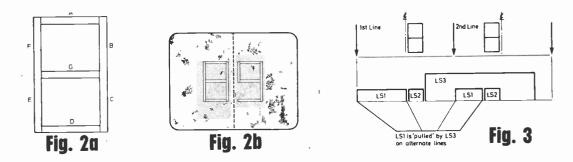
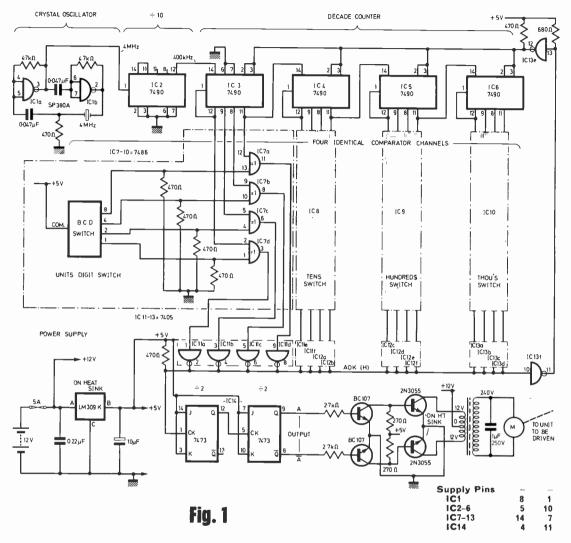


Fig. 1b





MOTOR DRIVER



THE circuit in Fig. 1 was originally designed to be highly stable, and to drive a small induction motor at various frequencies, which would in turn drive a telescope at different tracking rates. The circuit was built using readily available cheap components.

Package IC1 forms the oscillator using a 4MHz crystal, and IC2 divides this symmetrically by 10. The 400kHz squarewave is then counted by the line of BCD decade counters (IC3 to IC6), the outputs of which are compared to the outputs of four binary coded switches, parity producing logical 0. Outputs from the 16 EXCLUSIVE OR gates are fed to a line of inverters. When all the gates show zero (the decade counters have counted up to the number set on the four switches), the AOK line is no longer held low. This condition lasts for 2.5μ s, during which time the JK flip-flop toggles, and the counters are cleared after the propagation delay of IC13e and IC13f. This cycle of events continues to repeat itself.

Hence, a number set on the switches, say 2000, will cause the circuit to count up to this figure in $2 \cdot 5\mu s$ pulses, taking 5ms, which is further divided by 4 (IC14) giving a symmetrical squarewave of 20ms (50Hz). Therefore the number set on the switches relates directly to the

wavelength of the output frequency. Current consumption for this part of the circuit is about 390mA depending on the switch settings.

This signal is used to drive the inverter shown, which provides 240 volts to drive the motor. The 2N3055s should be mounted on a heatsink, and also the LM309K.

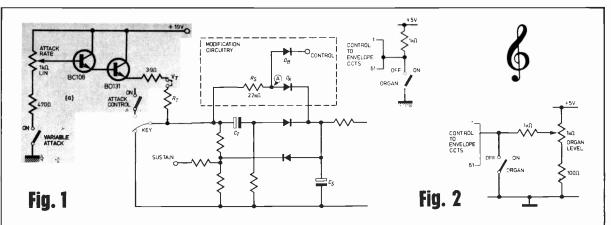
The circuit has many possible applications, and could be adapted to work outside its present range of 99.99ms to 0.01ms.

A. P. Wilkinson, Morley, Leeds

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TRANSISTORS AND DIODES A117 10 BC558 15* E430 120 AA119 10 BC559 15* E430 120 AA119 10 BC559 15* EH900 36 AA217 30 BCY39 127 H16.0 70 AC127 18* BCY39 27 H16.0 70 AC127 15* BCY30 15 ML243 45 AC128 18* BCY71 15 ML2345 14 AC168 18* BCY17 15 ML2355 140 AC168 18* BCY17 15 ML2355 140 AC168 18* BCY17 15 ML2355 140 AC1610 18* BC131 44* MA2355 140 AC164 49* BC142 49* CA47 30	ELEKTOR KI Kits include sil the components detailed in the original article subsequent Missing Link columns. Kits come complete with the EPS printed circuit board(s) what also be provided on the react of the subsection of the signal of the subsection of the section of the subsection of the from the clock timing pulses which may be used to control other in TY BOUND The audio output stags of many domastic TV ref hydh quality transmitted F. Mis.ound signal to be fed into a dome VERBITLE LOGIC PRDBE (Ets) A seven segment display indice test	together with any additional items specified in see published and suitable power supplies can clocks this design allows the user to attract appliances cevers is rarely HI FI. This project allows see satic HI Fixetem also the logic teless of the TTL circuits under ten	CMOS 4000 21 4013 60 4040 186 4001 21 4013 60 4049 186 4002 23 4017 63 4050 50 4007 21 4020 124 4050 25 4007 21 4020 124 4050 25 4007 6 60 4024 45 4033 80 4011 21 4025 21 4528 117 4012 21 4025 20
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Zener diode 1W 3·3V to 220V E24 Series 30p* Zener diode 400 mW 2·7V to 47V E12 series 12p* Large Stock of Japanese Transistors		HYRISTORS AND TRIACS	TCA730 DC volume 490 TCA740 DC tone 490 TCA940 audio 200° TDA1022 bucket bridge 935
PANEL METERS RECTANGULAR MODERN STYLE PANEL METERS. Normal size 60 x 45 mm. mounting depth 40 mm	7401 16 7448 75 74136 75 74 7402 16 7451 16 74137 80 6 7403 16 7451 16 74141 70 16 7404 16 7451 16 74144 70 16 7404 16 7453 16 74145 70 16 7406 10 7454 17 74156 150 160 7407 30 74155 150 160 16 74150 160 7407 30 74156 150 150 160 16 7408 18 7472 28 74151 75 16 7409 22 7473 28 74153 60 16	HYRISTORS 14 400V T05 75 6A 400V T0220 125 6A 400V T0220 350 1AC R 900 2832V 38 IR 100 20 18 100 20	TOA1190 TV sound 300° TDA2202 audio 400° U122B triac control 14 dil 280 U132B remots switch 4 dil 110 UAA170 led driver 16 dil 230 UAA180 led driver 16 dil 230 uA78G regulator TO220 235 uA79G regulator TO3 250
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CRYSTALS AND FILTERS 27.768/Hz MTG 32.12 6.00 1.00Hz HC 33.01 3.00 3.2786/Hz HC 33.01 3.00 4.194304Hz HC 33.01 3.00 4.304064Hz HC 33.01 3.00 6.364068/Hz HC 33.01 3.00 72768/Hz HC 33.01 3.00 6.364068/Hz HC 33.01 3.00 7276/Hz HC 25.01 2.15 38/Hz HC 25.01 2.15 38/Hz HC 25.01 2.15 SFE 6.01 MA 60 SFE 6.01 MA 60 SFE 6.01 MA 60 SFE 6.03 MA 60 SFF 0.455B 70	7422 20 7489 200 74166 120 S 7423 30 7491 70 7474 120 S 7424 30 7491 70 74174 120 S 7424 30 7491 70 74175 80 S 7425 30 7492 45 74177 90 S 7425 24 7493 45 74181 300 S 7426 24 7493 57 74181 300 S 7427 28 749,710 75 74182 84 S 7430 17 74100 36 74180 150 V 7438 27 74100 36 74180 100 2 7439 27 74110 85 74180 100 2 7439 27 74110 85 74180 100 2 7449 66 <td>SECONDÁRY—Two Independent fully leo- nected'n series or parallel. e.g. 6V 20VA transformer can give ethere f& at 3:2A or 12V (60-6) at 1:6A Secondary 235 20VA Value 235 20VA Secondary Current 5VA Secondary 20VA Style ×4 5V 2 × 0:5A × 15V 2 × 0:5A</td> <td>Net a dy Lood at densame auto might Dete and oversaes customers do not add VAT DELIVERY. All orders processed on day of re- ceipt. Express delivery available by CIV Link or securicor if required. Phone for details. PAYMENT cash. Cheque or Postal order with order. Access, Eurocard, Masterchange or Barclayard number by phone or istler for GUARANTEE All devices are brand new and full topec. unless otherwise tatled. Any feuty lism returned and in good condition within seven days will be exchanged or refunded. Back copies of Elektor and Elektor EPS circuit boards available at published prices available at published prices 1977</td>	SECONDÁRY—Two Independent fully leo- nected'n series or parallel. e.g. 6V 20VA transformer can give ethere f& at 3:2A or 12V (60-6) at 1:6A Secondary 235 20VA Value 235 20VA Secondary Current 5VA Secondary 20VA Style ×4 5V 2 × 0:5A × 15V 2 × 0:5A	Net a dy Lood at densame auto might Dete and oversaes customers do not add VAT DELIVERY. All orders processed on day of re- ceipt. Express delivery available by CIV Link or securicor if required. Phone for details. PAYMENT cash. Cheque or Postal order with order. Access, Eurocard, Masterchange or Barclayard number by phone or istler for GUARANTEE All devices are brand new and full topec. unless otherwise tatled. Any feuty lism returned and in good condition within seven days will be exchanged or refunded. Back copies of Elektor and Elektor EPS circuit boards available at published prices available at published prices 1977

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JOANNA MODIFICATION

B^Y means of simple modifications. the PE "Joanna" by A. J. Boothman, may be given an alter-native organ sound. Figure 1 shows one Joanna envelope circuit. One resistor (RS), and two diodes (DA and DB) are added to each key circuit, and one control switch with pull-up resistor are common to all sixty-one circuits. A switch is also added to the 19 volt connection to the keyswitch busbar, labelled attack control.

With the organ switch "on" and the attack control "off", when a key is depressed CT charges through RT but has no effect on the signal CS charges through RT, output. RS and DA, up to about +5 volts, at which point DB conducts and prevents any further increase in voltage. When the key is released, the discharge of CS is via the normal sustain pedal. When the organ switch is "off", diode DB clamps point A to about +0.7 volts, and prevents CS charging through DA.

The result of this is an output signal which rises slowly, remains constant as long as the key is held down, and fades out slowly when the key is released. There are now a number of variations in the sound available. Resistor RS determines the rate of attack, and may be between $10k\Omega$ and $100k\Omega$ as required. A variable attack time may be provided by varying VT as shown in Fig. 1. With both organ and attack control switches "on", the keyboard touch sensitivity gives a further increase in the speed of attack. The organ output level may be varied by altering the clamp voltage of DB (as in Fig. 2), and when set to a low level gives the familiar organ percussion sound.

If the organ control line and +19 volt keyswitch busbar are split into two at note 25, with suitable switching the lower two octaves can play the organ, and the top three octaves can play the piano, or vice versa. In this case it is advisable

to provide two separate organ level controls, otherwise the piano can be drowned by the organ sound. Extra filters to give the "flute" organ voice are also useful, especially when sustained chords are played in the lower octaves.

The above modification adds less than five pounds to the cost of the Joanna. However, for anyone who is prepared to completely rebuild the master oscillator board, a circuit is given in Fig. 3 which includes vibrato, and the possibility of switching to 2, 4, 8, or 16 foot pitches as required. The higher pitches, used with the organ stop, slow attack, and vibrato, give a useful "string syn-thesiser" sound. As the frequencies of the piano filters are fixed, alternative voice filters are required to make the best use of the higher pitches. The pitch selector inputs are shown in Table 1.

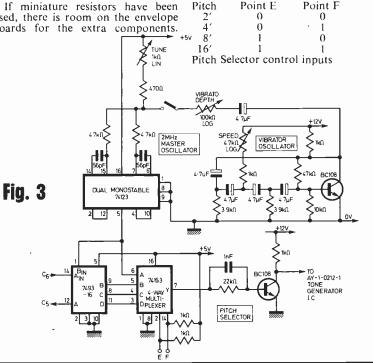
If miniature resistors have been used, there is room on the envelope boards for the extra components.

Three extra holes are drilled for each envelope circuit, and RS fits between R57 and C16, DA between D12 and D17, and DB between D17 and R72 with a bend in the anode lead to pass between R57 and D17. The DB cathodes are strapped together on the copper side of the board, using uninsulated wire, and are brought out to a Veropin near the +5 volt connection.

All earth wiring should be as low resistance as possible, otherwise the discharge of CT when a key is released, can cause a "thump" to be heard! Finally, a warning is given that heavy vibrato, when using one of the piano stops can produce a most sickening sound.

Table 1.

D. A. Boyd, Walton, Liverpool.



PROGRAMMABLE MELODY GENERATOR Si +9V \$R2 ·7kΩ 470 TR4 VR12 5k0 TR1 2N2646 0·22µF 39k0 IC1 7490 IC2 -11 0 22µ R3< C1 1000 < 10 L VR2 TO VR1 15 25kn SUB-MIN 800 Fig. 1 010

THE melody generator shown in Fig. 1, makes use of subminiature preset resistors acting as a small analogue "memory bank", enabling a short tune to be stored. A continuous cycle of ten notes of equal duration is produced, permitting a considerable variety of simple tunes to be set up.

The overall circuit consists of three blocks; a pulse generator, a decade counter, and an audio frequency oscillator driving a loudspeaker. The decade counter consists of two TTL i.c.s which require a power supply of 4.5 to 6 volts. This is derived from the 9 volt supply via resistor R10.

The pulse generator comprises TR1 and TR2, and is powered by the same 5 volt supply line used by the i.c.s, to avoid too high a voltage being applied to the input of IC1 from the collector of TR2. TR1 is a unijunction transistor operating as a relaxation oscillator producing pulses at intervals dependent on the setting of VR1 (approx. 0.5 to 5Hz with specified values). This pulse is boosted by TR2, which operates as a switching transistor to advance the decade counter.

The counter consists of IC1 and IC2, in which the former counts the pulses appearing at pin 14, and represents the count in binary at pins 12, 9, 8 and 11. The 7490 counts from zero, and may be reset to zero after any number. As wired in Fig. 1 the circuit resets to 0 after 9 in order to make full use of IC2. The latter i.c. converts the binary output of IC1 to decimal in the ten output lines, each one sequentially dropping to almost the potential of the negative supply rail, while the other nine remain at very high resistance.

Transistors TR3 and TR4 form an oscillator producing an audio frequency square wave which is amplified by TR5 and fed to the loudspeaker. Frequency may be varied by changing the time constant at the base of TR3. Thus by connecting ten different resistances in the outputs of IC2, the time constant at TR3 base, and therefore the frequency of oscillation, will change as the decade counter goes through its cycle. By making these resistors independently variable, a tune may be programmed into the device.

To ensure that the multivibrator will oscillate when the presets are at their minimum setting, VR12 is used to provide a threshold resistance so that each preset may cover two full octaves.

A note may be blanked by inserting a slip of paper between the wiper and track of the relevant preset. A good tune to start with is the chime of Big Ben: A F G C blank C G A F blank.

The most obvious application of the unit is a doorbell. Callers, and the constructor can both be kept entertained by ever changing themes. Alternatively the unit might be included in a communication system as a station identifier. The prototype ran from a 6 volt 100mA mains power pack, but would work from a 9 volt battery.

J. R. Skeels, Upminster, Essex.

POINTS ARISING

DIGITAL VOLTMETER (April 1977)

In the specification table, the figure for accuracy should read 0.1mV per °C.

PE SUPPLEMENT "PUTTING IT TOGETHER" (April 1977)

In the section on Wiring Pens, it was stated that only one colour of wire was available. This is incorrect. The Verowire system offers four different colours.

SOLAR HEATING CONTROLLER (February 1977)

The relay contact RLA1, connected across R5, should be a normally closed contact, shorting out R5 until the relay has operated.

The value of R5 may need to be reduced, depending upon the type of relay used. In some cases, a value as low as 22Ω has been found necessary to ensure reliable operation.

HAZARD WARNING FLASHER (November 1976)

A reader has pointed out to us a potential danger in this unit. If the hazard flasher is switched on whilst the turn indicator switch is not returned to the off position, power can be fed back through the latter switch into the ignition system, possibly causing damage.

This may be overcome by inserting a diode in series with the live supply feed to S1 in Fig. 1, with polarity appropriate to the car's electrical system (anode to supply for negative earth systems, and vice versa). A 10A device is required, such as the RS Components 261–019, mounted on a suitable heat-sink with an insulating kit.

pH METER (March 1977)

As a result of the pH Meter featured in our March 1977 edition, Uniprobe Instruments Ltd have made a special offer to the PE reader, of the combination glass and reference electrode used with their 300 series pH meters.

Suitable for the PE pH Meter, this probe set is available at the special price of £9.50 including VAT, from:

Uniprobe Instruments Ltd, Clive Road, Cardiff, CF5 1HG.

DRILL CONTROLLER Electronically changes speed from approxi-mately 10 revs to maximum. Full power at il speeds by finger-tip control, Kit includes all parts, case, Werything and full instructions.
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CONTROL

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2.4

5

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MULTISPEED MOTORS MULTISPEED MOOTORS Six speeds are available 500, 850 and 1,100 r.p.m. and 7,000, 9,000 and 11,000 r.p.m. Shaft is <u>1</u> in. Unage 230/240v. Its aspeed may be further controlled with the use of our Thyrisor controller. Very powerful and useful motor size approx, 2 in. dla x <u>2</u> in. long. Price <u>82:00</u> including Post <u>4</u> VAT.

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for stone polishing; in fact, there are no ends to its uses. Normal mains operation. \$3:25 including POST & VAT.

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SMITHS CENTRAL HEATING CONTROLLER



SMITHS CENTRAL HEATING CONTROLLER Push button gives 10 variations as follows (1) continuous hot water and con-tinuous central heating (2) continuous hot water but central heating off at night (3) continuous hot water but central heating both on but day the day (4) hot water and central heating only for 2 periods during the day but central heating only for "Or 2 periods during the day time only—then for summer on for 2 periods during the day time only—then for summer (8) hot water day time only (9) hot water at water twice daily (10) everything off. A handsome booking unit with 24-hour movement and the switches and other parts necessary to select the desired programme of heating. Supplied complete with wiring diagram. Originally sold we believe at over \$12.50 everything the tower off.

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A mains operated 4 + 4 stereo system. Rated one of the finest performers in the stereo field this would make a wonderful gift for almost any one in easy-to-assemble molular form and complete with a pair of Goodmans speakers this should sell at about \$30—but due to a special bulk buy and as an incentive for you to buy this month we offer the system complete at only \$14.00 including VAT and postage. at about £30

250 WATT TRANSFORMER

250 WATT TRANSFORMER Heavy duty power transformer which can be used for many purposes. Rated at 250 watts it is very well built with frames for upright mounting and is varnish impregnated. Its primary is for 230/240 volts 50 cycles, it has four secondaries each 10v very high current windings. Just a few of the circuits it can power are 10-0 var up to 12 amps; 20-0-20v at up to 6 amps; single 10v at 25 amps; single 20v at 124 amps; single 30v at 9 anps, single 40v at 6-5 amps. The trans-former can be used for power circuits (charging, etc.) or for amplifiers, there being an earth screen between primary and secondaries. A transformer like this today would cost at least 215 from the makers, however, we are making a special offer at 55/96. Post & VAT Paid. Grab some while you can our stock may not last long. may not last long.

SHORTWAVE CRYSTAL SET

Although this uses no battery it gives really amazing results. You will receive an amazing assortment of stations over the 19, 25, 31, 29 metre bands. Kit contains chassis front panel and all the parts \$2180-crystal earphone 55p including VAT and postage.

POWERFUL BATTERY MOTORS For models, Meccano's, diplanes, boats, etc., ctc. £2-00. drills, remote control





ROTARY PUMP Self priming, portable fits drill or electric motor, pumps up to 200 gallons per hour depending upon revs. Virtually uncorrolable, use to suck water, oil, petrol, fertilizer, chemicals, anything liquid. Hose connectors each end. 22:00 Post Paid.

MOTORISED DISCO SWITCHES With six 10 anp changeover switches. Multi adjustable switches are rated at 10 anp each so a total of 2000w's can be controlled and 80 a total of 2000w's can be controlled and this would provide a magnificent display. For mains operating £4:25 post & VAT Paid. Ditto 9 switch £4:95 Post & VAT Paid. DITTO BUT 12 SWITCH £5:75 POST & VAT PAID.

MAINS RELAYS

With triple 10 any changeover contacts operating coil wound for 230 volts AC, chassis mounting, one screw fixing, ex unused equipment **60p** each, 10 for **55** post and VAT pail.

TWIN OUTPUT POWER PACKS These have two separately R.C. smoothed outputs so can operate two battery radios or a stereo amp without cross modulation (they will of course operate one radio/tape cassette/calculator, in fact any battery appllance, and will save their cost in a few monthe). Byees: Full wave rectification, double insulated mains transformer-total enclosed in a hard P.V.C. case-three core mains lead-terminal output-when oriering please state output voltage 4/s, 8x, 7/s, 9x, 12x or 24x. Price £3*95, Post and VAT included.

CAR STARTER

D

CAR STARTER Most drivers who use their car around town find that on a very cold morning they have difficulty in starting because the battery has not any charge left in it. This always seems to happen on the morning when you are in a particular hurry is a charger/starter is well worth having. Our kit for this comprises: a heavy duty 250w transformer and full wave rectifier which for short periods will deliver 20 amps. This is usually enough to get the car started, directly it has started of course it can be disconnected from the battery as the car's internal charger will take over. Special offer price of this is **26:50p** including post and VAT.

TERMS: Where order is under \$6 please add 50p surcharge to offset packing



ITS FREE!

Our monthly Advance Advertising Bargains List gives details of bargains arriving or just arrived—often bargains which sell out before our advertisement can appear—its an interesting list and its free—just send S.A.E. Below are a fey of thisBargains still available from

few of the Bargains still available from a recent list. 100 wait Minis Transformer, 240v 50hz primary wound in separate halves, primary screen. This has three secondries 24-0-24v, 0-49v and 0-88v tapped to 80v. Any one of these secondries may be loaded up to the 100 waits or the load can of course be spread over to all the secondries. Upright mounting, impregnated and varnished. Size 4" \times 34" \times 3". Price \$3:50 + \$8p. Post 80b + 60. Size 4" × 80p + 6p.

Size 4" \times 31" \times 3". Price \$3.50 + 28p. Post S0p + 6p. Connector Strip for Push on tags. 7 pole, each pole accepting 6 push on tags on strong plastic base with fixing holes. Price 45p + 4p. Stereo Flex twin acreen fig. 8 type. PVC insulated overall. 10 metres for \$1 + 8p. Latching Relay by Guardian Electric, mains operated it is in fact two relays mounted in such a way to ensure that when one closes the other opens and vice versa thus when closed relay A would remain locked until manually released to electrically released by energiaing relay B. Each relay has 2 sets of 10 ann change-over contacts. Ahould be ideal for burglar alarms and similar applications. Price \$1.35 + 16p. Connecting Wite. 7 Strandel PVC insulated on Spice 54 relations. 6 differ 60 burger alarms and similar applications. Price \$1.95 + 16p. Connecting 41 relations of the state of the state of the please of the alternative volumer whon ordering. Mains Motor—double ended with normal mounting Marmist Chet Price 75p + 6p. Post 30p + 3p.

reet, ideal for small fan heater, etc. Price 759 + 69. Post 30 p + 39. Magnetic Clutch zerox ref. nos. 121 5494 and PN 866-10. This is a two part device, the main section with the coil fits to the spindle of the machine and the break plate only has to be mounted close to the main section. Price \$3.00 + 249. Post 30 p + 39. Photo Multiplier Tube. American RCA, their type no. 4555. These tubes have a gain of a million or more, regular price over £20 but we offer these brand new 1\$4.50 + 369. Push Button Double Changeover Switch. Honey-strong enough to be used as a foot switch. Price 50p + 49.

200 voits, a togin spring return switch definitely strong enough to be used as a foot switch. Price 50p + 4p. Clockwork Time Switch for delaying switch for up to 12 hours. Being clockwork this is inde-pendent of the mains and is therefore ideal for remote places, worked by batterles. Front dial, which is calibrated in hours, is turned through the required degrees together with preset time up to 12 hours. Double pole switch rated 15 amps 30 voits. Price \$3.00 + 2\$p. Delay Switch electrically operated—will give delay of up to 2 hours—contact suitable for 2×10 and papilances. Price \$5p + \$p. Open Type Relay with 10 ann Changeover contacts. Mains operated coil, ex-equipment but perfect. Price \$0p + \$p. Portable Radio Case, size 11" × 8" × 31" mount-ing studs for standard 5" speaker and panel to take the normal three controls. Price \$1.80 + 19p. Post \$1 + 12p.

take the normal three controls. Price \$1.50 + 19p. Post \$1 + 12p. Tuning Dial, \$1s normal slow motion type con-denser suitable for above cabinet. Price \$0p + 6p. Self adhesive scale $L \notin M$, price 25p + 3p. Disco Black Light Lamp. We have just received a new delivery of these 175w ultra violet lamps but regret to say that the price has increased and the new price is now \$6:75 + 86p. Post \$1:00 + 12p. For the benefit of of our readers not knowing this

c1:00 \pm 12p. For the benefit of ot our readers not knowing this hamp we wish to point out that it is probably the most useful ultra violet lamp available especially for disco work as it does not require choke or starter. It can simply be plugged into the nearest lamp holder. It is a true black light and has proper "Woods" filter glass. Ferric Chloride Crystals for etching copper, making printed circuit boards, etc. Special purchase enables us to offer this in 11b bags at 50p + 4p + 20p + 2p. 7 Digit Counter. Another special purchase enables us to offer this mains objerated counter for only about a quarter of its proper price. It works off 240v 50hz mains and requires no step down. There is only one point about this that is it counts in even numbers only, 2, 4, 6, 8, 10, etc. If you want to count single you must divide the final figure by 2. Price 50p + 4p. Post 10p + 1p. 2kw Hester Assembly. Spiral elements in metal frame, size 64' × 24' elements centre tapped so can be used for 1kw or 2kw. Unusual shape, looking at it sideways the frane looks rather like a ship's ventilator. Probably made for some well known fan heater. Price \$1 + 8p. Post 30p + 3p.

30p + 3p. Stereogram Cabinet. Long low modern teak sup + 3p. Siereogram Cabinet. Long low modern teak venereri cabinet size approximately 4' 2" x 15' x 5' probabic cost to make today over #20. We have a few of these, they are slightly second; at prices ranging from ± 5 each, depending on consistion—sorry but these are for callers only interpreterment of the basically a moving coll voltmeter. Instead of having a pointer, however, the each is punched out with numerais, 0, 1, 2, 3, 4 and so on. When you apply a voltage for instance 8 volta-fig. 8 comes in front of a lamp and is focussed on to the screen. This device which must have cost a small fortune to develop and make can also be used for a variety of other purposes. Voltage regulation by making beam of light trigger a photocell, etc., etc. price $\pm 5 + 40p$. Valves—send for your requirements, most types in stock.



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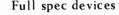
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Practical Electronics May 1977



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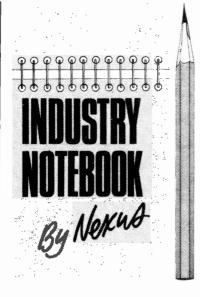
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Sec. Sec. 1

MOVING IN . . .

The President of Fairchild Camera and Instrument Corporation, Wilfred Corrigan, has now got his Stock Exchange listing allowing Fairchild shares to be traded through London. Sales world-wide were up 52 per cent last year to 433'2 million dollars and Corrigan says Fairchild has moved aggressively to obtain funds for future growth.

Reading between the lines, it would seem that he sees the growth coming in Europe and specifically in the United Kingdom where he is looking for take-over opportunities. If Fairchild does start buying in companies then the extra capital will probably be raised in the UK. Fairchild is also reported to be thinking of applying for listings on other major stock exchanges in Europe, as well as in Japan and Hong Kong.

The company is currently strong in digital watches and TV games in the consumer market. The newest Fairchild TV game is actually an entertainment system centred round an F8 microprocessor and deliveries have already started in the USA.

... AND RATIONALISING

With 15,000 semiconductor devices in the product catalogue Motorola has decided to call a halt, at least in Europe. After conducting an analysis of all applications and who uses what for which purpose, Motorola's back room boys in Geneva have concluded that Europe ought to be able to manage with only 4,000 devices to choose from. These have now been grouped in a new catalogue "The European Selection'' expected to cover at least 90 per cent of European requirements.

The other 11,000 devices will still be available but may be harder to obtain as distributors may no longer stock them, and prices may go up to discourage their use. On the other hand, distribution costs will go down on the preferred range of devices and although there is no mention of price reduction the move clearly will help keep Motorola in a strongly competitive position, delaying price rises that otherwise might be needed.

COMPONENT BOOST

The British electronic components industry is one of five industry sectors selected for special government assistance in the industrial strategy programme. A figure of £20 million is suggested as the sort of support expected to be made available. Brave plans for a brave future!

This new initiative is a re-run of a similar initiative of ten years ago. Only the names have changed. In the old days the slogan was "the white heat of technological revolution" and its instrument was the Ministry of Technology. Today the slogan is "the regeneration of British industry" and its focal point is the NEDC. The only real change is that today the trade unions have an equal say in the strategy, more than an equal say according to some commentators. Even the amounts of money involved, allowing for inflation, are practically the same.

These endlessly sitting industrial strategy committees are no substitute for action. In fact they are mostly counter-productive. Who in their right minds will use their own money today when government money will be available tomorrow? So those who hope to gain from the scheme defer their investment plans until more details are available, the applications for aid submitted, further committee meetings held, the recommendations of the civil servants put forward, the Minister's and Union's final approval given, the ordering of new equipment, the arguments at shop floor level over new manning agreements, the negotiation of new rates of pay. Let's be optimistic and say that this tortuous process takes only two years. By that time, in a volatile industry like electronics, the market has changed and the opportunity missed. And like all forms of heavily institutionalised charity the administrative costs eat deeply into the capital sum.

How much simpler and faster to give the money in the form of tax relief in general and an additional relief on profits from exports, thus assisting all firms equally. As it is. only firms with schemes costing £50,000 or more will qualify for aid, which eliminates many worthy enterprises. It is also uncertain what the attitude of the Government and Unions will be towards aid for foreign-owned multinationals in Britain which, between them, form a very large part of the components manufacturing industry, especially semiconductors.

All signs point to a stiffening of resistance by well-run companies against Government meddling. A number of public companies are thinking of re-forming themselves into private companies to obtain more freedom of action, and I have already heard of contingency plans being made by some businessmen to move overseas should the Bullock Report be implemented in its present form by the Government.

WINNERS

Despite all the difficulties there is still ample vigour in the industry. Hewlett-Packard Ltd., the UK arm of H-P has not only turned in its best results ever but is the most profitable of all H-P's 27 manufacturing plants round the world. The Scottish plant is the world design centre for communications instrumentation with 80 per cent of production exported to 65 countries. Export sales are up 49 per cent, home sales up 32 per cent in a turnover of £36 million and pretax profit was nearly £6 million.

The electronics content of record aerospace exports (£904 million in 1976) has risen dramatically. The category of airborne radio, navigation and radar aids exported shot up to over £15 million and instruments to almost £40 million. Plessey Electronic Systems has an export order book of over £130 million. The BAC Rapier Missile System export order book has now topped £600 million. The add-on radar units for Rapier are built by Marconi and feed millions into the electronics industry.

If only all manufacturing industry did as well, or is that asking too much?

LIKE JEWELLERY --- BUT DEARER

I have often commented on semiconductor prices tumbling so that simple items like transistors and diodes are two a penny—or almost. But move up to the quality end of the market and look at the prices. I see from H-P some new Schottky diodes at £17.89 each in lots of 10-100 and if you want a lownoise microwave transistor you can pay as much as £97.56 each in lots of 1-10. But you do get titanium/platinum/gold metallising thrown in.



ENERGY CONVERTER BP 1 458 702

In BP 1 458 702, Siegfried Reinhold Lehr of Munich claims a miniature device for converting mechanical to electrical energy. Although this is intended for implant into the human body it may well have other applications.

The claimed object is to provide a converter small enough to be implanted into a blood vessel or heart muscle which will produce enough power to trigger a heart pacemaker and so makes the additional implant of nuclear or storage batteries unnecessary.

The patent shows a small cobaltsamarium magnet and a soft magnetic core with yokes. An armature completes the magnetic circuit and the generator coil is wound round the soft magnetic core. The armature is secured to a spring steel membrane surrounded by a corrugated support.

The spring and corrugation force constant are balanced with respect to the magnetic force, so that when the ambient pressure around the device increases, for instance due to contraction of a muscle in which it is implanted, the membrane and armature move closer to the yokes and snap-latch. This stores energy in the corrugations. When the ambient pressure falls, as for instance when the muscle relaxes, the spring force exceeds the magnetic force and the membrane jumps back into its rest position, inducing an e.m.f. in the coil as it does so.

It is claimed that because most of the energy available from the pressure change is converted instantly when the armature jumps, simple diode rectification, with half the voltage drop compared to full wave rectification, can be employed. The rectified output may be fed direct to a pacemaker, which actuates a stimulating tip via another core of the same cable which connects the generator to the pacemaker.

SURROUND SOUND

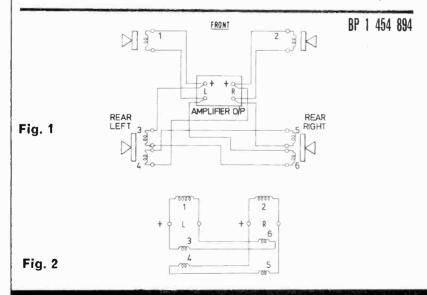
Two patents, BP 1 348 643 (taken out a few years ago) and more recently BP 1 454 894, for an interestingly different approach to the derivation of 4-channel surround sound from a 2-channel stereo amplifier are held by EMI.

The conventional approach is to use an extra pair of loudspeakers connected in so-called Hafler fashion, to reproduce the difference signal between left and right at the rear. For the Hafler system, the extra rear loudspeakers are of conventional type; the EMI proposal is to use modified loudspeakers at the rear.

Each of the two extra rear loudspeakers has not one but two voice coils, operable on the same cone. The two coils of each loudspeaker are dissimilar in impedance and are wound in opposite sense. The coil windings and their connection to a stereo amplifier are shown in detail in Fig. 1 and in simplified equivalent form in Fig. 2.

Voice coils 3 and 6 are connected in series to the left channel output of the stereo amplifier and coils 4 and 5 in series to the right channel output. Coils 3 and 5 have 5 ohm impedance and coils 4 and 6 have 11 ohm, to give an impedance ratio of 2.2:1. Coil 3 operates in opposite sense to coil 4, and coil 5 operates in opposite sense to coil 6.

As a result of this combination. rear right loudspeaker reproduces sound representing a proportion of the left channel output subtracted from a proportion of the right channel output; rear left loudspeaker reproduces sound representing a proportion of the right channel output subtracted from a proportion of the left channel output. The front left and front right outputs are unaffected.



IN BRIEF

BP 1 451 090—Nippon Gakki Seizo KK: Audio Power Amplifier, Circuit for an audio amplifier, including a power stage and a driver stage, with the driver stage bias switch-able between three settings, to provide a choice of A. B. or AB class operation.

BP 1 456 541—Greenwood Mills: *Colour Measuring Spectrophotometer*. A digital spectrophotometer intended to enable the colour of a cloth or material to be scientifically analysed and recorded.

Several different photosensitive "eyes" are used, each with a different sensitivity to different colours. The outputs of their devices are registered and a definitive analysis of the object's colour derived from a comparison.

Copies of Patents can be obtained from the Patent Office Sales, St. Mary Cray, Orpington, Kent. Price 95p each

The HY5 is a mono hybrid amplifier ideally suited for all applications. All common input functions (mag Cartridge, tuner, etc.) are catered for internally, the desired function is achieved either by a multi-way switch or direct connection to the appropriate pins. The internal volume and tone circuits merely require connecting to external potentiometers (not included). The HY5 is compatible with all I.L.P. power amplifiers and power supplies. To ease construction and mounting a P.C. connector is supplied with each pre-amplifier.

TEATURES: complete pre-amplifier in single pack: multi-function equalisation, low noise, low distortion, high overload, two simply combined for stereo. APPLICATIONS: hi-fi, mixers, disco: guitar and organ; public address. SPECIFICATION: inputs-magnetic pick-up 3mV; ceramic pick-up 30mV, tuner 100mV, microphone 10mV, auxiliary 3-100mV; input impedance 47kA at 1kHz. Outputs-tape 100mV, main output 500mV t. R.S. Active Tone Controls-treble ±12dB at 10kHz; bass ±12dB at 100Hz. Distortion-0 1% at 1kHz, signal/noise ratio 68dB. Overload--38dB on magnetic pick-up. Supply Voltage-±16-50V Price £5.22 + 65p VAT. P. & P. free

HY5 mounting board B.1. 48p + 6p VAT. P. & P. free

The HY30 is an exciting New kit from I.L.P. It features a virtually indestructible I.C. with short circuit and thermal protection. The kit consists of I.C., heatsink, P.C. board, 4 resistors, 6 capacitors, mounting kit, together with easy to follow construction and operating instructions. This amplifier is ideally suited to the beginner in audio who wishes to use the most up to date technology available.

Exatures: complete kill, low distortion, short, open and thermal protection; easy to build APPLICATIONS: updating sudio equipment, guitar practice amplifier, test amplifier, audio oscillator SPECIFICATION: Output Power—15W R.M.S. into 8 Ω Distortion—0 1% at 15W. Input Sensitivity— 500mV. Frequency Response—10Hz-16KHz = 3dB.

Price £5.22 + 65p VAT. P. & P. free

15-24

HY5

Preamplifier

HY30

HY50

25W into 8Ω

HY120

60W into 8Ω

HY200

120W into 8Ω

HY400

240W into 4Ω

15W into 8Ω

The HY50 leads I.L.P.'s total integration approach to power amplifier design. The amplifier features an integral heatsink together with the simplicity of no external components. During the past three years the amplifier has been refined to the extent that it must be one of the most reliable and robust High Fidelity modules in the World. FEATURES: low distortion, integral heatsink; only five connections, 7 amp output transistors; no

Price £6-82 + 85p VAT. P. & P. free

The HY120 is the baby of I.L.P.'s new high power range, designed to meet the most exacting requirements including load line and thermal protection this amplifier sets a new standard in modular design.

EATURES: very low distortion. Integral heatsink: load line protection: thermal protection, five connections, no external components. APPLICATIONS: hi-fi, high quality disco, public address: monitor amplifier; guitar and organ. SPECIFICATION: hi-fi, high quality disco, public address: monitor amplifier; guitar and organ. SPECIFICATION: hi-fi, high quality disco, public address: monitor amplifier; guitar and organ. SPECIFICATION: hi-fi, high quality disco, public address: monitor amplifier; guitar and organ. SPECIFICATION: hi-fi, high quality disco, public address: monitor amplifier; guitar and organ. SPECIFICATION: hi-fi, high quality disco, public address: monitor amplifier; guitar and organ. SPECIFICATION: hi-fi, high quality disco, public address: monitor amplifier; guitar and organ. SPECIFICATION: hi-fi, high quality disco, public address: monitor amplifier; guitar and organ. SPECIFICATION: hi-fi, high quality disco, public address: monitor amplifier; guitar and organ. SPECIFICATION: hi-fi, high quality disco, public address: monitor amplifier; guitar and organ. SPECIFICATION: hi-fi, high quality disco, public address: monitor amplifier; guitar and organ. SPECIFICATION: hi-fi, high quality disco, public address: monitor amplifier; guitar and organ. SPECIFICATION: hi-fi, high quality disco, public address: monitor amplifier; guitar and organ. SPECIFICATION: hi-fi, high quality disco, public address: monitor amplifier; guitar and organ. SPECIFICATION: hi-fi, high quality disco, public address: monitor amplifier; guitar and organ. SPECIFICATION: hi-fi, high quality disconse disco

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The HY200 (now improved to give an output of 120 watts) has been designed to stand the most rugged conditions such as disco or group while still retaining true hi-fi performance.

FEATURES: thermal shutdown very low distortion load line protection; integral heatsink; no external components

The HY400 is I.L.P.'s "Big Daddy" of the range producing 240W into $4\Omega^{\dagger}$ It has been

designed for high power disco or public address applications. If the amplifier is to be used at continuous high power levels a cooling fan is recommended. The amplifier includes all the qualities of the rest of the family to lead the market as a true high

FEATURES: thermal shutdown; very low distortion; load line protection, no external components. APPLICATIONS: public address, disco, power slave, industrial. SPECIFICATION: Output Power-240W R.M.S. into 4Ω, Load Impedance-4-16Ω, Distortion--0, 1%

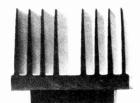
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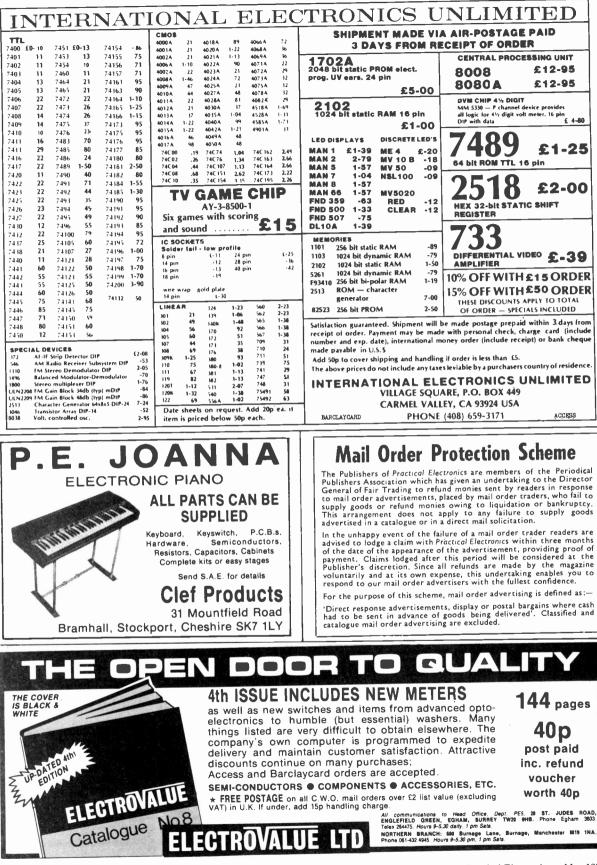
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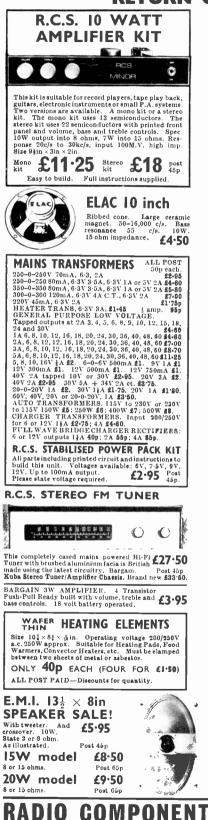
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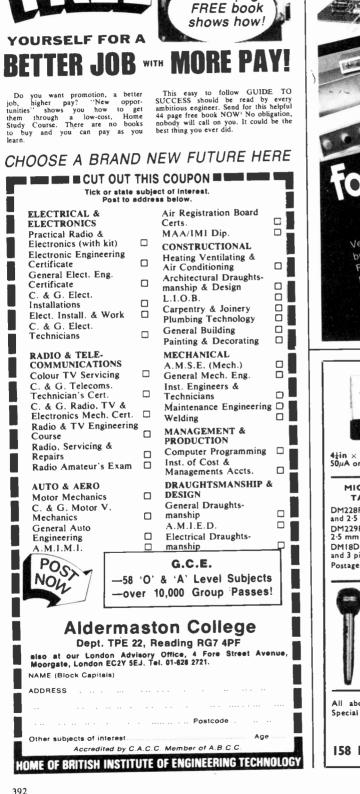




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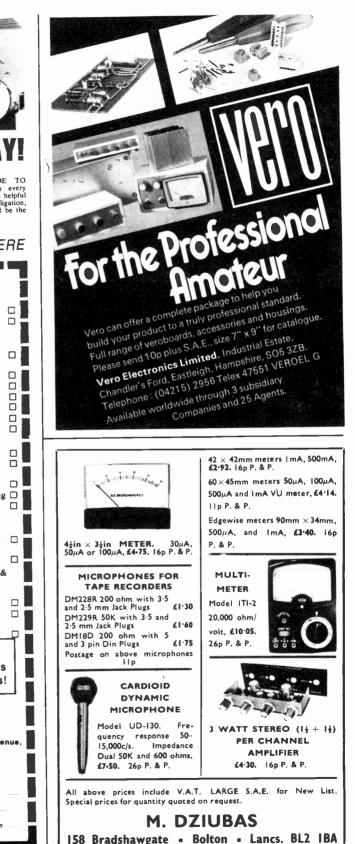
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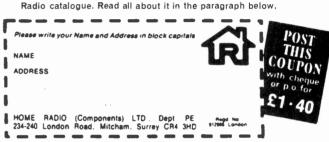
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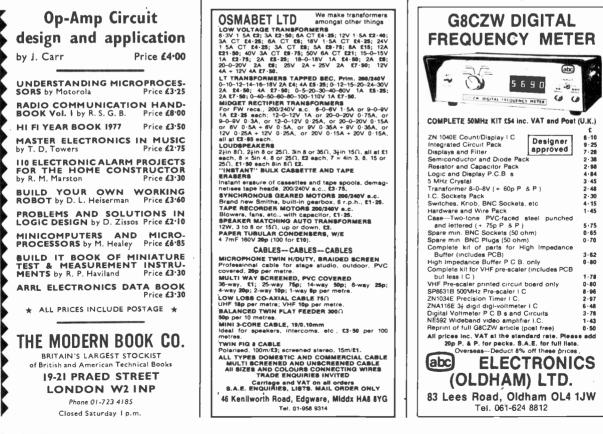
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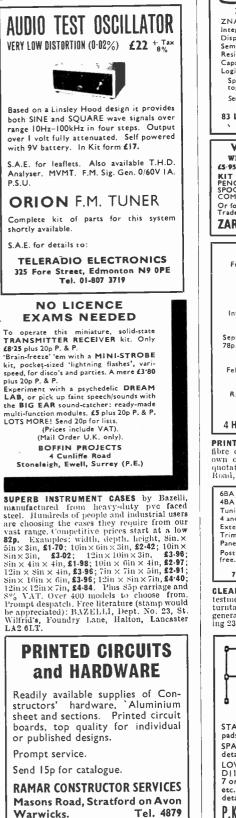
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7427 40p 74150 155p 7430 18p 74151 77p	4046 150p LM389N 175p		PF104/5 40p	04/02/40 48m	125p 1A 400V TO5 56p by Texas
7432 340 74153 920	4047 110p M252 850p 4049 660 MC1310P 200p		PSA06 37p	001000414 99m	Pop 3A 400V Stud Inp 1 and 10
7437 37p 74154 164p	4049 68p MC1310P 200p 4050 54p MC1351P 104p			CRC/04	164 600V Diastia 240a 14 pm 12a
7438 37p 74155 97p 7440 18p 74155 97p	4055 120p MC1495 370p		PSA12 61p	400/3	
7440 18p 74156 97p 7441 85p 74157 96p	4055 140p MC1496 115p		PSA56 37p	2N2906/A 24p	C106D 4A 400V Plastic 64p I to pill
7442 75p 74158 160p	4056 145p MC3340P 180p		PSU06 78p	2N2926RB PP TRIACS	MCR101 0 5A 15V TO92 30p 18 pin 32p
7443 130p 74159 220p	4060 120p MFC4000B 90p 4069 40p NF540L 175m	BC184 14p M	PSU56 98p	2N2926OYG Plastic	2N3525 5A 400V TO66 130p 24 pin 54p 2N4444 8A 600V Plastic 200p 28 pin 60p
7444 130p 74160 129p	4069 40p NE540L 175p 4071 29p NE555 40p	BC187 32p O	C28 79p	11p	2NE060 0 84 201/ TO02 34- 00 000 1
7445 108p 74161 120p 7446 108p 74162 120p	4072 29p NE556 90p	BC212 14p 00	C35/36 79p	2N3053 29p Amp Volts	2N5064 0 8A 200V TO92 45p. 40 pin /5p
7446 1060 74162 120p 7447 90p 74163 120p	4081 25p NE561 425p	BC213 120 00	C71 25p	FLIDOOD AND TO THE	85p BRIDGE 6A 100V 100p 1N914 4p
7448 90p 74164 130p	4082 29p NE562 425p 4093 95p NE585 200p	BC214 180 R	2006B 225p		107p 011100E 64 400V 190p 1N4001 8p
7450 20p 74165 150p	4510 142p NE566 200p		2010B 2250	2N3442 151p 6 500	130p HEUII- 1N4002 6p
7451 20p 74166 138p 7453 20p 74167 340p	4511 160p NE567 200p		P29A 50p	2N3702/3 14p 10 400	150p TIERO
7454 20p 74107 250p	4516 140p 2567 400p		P29C 62p	2N3704/5 14p 10 500	170p 1A 50V 25p BY100 35p 1N4007 8p 1A 100V 27p BY126 12p 1N4148 4p
7460 20p 74173 160p	4518 140p SG3402N 275p 4528 130p SN72710N 54p		P30A 60p	2N3706/7 14p 15 400	200p 1A 400V 30p BY127 12p 1N5401 15p
7470 32p 74174 130p 7472 32p 74175 92p	4553 575p SN72733N 156p		P30C 72p		220p 1A 600V 35p OA47 8p 1N5404 20p
7472 32p 74175 92p 7473 36p 74176 130p	SN76003N 275p		P31A 58p	2N3773 3200 40430	130p 2A 50V 35p OA81 15p ZENERS
7474 37p 74177 130p	SN76008 275p				130p 2A 100V 40p OA85 15p 2 EIVERS 130p 2A 200V 46p OA90 7p 400mW 11p
7475 48p 74180 118p	MEMORY SN76013N 175p		P31C 68p	2N3820 50p	3A 600V 70p OA91 8p 1W 22p
7476 37p 74181 324p 7480 54p 74182 88p	RAM SN76018 275p RAM SN76023N 175p		IP32A \$3p		4A 100V 900 OA95 90 OTHER
7480 54p 74182 88p 7481 108p 74185 144p	2102 270p SN76033N 275p		IP32C 85p		4A 400V 96p 0A200 9p
7482 85p 74185 144p	2112 450p SN76660N 85p		IP33A 97p	2N3866 95p BR100	32p 6A 50V 96p OA202 10p Z5J 125p
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INDEX TO ADVERTISERS

A.B.C. Electronics	395,	398
A.B.C. Electronics Adam Hall (P.E. Supplies) Alben Engineering Alcon Instruments Ltd. Alcorn, I. E. Astro Electronics		398
Alben Engineering		336
Alcon Instruments Ltd	322	358
Alcorn E	OLLI	307
Alcorn, I. E. Astro Electronics Automated Homes Bamber, B., Electronics Bandridge Ltd. Barclay Electronics Berchay Electronics Beecham Pharmaceuticals Bib Hi-Fi Accessories Ltd. Bi-Pak Bi-Pak Birkett, J. Boffin Projects Brewster, S. & R., Ltd. British Institute of Engineering Technology		304
Astro Electronics		209
Automateu Homes	• • •	350
Bamber, B., Electronics	050	330
Bandridge Ltd.	. 353,	354
Barclay Electronics	323,	398
Barrie Electronics		326
Beecham Pharmaceuticals		399
Bib Hi-Fi Accessories Ltd.		324
Bi-Pak	334,	335
Bi-Pre-Pak	cov	erii
Birkett J		384
Boffin Projects		398
Brewster S & B Ltd		322
British Institute of Engineering		
Birkett, J. Boffin Projects Boffin Projects Brewster, S. & R., Ltd. British Institute of Engineering Technology British National Radio & Electronics School Bull, J. (Electrical) Ltd. Cambridge Learning Chiltmeed Ltd		202
Pritich National Padia & Electronica		335
Shish National Hadio & Electronics		220
School	• •	329
Bull, J. (Electrical) Ltd.		383
Cambridge Learning		331
Chiltmead Ltd.		400
Clef Products		390
Components Centre, The		384
Copper Supplies		398
Crescent Radio		330
C. B. Supply Co.		396
Davian Electronics		326
DEWIlds		397
Doram	326	358
Drivbon M	520,	300
Eagle Internetional		220
Eagle International		220
Eaton Audio		324
School Bull, J. (Electrical) Ltd. Cambridge Learning Chiltmead Ltd. Cilef Products Copper Supplies Crescent Radio C. R. Supply Co. Davian Electronics D.E.W. Ltd. Doram Dziubas, M. Eagle International Electronic Design Assoc. Electrospares Electrospares Electrospares Electrospares Electrospares Electrospares Electrospares Electrospares Government Communications		398
Electronic Design Assoc.		328
Electrospares		336
Electrovalue		. 390
Gainsborough College of Further		•
Education Government Communications		399
Government Communications		
Headquarters		399
Greenbank Electronics		328
Greenweld Electronics		322
G R Electronics		333
Greenweid Electronics		370
H.M. Electronics		308
	• • • •	. 330

Home Radio I.L.P. Electronics Ltd. Industrial RF Services International Electronics Unlimited Intertext ICS Island Devices Jones, J. C. Josty Ktt (U.K.) Ltd.	
1 L.P. Electronics Ltd.	
Industrial BE Services	396
International Electronics Unlimited	390
International Electronics offinities	394 397
Intertext 100	306
	306 307
Jones, J. C	330, 337
Josty Kti (U.K.) Ltd. J.W.B. Radio Linway Electronics London Electronics College Lynx Electronics Magnum Publications Magnin Electronic Supplies	306
J.W.B. Radio	
Linway Electronics	. 397
London Electronics College	397
Lynx Electronics	380
Magnum Publications	397
Maplin Electronic Supplies	cover iv
Marshall, A., & Sons	
Metac	. 324
Micronics Electronics Eng. Services	. 397
Minikits Electronics	397
Modern Book Co.	
Osmahet	395
P A Electronics	397
Phonosonics	332 333
PKG Electronics	308
Presiden Potito Ltd	389
Precision Ferne Liu	
Marshall, A., & Sons Metac Micronics Electronics Eng. Services Minikits Electronics Osmabet P.A. Electronics P.K.G. Electronics P.K.G. Electronics Precision Petite Ltd Proto Design Pulse Electronics Radio Component Specialists Radio Exchange Ltd Ramar Constructor Services RST Valve Mail Order Co. R.T. Services Saxon Entertainments	
Pulse Electronics	303
Hadio Component Specialists	. 391
Hadio Exchange Ltd.	
Hamar Constructor Services	
RST Valve Mail Order Co.	330
R.T. Services	396
Saxon Entertainments	357
Scientific Wire Co.	398
Service Trading Co.	.cover iii
Stevenson, C. N	. 396
Sugden, A. R., & Co. Ltd.	. 336
Swanley Electronics	
R.T. Services Saxon Entertainments Scientific Wire Co. Service Trading Co. Stevenson, C. N. Sugden, A. R., & Co. Ltd. Swanley Electronics Tamba Electronics Technomatic Ltd. Teleradio Electronics Tempus Trampus Electronics Ltd. T.U.A.C.	388
Technomatic Ltd.	. 400
Teleradio Electronics	. 398
Tempus	394
Tramous Electronics Ltd	384
T U A C.	
T.U.A.C. Vero Electronics Ltd.	392
West London District Supplies	388
West London District Supplies	307
Veues Electropics	330
Young Electronics	
Zaruomx	



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