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*Please send me....set(s) of Design of Digital Systems at $\$ 7 \cdot 00$ each. p \& $p$ included
*Or . set(s) of Digital Computer Logic and Electronics at $55 \cdot 00$ each. $p \& p$ included
*or . .combined set(s) at $£ 10.50$ each. $p \& p$ included
Name
Address
-delete as applicable
No need to use a stamp-just print FREEPOST on the envelope. PE5

## NEWS FROM JOSTY MTMO

## JOSTYKIT-a product from Denmark

HF 61-2 DIODE MEDIUM WAVE RECEIVER


By means of a very simple technique reasonable reception ts attained HF 61-2 is built on asmall circuit oard of the same size as the general purpose amplifier AF 380. The two assemblies should be connected to produce power for a loudspenker HF 61-2 is especially useful for eginnors, who have not tried to essemble electronic kits before.
£4.30
HF 305 VHF RADIO-CONVERTER
Extend the range of your transstor
radio. Listen to Amateurs (2
metre band), Arcraft,
Trawsistor circuit with pinted circuit coils varactor
diodes and
uperior circuit desion. Converts adio
signals in the $100-200 \mathrm{MHz}$
range to output signse at
WHF receiver and you're in
new dimension.

## AT 365 3-CHANNEL DISCO LIGHT



A now concept in psychodelic lighting. Uses bultt-in microphone. Avoids awkward connections to amplifiert. Position light-show to best advantage without long trailing leads-just plug in to nearest power point. Circuit combines latest integrated circult tech-
niques with solid-state power control. Quad op. amp. makes selection of bass, midrange and treble frequencies easy. Three thyriators (SCRs) control three separate lampbanks. Kit includes fused dc
power supply and FET zero light adjustment. WARNING
Only experienced persons should attempt the intercon- 117.00 nection of mains equipment.

## HF 385-2 VHF/UHF AERIAL AMPLIFIER

A quality, printed circuit, no trimming, aerial amplifier. Fantastic frequency range due to use of printed coils. 21dB amplification at 400 MHz . Two separate inputs tor UHF and VHF. No loss of signal or intercommunication problems


NT 410 AERIAL AMPLIFIER CURRENT SUPPLY


NT 410 is a current supply, speclally built for aerial mplifiers, such as HF 385-2, but can also be used for other aerial amplifiers. NT 410 is supplied with input and output clamps tor 75 ohm or 50 ohm aerial cablet. It ls therefore not necessary to solder -just cut and strip the aerial cable and attach the Nial Thelifier to the from the aerial amplifier to the receiver passes withour complicallifier and the current to the aerial amolifier passes through the same together with HF 395 and HF 385-2.
£4.50


MAIL ORDER DIVISION
P.O. BOX 68, MIDDLESBROUGH,

CLEVELAND, ENGLAND B1 5CO
SEND FOR OUR FREE CATALOGUE

## SYNTHESISER AND SOUND EFFECT KITS

## PHONOSONICS

MAIL ORDER SUPPLIERS OF QUALITY PRINTED CIRCUIT BOARDS，KITS AND COMPONENTS TO A WORLD－WIDE MARKET．

COMPONENTS SETS include all necessary esistors，capacitors．semiconductors potentio－ meters and transformets Hardware such as cases sockets，knobs．etc are not included but most of these may be bought separately Fuller details of kuts PCBs and parts are shown in ou lists
CIRCUIT AND LAYOUT OIAGRAMS are supplied ree with all PCBs designed by Phonosonics

HOTOCOPIES of the PE lexts for most of the kits are available－prices in our lists


P．E．SYNTHESISER（P．E．Feb． 73 to Feb． 74
The well acclaimed and highly versatile large－scale mains－operated Sound Synthesiser complete with with the Synthesiser to good advantage．notably P．E Minisonic．Phasing Unit．Wind and Rain，Rhythm Generator．Sound Bender．Voltage Controlled Filter． Guitar Effects Pedal and Overdrive．Fuzz．Tremolo and Wah－Wah units．
The Maln Synthesiser：PSU． 2 linear VCOs． 2 ramp oenerators． 2 input amps sample hold noise generator reverb amp．ring modulator，peak tevel cricuit envelope shaper．voltage controlled amp Full details in lists

Set of basic component kits
883.03
$\$ 11.45$

The Synthesiser Keyboard Circults（can be used without the Main Synthesiser to make an independent musical modulation amps．mixer． 2 envelope shapers and additional PSU．Full details in our lists

Set of basic component kits
Set of printed circuit boards
548.18
57.66

P．E．MINISONIC Mk． 1 SYNTHESISER（P．E．Nov． 74 to Mar． 75
A portable，battery or mains－operated Miniature Sound Synthesiser．with keyboard circuits Although having slightly fewer facilities than the large P E Synthesiser the
functions offered by this design give it great scope and versatility Consists of 2 log VCOs．VCF． 2 envelope shapers． 2 voltage controlled amps．keyboard hold and control circuits．HF oscillator and detector．ring modulator，noise generator．output amp and mixer． temperature stabiliser，power supply
Set of printed circuit boards．Whale stocks $\mathbf{\text { S } 4 . 5 1}$

## P．E．MINISONIC MK． 2

A more sophisticated version of the Mk． 1
$\begin{array}{ll}\text { Set of basic component kits } & \text { from } \mathbf{5} 54.25 \\ \text { Set of printed curcuit boards } & 89.71\end{array}$
ELEKTOR＂FORMANT＂SYNTHESISER（Elektor Magazine 1977）

## Details of component kits and PCBs are in our lists

GUITAR EFFECTS PEDAL（P．E．July 75）
Modulates the attack．decay and filter characteristics of an audio signal not only from a guitar but from any audio be further modified by manual controls Possibly the mos interesting of all the low－priced sound effects units in our range．Circuit does not duplicate effects from the Gutar
Overdrive Unit
Component
switches
Alternative
Alternative component set with panel mounting
switches
Printed circuit board

SOUND BENDER（P．E．May 74）
A multi－purpose sound controller．the functions of which nclude envelope shaper．tremolo，voice－operated tade
automatic fader and frequency－double
Component set for above functions（excl SWs）$\quad \mathbf{~} 7.84$ Printed circuit board
Optional extra－additional Audio Modulator，the use o which．in conjunction with the above component set．can Component set（incl PCB）

PHASING UNIT（P．E．Sept．73）
A simple but effective manualiy controlled unit for introducing the phasing sound into live or recorded music．
Component set（incl PCB）
PHASING CONTROL UNIT（P．E．Oct．74）
For use with the above Phasing Unit to automatically Component set（incl PCB）

WAH－WAH UNIT（P．E Apr 76）
The Wah－Wah effect produced by this unit can be controlied manually or by the integral automatic controller
Component set（Incl PCB）
［3． 55

## POST AND HANDLING

U．K orders－under $£ 15$ add $25 \rho$ plus VAT．over $£ 15$ add $50 p$ plus VAT．Keyboards $\mathbb{} 1$ ． 50 plus VAT
Optional Insurance for compensation against loss or damape in post．add 35p in addition to above post and handling．
Erre．C1．BFPO．and other countries are subject to Export postage rates

## AUTOWAH UNIT（P E Mar 77）

Automatically produces Wah－pedal and Swell－pedal sounds each time a new note is played

Component set and PCB．with spectal foo
switches
Component set and PCB，with panel switches
£7．27
P．E．JOANNA（P．E．May／Sept．75）
A five－octave electronic plano that has switchable alternative voicing of Honky－Tonk plano．ordinary pano harpsichord．or a mixiure of any of the three．together with facilities including fast and slow tremolo．loud and sof pedal switching．and sustain pedal switching．The powe amplifier typically delivers 24 watts into 8 ohms The PCB have been redesigned by ourselves making improved us Main power avaply
oicing and prep．Tone Denerator． 61 envelope shapers
so a pon

Set of basic component kits for above Sel of printed circutt boards for above Power amplifier

RHYTHM GENERATOR（P．E．Mar Apr．74）
Programmable for 64,000 ehythm patterns from 0 eftect circuits（high and low bass and pare drums 8 effect short brushes．blocks and soft cymbal）and with varieb time signatures and rhythm rates．Reelly fascinating and useful．
Tempo．Timing．Logic． 8 Effects circults PSU Set of basic component kits for above $\$ 36.14$
57.03

> SEE OUR OTHER ADVERT FOR KEYBOAROS, OUR LISTS FOR OTHER COMPONENTS AND ACCESSORIES STOCKED-ALSO SOME NEW KITSI

REVERBERATION UNIT（P．W．Nov Dec．72）
A high quality unit having microphone and line inpu pro－amps．and providing full contral over reverberation evel

Component set（excl．spring unit）
c9． 73 printed circuit
gin spring unit
9inspring unit
Panel meter $(50 \mu \mathrm{~A})$（optronal）

## WIND AND RAIN UNIT

A manually controlled unit for producing the above－named
Component set（incl PCB）
GUITAR OVERDRIVE UNIT（P．E．Aug．76）
Sophisticated．versatile Fuzz unit，including variable and switchable controls affecting the fuzz quality whilst taining the attack and decay，and also providing filtering can be used with it and with other elecironic can be with it and with other electronic sumpons
$\begin{array}{ll}\text { Component set using dual slider pot } & \text { \＄6．35 } \\ \text { Component set using dual rotary pot } & \mathbf{5 6 \cdot 2 0}\end{array}$ Printed circuit board
$\mathbf{2 6} \cdot 26$
$\mathbf{5 1 . 2 0}$
$\mathbf{5 1 . 6 2}$

## FUZZ UNIT

Simple Fuzz unif based upon PE Sound Design Com

Component set（incl PCB）
02.03

## TREMOLO UNIT

Based upon PE Sound Design circuit Component set（incl PCB）
TREBLE BOOST UNIT（P．E．Apr 76
Gives a much shrilter quality to audio signals fed through The depth of boost is manually adjusiable

## ENVELOPE SHAPERS

Both of the kits below have manual control over their Attack．Decay．Sustain and Release functions Both kits Enve PCB（VCA means Voltage Controlled Amplifier）


LIST－Send Stamped Addressed Envelope with all UK
requests for free list giving fuller detalls of PCBs．kits and requests for free lis
other components OVERSEAS enquiries for list send $40 p$

## DON＇T FORGET VAT！

Add 12；（or current rate il changed）to tull total of goods．

## VOICE OPERATED FADER（P．E．Dec．73）

For automalically reducing music volume during tak－over－－particularly useful for Disco work or
Component sel（inct PCB） \＄3． 97
VOLTAGE CONTROLLED FILTER（P．E Oct．74）
An independently designed VCF that can be used with the E Synthesiser
Printed cricuit board
$\$ 3 \cdot 80$
$\$ 1.38$
SOUND－TO－LIGHT（P．E．Aurora）（P E．Apr．－Aug．71）
Four channels each responding to a different sound frequency and controlling its own light Can be used with most audio systems and lamp intensities Basic component set（excl thyristors）
Printed circuit board tor above
Power supply
PCB for power supply
515.92
53.90
53.90
65.78

3－CHANNEL SOUND－TO－LIGHT（P．E．Apr 76
A simple but effective sound－to－light controller capable of operating 3 lamps each of approximately 700 watts ncludes power supply，thyristors．and by－pass switches
Component set（incl PCB）
£11－95

## DISCOSTROBE（P．E．Nov．76）

4 －channel light－show controller giving a chotce of
sequential，random，or full strobe mode of operation
Basic component set
$£ 18.19$
53.45
P．E．TUNING FORK（P E Nov．75）
Produces 84 swith－selected frequency－accurate tones．An
Produces 84 swith－selected frequency－accurate tones．An
LED monitor clearly displays all beat note adjustments ideal for tuning acoustic and electronic musical instruments alike musical Main component set（incl PCB）
$\begin{array}{r}15.59 \\ \\ \hline 7.83\end{array}$

## P．E．SYNCHRONOME（P．E．Mar．76）

An accented－beat electronic metronome providing duple iriple and quadruple fimes with full control over the bea aie Can also be used as a simple drum－beat rhythm
generator Includes power supply Component set（incl loudspeaker）
Printed circull board
111.62
52.04

PEAK LEVEL INDICATOR（PE．Mar．76）
A twin－channel visual display unit for monitoring the peak
level of audio signals Well suited for use when level of audio signals Well suited for use when inter－coupling our many sound producing kits to help avord signal over－loading
Component set（incl PCB）（as published）
c3．8新

## BIOLOGICAL AMPLIFIER（P E Jan．Feb．73）

Multi－function circuits that，with the use of other external equipment．can serve as lie－detector alphaphone ardiophone etc
Pre－Amp Module Component set（incl PCB） 54.22
Basic Output Circup
sel with PCBs．for alphaphone cardiophone
Trequency meter and visual feed－back lamp－
driver circuits

## TAPE NOISE LIMITER

Very effective cricuit for reducing the hiss found in most ape recordings All kits include PCBs
Standard tolerance set of components
Superior tolerance set of components
12.96

Superior tolerance set of components
Regulated power supply（will dive 2 sets）
$\mathbf{8 2} .76$
$\mathbf{~} 4.69$
SEMI CONDUCTOR TESTER（P E．Oct．73）
Essential test equipment lor the enterprising home onstructor While stocks last
potentiometers，makaswitches and PCB Panel meter $(500 \mu \mathrm{~A})$

## 8－INPUT MIXER

A simple mixer having 8 inputs each of which has a preset evel control and which are combinad into one output channel having a preset Over－all level control and a master output volume control Designed for inter－coupling our Component set（incl PCB）
¢3．95

## Prices are correct at time of press．E．\＆O．E．dellivery abject to avallability subject to avallabillty．

EXPORT ORDERS are welcome，though we advise that a current copy of our list should be obtained before ordering as 11 also shows Export postage rates All payments mus e cash－with－order．in Sterling and preferably by obtain IIst send 40 p Order or through an English Bank To

\section*{The Finest

The S．K．A．Plastic Keyboard was developed by Kimber Allen Ltd．In co－operation with a Swedish company and the manufacturers state that in their opinion it is the finest moulded plastic keyboard made and is not to be confused with cheaper keyboards available

The keys are moulded in Acrylic plastic，a material chosen for its hard wearing properties and ideal feel to the touch．They are moulded in two parts，the key face，which has to be perfect in appearance and finish，and the action，which has to be strong and carry the mechanism．The strong section of aluminium extrusion upon which they are mounted is specially designed to take all the pressures of playing．Springs，felts，and contact actuators are supplied ready－fitted

The contact assemblies are constructed of laminated bakelite．thus giving smooth stot walls and completely free movement of the gold－clad contact wires． Types avalable as follows（Contact pairs normally open）

| GJ－SPCO | $24 p$ each | GH 5 pairs | 57p each |
| :--- | :--- | :--- | :--- |
| GB－2 pairs | 27 p each | $4 P S-S P C O$ and 3 pairs | $53 p$ each |
| GC－3 pars | $36 p$ each | Palladium Wire Bus Bars． 1 octave |  |
| GE－4 pairs | $45 p$ each | lengths 50 e each |  |

SEE OUR OTHER ADVERT FOR SYNTHESISER AND SOUND EFFECT KITS AND SEE OUR LISTS FOR OTHER COMPONENTS AND ACCESSORIES STOCKED SEND S A．E．FOR FULL LIST（OVERSEAS SEND 40p）

## PHONOSONICS

## DEPT．PE55， 22 HIGH STREET

 SIDCUP，KENT DA14 6EH\section*{KEYBOARDS

## KEYBOARDS 

U．K．Post and Handling：
Keyboards $\{1.50$ each
Contacts：orders under $£ 15$ 25p orders over £15：50p

37 Note C－C Keyboard §25． 50
49 Note C－C Keyboard
〔32． 25
61 Note C－C Keyboard〔39．75

VAT：Add $12 \frac{1}{2} \%$ to final total on all U．K．orders
EXPORT ORDERS ARE WELCOME but please see our price list for Export Postage Rates．N．B．Eire，Channel Isies and B．F．P．O． classify as export．

Mall Order and C．W．O．only－Sorry but no callera plosee
Prices are correct at time of Prese．E A O．E．Dellivery subiact to avaltebillty


Pocket TTY can be used for microprocessor programming． production data entry，warehousing，mobile data collection training and education．

## complete digital clock kits <br> TEAK CASES


＂DELTA＂
GENUINE TEAK OR PERSPEX CASE indication．Beautiful Burma Teak Case or Pretty Perspex in white black blue red green．Power failure is indicated by llashing display
MODULES：Kits can be bought without case－Non－Alarm E9；Alarm $\mathbb{1} 12.50$ inclusive．
READY BUILT：Buy a working tested module and fit your own case－Non Alarm 59.50 ．Alarm £13；Complete clock ready built，2yr guarantee－Non－ Alarm £13－50：Alarm E18－50．
ALARM FEATUAES：Pulsed tone．Tilt operated 10 minute＂Snooze＂period Single switch setting．Optional extra mercury switch（45p）sllows alarm reset by tilting clock．Digit brightness is automatically controlled to suit lighting level．



## Pulse Electronics Ltd

Dept．PE11， 202 Shefford Road Clifton Shefford，Beds．

Telephone：Hitchin（0462） 8144,77


## THE 'NUTS \& BOLTS' OF THOSE PROJECTS

## TRANSFORMERS

| MINIATURE MAINS Primary 240 with two independent secondary |  |
| :---: | :---: |
| No | Type |
| 2024 | MT280 0-6V. 0 |
| 2025 | MT $15000-12 \mathrm{~V}, 0-1$ |
| miniature mains Primar |  |
| No. | Second |
| 2021 | $6 \mathrm{~V}-0-6 \mathrm{~V} 100$ |
| 2022 | 9 V -0-9V 100 |
| 2023 | $12 \mathrm{~V}-0-12 \mathrm{~V} 10$ |
| 1 AMP MAINS Primary 240 V |  |
| No | Secondary |
| 2026 | $6 \mathrm{~V}-0-6 \mathrm{~V} 1 \mathrm{amp}$ |
| 2027 | $9 \mathrm{~V}-0-9 \mathrm{~V} 1 \mathrm{amp}$ |
| 2028 | 12v-0-12V 1 amp |
| 2029 | $15 \mathrm{~V}-0-15 \mathrm{~V} 1 \mathrm{amp}$ |
| 2030 | $30 \mathrm{~V}-0-30 \mathrm{~V} 1 \mathrm{amp}$ |

STANDARD MAINS Primary 240 V
Multi-tapped secondary mains transformers avalable in t amp. 1 amp and 2 amp current rating. Secondary taps are 0-19-25-33-40-50V
Voltages available by use of taps

| No. $2031$ | Rating tamp | $\begin{aligned} & \text { Price } \\ & \text { E3:42* } \end{aligned}$ |
| :---: | :---: | :---: |
| 2032 | 1 amp | ¢4.40* |
| 2033 | 2 amp | £5.45* |

AUDIO OUTPUT Primary 12 k , Secondary 5 onms
200 mW Dimensions $20 \times 16 \times 15 \mathrm{~mm}$
Order No 203
50. $25^{*}$

MINIATURE INTER/DRIVER
$\begin{aligned} & \text { Primary 20k } \Omega \text {. Secondary } 1 \mathrm{k} \Omega \text {. Ratio } 51 . \\ & \text { Order No. } 2038\end{aligned} \quad \mathbf{~} 0.23$.
LTT10 MIN. INPUT
Primary $100 \mathrm{k} \Omega$, Secondary $1 \mathrm{k} \Omega$. $15 \times 13 \times 13 \mathrm{~mm}$. $. ~ . ~$

Primary 10k $\Omega, \begin{aligned} & \text { LT711 MIN. DRIVER } \\ & \text { Order No } 2040\end{aligned}$
$\begin{aligned} & \text { Dendary } 2 \mathrm{k} \Omega, \text { C.T. }\end{aligned} 15 \times 13 \times 13 \mathrm{~mm}$
E. 0.35
LT712 MIN. OUTPUT
Pimmary 500 ohm. Secondary 8 otims $100 \mathrm{~mW}, 15 \times 13$

| 13 mm |
| :--- |

Order No 204
LT717 MIN. INPUT
Primary $150 \mathrm{k} \Omega$. Secondary $1 \mathrm{k} \Omega, 20 \times 15 \times 15 \mathrm{~mm} \quad \mathbf{~} 0.52^{\circ} \mathrm{C}$
Order No 2042
Primary $20 \mathrm{k} \cap$ LT719 MIN. INPUT
Primary 20 N, Secondary $1 \mathrm{k} \Omega, 20 \times 15 \times 15 \mathrm{~mm}$
LT722 MIN. DRIVER
Primary $10 \mathrm{k} \Omega$, Secondary $2 \mathrm{k} \Omega, \mathrm{CT} 20 \times 15 \times 15 \mathrm{~mm}$.
Order No 2044
IO. $32^{*}$ Order No 204
LTZ24 MIN, OUTPUT
Pumary $1.2 \mathrm{k} \cap \mathrm{C}$. T Serondary 3.2 and 8 ohm, 200 mW
Dimensions $20 \times 15 \times 15 \mathrm{~mm}$ Dimensions $20 \times 15 \times 15 \mathrm{~mm}$
Order No 2045
£0. 38 *
Primary 500 ohm, Secondary 32 and 8 onm, 200 mW Dimensions $20 \times 15 \times 15 \mathrm{~mm}$
Order No. 2046
Primary $1 \mathrm{k} \cap$ C.T.TRB MIN. DRIVER
Secondary 500 ohm C T. Dimensions $\begin{array}{ll}25 & 20 \times 20 \mathrm{~mm} \\ \text { Order No } 2047\end{array}$

Primary 200 IT7T9 MIN OUTPUT Dimensions $25 \times 20 \times 20 \mathrm{~mm}$
Order No 2048
LTT30 MIN. OUTPUT
Primary 500 ohm C.T. Secondary 32 and 8 ohm, 500 mW Dimensions $25 \times 20 \times 20 \mathrm{~mm}$
Order No 2049
L.E.D. 8

| Type | Stze | Order No | Colour | Price |
| :--- | :--- | :---: | :--- | ---: |
| THL209 | $0.125 i n$ | 1501 | RED | $12 p$ |
| TIL211 | $0.125 i n$ | 1502 | GREEN | $25 p$ |
| TIL213 | $0.1251 n$ | 1503 | YELLOW | $25 p$ |
| FLV115 | 0.2 in | 1504 | RED | 12 p |
| FLV310 | 02 nin | 1505 | GREEN | $25 p$ |
| FLV410 | 022 n | 1506 | YELLOW | $\mathbf{2 5 p}$ |

## 2nd Grade L.E.D.s

A pack of standard sizes and colours which tail to perform to their very ridged specification. but which are ideat fo experiments. Order No 1507
50.90
L.E.D.CLIPS

| L.E.D.CLIPS |  |  |  |
| :---: | :---: | :---: | :---: |
|  | Size | Order No. | Pilce |
|  | 0 125in | $1508 / 0.125$ | 15 p |
| Pack of 5 | 0.2 in | 15080.2 | 30 p |
| Pack of 5 |  |  |  |

NUTS AND BOLTS
BA BOLTS-packs of BA threaded cadmium-plated screws. slotted cheese head


BA NUTS-packs of cadmium-plated full nuts in multiples of 100


| INSTRUMENT CASES. In two sectione vinyl covered top and sides, aluminlum bottom, front and back. |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| No. | Length | W/dth | Helght | Price |
| 155 | 8 in | 5 fin | 210 | £1-40* |
| 156 | 11 in | 6 in | 310 | £1.80* |
| 157 | 6in | 4tin |  | ¢1.25* |
| 158 | $91 n$ | 5 tin | 2tin | ¢1-60* |
| ALUMINIUM BOXES. Made from bright all., folded conetruction each box complete with half inch deep lid |  |  |  |  |
|  |  |  |  |  |
| and screws. |  |  |  |  |
| No. | Length | Whath | Height | Price |
| 159 | $5 \frac{1}{6}$ | 2\%1n | 1 fin | 62p* |
| 160 | 4 in | 4 in | $1 \frac{1}{\text { in }}$ | 62p* |
| 161 | din | 2 2in | 1tin | $62 p^{*}$ |
| 162 | 5 ¢ın | 417 | 1, in | 74p* |
| 163 | $4 \mathrm{in}^{\text {n }}$ | $2 \frac{1}{2}$ in | 2 in | 64p* |
| 164 | 31 n | 2 in | 1 in | 44p** |
| 165 | 7 n | 5 in | 2in | \$1.04* |
| 166 | 8 in | 6 in | 3 n | [1.32* |
| 167 | 6 in | 4 in | 2 in | 86p* |

## BRIDGE RECTIFIERS

| SILICON 1 amp |  |  |
| :---: | :---: | :---: |
| Type | Order No | Price |
| 50 V RMS | BR1/50 | \$0. 25 |
| 100V RMS | BA1/100 | 50.28 |
| zoov RMS | BR1/200 | c0. 30 |
| 400 V RMS | BR1/400 | [0. 35 |
| SILICON 2 mmp |  |  |
| 50 V RMS | BR2/50 | 10. 40 |
| 100V RMS | BA2 100 | c0.45 |
| 200 V RMS | B42 200 | 10.50 |
| 400 V RMS | BR2:400 | ¢0. 55 |
| 1000 V RMS | BR2 1000 | \$0.65 |

## FUSE HOLDERS AND FUSES

Description
$20 \mathrm{~mm} \times 5 \mathrm{~mm}$ chassis mounting
1 in $\times$ in chassis mounting
lifin car inline type

| Order N | Pric |
| :---: | :---: |
| 506 | ¢0.07* |
| 507 | c0.10* |
| 508 | ¢0. 20 - |
| 509 | 50.22 |

Panel mounting 1 tin

| QUICK BLOW 20 mm |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Type | No. | Type | No | Type | No. |
| 150 mA | 611 | 1 A | 615 | 3A | 619 |
| 250 mA | 612 | 1.5 A | 616 | 4 A | 620 |
| 550 mA | 613 | 2A | 617 | 5A | 621 |
| 800 mA | 614 | 2 5A | 618 | All 5 |  |

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## SWITCHES



ROCKEA SWITCH
A range of rocker
switches
SPST
moulded in hion insula-
moulded in high insula a choice of colours ideal for small apparatus
Description
Description
Miniature SPST toggle. 2 amp
250 V a.c.
250 V a.c.
Miniature DPD toggle, 2 amp
Miniature
250 V ac
Miniature DPDT toggle centre oft, 2 amp 250 V a.c.
Push button SPST. 2 amp
Push bution SPST. 2 amp
250 V a.c.
Push button SPST. 2 amp
250 V a.c.
Push bution DPDT. 2 amp
250 V a.c.
MIOGET WAFER SHIT- suitable for switching at 250 V a.c 100 ma of 150 V of c . in non-reactiver loads make-before break contacts These switches have a spindle 0.25 in dia and $30^{\circ}$ indexing
Description
1 pole 12 way
2 pole $\quad 6$ way
3 pole
$\begin{array}{ll}2 \text { pole } & 6 \text { way } \\ 3 \text { pole } & 4 \text { way } \\ 4 \text { pole } & 3 \text { way }\end{array}$
MICRO SWITCHES
PlastIc bution glves simple
on-off action
Rating 10 amp 250 V a.c.
Button give 1 pole change
Button givee 1 pole ch
over actiom
Rating 10 smp 250 V a.c.
Colou
RED
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## NEW VOICE FOR COMPONENT INDUSTRY

THe formation of the Electronic Components Industry Federation (ECIF) by a marriage between the Radio and Electronic Component Manufacturers' Federation and the Electronic Components Board provides the UK components industry with a single and powerful voice to represent its interests.
The creation of this new body coincides with the recognition by the Government of the component industry's importance to our economic affairs. For the electronic component industry is one of five selected industries from an original list of 40 , that have been deemed of strategic importance for the future. With this recognition comes the establishment of a Department of Industry support scheme for British-based component makers, with an initial sum of $£ 20 \mathrm{~m}$ to be made available. The new streamlined association ECIF will be working in very close cooperation with the NEDO Sector Working Party on components.
The ECIF is important in that it represents about 90 per cent of the entire component manufacturing business in the United Kingdom. The list of 145 member companies embraces the smaller firms, many of these producing the less exalted components without which the glamorous components like semiconductor devices would be deprived of their chance to shine, as well as the big companies with household names. Included amongst the latter are well-known American semiconductor makers who have established factories in the UK.

But the exclusion of other well-known names in the semiconductor field is equally notable. General Instrument Microelectronics, for one, has a manufacturing establishment in Scotland and many of their current i.c. devices have been designed and developed here. Yet this multi-national company has declined the invitation to join ECIF. Apparently GIM are rather sore at being treated as "foreigners" whenever there is any Government hand-out to help finance research and development work by UK firms. So they feel reluctant to chip-in for the common cause by contributing to an organisation such as ECIF.

Wholly British owned firms such as Ferranti, GEC and Plessey have, from time to time, received financial backing from the Government. This is part of a determined effort to strengthen the indigenous semiconductor industry. But facts of life concerning the semiconductor industry in general suggest that those US firms that have set up development and manufacturing facilities in the UK should be equally encouraged in their efforts, which do of course contribute directly to our economy. On the very important technological plane, these companies keep the UK in the main stream of big developments originating in their home bases across the Atlantic. How better to encourage them develop their roots over here than by making them eligible for consideration for financial backing? There are signs that the Government is beginning to think this way; if so, any coolness towards the ECIF will no doubt disappear and that body will become all the more relevant to the component situation in the UK and thus be better able to fulfil its intended purpose: to help maintain a thriving and forward looking components industry in this country.
F.E.B.

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Fig. 1. Circuit diagram of Organ Tremolo Unit. LED1 is optically coupled to R23, and likewise LED2 with R24


Fig. 2. (a) Vector diagram of inductive stage. (b) Capacitive stage. (c) Phase shift versus frequency

## TREMOLO EFFECTS

The unit was specifically designed for injecting phase modulation into the electrical signal available at the organ's auxiliary output socket; this signal then being fed to a conventional power amplifier loudspeaker extension. The tremolo speed is approximately 5 Hz , which will blend acoustically with the tremolo from the organ's Leslie speaker system, regardless of the latter's precise speed. With both tremolos on, the effect is similar to theatre organ tremolo, and a modified effect can also be obtained by running the organ's unit at "chorus" speed (about one cycle in 1.5 seconds), with full external tremolo. If the control of the external tremolo is now eased back to a low level, a very reasonable "straight" organ sound is obtained.

Although the author disliked the idea of having both the internal and external units running at chorus speed, there is no reason why an experimental oscillator, running at the slower speed, should not be added to this unit in conjunction with suitable changeover switching.

Obviously the electronic tremolo unit can be used on its own, with an organ not fitted with its own system In this case the tremolo unit and the PA/LS extension can be regarded as a relatively inexpensive alternative to a rotation loudspeaker type extension, with considerable improvement in tone ambience as a whole.

## CIRCUIT DESCRIPTION

Referring to the diagram in Fig. 1, the organ signal passes through two phasing stages, and then a conventional output stage. The first stage contains an inductance/resistance combination and the second stage, a capacitance/resistance combination, the latter being subjected to a 180 degree phase inversion by the second transistor.

The resistance element in each case is a light dependent resistor coupled to a light emitting diode, and mounted in a light-proof container.

The oscillator section (TR6 and TR7) drives the two transistors TR4 and TR5 through the tremolo control potentiometer VR4. Because TR4 is npn and TR5 is $p n p$ the two l.e.d.s are driven in antiphase. This causes the respective resistances of R23 and R24 to swing alternately between the arbitrary limits of $5 \mathrm{k} \Omega$ and $20 \mathrm{k} \Omega$.
The phase changes imposed on the signal by the two stages are shown in Figs. 2a and 2b, and as can be seen, the inductive stage creates the phase lag $\theta_{1}$, and the capacitive stage produces a phase lead of $\alpha_{1}$. The maximum total phase variation for any given frequency can be read from the graph in Fig. 2c, and this is $\theta_{1}+\alpha_{1}$ degrees. The plots were produced mathematically with formulae derived from the vector diagrams.

## ORGAN TREMOLO BOARD



Fig. 3. Component layout on $0 \cdot 1$ inch Veroboard. The arrangement of the optical couplings using cabinet feet is shown

## COMPONENTS . . .

Resistors

| R1 | $33 \mathrm{k} \Omega$ | R13 | $47 \mathrm{k} \Omega$ |
| :---: | :---: | :---: | :---: |
| R2 | $47 \mathrm{k} \Omega$ | R14 | $47 \mathrm{k} \Omega$ |
| R3 | 100kS | R15 | 1kS |
| R4 | 1.5kS | R16 | $330 \Omega$ |
| R5 | $1.5 \mathrm{k} \Omega$ | R17 | $1.5 \mathrm{k} \Omega$ |
| R6 | $27 \mathrm{k} \Omega$ | R18 | $1.2 \mathrm{k} \Omega$ |
| R7 | $1 \mathrm{k} \Omega$ | R19 | $18 \mathrm{k} \Omega$ |
| R8 | $1 \mathrm{k} \Omega$ | R20 | $18 \mathrm{k} \Omega$ |
| R9 | $27 \mathrm{k} \Omega$ | R21 | $150 \Omega$ |
| R10 | $2 \cdot 2 \mathrm{k} \Omega$ | R22 | 2.2k $\Omega^{*}$ |
| R11 | $680 \Omega$ | R23 | ORP12 I.d.r. |
| R12 | $1 \mathrm{k} \Omega$ | R24 | ORP12 I.d.r. |

Potentiometers
VR1 $4.7 \mathrm{k} \Omega$
VR2 100k $\Omega$
VR3 100ks
VR4 $10 \mathrm{k} \Omega$ linear slider
VR5 $220 \Omega$
VR6 $10 \mathrm{k} \Omega$
All miniature horizontal presets unless otherwise stated

## Capacitors

| C 1 | $4.7 \mu \mathrm{~F}$ tantalum bead |
| :--- | :--- |
| C 2 | $100 \mu \mathrm{~F} 16 \mathrm{~V}$ Teletron |
| C 3 | $0.022 \mu \mathrm{~F}$ polyester |
| C 4 | $470 \mu \mathrm{~F} 16 \mathrm{~V}$ Teletron |
| C 5 | $0.1 \mu \mathrm{~F}$ or $4.7 \mu \mathrm{~F}$ |
| C 6 | $10 \mu \mathrm{~F} 16 \mathrm{~V}$ elect |
| C 7 | $10 \mu \mathrm{~F} 16 \mathrm{~V}$ elect |
| C 8 | $470 \mu \mathrm{~F} 16 \mathrm{~V}$ Teletron |
| C 9 | $1 \mu \mathrm{~F} 25 \mathrm{~V}$ elect. or tantalum |
| C 10 | $1 \mu \mathrm{~F} 25 \mathrm{~V}$ elect. or tantalum |

## Inductors

L1 800 turns of $40 \mathrm{~s} . \mathrm{w} . \mathrm{g}$ enamelled copper wire on FX2239 ferrite pot core assembly*

## Semiconductors

TR1-TR4 BC108
TR5 BCY70
TR6-TR7 BC108
D1-D2 0.2 inch dia, red
(must be clear encapsulation)*

## Miscellaneous

0.1 inch Veroboard ( $127 \times 95 \mathrm{~mm}$ )

Veropins
$2 \times$ rubber bushes for l.e.d./l.d.r subassembly*

* see text

CONSTRUCTOR'S NOTE
The following components:
Clear encapsulated red l.e.d.
Suitable plastic cabinet feet for optical couplers
Ferrite pot core assembly, and suitable wire,
are available from
Greenweld Electronics, 443 Millbrook Road, Southampton SO1 OHX.

From the trigonometrical ratios applicable to a right-angle triangle:
$\operatorname{Tan} O_{2}=\frac{\omega \mathrm{L}}{\mathrm{R}} \ldots$. equation (1)
It can be shown that $\theta_{1}=2()_{2}$ degrees.
Since $\left.\theta_{3}=90-1\right)_{2}$ degrees
and also $\Pi_{3}=\frac{180-)_{1}}{2}$ degrees.
Therefore $90-1)_{2}=\frac{180-11_{1}}{2}$
Multiplying by 2: $180-2()_{2}=180-0_{1}$
$\therefore \theta_{1}=2 \theta_{2} \ldots .$. equation (2)
Combining equations (1) and (2) we get
$\theta_{1}=2 \tan ^{-1}\left(\frac{\omega \mathrm{~L}}{\mathrm{R}}\right)$
or $\theta_{1}=2 \tan ^{-1}\left(\frac{2 \pi \mathrm{fL}}{\mathrm{R}}\right)$
Similar rules define $\alpha_{1}$ as being $2 \tan ^{-1}(2 \pi \mathrm{fCR})$.
The graphical plots of these formulae correspond closely to measurements taken with the unit in operation.

Most electronic organs have a high signal level at their extension output sockets, and for this reason the value of $33 \mathrm{k} \Omega$ has been chosen for R1, to give an audio output signal which is about -10 dB with respect to the input level. This attenuation can be reduced if required, by lowering the value of R1.

The purpose of R22 is to regulate the maximum depth of tremolo available at VR4. The value of $2 \cdot 2 \mathrm{k} \Omega$ found suitable in the prototype, can be changed up or down in value, to cover tolerances in l.e.d. performance.

Capacitor C5 should be $0 \cdot 1 \mu \mathrm{~F}$ for a $200 \mathrm{k} \Omega$ output, or $4.7 \mu \mathrm{~F}$ for a $10 \mathrm{k} \Omega$ impedance output.

If the second oscillator is required for the chorus effect, it can be constructed using the same circuit as the oscillator section of Fig. 1, and making R19-22k $\Omega$, $\mathrm{R} 20-12 \mathrm{k} \Omega, \quad \mathrm{C} 9-10 \mu \mathrm{~F} \| 4 \cdot 7 \mu \mathrm{~F} \quad(14 \cdot 7 \mu \mathrm{~F})$, and C 10 $-50 \mu \mathrm{~F}$. The decoupling components R 21 and C 4 may be omitted in this instance. The period of oscillation is about 1 to 1.5 seconds.

## CONSTRUCTION

The two l.e.d./l.d.r. sub assemblies need no explanation, being put together as indicated in Fig. 3. It should be borne in mind that the l.e.d.s are required to be withdrawn from their housings, to carry out the adjustments detailed later.

The winding of the inductor coil can be done using a handbrace held in a bench vice. The bobbin is lightly clamped between two oversize washers on a 50 mm long 2BA screw, and secured with a nut, the end of the screw being held in the handbrace chuck. The nut is lightly tightened, after the bobbin has been centred to rotate concentrically. It is advisable to clean the end of the wire and anchor it to the chuck with adhesive tape, before starting to wind.
Have a piece of sticky tape cut to size and ready to wrap around the coil after the 800 turns of 40 s.w.g. enamelled copper wire have been wound on. There is plenty of room on the bobbin, and ordinary "pile" winding is satisfactory.

The coil ends should be brought out of diametrically opposite slots, which after assembly of the inductor,

are soldered to the pins as shown on the circuit board layout. An carth connection is soldered between the fifth pin, and the adjacent clip.
Components are mounted on Veroboard, the layout of which is shown in Fig. 3. It is recommended that the complete panel should be mounted in a vacant space inside the organ's housing, to allow the audio and tremolo control connections to be made more readily. The only outgoing signal will then be the phase processed output lead.

## POWER SUPPLY

The total current consumption of this 12 volt positive earth system is 25 mA , and may be available from the organ's supply. If this is not possible, and a mains power pack is not desired, then two PJ996 or similar batteries would provide a generous source of supply.

## ADJUSTMENTS

(1) Before switching on, VR2 and VR3 should both be turned to an almost fully clockwise position, thus cutting off TR4 and TR5.
(2) After switching on, VR5 and VR6 should be adjusted to obtain a sinusoidal waveform from the oscillator. This is best done by connecting an oscilloscope across VR4, but it can also be done by ear, as a non-sinusoidal waveform will produce audible harmonics if connected to the P.A./L.S. extension. This of course would be done after installation.
(3) With zero drive from the oscillator, and with the 1.e.d.s removed from their housings, turn VR2 anticlockwise until LEDI begins to glow. Similarly turn VR3 anticlockwise until LED2 begins to glow. Replace the two l.e.d.s in their housings.
(4) Adjust VR1 to the point where the unit accepts full organ signal without overloading.
This completes the adjustments, and the unit should now be ready for use.


This alarm is suitable for all types of security application, in a house, shop or any other place that needs to be protected from an intruder.

It can be used with open and closed circuit sensors, and is of bridge design to give the maximum protection. Entry and exit delays are provided, allowing one to enter and leave the building without need for a key switch at the door.

The alarm is run from internal batteries, an HP1 was used in the prototype. This battery will give up to six amperes. Since the unit consumes only 10 mA in the Guard condition, the cost of providing a mains power supply unit was hardly felt to be justified.

THE CIRCUIT
As will be seen from Fig. 1, the alarm is built around a 741 operational amplifier, operating in the open loop mode as a voltage comparator. The alarm voltage $\mathrm{V}_{1}$ is set by the divider chain comprising resistors $\mathrm{R}_{\mathrm{x}}$ and $\mathbf{R}_{y}$. The voltage at the junction is applied to the inverting input (pin 2) of ICl. A similar voltage $\mathrm{V}_{2}$ is set on the non-inverting input (pin 3) by the potential divider formed by R1, VR1 and R2.

Providing that $V_{1}$ remains more positive than $V_{2}$, the output of the 741 (pin 6) will stay at about two volts.

COMPONENTS . . .

| Resistors |  |  |  |
| :--- | :--- | :---: | :---: |
| R1, R2 | $3.9 \mathrm{k} \Omega$ |  |  |
| R3, | R4 |  |  |
|  | $3 \mathrm{k} \Omega$ |  |  |
| R5 | $12 \mathrm{k} \Omega$ |  |  |
| R6 | $22 \mathrm{k} \Omega$ |  |  |
| R7 | $560 \Omega$ |  |  |
| R8 | $47 \Omega$ |  |  |
| R9 | $220 \Omega$ |  |  |
| All |  |  |  |
| Anspecified resistors $\frac{1}{2} \mathrm{~W}$ |  |  |  |

Potentiometers
VR1 $50 \mathrm{k} \Omega \mathrm{lin}$. VR2 $1 \mathrm{M} \Omega \mathrm{min}$. preset
Capacitor
C1 $470 \mu \mathrm{~F} 16 \mathrm{~V}$ electrolytic
Semiconductors

| TR1 | BFY51 | TR2 | TIS43 |
| :--- | :--- | :--- | :--- |
| IC1 | 741 | D1 1 N4148 |  |
| CSR1 | BTY79-400R (400V 6 A thyristor $)$ |  |  |

Miscellaneous
S1 Push-on, delay switch (see text)
S2 3-pole, 4-way key-operated switch*
S3 S.P.C.O. microswitch
RLA D.P.C.O. relay, $185 \Omega 12 \mathrm{~V}$ coil, continental type
WD1 Audible warning device, 12 V (RS Compo-
LP1 12 V lamp with holder, green lens
LP2 12V lamp with holder, red lens
B1 HP1 battery
Plug for B1; heat sink for CSR1 (see Fig. 2); socket for IC1 (if required); 12-way terminal strip; aluminium box (see text); $\underset{\text { text) }}{\text { alarm bell, } 12 \mathrm{~V} ; \mathrm{R}_{\mathrm{x}}, \mathrm{R}_{\mathrm{y}} \text { and sensors (see }}$ text)
*The key-operated switch is available from F \& G Electronics (Manchester), 28 Middleham Street, Manchester M14 7NG

In this condition, the forward bias on TR1 is not sufficient for RLA to be energised. If, however, the resistance of line A increases or line B decreases, the balance will be upset, changing the state of the input stage and making the output rise to about 10 volts. As soon as this happens, transistor TR1 will saturate and energise relay RLA/2. When the unit is in the Guard condition, the relay will latch when set off, thus keeping the bell powered. The only way to stop it is by means of the key-operated switch S 2 .

When the alarm is triggered, power will be applied via RLA2 and S2c to the timer, based on the unijunction TR2. The delay, set by the charging rate of Cl via R6/VR2, will allow up to eight minutes to enter and switch off the alarm. Power will also be applied to an audible warning device WD1, to indicate that the timer has started. The bell will not ring until the timer has completed its cycle and fired thyristor CSR1.

A microswitch $\mathbf{S 3}$ fitted to the case operates if the lid is opened. The switch contacts place a short circuit across the gate and anode of the thyristor, causing the bell to ring if the unit is tampered with.

## EXIT DELAY

The exit delay is simply a push-to-make, delay switch Sl connected across the base emitter junction of TR1 to hold off the alarm until the exit door has been closed. This type of switch is intended for use on the stairways of blocks of flats, etc. turning the lights on when pushed, but turning them off again after a period of a couple of minutes or so, depending
upon its setting, to save power. Such switches are available from most good electrical shops.

If an intruder enters he will not be able to silence the alarm by pushing $S 1$, as the relay will have latched on via its contact RLA1. Lamps LP1 and LP2 are for setting the unit, and for indicating the state of the alarm loops when switching the unit on. If the alarm loops are upset, RLA will be energised, illuminating the red lamp instead of the green when $\mathbf{S 2}$, is turned to the Set Balance position. The doors and windows should then be checked before turning S2 to Guard, otherwise the bell will ring.

Resistor R9 maintains a holding current of about 30 mA through the thyristor, which would otherwise switch off when the bell trembler contacts open.

## CONSTRUCTION

The prototype unit was built in an aluminium box $180 \times 205 \times 75 \mathrm{~mm}$, large enough to accommodate the battery and all the electronics. Most of the components are mounted on a printed board, as shown in Fig. 2. Of the remainder, VR1, LP1, LP2, S1 and S2 are mounted on the lid, and the audible warning device is mounted at the top of the box, with a hole to let out the sound. All external connections are made via a 12 -way terminal strip.

## SETTING UP

To set up the alarm, fit $R_{x}$ and $R_{y}$, which can be any value from $10 \mathrm{k} \Omega$ to $56 \mathrm{k} \Omega$. In the prototype system, $33 \mathrm{k} \Omega$ and $27 \mathrm{k} \Omega$ were used. Place the key in S 2 and


Fig. 1. Circuit diagram of the complete burglar alarm. The values of $R_{x}$ and $R_{y}$ are discussed in the text. Circled numbers identify connections to the $\mathbf{1 2 - w a y}$ terminal block

## Burglar Alarm Board



Fig. 2. Printed board pattern shown full size and component layout. The thyristor heat sink is in contact with the mounting stud (anode) and is therefore live. Keep clear of earthed metalwork or wiring
turn to Set Balance. Adjust the balance potentiometer VRI to the point where the green lamp just comes on, then remove $R_{x}$ and short out $R_{y}$ in turn. In each case the green should go out and the red come on. This indicates that all is well.

Next turn S2 to Test, and as before open and short circuit the resistors. The relay should latch and not reset until S2 is returned to Set Balance.

To set the timer, connect two links, one from No. 7 on the terminal strip to battery positive, and one from No. 8 to battery negative. Disconnect the audible warning device to stop the noise while setting up. Connect a 12 volt lamp to terminals 9 and 10 to indicate when the thyristor has fired, and adjust preset VR2 to give the required delay.

The only remaining item to be tested is the tamper switch. To do this remove all the links and reconnect the audible warning device. Remove the temporary lamp from terminals 9 and 10 and wire in the bell. Turn S2 to Guard, i.e. alarm on, and remove the lid, whereupon the bell should ring.

The setting of VRI should be checked when the unit is installed with the external sensors connected to lines A and B. These sensors may be reed switches. microswitches, pressure mats, etc. as required.

To make the alarm more difficult for an intruder to bypass, line A and line B can be run in one four-core cable. The intruder will then not know the correct ones to cut or join.


Competition winners A. Mackintosh and A. Challinor receiving their Introkit prizes from Mrs. J. L. Marshall
facturers, and A. Marshall (London) Ltd., one of the largest distributors of electronic components.

Audiences of about 200 attended each staging of the Forum at Berners Hotel, London W1, listening attentively to two National Semiconductor engineers, Dave Brown and Stewart May, who spoke on different aspects of microprocessors and answered questions from the audience.

A feature of the first Forum was the presentation by Mrs J. L. Marshall of prizes to the first two winners of the PE Microprocessor Competition, A. Mackintosh of Brighton, and A Challinor of Stoke-on-Trent.

An eliminati. competition is under way to find the third main prize winner, and we hope to publish a full list of winners in our June issue.




#### Abstract

A "Miss World" contest for microprocessor chips would be difficult to judge. There would be those who liked their MPUs well stacked (see Part 2), the bipolar brigade who preferred speed to comfort, and of course the "leg men" hooked on long graceful instruction sets! At the end of the day, though, the vital statistic which would receive the most scrutiny, and receive the most admiration and wolf-whistles, would undoubtedly be the instruction set. It is the very existence of the instruction set which sets MPU chips apart from standard LSI, and as far as we are concerned, "Vive la difference!"


'T"HE instruction set is, in effect, a rudimentary "language" through which we earthlings can communicate our desires to the inner recesses of the semiconductor chip, a language which is designed by the chip makers in most cases. Of course the ideal language for microprocessor programming would be English, or if you happen to be French, French, but microprocessors are far removed from our intellectual level (thank goodness!) and require us to "talk down" to them using a basic language with a very limited vocabulary, namely, the instruction set.

With larger computer systems much effort has been expended in writing special programs called compilers which translate instructions written in "almost English" into the currency of the computer instruction set, but while this is a universally practised technique for large systems, it is a newcomer to microprocessors and can only be used on MPU systems with a lot of memory space available.

It seems unlikely that we will see microprocessor systems in our price range which are capable of running compilers for some time yet, which means that when we do use microprocessors we have to think in terms of the basic instruction set that the MPU is born with, and be familiar with the way the MPU chip operates in a hardware sense. This fact of life is not unpalatable since it gives the whole subject a distinct "hardware" or "gates and wire" flavour which should make using microprocessors a natural and exciting experience for most electronics enthusiasts.

## INSTRUCTION CODING

Fig. 3.1 gives some examples from an mpu instruction set, and as you can see, there is an English sentence or two to describe the effect of the instruction to us mortals, and a binary code word which specifies the instruction to the MPU. Now the MPU can't read English, and we find it very difficult to remember and use patterns of l's and 0's, and so to write a program we would use a list of instructions like the one in Fig. 3.1 to "look-up" the binary code for a specific operation so that we can use it in our program.

To make it a little easier for us to remember the binary patterns of MACHINE CODE as it is called, most people find it best to use a notation called HEXADECIMAL which represents groups of four binary bits as a single character from the set 0 to 9 and $A$ to $F$ which provide the necessary 16 possibilities.

Using hexadecimal the eight-bit instruction 11101000 becomes E , which is both easier for us to use and straightforward for a program to convert into binary if required.

An alternative to hexadecimal is octal code where the binary is split into groups of three bits, each represented by a decimal digit from the set $0-7$, so that the binary instruction above would be coded as 350 in this notation.

Octal is losing a lot of ground these days to hexadecimal which is undoubtedly a more powerful and easy-to-use technique for microprocessor use, especially since it has the added protection that in a list of

## INSTRUCTION SET

| Summary of Processor Instructions |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| -Two Cycle Instructions |  | Instruction Code |  |  |  |  |  |  |  |
| Mremonic | Description |  | OP | R |  |  | DPA |  |  |
|  |  | $\mathrm{D}_{3}$ | $\mathrm{D}_{2}$ | $\mathrm{O}_{1}$ | $\mathrm{D}_{0}$ | $\mathrm{D}_{3}$ | $\mathrm{O}_{2}$ | 01 | $\mathrm{D}_{0}$ |
| MACHINE GRDUP |  |  |  |  |  |  |  |  |  |
| NOP | No Operation | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| HLT | Halt | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 |
| BBS | Branch Back and SRC | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 |
| LCR | Commiand Register to Accumulator | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 |
| OR4 | Logical QR, Index Register 4 and Accumulator | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 |
| OR5 | Logical OR, Index Register 5 and Accumulator | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 |
| ANG | Logical AND, Index Register 6 and Accumulator | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 0 |
| AN 7 | Logical AND, Index Register 7 and Accumulator | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 |
| D80 | Designate ROM Bank 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 |
| 081 | Designate ROM Bank 1 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 1 |
| SBO | Select Index Register Bank 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 0 |
| SB1 | Select Index Reguster Bank 1 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 1 |
| EIN | Enable Interrupl | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 0 |
| DIN | Disable Interrupt | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 1 |
| RPM |  | Half-Byte per Instruction |  |  |  |  |  |  |  |
| *JCN | Jump Conditional to Address | 0 | 0 | 0 | 1 | $C_{1}$ | $\mathrm{C}_{2}$ | $\mathrm{C}_{3}$ | $\mathrm{C}_{4}$ |
| $\begin{array}{ll}A_{2} A_{2} A_{2} A_{2} A_{1} A_{1} A_{1} A_{1} & A_{2} \\ A_{2} & A_{2} \\ A_{2} & A_{1} \\ \text { Condition Code, } C_{1} C_{2} C_{3} C_{4}\end{array}$ |  |  |  |  |  |  |  |  |  |
| * FIM | Fetch Immediate, ROM Data $\mathrm{D}_{2} \mathrm{D}_{1}$ | 0 | 0 | 1 | 0 | R | $R$ | $R$ | 0 |
|  | to Index Register Pair R RR | $\mathrm{D}_{2}$ | $\mathrm{D}_{2}$ | $\mathrm{D}_{2}$ | $\mathrm{D}_{2}$ | $\mathrm{D}_{1}$ | $\mathrm{D}_{1}$ | $\mathrm{D}_{1}$ | $\mathrm{D}_{1}$ |
| SRC | Send Register Control | 0 | 0 | 1 | 0 | R | R | R | 1 |
| FIN | Fetch Indirect. Data from ROM to Index Regrster Parr RRR | 0 | 0 | 1 | 1 | $R$ | $R$ | R | 0 |
| JIN | Jump Indirect to Address in Register Pair RRR | 0 | 0 | 1 | 1 | $R$ | R | $R$ | 1 |
| *JUN | Jump Unconditional to Address | 0 | 1 | 0 | 0 | $A_{3}$ | $A_{3}$ | $\mathrm{A}_{3}$ | $\mathrm{A}_{3}$ |
| $A_{3} A_{2} A_{1}$ |  | $\mathrm{A}_{2}$ | $\mathrm{A}_{2}$ | $A_{2}$ | $A_{2}$ | $A_{1}$ | $A_{1}$ | $A_{1}$ | $A_{1}$ |
| *JMS | Jump to Subroutine at Address | 0 | 1 | 0 | 1 | $\mathrm{A}_{3}$ | $\mathrm{A}_{3}$ | $\mathrm{A}_{3}$ | ${ }_{3}$ |
|  | $A_{3} A_{2} A_{1}$ | $\mathrm{A}_{2}$ | $\mathrm{A}_{2}$ | $\mathrm{A}_{2}$ | $\mathrm{A}_{2}$ | $\mathrm{A}_{1}$ | $A_{1}$ | $\mathrm{A}_{1}$ | $A_{1}$ |
| INC | Increment Register ARRR | 0 | 1 | 1 | 0 | R | R | R | A |
| ${ }^{4}$ ISZ | Increment Register RRRR. Go 10 | 0 | 1 | 1 | 1 | R | R | R | $R$ |
|  | Address $A_{2} A_{1}$ if result is not zero, otherwise go to next instruction | $A_{2}$ | $\mathrm{A}_{2}$ | $A_{2}$ | $A_{2}$ | $A_{1}$ | $A_{1}$ | A) | $A_{1}$ |
| ADO | Add Register RRRR to Accumulator with Carry | 1 | 0 | 0 | 0 | R | R | R | $R$ |
| SUB | Subtract Register RRRR from Accumulator with Borrow | 1 | 0 | 0 | 1 | A | R | R | R |
| LO | Load Contents of Register RRRR to Accumulator | 1 | 0 | 1 | 0 | R | 8 | R | R |
| XCH | Exchange Contents of Register RRRR and Accumulator | 1 | 0 | 1 | 1 | R | R | 8 | R |
| BBL | Branch Back and Load Data DDDD to Accumulator | 1 | 1 | 0 | 0 | D | D | D | 0 |
| LDM | Load Data OROD to Accumulator | 1 | 1 | 0 | 1 | 0 | 0 | D | 0 |

Fig. 3.1. The complete instruction set of the Intel 4040. Note that although the 4040 is a 4 bit MPU, it uses an 8 bit instruction code
"hex" code there are always a few letters to remind you that this is "hex" whereas a list of "octal" can look very like a list of decimal numbers which of course it is not, since, for example, 77 octal equals 63 decimal when interpreted as a numerical value. See Fig. 3.2 and Fig. 3.3.

## INSTRUCTIONS AND NUMBERS

This last point concerning octal code raises the interesting question of just how the mpu differentiates between 11101000 the instruction which means (say) "subtract from memory", and 11101000 the number

| Mnemonic | Description | Instruction Code |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | DPR |  |  |  | DPA |  |  |  |
|  |  | $\mathrm{O}_{3}$ | $\mathrm{O}_{2}$ | $\mathrm{D}_{1}$ | $\mathrm{D}_{0}$ | $0_{3}$ | $\mathrm{D}_{2}$ | $\mathrm{D}_{1}$ | $0_{0}$ |
|  | I/O and RAM GROUP |  |  |  |  |  |  |  |  |
| WRM | Accumulator to Selected RAM Maın Memory Character | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 |
| WMP | Accumulator to Selected RAM Dutput Port | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 1 |
| WRR | Accumulator to Selectad RDM Dutput Port | 1 | 1 | 1 | 0 | 0 | 0 | 1 | 0 |
| WPM | Accumulator to Selected Half-Byte in Read/Write Program Memory | 1 | 1 | 1 | 0 | 0 | 0 | 1 | 1 |
| WRO | Accumulator to Selected RAM Status Character 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 |
| WR 1 | Accumulator to Selected RAM Status Character 1 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 1 |
| WR2 | Accumulator to Selected RAM Status Character 2 | 1 | 1 | 1 | 0 | 0 | 1 | 1 | 0 |
| WR3 | Accumulator to Selected RAM Status Character 3 | 1 | 1 | 1 | 0 | 0 | 1 | 1 | $\dagger$ |
| SBM | Subtract Selected RAM Main Memory Character from Accumulator with Borrow | 1 | 1 | 1 | 0 | 1 | 0 | 0 | 0 |
| ROM | Selected RaM Main Memory Character to Accumulator | 1 | 1 | 1 | 0 | 1 | 0 | 0 | 1 |
| ROR | Selected ROM Input Port 10 Accumulator | 1 | 1 | 1 | 0 | 1 | 0 | 1 | 0 |
| ADM | Add Setected RAM Maın Memory Character to Accumulator with Carfy | 1 | 1 | 1 | 0 | 1 | 0 | 1 | 1 |
| RDO | Selected RAM Status Character 0 to Accumulator | 1 | 1 | 1 | 0 | 1 | 1 | 0 | 0 |
| ROI | Selected RAM Status Character 1 to Accumulator | 1 | 1 | 1 | 0 | 1 | 1 | 0 | 1 |
| RD2 | Selected RAM Status Character 2 to Accumulator | 1 | 1 | 1 | 0 | 1 | 1 | 1 | 0 |
| RO3 | Selected RAM Status Character 3 to Accumulator | 1 | 1 | 1 | 0 | 1 | 1 | 1 | 1 |
| ACCUMULATOR GROUP |  |  |  |  |  |  |  |  |  |
| CLB | Clear Accumulator and Carry | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 |
| CLC | Clear Carry | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 1 |
| IAC | Increment Accumulator | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 0 |
| CMC | Complement Carry | 1 | 1 | 1 | 1 | 0 | 0 | 1 |  |
| CMA | Complement Accumulator | 1 | 1 | 1 | 1 | 0 | , | 0 | 0 |
| RAL | Rotate Left, Accumulator and Carry | 1 | 1 | 1 | 1 | 0 | 1 | 0 | 1 |
| RAR | Rotate Right, Accumulator and Carry | 1 | 1 | 1 | 1 | 0 | 1 | 1 | 0 |
| TCC | Transmit Carry to Accumulator. Clear Carry | 1 | 1 | 1 | 1 | 0 | 1 | 1 | 1 |
| DAC | Decrement Accumulator | 1 | , | 1 | 1 | 1 | 0 | 0 | 0 |
| TCS | Transfer Carry Subtract and Clear Carry | 1 | 1 | 1 | 1 | 1 | 0 | 0 |  |
| STC | Set Carry | 1 | 1 | 1 | 1 | 1 | 0 | 1 |  |
| DAA | Decımal Adjust Accumulator | 1 | 1 | 1 | 1 | 1 | 0 | 1 |  |
| KBP | Keyboard Process | 1 | 1 | 1 | 1 | 1 | 1 | 0 |  |
| DCL | Designal Command Line | 1 | 1 | 1 | 1 | 1 | 1 | 0 |  |

NOTES:
11) The condtition code is assigned as follows
$\mathrm{C}_{1}=1$ Invert jump condition
$\mathrm{C}_{1}=0 \quad$ Not invert jump condition
$C_{2}=1$ Jump if accumulator is zero
$C_{3}=1 \quad$ Jump if carry/link is a 1
$C_{4}=1$ Jump if test signal is a $D$
(2) RRR is the addréss of 1 of 8 index register pairs in the CPU.
(3) RRRR is the address of 1 of 16 index registers in the CPU.
(4) Each RAM chip has 4 registers, each with twenty 4 bit characters subdivided into 16 main memory characters and 4 status characters. Chip number, RAM register and main memory character are addressed by an SRC instruction. For the selected chip and register. however, status character locations are selected by the instruction code (OPA).
which has a value of 232 in decimal.
The answer is that the MPU cannot differentiate between these two possibilities at all; it interprets anything that is placed in its instruction register as an instruction, and it will interpret any part of the store content (including instructions) as data if told to do so by the program.

This means in practice that if, for example, a programmer inadvertently jumped the program counter into a table of data in store, the MPU would endeavour to use the data as a program sequence, probably with bizarre results!

## INSTRUCTION TYPES

Even small, four-bit MPU chips have a repertoire of about fifty distinct instructions, and powerful eightand sixteen-bit devices may boast one hundred or more. At first reading, the instruction repertoire of a microprocessor can be a bit bewildering, and it's not easy to see the potential usefulness of such instruction names as RAR, POP, PSW, JCN, or even bra!

Rather than examine each instruction in isolation it is better to group instructions which perform similar operations under common headings, and very often the manufacturers do this for us in their handbooks.

Unfortunately, no two manufacturers have the same ideas as to what the group titles should be, and so we have picked four general headings into which it should be possible to place any of the instructions of any microprocessor currently available. (You might like to examine the instruction set of Fig. 3.1 and decide which group each instruction belongs to.)

## (a) DATA TRANSFER INSTRUCTIONS

Data transfer instructions are used to move data about in the microprocessor system, either word by word (parallel transfers) or bit by bit (serial transfers). Data is the raw material upon which the MPU chip operates, and it is important that the mpu chip should be able to fetch data from input ports, store data in RAM, move data from register to register and manipulate the position of individual bits in the accumulator, and so on. Examples are.
STA - Store accumulator in memory (Intel 8080)
TSX - Transfer stack pointer to index register (Motorola MC6800)
ld - Load data from memory into the accumulator (National SC/MP)
RAR - Rotate accumulator and carry to the right (Intel 4040)

| Binary | HEX | decimal |
| :---: | :---: | :---: |
| 0000 | 0 | 0 |
| 000 . | 1 | 1 |
| 0010 | 2 | 2 |
| 001 : | 3 | 3 |
| 0.00 | 6 | $\checkmark$ |
| $010 \%$ | 5 | 5 |
| $01: 0$ | 6 | 6 |
| 0111 | 7 | 7 |
| 1000 | 8 | 8 |
| 1001 | 9 | 9 |
| 10:0 | A | 10 |
| 10:1 | B | 11 |
| +100 | c | 12 |
| 1101 | 0 | 13 |
| $11: 0$ | E | 14 |
| $111 \%$ | F | 15 |



Fig. 3.2. Binary to hexadecimal conversion table


Fig. 3.3. Binary to octal conversion table

## Glossary of Terms

ASSEMBLERS-Software programs which translate instructions in Assembly Code (e.g. mnemonics) into Machine Code instructions which can be recognised by an mpu chip.
COMPILERS-Software programs which are used to translate instructions written in a High Level Language into Machine Code instructions which can be recognised by an MPU chip
DIRECT ADDRESSING-An addressing mode where the address of the operand is contained in the instruction.
HEXADECIMAL-A base-16 number system using the character set 0 to 9 and $A$ to $F$ which can be used interchangeably with binary. This coding system is easier to use and remember than binary, and is widely used in microprocessor literature.
HIGH LEVEL LANGUAGE-A computer language which is easy to use and understand but which requires extensive translation (compiling) into Machine Code before it can be used to control an MPU chip.
IMMEDIATE ADDRESSING-An addressing mode which uses part of the instruction itself as the operand data.

INDEXED ADDRESSING-A form of Indirect Addressing which uses a special location known as an Index register to hold the address of the operand. Index registers can be incremented or decremented under program control.
INDIRECT ADDRESSING-An addressing mode where the address of the location where the address of the operand is located is contained in the instruction.
LANGUAGE—A systematic means of communicating instructions and data to a microprocessor (or computer) system.
MACHINE CODE-A "low-Level" language understood directly by a microprocessor chip and using binary notation.
MULTI-WORD (BYTE) INSTRUCTIONS-Instructions which require more than one line or location, in program memory.
OCTAL_A base-8 number system using the character set 0 to 7 which can be used interchangeably with binary. Not as popular as Hexadecinial.
OPERANDS-Data used in machine operations (e.g. Addends, Subtrahends, Dividends, etc.).
RELATIVE ADDRESSING-An addressing mode where the address of the operand is built up by combining the current program count with a displacement value which is part of the instruction.
(b) ARITHMETIC AND LOGICAL INSTRUCTIONS

Arithmetic and logical instructions generally operate on a pair of data words, one of which is resident in the accumulator. The result of the instruction is generally a single word which is stored back into the accumulator. The execution of this group of instructions requires the arithmetic and logic unit (alU) described last month.

## Examples are:

or 4 - Logical or the accumulator with index register 4 (Intel 4040)
ADC - Add with carry (Motorola MC6800)
subr - Subtract register from accumulator (Intel 8080)

DAD - Decimal add to accumulator (National SC/MP)

## (c) BRANCH INSTRUCTIONS

Branch instructions are used to modify the sequence in which the instructions in a program are carried out. Without these instructions program operation would start at address zero and continue by incrementing the address counter after each instruction until address N was reached and the counter recycled to zero.
Branch instructions allow the contents of the program counter to be replaced by some address which may be unrelated to its previous contents, so that program flow continues by incrementing from the new start address.

This group contains a sub-group which may be termed the "conditional-branch" group, which is especially important because it allows a microprocessor to make decisions on the basis of the nature of data with which it is provided. Conditional branch instructions replace the program counter content only if some specified conditions are true; if these conditions are false, program flow continues unchanged, by counter increment.
Examples are:
JNZ - Jump if accumulator content is not zero (National SC/MP)
jun - Jump unconditionally (Intel 4040)
JSR - Jump to subroutine (Motorola MC 6800)
RET - Return (e.g. from subroutine) (Intel 8080)

## (d) CONTROL AND MISCELLANEOUS

The group control and miscellaneous contains a bit of a hotch-potch of instruction types, and is included really to provide a home for those instructions which do not fit into the other three groups!

## Examples are:

halt - Halt processor and do not carry out any further instructions (National SC/MP)
nop - No operation. Do nothing but move on to next instruction (Intel 8080)
EIN - Enable interrupt detection logic (Intel 4040)
CLR - Clear (Motorola MC 6800)

## INSTRUCTION FORMAT

By now you should be getting a feel for the sort of things we can tell the MPU chip to do when we write an instruction in a program, but you may be wondering how the microprocessor knows which memory locations it must use as operands when carrying out instructions such as LOAD, or ADD.

Taking an ADD instruction as an example, just what does the microprocessor need to be told, or to assume, in order to carry out the addition in the required way? Well, it needs to know . . .
(i) The operation to be performed (In this case ADD) ,
(ii) Where the first operand is
(iii) Where the second operand is
(iv) Where it must put the result
(v) Where the next instruction is

An instruction word could be imagined where all these facts are specified by certain sections of the instruction code, but a moment's thought reveals that this would demand a very long word length, say four bits to specify ADD, eight bits for the first operand address, eight bits for the second, and so on.
An MPU chip using explicit instructions like this would be extremely versatile and powerful, but impossibly clumsy and difficult to work with, and so unsuited to the low cost applications microprocessors are intended for. A less versatile but much more compact instruction word format is generally used for MPU chips where most of the required information is assumed by microprocessor rather than being explicitly stated when the instruction is written into the program.
(i) The operation to be performed remains explicit (e.g. ADD)
(ii) The first operand remains explicit and is coded as a memory or register address
(iii) The second operand address is assumed to be the accumulator
(iv) The result is automatically stored back into the accumulator
(v) The next instruction address is assumed to be the next in numerical sequence (unless a branch instruction is involved) and so is found by incrementing the program counter.
Even with this more compact instruction format it is often necessary to use more than one instruction word to store the necessary code, giving rise to what are termed "multi-word" or "multi-byte" instructions, which of course carry with them the necessity to increment the program counter by more than I to find the next separate instruction in sequence.

## ADDRESSING MODES

We saw above that most microprocessor instruction words contain a single address, the address of the first operand, but we now have to examine the way in which the address is specified because there are a variety of addressing modes which can be employed, and using them wisely can make our programs more efficient. As an example, the Motorola MC 6800 has no fewer than seven addressing modes, which puts it ahead of the SC/MP and 8080 with four, and the 4040 with three, unless you happen to be of the opinion that seven modes are excessive and tend to leave you "spoilt for choice".
mPU chip manufacturers all have their own ideas about what their addressing modes should be called, and how they should operate, and it will be necessary to study these in the handbooks when the use of a particular chip is contemplated.

For the purposes of this introductory series we have chosen to describe four modes which can be considered fundamental, and from which the others have evolved.

You may find it useful to compare the addressing modes of a particular MPU chip with the modes described here, trying to spot the similarities and the differences, where there are any.

## IMMEDIATE ADDRESSING

The most straightforward mode of the lot is lmmediate addressing because the instruction does not actually contain an address: it contains the data itself which may be part of the first instruction word or contained in a subsequent word.

This mode is useful for fetching constants which do not change once the program has been written; this "unchanging" quality is vital because instructions, and therefore immediate data, are often stored in ROM.

## DIRECT ADDRESSING

direct addressing is the mode which one would instantly recognise as being a necessary part of any processing system. In this mode the instruction contains the address in an explicit form, so that for an ADD instruction for example, the MPU chip is told precisely where to go in store to find the number to be added to the accumulator.

## INDIRECT ADDRESSING

Indirect addressing is a very useful but at first rather tricky-to-understand mode where the instruction contains the address of the address of the data (now read that again!). That is to say that the address contained in the instruction does not point to the data itself but to a location (which may be a location in store or a register) which contains the address where the data is to be found. A particularly common form of indirect addressing is called indexed addressing where the instruction points to a special register called the index register which contains the required address.

The strength of indirect addressing lies in the fact that it is not necessary to know precisely what the final address is when the program is being written, it can be computed in, say, the index register so that it is in effect data dependent rather than program dependent.

## RELATIVE ADDRESSING

In relative addressing the instruction contains not the address of the data, but a displacement value which is added to the program counter contents to make the effective address. This addressing mode is used only with branch type instructions where it allows looping backwards or forwards through a program without the need to specify absolute addresses. Looping backwards is possible because the displacement can be a negative number in "twos-complement" binary notation.

## PROGRAMMING TECHNIQUES

Programming a microprocessor is a fascinating and rewarding experience and well worth the investment in time required 10 learn the necessary fundamentals. The best way to learn is by actually doing, and to get to the stage where you are ready to try your hand at simple programs, you should start by studying examples of the type which MPU manufacturers often provide in their literature. The Intel "MCS-40 USERS MANUAL"" is particularly rich in programming tips
and examples, although of course these are restricted to the particular case of the 4040 chip.

Even if you are quite used to writing programs for a large computer in a high level language like Fortran, you will find the strict format of microprocessor machine code a challenge at first, but by becoming familiar with the hardware and instruction set of your choice of MPU you will soon become adept at the art!

## HARDWARE/SOFTWARE TRADE OFF

The success of the microprocessor is due to the fact that it enables a large amount of hardware to be replaced by changeable software in the form of programs. When an MPU is applied to a particular application, however, the designer has to decide just how to create a balance between the amount of hardware and the amount of software employed, so as to suit his particular circumstances.

Take the case of a decimal keyboard connected to an mpu system. Should the designer provide a series of gating functions to turn each of the "one out of ten" .key closures into a corresponding four bit BCD code, or should he read all ten lines into the MPU and provide a program which does the job? Should he provide a TTL monostable key-debounce circuit, or should he incorporate some timing routines into his program which achieve the same result?


Fig. 3.4 Switch Check flow chart

These are questions which can only be answered in context, depending as they do on how much program space is available and how much it costs, and how much room there is for extra hardware, and how much it, in turn, costs.

## A PROGRAMMING EXAMPLE

We do not have sufficient space here for a comprehensive programming course, but in order to at least set the scene, we can trace through the evolution of a particular program example.

The job is to examine four toggle switches and depending on which one of them is set (if any) to jump to a particular segment of a program. The microprocessor to be used is the Intel 4040, and the switches are connected to ROM input port number four.

The first step in writing the program is to draw up a flow chart which describes in an easy to understand, graphical form the operations necessary to complete


Fig. 3.5. Action of RAR instruction
the task. Flow charts can be of a very general form which can be understood by everyone and could be adapted for use with any microprocessor, or they can be detailed, and suited only to a particular chip architecture and instruction set. We find it helpful to let flow charts evolve from the general to the detailed form as ideas are added and the constraints of the chip are taken into account. Our final flow charts usually detail the actual instructions we intend to use.

## SWITCH CHECK

Fig. 3.4 shows the flow chart for our Switch Check program in an "Inbetween" stage which is flavoured by the 4040 but nevertheless easy to follow for those new to the concept.

The flow chart already shows that a strategy has been chosen to achieve the goals set down, but it should be remembered that there are several other ways to achieve the same ends, and this one is not necessarily the best. The strategy used is that of a "skip chain", a simple but effective technique for examining the state of input lines, which in this case also incorporates a "wait loop".

When the Switch Check routine is entered (from the larger program of which it is a part) it begins by reading the state of the four switches into the accumulator by first addressing port 4 (FIM, SRC) and then reading it (RDR). The switch states can be examined by shifting each bit in turn into the carry flip-flop and using the JCN (jump on condition) instruction to either vector the program to the appropriate routine if a switch is SET, or to continue round the loop if it is not.

Once "Switch Check" has been entered there is no way of leaving it until a switch is closed, hence the "wait loop" tag, although this feature could be dispensed with, if appropriate, by leaving out the JUN instruction and allowing a further exit from the bottom of the routine if no switches are set.

Table 3.1
"SWITCH CHECK" PROGRAM SEGMENT

| LABEL | MNEMONIC | OPERAND | COMMENT |
| :---: | :---: | :---: | :---: |
| LOOP | FIM | RP0, 40 Hex | Load port address code to register pair 0. |
|  | SRC RDR | RP0 | Select port. <br> Read port into |
|  |  |  | accumulator. |
|  | RAR |  | Rotate switch 1 into CARRY. |
|  | JCN | CARRY SET, <br> "ROUTINE 1" | Jump to address "Routine |
|  |  |  | 1 '' if CARRY is set. |
|  | RAR |  | Switch 2 to CARRY. |
|  | JCN | CARRY SET, <br> "ROUTINE 2" | Test switch 2. |
|  | RAR |  | Switch 3 to CARRY. |
|  | JCN | CARRY SET, "ROUTINE 3" | Test switch 3. |
|  | RAR |  | Switch 4 to CARRY. |
|  | JCN | CARRY SET <br> "ROUTINE 4" | Test switch 4. |
|  | JUN | "LOOP" | Jump to LOOP since no switches set. |

Notice that the switches are ranked into a priority order by the fact that switch one is checked first and so on. If the routine is entered with more than one switch set, the lowest number switch will be the only one recognised.

The JCN instruction can be used to test for a variety of different conditions such as jump if carry is 0 , jump if carry is 1 , jump if accumulator contains all zeros, etc. In our case we wish to test bits individually and so we rotate them into the carry flip-flop one at a time using rar which rotates the accumulator and the carry data one bit at a time as shown in Fig. 3.5.

The complete program, using mnemonic instruction codes is shown in Table 3.1. Before this could be entered into a PROM and used, the mnemonics would have to be replaced by their hex and binary equivalents and the addresses which here are represented by names such as loop or routine no 1 would have to be specified absolutely.

## ASSEMBLERS

If an ASSEMBLER facility was available on a larger computer or a development system it would be possible to enter the mnemonic codes as shown in Fig. 3.6, complete with address names, or labels as they are called. In this case the assembler program would convert the mnemonics into the required machine code and insert absolute addresses where necessary, a facility which helps the programmer a great deal but which of course is expensive in storage space and not available for use with the cheaper prototyping and development systems.
NEXT MONTH: Peripheral Chips and Hardware.


## VENUS

The Russians have tackled the problem of the brilliant but hostile planet Venus with great vigour.

Mythology would have us believe that Venus represented love and happiness in the form of a goddess and that Mars was the warlord and the mark of aggression. In the event it is clear that the roles are reversed. However, in spite of the millenia of observations it is only in the last few years, the space years, that knowledge of the planet and its puzzles has been brought to the stage of acceptance in some detail.

Before the advent of the Mariner and Venera spacecraft the cloud cover was all but completely opaque. Many enterprising amateurs spent countless hours observing the planet. The members of a section of the British Astronomical Association devoted to observations of Venus produced drawings which were built up into a conjectured picture of the surface features. As it turned out these pictures and the drawings were remarkable for their anticipation of the real surface. These features were first confirmed by radar measurements.

## CHANGES

The rotation period of the planet had undergone many changes and the figures varied from a few days to hundreds of days. Finally it was found to be 243 days, and that its direction was opposite to that of the Earth. Thus the Sun would rise in the west and set in the east. Its year is shorter than that of the Earth, just over 224 days. The poles are only tilted by 3 degrees and therefore the planet does not experience seasonal changes like the Earth.

## CLOUDS

The cloud cover rotates very rapidly and the tops of these clouds have a rotation period of about four days. Their velocity is therefore very high reaching as much as 100 metres a second.

The white clouds of the Earth consist of drops of water but Venus is very different. The quantity of water vapour is very small being only 5 parts in $10^{4}$. This was established spectrographically. The nature of the spectogram showed that there were liquid drops in the clouds. Such a condition at the temperature of 233 K indicated that the liquid could not be water.

The surprise that awaited the investigators was that it could only be due to sulphuric acid. It was found that these liquid drops were 75 per cent concentrated sulphuric acid. This was finaliy resolved in 1973.

Between the years 1967 and 1975 the Russians launched seven vehicles of the Venera class and in addition there were probes which entered the atmosphere. In 1967 Venera 4 indicated that there was at least 97 per cent of carbon dioxide in the atmosphere.

Though liquid water cannot exist on the surface of Venus, a reaction between the surface and the carbon dioxide of the atmosphere takes place. On Earth this reaction is a slow one. On Venus, with a temperature of the order of 750 K , such a reaction is very rapid. The other similar reactions that take place include hydrochloric and other acids. The whole chemical effect in the atmosphere contrasts with that of the Earth.

There is little or no magnetic field on Venus but an ionosphere does exist. This does fit in with current thinking, that the intensity of a dipole field depends on the angular velocity. A great deal of data is now available about the Venusian atmosphere and the ionosphere. One interesting fact that emerges is that short wave radio signals would be limited to the daylight hours for global communication.

There are few signs of mountains though some radio-astronomy telescopes have shown 2 km high hills. It is possible that as a result of the high surface temperature, the crust is more malleable and that the possibility of mountain formations is remote. There is no sign of impact modification of the surface and this could mean that almost all the effects will have been by volcanic action.

Much still remains to be done before a conclusive picture of Venus can be drawn. The effect of the solar wind shows quite a different picture as compared with the Earth. The density of the corona is directly dependent on solar activity.

## HAWAIIAN INFRA-RED TELESCOPE

A new development is possible in connection with the Hawaian telescope. The $£ 2.5$ million telescope is being built by Grubb Parsons and is to be put into service next year. The mirror is 3.8 metres in diameter and is the largest of its kind in the world.

Designed for infra-red operation the tolerances can be much wider than for optical work. This is reflected also in the guidance systems. The original tolerance was set at 2-3 arc seconds, but Grubb Parsons have indicated that the mirror now in the polishing stage is so good that a little extra polishing could bring the tolerance to 1.0 arc second.

This would effectively halve the time for observations. This will not be to the standard for optical work except for very short exposures. In spite of this there would be a gain for optical astronomers. The improvement for the infra-red application is very great.

The cost of doing this extra polishing is $£ 12,000$. Surely this is a small price to pay for such an improvement. Since this means that the instrument will carry out a programme in half the time originally scheduled it is the same as saying that for an extra $£ 12,000$ the astronomers will have the equivalent of two telescopes at a cost of $£ 2.5$ million each. This is a bargain too tempting to be ignored. No doubt the SRC will see it this way.

## LANDSAT

The value of the services that can be offered by Landsat has been so much in evidence that the Soviet Union is pursuing plans to build a terminal for themselves. So far applications have come from a number of countries. These include Australia, Norway and Sweden, India, Japan and at least one Arab State.

For over a year a new 13 -channel scanning system has been the subject of a cleaning up process. Interference has been very considerably reduced and the false colour reproduction developed to a specialised degree. This enables colour to indicate differing conditions of surfaces, leaves and plants as well as tree growth.

Extensive research has shown that water, because of its changed state when taken up in plants, can be an indicator of great value. It is certainly the case that the value of the pictures for countries which have plans for long term development in agriculture and forestry will be considerable.


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T100
TL340
F100L

## HI-Z PRE. AMP

Are you bored with analogue integrated circuits which do everything but polish your shoes, and digital integrated circuits which boast so many gates and functions that it sometimes seems that their I.Q. is higher than yours? Do you yearn for the simple life again, a return to the days when an individual transistor was treated as something to be revered, rather than as a sort of plastic giveaway? You do? Well then perhaps I can interest you in the T100 and T300 devices from Siliconix, which do not boast kilo-transistors or mega-gates, but simply a well designed single j.f.e.t. stage which with the aid of a simple four-pin package, forms a useful and capable high-Z preamplifier stage for use with microphones and other transducers.
Inside the TO72 can there lives just the j.f.e.t., a source load resistor, and back-to-back Schottky diodes which form the high impedance gate bias resistance. The devices feature an input impedance of at least 200 megohms, and an output resistance of between 500 and 1,300 ohms, together with a very low noise voltage output of less than 4.0 microvolts in a 10 kHz audio bandwidth.

These integrated circuits are, of course, high-to-low impedance converters, and can be used wherever such a function is required. The T100 is intended specifically for low power applications such as hearing aid input stages, where its 50 microamp supply drain is important, while the T300 is a general purpose device drawing 350 microamps. Both can operate from supplies of from 1.3 volts to 30 volts, and are ideal for matching to ceramic, electret-capacitor, air-capacitor, and piezo-electric microphones.

## GOODBYE ZENERS

If you could design your own ideal Zener regulator diode what would you list as desirable characteristics? I would want low impedance, a sharp knee, temperature compensation, low noise, high dissipation, and a really easy to use package. If you added to
that list a programmable voltage rating, so that only one type of component has to be purchased to provide any voltage between three and thirty volts, you would have designed yourself a Texas Instruments' TL430 which must surely replace standard Zeners in most applications.

The manufacturers call it a "precision three terminal shunt regulator" but it is almost as easy to use as a standard Zener, and nowhere near as pricey as it sounds. "Zener" voltage is normally programmed with the aid of two fixed resistors, but if you like, you could use a skeleton pot, and hence build yourself a "VariableZener" which could be very useful in a wide variety of applications which at present require numerous resistors and an op. amp., in addition to a conventional reference diode.
Performance of the TL430 is better than its predecessor in every respect, slope impedance is only 1.5 ohms, over the current range 600 mic oamps to 100 milliamps, as compared with 5 ohms to 100 ohms for standard Zeners over a more restricted current range. Temperature stability is 0.005 per cent per degree Celcius, which is as good as the most expensive Zener devices, which are themselves restricted to a voltage of around 5 to 6 volts to achieve that sort of performance. Noise output is low, and the three-lead TO92 or eight-pin Minidip package will dissipate a creditable 775 milliwatts to make the TL430 almost twice the regulator that the standard 400 milliwatt BZY88 is!

## BRITANNIA RULES O.K.?

I sometimes get a bit demoralised when I realise that most of the devices I talk about in this column actually originate in that former colony of ours, where they eat hamburgers and chew gum!
Wouldn't it be nice, I muse, if just for once, a British firm could summon the courage, the know-how and the money, to produce a real worldbeater of a product which would give us something to be really proud of, something which would demonstrate that we really do have a part to play
in advanced semiconductor technology.

Well, I can stop musing, because Ferranti have actually gone out and done it, by producing a super, wonderful, powerful, 16 -bit, and above all, BRITISH microprocessor which really promises to deliver the goods in this, the most challenging semiconductor technology of them all!
The name of this patriotic new chip is the F100L, and it has the distinction of being the first microprocessor which has been wholly designed, developed and manufactured in Europe, without any American connection at all. Development was sponsored by the Ministry of Defence, God Bless them, and this will no doubt guarantee a market in a host of advanced defence projects and give a hefty boost towards profitability. The F100L can stand on its own, though, and should provide some healthy competition for other 16 -bit micros such as the Texas 9900 and the National Pace, which it can outperform in many respects.

The chip is produced in the homegrown Ferranti CDI process which is a bipolar technology providing LSI circuit density and low power ( 375 mW ) along with typical instruction execution times of only 3 to 4 microseconds. The chip is just 0.23 inches square and uses no less than six feet of aluminium track interconnections to hook up about 7,000 separate components into a microprocessor which is not far short of a full sized computer in the performance that it offers.

As far as we, as amateurs, are concerned, the F100L will enable us to put together a home computer of formidable power and ability when the chips become freely available during the next year or two. The single phase TTL clock and the simple 5 volt supply requirements should make the assembly of an F100L computer fairly straightforward, and Ferranti are going to back up the basic MPU chip with a range of CDI interface chips in the near future to replace the standard TTL components presently necessary.
This is a chip we can all be proud of (after all we do have an investment in it) so let us wish it every success for the future.

MICRO '77 is the name of Cramer's current series of microprocessor seminars being given at various points in the country, culminating with one in London on April 25. Cramer call themselves the "Heavyweight Microcomputer Team". perhaps because for each of their three main franchises, Motorola, Texas Instruments, and Zilog, they have two specialists available, one with hardware and one with software knowledge.

We went along to the Cambridge seminar held on February 8.

## THE PAST AND THE FUTURE

The first part of the programme was an extremely lively introduction to microprocessors presented by Ian Perry. It is almost impossible to talk about the history of microprocessors without mentioning Intel; the first microprocessor was the 4004, designed in 1971 by them for Decimo, the calculator manufacturer, who saw that the way their small firm could compete in the mass calculator market would be to divide the single calculator chip into its four parts: the CPU, Ram, ROM and I/O. A whole range of calculators could then all use the same cPU and Ram, differing only in the rom programme and in the $1 / 0$ and keyboard.

Today about 50 per cent of cPus produced go into data terminals. Most second-generation CPUS, such as Motorola's 6800 and the Intel 8080, have followed along similar lines but now the trend seems to be going full circle with the re-integration of all the parts onto one chip. Again the innovator is Intel, and their 8748 , to be released later this year, will combine the CPU, a PROM and programmable $1 / \mathrm{o}$ onto one chip.

In a glance into the future Ian Perry predicted some interesting advances. Memory is to grow ever cheaper, and 16 K dynamic rams should soon be available at a competitive price. The first low-power cmos prom is now available from Harris, though the price is high. The greatest possiblities lie in the area of subfunctions: chips which work alongside a CPU to extend its capabilities.

For example, Advanced Micro Devices have a numbercruncher chip on its way which will give multiply and divide functions; also in the pipeline are floppy-disc controllers, c.r.t. controllers, keyboard interfaces and sophisticated I/O packages, all of which leave the CPU free to do greater things.

Low power systems are still a year away; although TI make an $I^{2} \mathrm{~L}$ version of their 9900 , the price is prohibitive. The present cmos cpus from Intersil and RCA have reduced instruction sets due to the unsolved problems of mass integration, but silicon on sapphire cmos may provide the answer.

## MOTOROLA

The M6800 from Motorola is now the most popular CPU in Europe, and perhaps because of its familiarity, this part of the programme was less interesting. This popularity may in part be due to Motorola's sensible policy of providing a wide spread of development products, and the whole range was on show at the seminar. Although Cramer will be glad to sell you an Exorciser plus TI Silent 700 terminal for around $£ 4,000$, for the impecunious among us there is now the self-contained D2 kit which. like the SC/MP kit with keyboard, eliminates the need for a terminal by enabling you to enter programs directly in machine code from a hex keyboard, with readout of the address and data on six seven-segment displays.

The problem of how to store programs is overcome by the provision of an audio cassette interface, and the system includes 256 words of RAM and a PIA. At $£ 188$ it seems good value. (A review of this kit will appear in a subsequent issue.)

In the mid-price range there is the Polyvalent Development System, comprising an ASCII keyboard, a tiny vDU with interface board, and various computer boards which can be bought separately, or together for $£ 771$. For example, the Display Interface board will convert any TV into a VDU with the addition of a keyboard, and costs $£ 200$.

## ZILOG

In presenting the Zilog Z80, Roger Phebey was understandably enthusiastic as the Zilog story seems to be one of uninterrupted success. Zilog Corp. was founded in 1974 by two Intel executives, who have since been joined by top men from Intel, Motorola and Fairchild. In 1975 M. Shima came from Intel, where he had designed the masks for the 8080 , and developed the 280 CPU . The first units were manufactured in mid-1976 by Mostek for Zilog while their factory was being completed, and now Mostek second-source it.

From the start Zilog decided to make a souped-up 8080 , and in fact the $\mathbf{Z} 80$ contains the actual 8080 codes as a subset. But the likeness ends there; the Z 80 has twice as many registers, twice the number of instructions including additional 16-bit operations, block transfer and search which in one instruction operate on blocks of up to 256 words of memory, and there are two modes of interrupt besides the 8080 mode. The chip uses a single 5 V power rail, and a single-phase clock. The $Z 80$ is not very accessible to the amateur as yet; the cheapest complete system includes two floppy discs and sells for $£ 4.200$, and the only board available as yet is the MCB at $£ 347$; for this you get 4 K of ram though.

## TEXAS

The Texas Instruments' TMS9900 is one of the few 16-bit cpus currently available, and with its multiply and divide instructions it seems very minicomputer-like. It has none of the usual on-chip registers; instead a block of 16 contiguous Ram locations pointed to by the workspace pointer provides 16 "primary" registers, making interrupt and subroutine handling especially efficient. The chip has 16 data lines and so needs an impressive and expensive 64-pin package; furthermore it requires a four-phase clock.

Prospective computer builders may prefer to wait for the TMS 9800 due later this year, which provides an onchip oscillator and uses multiplexing to fit the same functions in a 40 -pin package. At present there is only the 990/4 development system available which with a Silent 700 terminal costs about $£ 4,000$, but Cramer is developing in collaboration with Ti a Micro-99 microcomputer board which, when it is available, will be $£ 310$.

Finally a point stressed at the seminar was: do not feel obliged to buy the whole system from one manufacturer. It is perfectly good practice to link up one CPU with a different memory and a third i/o device. Also once the application is decided upon it is probably cheaper to replace general purpose $1 / 0$ devices by custom wired latches. Amateurs now have an extremely wide range of devices to choose from, and can construct a complete microcomputer for well below the cost of comparable ready-built development systems.

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adjust ment of worn of pitted contact breakers. based on the electrical contact time and not on the mechanical distance between the points The improved accuracy obtatnable by using this self contained unit ensures greater precision than may be obtained using Feeler Gauges.

[^2]

This unit was designed with the younger generation in mind. However, it has certain differences with its popular electromechanical counterpart. First. instead of the usual apples, plums and cherries forming combinations, numbers from 0-9 are displayed in the form of three seven segment displays. Second, the machine will only indicate when a player has won, but will not actually pay out. However, it is possible to bring a signal out to operate an electromechanical device if a prize giving feature is required.

## FEATURES

Six winning numbers are internally set-up and when displayed the "Win" l.e.d. will light. As can be seen from the prototype these selectable numbers must be carried on the panel for the player's reference when making a decision for "holding" one or two numbers.

It is possible to change the six winning combinations from time to time as desired.

74 series TTL logic is used in the main which allows a compact unit to be made- $\mu \mathrm{p}$.

## TIMING CIRCUITRY

When the start lever is operated a positive going pulse is produced as a result of the microswitch opening (Fig. 1). This pulse triggers monostable IC2 into operation so that pin 6 goes high $(+5 \mathrm{~V})$ for approximately five seconds. The pulse period is not critical, hence the use of the electrolytic in the timing circuit.

As pin 6 is high the three gates that form IC3 are all enabled so that pulses from the three unijunction oscillators can pass through and be counted by the three 7490 divide-by-ten counters.

By K. Amor




Fig. 1. The timing, display and win logic circuitry for the One Armed Bandit. The shaded panel embraces the Veropins used for patching arbitrary winning combinations. The six combinations used in the prototype are in the top table with patching for combination " $A$ " indicated by linking the $X s, Y s$ and $Z s$. Pin diagrams for all semiconductors are given
 connected to the emitter and the actual running frequency value is $1 / 0.7 \mathrm{CR} \mathrm{Hz}$.

As can be seen from Fig. I the oscillators run at three different frequencies of approximately $4 \cdot 5,7 \cdot 6$ and 9 kHz .

These frequencies are sufficiently high and different so as to reduce the possibility of the same number appearing in successive operations of the machine. Also, since they are free running, random number selection is better.

## NUMBER DISPLAY

The gated serial pulses from the unijunction oscillators are converted to parallel binary form at the counter outputs. When the monostable pulse finishes, the gates of IC3 are cut-off inhibiting the drive to the counters. Since these will have rippled through many times during the five second period, random binary numbers will be stored at their outputs.

To convert these to decimal at the l.e.d. displays they are fed to 7447 decoder/drivers. Resistors R13-R33 are for limiting current to the segments. With the values given the current is approximately 10 mA /segment which is an economical figure but provides adequate display brilliance.

## "HOLD" CIRCUITRY

The negative going edges of the five second start pulses are counted by IC1 which forms the basis of the "Hold" circuitry.

Every four start pulses will cause ICl pin 8 to go low. This stays low for a further six operations of the machine.

During this time a "Hold" will be available since the 7410 gates can be inhibited by the operation of the switches S2-S4. When any one of these are thrown the associated gates are connected to the "Hold" counter which means that the number in this arm is retained.

To show "Hold Available" the output from ICl is inverted by IC10 to drive TR1 so lighting the l.e.d.

At the end of a "Hold Available" period the circuit will automatically release the display irrespective of switches in the "Hold" position. However, it is advisable to clear switches prior to the "Hold Available" light coming on again.

## "WINNING" LOGIC

Six winning number combinations in groups are available but the selection of groups is arbitrary. In the prototype these were $888,750,625,542,427$ and 314.

To implement the "Win" light signal Veropins are arranged as in Fig. 1. Three groups of ten take the 0-9 outputs from the decoders and six groups of three provide the inputs to inverter.


Fig. 2. The power supply circuit


Fig. 3. Suggested i.c layout for the 0.1 in matrix win logic Veroboard. A Veropin arrangement for patching is shown

To wire a "Win" combination, say 888 , leads are connected from the 8 s at the decoder outputs and then to a group of three pins (the order of wiring is shown as $X, Y, Z$ in the figure).

The inverters and gates are used to get the logic levels right.

An eight input NAND gate receives the six levels from IC18 and IC19. When any one of these is low a "Win" condition will be indicated due to base current flow. For illustration, the various required logic levels to achieve an 888 win are shown.

## POWER SUPPLY

The power unit is shown in Fig. 2. Here a mains transformer supplies 6.5 V r.m.s. to a full wave rectifier. The rectified output appears at C5 as +9 V for supplying the u.j.t. circuits.

For the logic and display a 5 V 1 A regulator is used. This is short circuit protected and will allow almost an amp to be drawn before limiting.


Fig. 4. In the prototype the timing and display logic circuitry was mounted on an ITT ISEP dilboard. Since this could be difficult to obtain and expensive, an alternative Veroboard layout for just the semiconductors is given


Showing the control panel and internal assembly of the prototype. The p.s.u. is mounted on the base panel. The timing and win circuit boards are shown sandwich mounted on the rear panel using nylon nuts and screws. Cable ties should be used with looms

## CONSTRUCTION DETAILS

A suitable case is the "U" type from H. L. Smith Ltd. With all the holes drilled and display cutout completed. the panel is ready for lettering. Before this is carried out the aluminium front panel should be rubbed with steel wool. This produces a very pleasing brushed surface.

The front panel lettering can now be carried out having decided what the winning sets of numbers are This type of dry transfer lettering can be purchased at a good stationer's.

Numbers. letters and lines come in sheet form and are best applied by rubbing over the top of the transfer with a soft lead pencil, taking care that no grease is allowed on the surface below. It is advisable to spray the whole panel with a protective lacquer to prevent lettering from being removed and the aluminium discolouring. Leave this to harden for about three
hours before mounting components. The back bottom, and sides may be finished off by spraying with a cellulose aerosol paint.

A dark colour is suggested so as to contrast with the front panel.

## LEVER ASSEMBLY

The mechanism in the prototype for the microswitch S l is made-up of an Arrow-Hart rotary switch shaft unit. The modification to the assembly for limiting the rotary switch action and operating S 1 is shown in Fig. 5.
It should be possible for constructors to modify some $\frac{1}{4}$ in shaft multiway switch. making up a handle and so achieve this assembly cheaply. Others will probably opt for a simple press switch rather than this elaboration for authenticity.


Fig. 5. Switch assembly modification for both limiting the shaft movement and actuating a microswitch

## 

## CAR IEATS



This simple unit provides an audible warning if lights are left on unintentionally at the end of a journey, but allows parking lights to be used when required.


PRACTICAL


OUR JUNE ISSUE WILL BE PUBLISHED ON FRIDAY, MAY 13, 1977


Amateur photographers often express a need for an accurate, reliable and repeatable timer for enlarger control or for timing various darkroom jobs; such a need also occurs in other hobbies. This article describes an accurate, repeatable solid state countertimer. By using digital techniques, an accuracy of about $\pm 0.5$ seconds is achieved with no need for calibration, and the circuit has been designed to be as simple and cheap as possible to construct.

## THE DESIGN

Analogue circuits which provide these facilities have been described in the past, but these always depend ultimately on CR networks for timing accuracy. As home constructors have found, it is neither cheap nor easy to obtain components, especially capacitors, of sufficient accuracy and stability to produce useful results. Furthermore, accurate calibration of the control element (usually a variable resistor) implies access to an accurate timer, which is rarely the case.
This circuit overcomes both these problems by using the mains power frequency as a standard frequency source. The electricity generating boards maintain this frequency to better than one per cent; since the frequency and thus the period of the mains is well known ( $50 \mathrm{~Hz}, 20 \mathrm{~ms}$ ), and the division factor in counting this frequency is known, the timer is self-calibrating.
Another requirement in darkroom timers is a visible indication of elapsing time. In an analogue circuit this can only be provided by a meter, with attendant problems of lighting and reading accuracy. It is often difficult for the home constructor to provide safe-light illumination of the meter movement. The present design takes advantage of light-emitting diode sevensegment displays as numeric digit indicators. The great majority of l.e.d.s emit in a narrow band of wavelengths in the red part of the spectrum, and consequently provide self-illuminating digits which are inherently "safe".

## CIRCUIT DESCRIPTION

Mains power is transformed down to 9 V r.m.s. which is rectified by D2 and regulated by IC3 to provide +5 V d.c. (see Fig. 2). This regulator is more expensive than a Zener diode or simple series-pass transistor regulator, but it is overload- and overheatprotected and virtually impossible to destroy. It automatically limits current to about one ampere and this feature provides excellent protection of expensive components during testing.

The 9 V a.c. is also passed through a current limiter to a Zener diode D1 which half-wave rectifies and clips the top off the remaining half cycle to provide an approximately square 50 Hz waveform, as shown in Fig. I. Note that the low level of the waveform is not 0 V due to the diode forward drop (about 0.6 V ). However, TTL manufacturers guarantee that any voltage below 0.8 V will be accepted as a low so this 50 Hz is fed directly into a scaler or divider. This consists of two chips, type 7490 , which each contain a divide-byfive scaler and a flip-flop to divide by two. The first chip ICl divides the 50 Hz by 5 to give 10 Hz , then the next, IC2, divides by 10 to produce a 1 Hz signal, i.e. a square wave of period exactly one second. This constitutes the standard frequency which is counted in the timer section.


Fig. 1. The a.c. output of T1 is rectified and clipped by Zener D1 to provide a 50 Hz square wave drive to the divider chain


Fig. 2. Circuit diagram of the complete darkroom timer. Relay RLA is a twincoil reed type with the two coils connected in parallel for 5 V operation (see Components list)

## TIMING

The actual timing is done by a chain of up-down decade counters type 74192. These devices count up from zero to nine, then provide a carry out to the next stage, or count down from nine to zero, then provide a borrow signal to the next stage. Control of direction of counting is simply a matter of routing pulses to either count up or count down inputs. As well as this serial mode (one bit at a time), the counters can be preset by a parallel load (all four bits which define a decimal digit are used at the same time). This facility is the key to the timer mode of operation. The contents of the counter appear as a parallel 4-bit binary-coded decimal ( $B C D$ ) digit on the four output lines, which can be displayed on a l.e.d. digit by using a $B C D$ decoder chip.

In this design, three such counters are connected in series; that is, the carry and borrow outputs of the low digit IC 10 (units) connect to the up and down inputs respectively of the higher digit IC9 (tens), and similarly for the tens digit to hundreds digit (IC8) connections. The bCD output from each stage is taken to a Fairchild 9368 decoder-driver which converts BCD to 7 -segment code suitable for driving commoncathode l.e.d. digits, which in this case are Fairchild FND 3570.375 in digits. If needed, more digits can be included by simply adding identical stages.

## PARALLEL LOAD

The 74192 chips are provided with a "clear" input which when pulsed high sets the contents to zero. These pins are connected together and taken to a pushbutton S4 to provide zeroing of the counter. Each chip also has its "parallel load enable" input taken to an individual button $\mathrm{S} 5-\mathrm{S} 7$, which when pressed,


Fig. 3. (a) A heavy current is drawn from the 5 V supply via the low value resistor if a BCD thumbwheel switch is used to drive the counter chip direct
(b) Using a BCD complement switch and one inverter considerably reduces the load on the 5 V supply as a higher value resistor can be used


The prototype timer was built in a specially made aluminium case providing separate compartments for the logic and mains wiring for improved interference rejection
causes the contents of that counter to be set to the BCD digit coded on its input lines. Since both these functions set a determined state in the counter, switch bounce cannot alter the contents, so simple, inexpensive switches can be used.

## THUMBWHEEL SWITCH

The preset input digit is set on a BCD-coded thumbwheel switch. The switch is wired up to produce complement or inverted code, that is, logic one is low and logic zero is high. The code is then inverted to the correct levels by part of a hex inverter type 7404 (IC10). The reason for this is that if the code is generated directly by the switch, each bit in the digit must be connected to 0 V by a low resistance to provide current sinking for the inputs of the 74192 chips when logic zero is selected; so when logic one is selected, this resistor draws heavy current from the +5 V supply (see Fig. 3). The inverter chip is needed anyway to provide the alarm clear function, to be described shortly.

Two gates of a triple three-input NAND chip are used to make a set-reset flip-flop to detect and operate the alarm function which indicates timeout in timer mode; this flip-flop is set by the start switch and reset by either the CLEAR switch or a carry output from the top digit of the counter chain. The outputs of this flip-flop and of the start switch are ored with the 1 Hz pulse input in lC4a to inhibit this pulse from reaching the counter if the flip-flop is reset or the sTART button is being held down.

The output of the flip-flop drives a reed relay RLA via a switching transistor TR1. This arrangement isolates the logic and mains portions of the circuit. The relay switches a triggering signal to a triac to connect and disconnect mains power to an output socket SKA. A s.p.s.t. switch in parallel with the relay allows the socket to be permanently powered independent of the timer for focusing, etc.

## OPERATION

To see how the unit operates, first consider the counter mode of operation. When the clear button is pressed, the flip-flop is reset which means the output which feeds back to the start gate IC4a is low, forcing the output of that gate to be high. This line is switched to the count up input of the counter chain, which contains all zeros. Thus the digits displayed are all " 0 ", and the carry output of each chip is high, and in particular, the top digit whose carry is connected to the flip-flop.

## COMPONENTS

| Resistors |  |
| :---: | :---: |
| R1, R2 | $1 \mathrm{k} \Omega$ |
| R3 | 200S 1W |
| R4, R8-R14 | $10 \mathrm{k} \Omega$ (8 off) |
| R5-R7, R15 | $220 \Omega$ ( 4 off ) |
| $\frac{1}{2} W$ unless | therwise specified |
| Capacitors |  |
| $\begin{array}{ll} \text { C1 } & 2,2 \\ \text { C2, C3 } & 0 \cdot 1 \end{array}$ | 20 F 16 V electrolytic F ceramic disc |

## Semiconductors

| IC1, IC2 | 7490 |
| :--- | :--- |
| IC3 | LM309K 5V regulator |
| IC4 | 7410 |
| IC5-IC7 | 9368 PC Fairchild (3 off) |
| IC8-IC10 | 74192 (3 off) |
| IC11 | 7404 |
| TR1 | BC107 |
| LED1-LED3 | FND357 Fairchild (3 off) |
| D1 | BZY88 C5V1 $5.1 V 400 \mathrm{~mW}$ Zener |
| D2, D3 | 1N4004 |
| CSR1 | 6A 400V Triac |

## Switches

| S1 | D.P.S.T. mains toggle |
| :---: | :---: |
| S2 | S.P.S.T. mains toggle |
| S3-S7 | S.P. push-to-make push buttons, see text ( 5 off) |
| S8 | 3 -pole 2 -way rotary or toggle (e.g. RS Components 339-471) |
| S9 | Thumbwheel edge switch, BCD complement output (e.g. RS Components 338 181, plus mounting cheeks 338-197) |

## Miscellaneous

T1 9 V 2 A secondary
RLA Reed relay, 5 V coil, 240 V a.c. contacts (e.g. RS Components coil 349-030, windings paralleled, plus reed switch 338-147)
LP1 240 V neon indicator; F1 2A fuse with holder; Heat-sinks for IC3 and CSR1

## Note:

The 9368 PC and FND357 are available from S.C. European Components Ltd., Unit 9, M40 Industrial Centre, Blenheim Road, High Wycombe, Bucks. Prices are $£ 2.07$ and $£ 1.29$ each respectively, plus 50 p post and packing per order, plus $8 \%$ VAT

When the sTart switch is pressed, it holds the start gate IC4a off but sets the flip-flop so that input to the start gate goes high. Releasing the start switch then allows the 1 Hz signal through the gate, where it is inverted, to the count up input of the counter chain. Thus seconds are counted and the current count is displayed. Note that up to one second error can occur here because the counter will increment when the 1 Hz signal goes high regardless of when in the previous second the button was released.

## TIMER

Operation in the timer mode is similar, except that the up-down switch S 8 connects the output of the start gate to the count down input of the counter chain, and the borrow output of the chain to the flipflop. The clear button initialises the logic as before. Then each digit of the time interval to be measured is set in the counter by dialling it on the thumbwheel switch S 9 and latching it into the counter by the appropriate parallel load switch S5-S7.

Thus to time 54 seconds the numbers 0,5 and 4 would be set into the hundreds, tens, and units digits respectively. When the start switch is pressed and released RLA is energised via TRI, and the 1 Hz pulses from the standard frequency source cause the counter to subtract seconds until zero is reached. When the digits are all zero, and the clock signal into the count down input goes low, the borrow output of each counter and thus the input to the flip-flop goes low which resets the flip-flop. This switches off the transistor and thus the relay which turns the triac off and interrupts the power to the output socket.

Note, however, that the clock signal goes low half a second after the high transition which caused the zero to appear. Thus the alarm operates half a second late: this effect couples with the $+0 .-1$ second error in starting to yield $\pm 0.5$ second accuracy. This is a fraction of a stop at normal processing times.

## CONSTRUCTION

The prototype unit was housed in a specially-made folded aluminium box, as shown in the photograph. With this arrangement it is possible to keep all mains voltages in the rear compartment which is shielded from the TTL circuit to avoid interference problems. The rectifier and regulator circuit is constructed on a piece of stripboard and mounted in this compartment. The regulator itself requires a $4^{\circ} \mathrm{C} / \mathrm{W}$ heat-sink.

The counter circuit is easily constructed on i.c. stripboard. This assembly bolts beneath the panel carrying the pushbuttons. In the prototype these buttons were home-made to provide large size (for finding in the dark) without being expensive.

## DISPLAYS

The seven-segment displays and decoder-drivers are mounted on a further piece of i.c. stripboard, forming a single module requiring only power and data bits to be connected. This module bolts behind the top panel which has a cutout to let the displays show through.

Layout is not critical in this circuit but it is best to keep all wires as short as possible and to shield the logic from mains, because in the high state logic gates have high input impedance.

# m <br> mR K E 

Items mentioned in this feature are usually available from electronic equipment and component retailers advertising in this magazine However, where a full address is given, enquiries and orders should then be made direct to the firm concerned. All quoted prices are those at the time of going to press.

## INFRA-RED LISTENING

Complete freedom of movement as well as uninterrupted listening to your favourite hi fi record or radio and television programme is possible with the new range of infra-red transmitters and headphone receivers manufactured by Sennheiser and being marketed by Hayden Laboratories.

For home use there are mono and stereo systems available. The prices are not cheap but for the housewife she can keep an eye on her children or carry on with the housework while listening to a programme. Using infra-red techniques means that there are no wires to the head phones for people to trip over. This makes it ideal for business and educational lectures.

The two channel headphone receiver, type HDI 434, is claimed to be a completely new development. A feature of the headphones is the three position channel selection. The centre gives stereo reception. In position 1 only the sound of the 95 kHz channel is fed to both headphone capsules. In position 2 the signal of the 250 kHz channel is received. This allows true twochannel operation even of completely different audio signals.

The volume and also the balance for stereo operation can be easily set by two slider controls. The receiving lens of the infra-red diodes is mounted in the edge of the earpiece.

The companion two-channel transmitter, type SI 434, is specially designed for covering an area the size of most large living rooms. The electronics for both transmitting channels and the 12 necessary infrared luminescent diodes are contained in a small case measuring only $200 \times$ $80 \times 17 \mathrm{~mm}$. The transmitter is powered from an external supply.

The transmitter audio connection cable is plugged in to a DIN headphone output socket of the selected equipment, i.e. radio, television or hi fi, and the transmitter is then modulated with the available signal. These modulated signals are picked up by the receiving lens in the headphones.

The cost of the two channel HDI 434 headphones is $£ 86.50$ and the SI 434 transmitter is $£ 72 \cdot 50$, excluding VAT. A lightweight mono stethoset type headphone type HDI 40 is available from $£ 44.66$ and a mono transmitter, type SI 406 , from $£ 44 \cdot 10$, excluding VAT.

Further information and technical details of the Sennheiser range of domestic and professional infra-rer? equipment is available from Hayden Laboratories Ltd., Hayden House, Church Road, Chalfont St. Peters, Bucks, SL9 9EW.

## CLOCK/CALCULATOR

"The all action time computer" is the simplest way of describing the new Casio CQ-1 Computer Quartz available from Tempus.

Ideal for the executive to salesman and the sportsmen/women to the student, the $\mathrm{CQ}-1$ is a timepiece (including day/date), stopwatch, alarm and calculator all in one very small case.

The clock function has a useful four alarm feature which can be used for numerous applications. The alarm can be set in four time positions and at intervals of one minute or more. The command for a.m. and p.m. setting is also possible. The alarm output is generated by a miniature buzzer and emits a different tone for each alarm setting.

For the sports enthusiasts the stopwatch facility has a stop/start key and a lap key. The stopwatch will read hours, minutes, seconds and lapsed time up to 9 hours, 59 min 59.9 secs. The lap key enables the timing display to be "frozen" for lap times to be recorded, the timing sequence continuing until the lap key is again pressed and the readout reverting to a total time elapsed.

The calculator is a basic four function (,,$+- \times, \div$ ) type including constant. Calculations up to 8 digits are possible and by operating

the keys, time and date calculations are possible. Also, as the calendar is programmed from 1901 to 2099 inclusive, any day of the week or the number of days for a certain period of time can be calculated.

With suggested functions such as setting for the time to leave for school, office, shopping, taking medicine, as well as a reminder for important business appointments, telephone calls and meetings. Not to mention the usefulness of the calculator for homework, household and business accounts, the Casio CQ-1 Computer Quartz would seem a reasonable family investment at $£ 29.75$; particularly for late risers and for keeping parking meter fines to a minimum.

Whilst still on time, Tempus have an excellent range of digital watches, including attractive ladies' slim-line types and a range of World time watches.

For full details of Casio CQ-1 Computer Quartz and the complete range of digital watches readers should contact Tempus, 19-21 Fitzroy St., Cambridge (0223-312866)

## BREAKDOWN TESTER

Some time ago now we published a design for a "Breakdown Tester". An instrument using similar principles is now being manufactured by Stoneleigh Electronics.

The type 3C15 Tester determines the breakdown voltage of transistors and diodes (including Zener diodes) at the current you select. The device to be tested is connected across the test terminals of the instrument and when the "test" button is pressed", a constant current source is applied to the device. The constant current source can be set within the range $1-15 \mathrm{~mA}$ and has an output potential of up to 300 V .

The breakdown voltage is displayed on a voltmeter which has switched ranges of $10,30,100$ and 300 f.s.d. A polarity switch allows rapid checking of forward and reverse characteristics. There is visual indication when the device is either open circuit or the breakdown voltage is in excess of the 300 volt capability of the instrument.

Details of price and further information on the 3 C 15 Break down Tester can be obtained from Stoneleigh Electronics, Ltd., Mawney Road, Romford, RM7 7SE.

## BOUNCING CHEQUES

We have been informed by A. Marshall (London) Ltd., that because so many cheques have not been honoured by several banks they are forced to announce that in future most cheques will be cleared before any goods are despatched.

Postal orders, cash and approved accounts will be dealt with in the normal way.

Old habits die hard, we are told, and this is particularly true when playing piano or organ. With the organ especially it is necessary to extemporise to a large degree and thus easy to develop bad habits such as chord clichés, the same old key changes and registrations. However hard the grey matter is working, one of the most difficult habits to break is playing "chords" on a monophonic keyboard. A number of synthesisers will give a rude response to this oversight.
The Wurlitzer organs were among the first to be fitted with a small synthesiser but the shape of its manual was a reminder in itself: white keys were only as long as the index finger (black keys being about an inch in length), so that it was almost impossible to play chords-let alone forget!
three voices, but is sufficient to provide a most desirable extra keyboard for the one-man performance.

Needless to say, it is essential to place the additional keyboard on top of the organ so that the keys line up up at $8^{\prime}$ pitch! The only real disadvantage is its short compass but extending this to 88 notes would rule out the advantage of portability. The classicist would, in any case, prefer to play the acoustic parent instrument.

## ORGANISATION

Mention of the Electronic Organ Constructor's Society may seem a little out of place at this point. My first reason is because a number of readers have enquired about this Society following my note in last December's edition on the demonstration of MES 53 at their London meeting.


## ONE MAN BAND

Most commercial organs include a Rhythm Unit these days which, used in moderation, is a useful device. Adding an R.U. is not difficult at present as chips which will generate the various patterns are freely available; several pulse-operated 'voices'" and the switching are the only additional requirements. Like TV sets, the essential control is the off switch! Even so, with the tempo control set to enable him to count the beats, the beginner will be helped to struggle through new music. The more expert performer is spurred on by his "sideman", especially in the LatinAmerican vein.
Something else to play as well? My choice would be Electric Piano, rather than synthesiser, because of its polyphonic nature. Faced with pedals, three keyboards and their controls, there is plenty to think about but the technique is the same for all.

For some weeks past I have experimented with organ/piano versus organ/synthesiser and there is no doubt in my mind which is the better combination. Although most organs are fitted with percussion, the piano envelope complements the organ admirably. The tonal capability of the piano is purposely limited to nethaps

The E.O.C.S. was founded some twenty years ago by a small group o' enthusiasts headed by Arthur is Boutillier. In those days there was very little technical information available and the idea of the Society then as now, was pooling of data anc experience. Publications sent to members eventually developed into the Society's own '"organ"', the Electronic Organ magazine. Membership now extends across the world and regular meetings are held in London, Coventry and Manchester.

The Society's brief does not cover electronic organs alone: it is interested in all forms of electronic music and pipe organs. Membership is mixed, from church organists to pop group performers, but most are simply concerned with building an organ to their own specification. Readers interested in this Society should write to Ralph Purdy (Membership Secretary, E.O.C.S.), 11 The Avenue, Station Road, Billericay, Essex.

## TECHNICAL ENQUIRY

While on the subject of E.O.C.S., Douglas Shaw has suggested in Sourd Design (nage 32) that enquiries
on addıng a "Minisonic $1 \|^{\prime \prime}$ to an existing organ might best be answered by joining that Society. Under the heading "Technical Enquiries' he also mentioned the different playing technique required and the possibility of using a gating system to ensure response from one key only.

In case my previous remarks on combining electric piano (such as "Joanna'", also in Sound Design), rather than synthesiser, with an organ sound biased, perhaps I should pursue my theme. For "live" playing, there is not enough time to do justice to a synthesiser; minute adjustments to its many controls are not possible, except perhaps for solo performance. Attempting to play it whilst also manipulating two other manuals, pedals and their controls must relegate the synthesiser to a "Univox" type of keyboard (which had a shortlived existence three decades ago).

Listening to the expert synthesiser player on disc, and reading the record sleeve notes, brings one message home forcibly. A few minutes of a L.P. recording probably involved many hours work. It needs time to obtain the precise effect you are looking for, so multi-recording at leisure is the only respectful way to treat one of Moog's progeny. My experience shows that the "Minisonic II" sounds impressive through a reverberated system and Leslie speaker but its resources can only be used in a cursory fashion when playing an organ with it.

## HOOK-UP

Notwithstanding these comments, there should be no problem in adding a "Minisonic" to an existing organ or including it in a constructional project. If space is at a premium, the synthesiser could be placed on top of the organ console: the music stand has to be raised in this case, bifocal-wearing readers should be warned!

The low-level outputs should couple into the organ's main amplifier quite satisfactorily, though it should be noted that d.c. levels are present. For this reason it is best to use 0.1 _F coupling capacitors, between synthesiser and organ as a permanent feature, otherwise there is the possibility of upsetting the amplifier by directly connecting the output of IC18 to a d.c. carrying point on the organ. In the case of a single-channel organ, these coupling capacitors can be joined at the organ end to produce a monophonic input signal.

Signals from the "Minisonic" can be inserted before or after the swell pedal according to taste. On the whole, I would suggest using the synthesiser unenclosed, i.e. after the swell control, if signal levels allow.


THIs month we deal with monitoring systems, speakers and ancillaries.

## VU METER

By far the most logical place to put any VU meters in your system is right at the end, so that they measure, albeit roughly, the actual power supplied to the loudspeakers. They should be calibrated so that 0 VU (the transition point from black to red) occurs comfortably before the amplifier starts to clip. This can be done with a test tone and an oscilloscope, or failing that, a sensitive pair of ears.

A suitable circuit for driving a VU meter direct from the output of a power amplifier is shown in Fig. 6a Note that it draws all its power from the signal itself.

If you wish to include a meter prior to the main volume control, it should be calibrated so that with the main output control at maximum, OVU on the console meter corresponds to $0 V \mathrm{~V}$ on the power amplifier(s). When the limiter is in circuit, 0 VU on the console should correspond to the maximum output from the limiter, and a preset volume control, connected in series with the main output control, will probably be needed to match the level from the limiter to the sensitivity of the power amplifier. A circuit for driving VU meters from signals in the range 200 mV upwards is shown in Fig. 6b.

## PRE-FADE

All disco equipment ought to have some form of prefade listening. This is a means by which the DJ can listen to any of his inputs on a pair of headphones, regardless of whether they are being used to play through the mixer or not. By this means he can cue up records and tapes while a record is playing, and with an effective system this can be done very efficiently and
simply. All that is basically required is a switch which will select any of the inputs, or the main output, and present them, via a low-power amplifier, to his headphones.


Fig. 6a. How to drive a VU or similar meter directly from the loudspeaker terminals of an amplifier


Fig. 6b. This circuit will drive VU meters from ordinary line-level sources

A simple rotary switch can be used, but more elegantly, PO keyswitches, of the type used on telephone switchboards, can be used in a series arrangement which supplies the main output to the headphones except when one switch is operated, whereupon the output of that particular channel will be heard instead. Each channel has a switch of its own, which can be arranged in a logical position on the control panel.

The wiring of a four-channel bank of pre-fade switches is shown in Fig. 7, with all the switches in their "normal" position. As you can see, operating any one of the switches causes the series line carrying the main output to be interrupted, and the channel output (taken from the top of the channel fader) is substituted for it.

It will be necessary to attenuate the main output in order to avoid deafening the operator when switching from the relatively quiet output of a record deck to the amplified main output. The preset shown in the diagram could be set so that with the VU meter reading normally, that is, peaking around -3 , the output from the record decks whether heard directly through prefade only, or after having passed through the disco mixer, is more or less the same.

## HEADPHONES

When choosing a pair of headphones, bear in mind that you could be wearing them for up to five or six hours at a stretch. Above all, therefore, they must be light and comfortable. Personal favourites are the AKG K 140, a fairly new model available at around $£ 13$ from the discount stores, with the lightweight Sennheisers, just as comfortable but a little shrill, a close second.


Fig. 7. How a bank of PO keyswitches may be used to select pre-fade inputs or the main output from the console mixer


Fig. 8. High quality Class $A$ headphone driver circuit for high impedance headphones ( $600 \Omega 2$ and above)

Table 1: POWER OUTPUT FOR VARIOUS SUPPLIES AND LOUDSPEAKER IMPEDANCES

| Supply Voltage | Load Impedance |  |  |
| :---: | :---: | :---: | :---: |
|  | $4 \Omega$ | $8 \Omega$ | $16 \Omega$ |
| 80 | 178W | 89W | 45 W |
| 70 | 134 W | 67 W | 34 W |
| 60 | 96 W | 48W | 24W |
| 55 | 80W | 40W | 20W |
| 50 | 64 W | 32 W | 16 W |
| 45 | 50 W | 25W | 13 W |
| 40 | 38W | 19W | 10W |
| 35 | 28W | 14 W | 7W |
| 30 | 20W | 10W | 5W |

## Notes:

1. The supply voltages reter to total supply volts e.g. 60 V or $\pm 30 \mathrm{~V}$.
2. Output figures are for typical amplifier modules using direct-coupled output stages, and assume unlimited current capability.

My preference is for high impedance headphones, as they can be driven by a simple but very high-quality Class A driver circuit (Fig. 8). Lower impedance phones, below about 300 ohms, will require a Class $A B$ driver-there are a multitude of integrated circuits which will do this job adequately.

## STEREO

Four years ago I would have said that stereo was a waste of time for a mobile discotheque. Nowadays, with nearly all new singles recorded in stereo, and having tried it in practice, I am not so sure. The problem with stereo is that people have to be roughly equidistant from the two loudspeakers in order to appreciate it, and with a mobile set-up this is difficult to achieve. Of course there is great benefit to be drawn from having all your circuitry duplicated, since any failure can, with the addition of a stereo/mono switch at input and output, be quickly by-passed, and the equipment run in mono.

## THE POWER AMPLIFIER

The power amplifier is the link between the duly processed low level signal from the control circuits of your console and the loudspeakers. Its purpose is to take a signal of a few hundred millivolts and amplify it to a power of tens or even hundreds of watts at an impedance level suitable for driving loudspeakers. It is better to have a power output less than the maximum your loudspeakers can handle so that a fault, such as an open-circuit earth connection on the input, can never cause enough power to be delivered to the speakers to damage them.

Kits for this application are legion and represent a cheap and effective way of obtaining the necessary power. Bear in mind, though, that the use of units originally designed for domestic applications may cause problems when they are subjected to the gruelling conditions at a disco. In short, the poor little things may overheat and expire, much to your dismay and embarrassment. So use one capable of more power than you actually require, and then limit its power output by using the minimum supply voltage which will deliver the necessary swing. A table of supply voltages for particular outputs is given in Table 1. The figures are approximate but should prove useful.

## LOUDSPEAKERS

Avoid using 12 or $15 \operatorname{in}$ bass units as your only link with the outside world. All you will get is a murky, muddy sound, and nobody will be able to hear the words of the songs, or anything you try to say to them. That many disco devotees do not seem to mind this is no excuse for sloppiness in this department!

Do not expect to right matters with the inclusion of a couple of tweeters from domestic hi-fi units. Either they will be too quiet to be effective, or you will blow them up.

Consider a domestic louds'peaker system, containing, say, one 8 in bass/midrange unit and a 3 in tweeter. This achieves a reasonable "balance of power" over the whole frequency range. But a 15 in bass unit, with four times as much cone area, will require four such tweeters to achieve the same balance. Plus, in all probability, some midrange units to fill in the gap between the two widely different systems. So unless you use tweeters specifically designed for high power use (and they should be labelled as such), or your tweeters fall off the back of lorries, you will have an expensive time making up multi-way systems for your disco.

A good idea is to use twin-cone units, where one large assembly handles most of the frequency range fairly comfortably, or, if you can afford them, the excellent Tannoy "dual concentric" speakers where the bass unit and matched tweeter are mounted concentrically together with a crossover all in a single chassis.

When making cabinets for your speakers, preferably stick to one of the manufacturers' recommended designs. Although the larger the cabinet the more efficient the system and the deeper the bass (as a rough rule), do not go berserk; you will, after all, have to cart them about. Castors, too, are one of man's greater inventions.

## PLACEMENT OF SPEAKERS

The absorption of sound by a thick layer of human flesh is quite rapid at frequencies above about 200 Hz and it simply does not make sense to put your speakers on the floor unless they are about 7 ft high and with the business end at the top! They must be placed so that the axes of the speakers are at least 6 ft from the ground so that people can hear them with as few obstructions as possible between speaker and ear.

If this is impossible, they should at least be tilted upwards so that sound may be reflected back over the audience from the ceiling. Having them tilted has the additional advantage that people will usually then refrain from putting their half-consumed drinks on them. Drinks at parties have a habit of getting spilt, and brown ale does little to improve the accoustic properties of a sound cabinet!

With your speakers up high you will be able to cover a greater area at less power than you would were your speakers below shoulder height. This technique is just as valid for small parties, where the music is provided by a normal domestic stereo system, usually pushed to the limits of its endurance. Placing the speakers on a high shelf or on top of a wardrobe will enable your guests to hear the music much more clearly when the room is full.

## THE LIGHT SHOW

Once upon a time, dances and discos frequented by young people took place in near darkness, and lots of unkind jokes were told about discoveries made by eager young men on bringing their chosen partner into better lighting conditions. Nowadays, however, matters are somewhat different, and many operators take an almost savage delight in exposing their audience to varying coloured lights.

The heart of many a lighting system is the miniature spotlight bulb, usually rated around 75 to 100 watts and driven by a thyristor or triac to flash either sequentially with its neighbours or in time to a selected part of the audio spectrum. These bulbs are available in two formats, BC, which is a bayonet fitting like an ordinary light bulb, and ES (Edison Screw) which, as its name suggests, screws into the holder like a torch bulb. BC is to be recommended for lower power lamps, up to 150 watts, as it is cheaper, and screw-in types tend to unscrew themselves. Higher power bulbs are almost universally screw fitting.

There is a large number of commercially-made units, as well as a host of constructional designs for flashing spotlights on and off. A well designed one should take an utterly minimal amount of current from the audio power amplifier but unfortunately this is not always the case. It is a good idea to use a rough old amplifier solely to drive your light show if you have any doubts on this score. The spikes produced by the thyristors turning on and off can play havoc with your sound system if the light cables come near any sensitive part of the mixer circuitry, so do keep them well out of the way.

## EFFECTS PROJECTORS

Effects projectors work on two principles; first there is the wheel, which is spun round like a gramophone record between the bulb and the lens, heating up and causing the coloured oils inside to drift in and out of each other.

Then there is the cartridge, which is driven from its edge and produces moiré patterns as its two sides interact with each other. Both can be very pretty, though you may have to look around before you see one you really like.

## STROBES

Stroboscopic lighting has been used for a long time in industry to observe moving pieces of machinery as if they were stationary by arranging that the moving part is always in the same place when the flash occurs. Recently, though, it has found a less precise application in conjunction with discotheques. You take a strobe light, set it at a few cycles per second, and shine it on the dancers. Hopefully you stop before they start falling over or being sick.

People with epileptic tendencies of which they may themselves be unaware, may even throw fits when exposed to lighting of a particular strobe frequency. That apart, it can still be fun but do not use the strobe for more than five minutes in any hour and if anyone complains or looks disturbed by the flashing light. stop using it immediately.

## SAFETY PRECAUTIONS

Your equipment should be earthed at all times to the mains earth and if you are using an extension lead it should be checked regularly for continuity along the
earth lead. All mains connections should be made with proper plugs and sockets and kept well out of any areas where drink may be spilled or where people may trip over them.

Connections to spotlights should be of stout cable and all the holders should be securely earthed. The greatest care should be exercised when operating on spotlights with the power on, since the output from sound/light or sequencer units is usually not isolated from the mains, as all audio equipment would be.

Always string your cables well out of harm's way: if you have them up along the ceiling they will not get dirty or wet but be sure they will not fall down, either.

## INTERCONNECTIONS

By far the easiest way of interconnecting your audio department is to use standard jack sockets, available everywhere. They are robust. positive and reliable, which is more than can be said for most of the minia ture connectors that adorn hi-fi systems. Of course, they do not have to stand up to such rigorous use. Have a set of leads exclusively for use with your disco. If you use jacks throughout, all you will need are a series of jack-to-jack leads of various lengths. It is useful to have a short and a long set of loudspeaker leads, so that you need not have an excessive amount of cable trailing around.
All the pluggery needed with sound to light units usually comes with the unit, or can be bought from the same shop. This should not present any problems if the plugs are wired securely.

## STORAGE

On an average night you will get through roughly a hundred singles. To ensure an adequate selection, you will need to take perhaps three to five times as many as this. Any more is really a bit excessive. Whatever sort of order you decide to keep them in, you will soon learn where to find everything provided nobody messes about with your collection.
Cardboard boxes are useful for keeping singles in, but have a rather limited life. Smart record boxes made out of half-inch chipboard can be fitted with a lid and a padlock to keep marauding fingers out while you go and fetch your car at the end of the evening.
A travelling bag can be filled up with leads, notepaper for requests, your advertising material, a soldering iron and all the other odds and ends that come in useful.

## MUSIC

It is outside the scope of this article to go deeply into the fraught waters of musical taste. However, the music papers feel free to pontificate on electronic equipment so perhaps a word or two on their subject might not be amiss.
Lately the disco audience has been polarising swiftly into two camps; broadly, soul and rock. There has always been a split of this kind but in the last couple of years it has become much worse. The problem now is that if you play two soul records in a row someone with more persistence than imagination will insist on the Rolling Stones or Status Quo.

Conversely, play too much rock and the soul freaks will descend on you breathing fire-their requirements are usually more diverse but no less enthusiastic. Of course, if the event is specifically a soul or rock or
reggae disco this problem will not arise, but you may still find yourselves bombarded with requests for things you have never heard of. Do not fret, but do take a note of what you are asked for and attempt to hear it on your next trip to the record shop.
Do not waste too much time buying up the latest releases; many people, quite understandably, will not dance to anything they have not heard at least a dozen times and by then the record in question is probably available from one of the second-hand shops at half the original price. Always note requests on a piece of paper, whether or not you have the records asked for; not only can you use this to guide your choice of material but it is also much easier to read a dedication from paper than it is to try and memorise it.

## COPYRIGHT

Round every record label is the legend "Unauthorised Copying, Public Performance and Broadcasting of this record prohibited", or words to that effect. Most DJs ignore it completely. The organisation concerned with administering the public performance of gramophone records is the Performing Rights Society Ltd., 33 Berners St., London, WIP 4AA, who will send you on request details of the legal position regarding the public performance of gramophone records, the gist of which is that you can please yourself what you do at Johnny's birthday party, and if you are playing somewhere where there is a fee for admission then the onus for obtaining a licence falls on the person hiring you.

## CONCLUSION

Don't kid yourself: despite the claims of the advertisers, "easy money earned with our $£ 350$ disco console!" (for whom?)-operating a mobile discotheque is really hard work--the public only sees the easy bit. You may not get home till 2 or $30^{\circ}$ clock in the morning and you'll be worn out when you do.
You will have to put up with requests for records you have played so often you wish they had never been recorded, and you may have to cart your equipment up and down stairs cluttered with people who display as much reluctance to move out of your way as they did to dance to your music. Don't underprice yourself; better to do two discos for $£ 40$ than three!
All the same, it can still be a rewarding pastime, and a great deal of enjoyment can be had from your mobile disco, especially when you've built some or all of it yourself. I hope this article has made clear to you something of the art of running a mobile discotheque. Good Luck!



A selection of readers original circuit ideas. It should be emphasised that these designs have not been proven by us. They will at any rate stimulate further thought.

Why not submit your idea? Any idea published will be awarded payment according to its merits.

Articles submitted for publication should conform to the usual practices of this journal, e.g. with regard to abbreviations and circuit symbols. Diagrams should be on separate sheets, not inserted in the text.

Each idea submitted must be accompanied by a declaration to the effect that it is the original work of the undersigned, and that it has not been accepted for publication elsewhere.

## TV TENNIS SCORE UNIT

THe TV tennis Score Unit is designed to attach to the Videomaster TV tennis game, to display the score on the television screen immediately after each goal or point is scored. The score is displayed in the seven segment format, with each score being shown on its own side of the screen, as shown in Fig. 2b. The game progresses until one or other player reaches a score of nine, at which point that player is the winner. This condition is detected $b_{y}$ G5, which prohibits the game from proceeding any further. A new game is started by pressing the reset button.

The four input gates ( Gl to G4), update each player's score counter (CTR A and CTR B). Switch A is controlled by the Line Sync timing circuits, and switches at the correct time during the sweep, to display each counter score on its side of the screen. The timing circuits consist of two Line "single shots" and three Frame "single shots", and these define the character position and size. The gating at the Video Output (G9 to G18), suitably blanks the seven segment decoded counters to give the correct television screen representation of the score, and also inhibits the display during play.

Fig. 3 shows how the third Line "single shot" (LS3) produces double score, by pulling up LS1, and switches the two-way switch giving "ghost" figures which do not mask the bats or ball.

The prototype was constructed on two d.i.I. circuit board offcuts, which fitted within the lid of the Videomaster unit. The logic is COSMOS throughout, which consumes very little current from the Videomaster batteries.
D. E. Launchbury,

Shirley,
Solihull.



Fig. ib


Fig. 20


Fig. 2b



## (D)ELECTRONICS <br> 54 Montagu Street, Kettering, Northants. Phone Kettering 83922 shop open Monday to Saturday 9.00 to 6.00 ; early closing Thursday 1.00 p.m.





## JOANNA MODIFICATION

BY means of simple modifications. the PE "Joanna" by A. J Boothman, may be given an alternative organ sound. Figure 1 shows one Joanna envelope circuit. One resistor (RS), and two diodes (DA and DB) are added to each key circuit, and one control switch with pull-up resistor are common to all sixty-one circuits. A switch is also added to the 19 volt connection to the keyswitch busbar, labelled attack control.

With the organ switch "on" and the attack control "off", when a key is depressed CT charges through RT but has no effect on the signal output. CS charges through RT, RS and DA, up to about +5 volts, at which point $D B$ conducts and prevents any further increase in voltage. When the key is released, the discharge of CS is via the normal path, variable by means of the sustain pedal. When the organ switch is "off", diode DB clamps point A to about +0.7 volts, and prevents CS charging through DA.

The result of this is an output signal which rises slowly; remains constant as long as the key is held down, and fades out slowly when the key is released. There are now a number of variations in the sound available. Resistor RS determines the rate of attack, and may be between $10 \mathrm{k} \Omega$ and $100 \mathrm{k} \Omega$ as required. A variable attack time may be provided by varying VT as shown in Fig. 1. With both organ and attack control switches "on", the keyboard touch sensitivity gives a further increase in the speed of attack. The organ output level may be varied by altering the clamp voltage of DB (as in Fig. 2), and when set to a low level gives the familiar organ percussion sound.

If the organ control line and +19 volt keyswitch busbar are split into two at note 25 . with suitable switching the lower two octaves can play the organ, and the top three octaves can play the piano, or vice versa. In this case it is advisable
to provide two separate organ level controls, otherwise the piano can be drowned by the organ sound. Extra filters to give the "flute" organ voice are also useful, especially when sustained chords are played in the lower octaves.

The above modification adds less than five pounds to the cost of the Joanna. However, for anyone who is prepared to completely rebuild the master oscillator board, a circuit is given in Fig. 3 which includes vibrato, and the possibility of switching to $2,4,8$, or 16 foot pitches as required. The higher pitches, used with the organ stop, slow attack, and vibrato, give a useful "string synthesiser" sound. As the frequencies of the piano filters are fixed, alternative voice filters are required to make the best use of the higher pitches. The pitch selector inputs are shown in Table 1.

If miniature resistors have been used, there is room on the envelope boards for the extra components.


Three extra holes are drilled for each envelope circuit, and RS fits between R57 and C16, DA between D12 and D17, and DB between D17 and R72 with a bend in the anode lead to pass between R 57 and D17. The DB cathodes are strapped together on the copper side of the board, using uninsulated wire, and are brought out to a Veropin near the +5 volt connection.

All earth wiring should be as low resistance as possible, otherwise the discharge of CT when a key is released, can cause a "thump" to be heard! Finally, a warning is given that heavy vibrato, when using one of the piano stops can produce a most sickening sound.

> D. A. Boyd,
> Walton,
> Liverpool.

Table 1.
Point E

| Pitch | Point E | Point F |
| :---: | :---: | :---: |
| $2{ }^{\prime}$ | 0 | 0 |
| 4 | 0 | - 1 |
| +5v $8^{\prime}$ | 1 | 0 |
| $16^{\prime}$ | 1 | . 1 |
| Pitch | ctor cont | inputs |



T
HE melody generator shown in Fig. 1, makes use of subminiature preset resistors acting as a small analogue "memory bank", enabling a short tune to be stored. A continuous cycle of ten notes of equal duration is produced, permitting a considerable variety of simple tunes to be set up.

The overall circuit consists of three blocks; a pulse generator, a decade counter, and an audio frequency oscillator driving a loudspeaker. The decade counter consists of two TTL i.c.s which require a power supply of 4.5 to 6 volts. This is derived from the 9 volt supply via resistor R10.

The pulse generator comprises TR1 and TR2, and is powered by the same 5 volt supply line used by the i.c.s, to avoid too high a voltage being applied to the input of ICl from the collector of TR2. TR1 is a unijunction transistor operating as a relaxation oscillator producing pulses at intervals dependent on the setting of VR1 (approx. 0.5 to 5 Hz with specified values). This pulse is boosted
by TR2, which operates as a switching transistor to advance the decade counter.
The counter consists of ICl and IC2, in which the former counts the pulses appearing at pin 14 , and represents the count in binary at pins 12, 9, 8 and 11. The 7490 counts from zero, and may be reset to zero after any number. As wired in Fig. 1 the circuit resets to 0 after 9 in order to make full use of IC2. The latter i.c. converts the binary output of ICl to decimal in the ten output lines, each one sequentially dropping to almost the potential of the negative supply rail, while the other nine remain at very high resistance.

Transistors TR3 and TR4 form an oscillator producing an audio frequency square wave which is amplified by TR5 and fed to the loudspeaker. Frequency may be varied by changing the time constant at the base of TR3. Thus by connecting ten different resistances in the outputs of IC2, the time constant at TR3 base, and therefore the frequency of oscillation, will change as the decade counter
goes through its cycle. By making these resistors independently variable, a tune may be programmed into the device.

To ensure that the multivibrator will oscillate when the presets are at their minimum setting, VR12 is used to provide a threshold resistance so that each preset may cover two full octaves.

A note may be blanked by inserting a slip of paper between the wiper and track of the relevant preset. A good tune to start with is the chime of Big Ben: A F G C blank C G A F blank.

The most obvious application of the unit is a doorbell. Callers, and the constructor can both be kept entertained by ever changing themes. Alternatively the unit might be included in a communication system as a station identifier. The prototype ran from a 6 volt 100 mA mains power pack, but would work from a 9 volt battery.
J. R. Skeels, Upminster,

Essex.

## pollits hilinin

## DIGITAL VOLTMETER (April 1977)

In the specification table, the figure for accuracy should read 0.1 mV per ${ }^{\circ} \mathrm{C}$.

## PE SUPPLEMENT "PUTTING IT TOGETHER"

 (April 1977)In the section on Wiring Pens, it was stated that only one colour of wire was available. This is incorrect. The Verowire system offers four different colours.

SOLAR HEATING CONTROLLER (February 1977)

The relay contact RLA1, connected across R5, should be a normally closed contact, shorting out R5 until the relay has operated.

The value of R5 may need to be reduced, depending upon the type of relay used. In some cases, a value as low as $22 \Omega$ has been found necessary to ensure reliable operation.

## HAZARD WARNING FLASHER (November 1976)

A reader has pointed out to us a potential danger in this unit. If the hazard flasher is switched on whilst the turn indicator switch is not returned to the off position, power can be fed back through the latter switch into the ignition system, possibly causing damage.
This may be overcome by inserting a diode in series with the live supply feed to S1 in Fig. 1, with polarity appropriate to the car's electrical system (anode to supply for negative earth systems, and vice versa). A 10 A device is required, such as the RS Components 261-019, mounted on a suitable heat-sink with an insulating kit.

## pH METER (March 1977)

As a result of the pH Meter featured in our March 1977 edition, Uniprobe Instruments Ltd have made a special offer to the PE reader, of the combination glass and reference electrode used with their 300 series pH meters.
Suitable for the PE pH Meter, this probe set is available at the special price of $£ 9.50$ including VAT, from:
Uniprobe Instruments Ltd, Clive Road, Cardiff, CF5 1 HG .


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may wot last long.

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ing studs for standard $5^{\prime \prime}$ speaker 3 , mount take the normal three controls. Price $21 \cdot 50+19 \mathrm{p}$, Post el +12 p .
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thal figure by 2 . Price $80 \mathrm{p}+4 \mathrm{p}$. Post $10 \mathrm{p}+1 \mathrm{p}$ Hinal hgure by 2 . Price $80 \mathrm{p}+4 \mathrm{p}$. Post $10 \mathrm{p}+1 \mathrm{p}$.
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the scale fo punched out with numerals, $0,1,2,3$, the bcale is punched out with numerals, $0,1,2,3$, 4 and so on. When 5 ou apply a voltage for and is focussed on to the screen. This device which must have cost a small fortune to develop and make can also be used for a varjety of other purposes. Voltage regulation by making beam of alves-send for your requirements, most type in stock.

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## MOVING IN ...

The President of Fairchild Camera and Instrument Corporation, Wilfred Corrigan, has now got his Stock Exchange listing allowing Fairchild shares to be traded through London. Sales world-wide were up 52 per cent last year to 433.2 million dollars and Corrigan says Fairchild has moved aggressively to obtain funds for future growth.

Reading between the lines, it would seem that he sees the growth coming in Europe and specifically in the United Kingdom where he is looking for take-over opportunities. If Fairchild does start buying in companies then the extra capital will probably be raised in the UK. Fairchild is also reported to be thinking of applying for listings on other major stock exchanges in Europe, as well as in Japan and Hong Kong.

The company is currently strong in digital watches and TV games in the consumer market. The newest Fairchild TV game is actually an entertainment system centred round an F8 microprocessor and deliveries have already started in the USA.

## AND <br> RATIONALISING

With 15,000 semiconductor devices in the product catalogue Motorola has decided to call a halt, at least in Europe. After conducting an analysis of all applications and who uses what for which purpose, Motorola's back room boys in Geneva have concluded that Europe ought to be able to manage with only 4,000 devices to choose from. These have now been grouped in a new catalogue "The

European Selection" expected to cover at least 90 per cent of European requirements.

The other 11,000 devices will still be available but may be harder to obtain as distributors may no longer stock them, and prices may go up to discourage their use. On the other hand, distribution costs will go down on the preferred range of devices and although there is no mention of price reduction the move clearly will help keep Motorola in a strongly competitive position, delaying price rises that otherwise might be needed.

## COMPONENT BOOST

The British electronic components industry is one of five industry sectors selected for special government assistance in the industrial strategy programme. A figure of £20 million is suggested as the sort of support expected to be made available. Brave plans for a brave future!

This new initiative is a re-run of a similar initiative of ten years ago. Only the names have changed. In he old days the slogan was "the white heat of technological revolution' and its instrument was the Ministry of Technology. Today the slogan is "the regeneration of British industry" and its focal point is the NEDC. The only real change is that today the trade unions have an equal say in the strategy, more than an equal say according to some commentators. Even the amounts of money involved, allowing for inflation, are practically the same.

These endlessly sitting industrial strategy committees are no substitute for action. In fact they are mostly counter-productive. Who in their right minds will use their own money today when government money will be available tomorrow? So those who hope to gain from the scheme defer their investment plans until more details are available, the applications for aid submitted, further committee meetings held, the recommendations of the civil servants put forward, the Minister's and Union's final approval given, the ordering of new equipment, the arguments at shop floor level over new manning agreements, the negotiation of new rates of pay. Let's be optimistic and say that this tortuous process takes only two years. By that time, in a volatile industry like electronics, the market has changed and the opportunity missed. And like all forms of heavily institutionalised charity the administrative costs eat deeply into the capital sum.

How much simpler and faster to give the money in the form of tax relief in general and an additional relief on profits from exports, thus assisting all firms equally. As it is.
only firms with schemes costing £50,000 or more will qualify for aid which eliminates many worthy enterprises. It is also uncertain what the attitude of the Government and Unions will be towards aid for foreign-owned multinationals in Britain which, between them, form a very large part of the components manufacturing industry, especially semiconductors.

All signs point to a stiffening of resistance by well-run companies against Government meddling. A number of public companies are thinking of re-forming themselves into private companies to obtain more freedom of action, and I have already heard of contingency plans being made by some businessmen to move overseas should the Bullock Report be implemented in fts present form by the Government.

## WINNERS

Despite all the difficulties there is still ample vigour in the industry. Hewlett-Packard Ltd., the UK arm of H-P has not only turned in its best results ever but is the most profitable of all H-P's 27 manufacturing plants round the world. The Scottish plant is the world design centre for communications instrumentation with 80 per cent of production exported to 65 countries. Export sales are up 49 per cent, home sales up 32 per cent in a turnover of $£ 36$ million and pretax profit was nearly $£ 6$ million.

The electronics content of record aerospace exports ( $£ 904$ million in 1976) has risen dramatically. The category of airborne radio, navigation and radar aids exported shot up to over $£ 15$ million and instruments to almost $£ 40$ million. Plessey Electronic Systems has an export order book of over $£ 130$ million. The BAC Rapier Missile System export order book has now topped $£ 600$ million. The add-on radar units for Rapier are built by Marconi and feed millions into the electronics industry.

If only all manufacturing industry did as well, or is that asking too much?

## LIKE JEWELLERY - BUT DEARER

I have often commented on semiconductor prices tumbling so that simple items like transistors and diodes are two a penny-or almost. But move up to the qualitv end of the market and look at the prices. I see from H-P some new Schottky diodes at $£ 17.89$ each in lots of 10-100 and if you want a lownoise microwave transistor you can pay as much as $£ 97.56$ each in lots of $1-10$. But you do get titanium/platinum/gold metallising thrown in.

## PRTENTE REDEENO

## ENERGY CONVERTER <br> BP 1458702

In BP 1458 702, Siegfried Reinhold Lehr of Munich claims a miniature device for converting mechanical to electrical energy. Although this is intended for implant into the human body it may well have other applications.

The claimed object is to provide a converter small enough to be implanted into a blood vessel or heart muscle which will produce enough power to trigger a heart pacemaker and so makes the additional implant of nuclear or storage batteries unnecessary

The patent shows a small cobaltsamarium magnet and a soft magnetic core with yokes. An armature completes the magnetic circuit and the generator coil is wound round the soft magnetic core. The armature is secured to a spring steel membrane surrounded by a corrugated support.

The spring and corrugation force constant are balanced with respect to the magnetic force, so that when the ambient pressure around the device increases, for instance due to contraction of a muscle in which it is implanted, the membrane and armature move closer to the yokes and snap-latch. This stores energy in the corrugations. When the
ambient pressure falls, as for instance when the muscle relaxes, the spring force exceeds the magnetic force and the membrane jumps back into its rest position, inducing an e.m.f. in the coil as it does so.

It is claimed that because most of the energy available from the pressure change is converted instantly when the armature jumps, simple diode rectification, with half the voltage drop compared to full wave rectification, can be employed. The rectified output may be fed direct to a pacemaker, which actuates a stimulating tip via another core of the same cable which connects the generator to the pacemaker.

## SURROUND SOUMD

Two patents, BP 1 (taken out a few years ago) and more recently BP 1454894 , for an interestingly different approach to the derivation of 4 -channel surround sound from a 2 -channel stereo amplifier are held by EMI.

The conventional approach is to use an extra pair of loudspeakers connected in so-called Hafler fashion, to reproduce the difference signal between left and right at the rear. For the Hafler system,

Fig. 1


Fig. 2
the extra rear loudspeakers are of conventional type; the EMI proposal is to use modified loudspeakers at the rear.

Each of the two extra rear loudspeakers has not one but two voice coils, operable on the same cone. The two coils of each loudspeaker are dissimilar in impedance and are wound in opposite sense. The coil windings and their connection to a stereo amplifier are shown in detail in Fig. 1 and in simplified equivalent form in Fig. 2.

Voice coils 3 and 6 are connected in series to the left channel output of the stereo amplifier and coils 4 and 5 in series to the righ channel output. Coils 3 and 5 have 5 ohm impedance and coils 4 and 6 have 11 ohm, to give an impedance ratio of $2 \cdot 2: 1$. Coil 3 operates in opposite sense to coil 4, and coil 5 operates in opposite sense to coil 6.

As a result of this combination. rear right loudspeaker reproduces sound representing a proportion of the left channel output subtracted from a proportion of the right channel output; rear left loudspeaker reproduces sound representing a proportion of the right channel outbut subtracted from a proportion of the left channel output. The front left and front right outputs are unaffected

## IN BRIEF

BP 1451 090—Nippon Gakki Seizo KK: Audio Power Amplifier. Circuit for an audio amplifier, including a power stage and a driver stage, with the driver stage bias switchable between three settings, to provide a choice of $A$. $B$, or $A B$ class operation

BP 1456 541-Greenwood Mills: Colour Measuring Spectrophoto meter. A digital spectrophotometer intended to enable the colour of a cloth or material to be scientifically analysed and recorded.
Several different photosensitive 'eyes" are used, each with a different sensitivity to different colours. The outputs of their devices are registered and a definitive analysis of the object's colour derived from a comparison.


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