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## A GAME WITH ASSOCIATIONS

AN electronic version of the popular game Mastermind was inevitable. The basis of the game is code-breaking. Cryptography, the writing of secret or concealed messages, has been practised from the earliest civilisations onwards, and the art or science of cryptanalysis (code-breaking) is of equal antiquity. The unravelling of the puzzle, the decoding of the secret message or sign, has always been an irresistible challenge. In its highest form of development as conducted by governments and their agencies, it becomes a matter of vital importance for national security and the most brilliant mathematicians, renowned chess exponents, and other intellectual giants are brought into the battle of wits where logic in its purest form provides the chief weapons.
The electronic connection with code-breaking is very definite and real. From official revelations made in recent years concerning British intelligence activities during the 1939-1945 war, we now know that valve circuits were pressed into service to perform the formidable number of computing operations necessary in the attempt to break down enemy messages. From this original application of electronic switching circuits in the field of cryptanalysis, it would appear that Britain has just claim to fathering the electronic computer: for this embryo computing machine (known as "Colossus") hastily designed and built during the last war, antedated by a few years the American ENIAC which is generally recognised as the world's first electronic digital computer.
In any event code-breaking, under the supreme exigencies of war, certainly laid the seeds for future electronic developments. Thus for those who like to delve into such matters our latest electronic game brings to mind interesting historic associations: with the ageless human pursuit of code-breaking, and also with the (by comparison) very young roots of electronic technology.
The Practical Electronics Mastermind-Super Mastermind is really a better title, since our design can be modified to extend to a four-from-ten colour game-uses tTL. techniques. At this moment this is the cheapest way to achieve an electronic simulation of the game. The number of i.c.s involved is quite large, around 50 devices. Yet such are the economics of TTL packages that the total cost is likely to be less than $£ 15$. This fact alone should provide a convincing answer to those who may ask why not use a microprocessor?
Without doubt microprocessors are a natural for a game such as Mastermind. Programmes have been written for playing this game and other games on microprocessor based minicomputers and evaluation systems. But no one is likely to devote an equipment costing $£ 150$ and upwards solely to this particular amusement. Dedicated microprocessor versions of the game will come along in due course, that is certain, but the price of these devices needs to fall considerably before they can offer a viable alternative to the TTL based version.
What is true of Mastermind in this respect must be equally true of other applications in general. So even as we enter the microprocessor era it will be wise to recognise those real advantages still offered by the logic approach based on standard inexpensive i.c.s.
F.E.B.

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## Can you beat it?

IHs series of articles will serve to present an electronic version of the very popular game using coloured pugs and secret codes; Mastermind. Unlike the vast majority of electronic games, however, Electronic Mastermind may be played by a single player with the machine acting as an opponent. Gone therefore is the frustration of having an expensive item of electronic gear lying aróund with nobody to be your human opponent just when you want to play. Of course, there is abselutely no reason why a group of willing competitors should not take turns to play a game with the machine. Indeed, it is the contrasting techniques of play adopted by different players which can produce considerable additional excitement.

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Fig. 1.1. Layout of the basic "four from six'" colour game

The basic game offers a choice of four colours chosen from six, but a series of simple modifications are to be described for a switchable "four from ten colour" alternative. Since this facility is not included in the prototype, the mainline of the text is to concentrate on the basic "four from six colour" game.

As an item of electronic hardware the equipment is quite complex, utilising some 49 TTL integrated circuits. We are including an overall list of components in this issue, so that the prospective constructor may judge for himself the likely cost and complexity of the project before embarking on any actual construction.

With careful shopping the total cost of TIL for the game is approximately $f 13$, including an additional three i.c.s for the "four from ten" colour game.

In this article, we shall describe the use and general principles of the machine, together with the physical features and the power supply. The construction of a well regulated power unit early on in the project will facilitate the comprehensive checking of the circuitry to be described in forthcoming issues.

## PRESENTATION

The layout of the basic game is shown in Fig. 1.1. To commence play, the "Call" button S7 is pressed, setting up internally a random choice of colours and simultaneously clearing the logical circuits. The
player then makes an initial choice of any four coloured pegs and places these in the first row of peg holes (1). These colours must then be sequentially entered into the machine, in a left to right order, using the appropriate push-buttons Sl-6. After the fourth colour has been entered the machine returns information in clue positions (2) and (3), indicating the comparative correctness of the entered colours with the internal ones. The,player then records these clues by entering the appropriate number of black and white key pegs into the first row of key peg holes (4).
Using this information, the player deduces a further combination of four colours, placing the appropriate coloured pegs in the second row of peg holes, and then proceeding to enter the colours using the push-buttons as before. The clues are this time recorded in the second row of key peg holes; and so on until the internal code is deduced.
A maximum of eight opportunities are provided to do this, although a good player may average between four and five deductions.

## THE CLUES

Two pieces of information are given for each deduction. Firstly, in clue position (2) a numerical indication of the coloured pegs which are correct for both colour and position is given (for convenience, we shall call
this the " $P$ " result). Secondly, an indication of the number of pegs correct for colour but incorrect for position is presented in clue position (3) (called the " 1 " result). Thus, as an example, the game terminates when the score is four in position (2) and zero in position (3).
Two further examples illustrating the scoring principles are shown below, the second demonstrates the convention adopted when dealing with repeated colours. Scoring is identical to that in the commercial game.
(i) Internal unknown code Red Green Blue Yellow Green Red Blue White Score: 1 ("P"), 2 ("I")
(ii) Internal unknown code Deduction of player Red Red Red Black Red Black Black Red Score: 1 ("P"), 2 ("I")
Conventionally, black key pegs are used to record the " P " results and white key pegs to record the " I " results.

## PRINCIPLES OF OPERATION

In the schematic diagram of Fig. 1.2, a random number generator ( RNG ) is used to produce the secret code. This simply comprises four modulo ("length") six counters, with the colours represented by four, three bit, binary codes. At the start of play, the depression of the "Call" button randomly programmes these counters, so that they then contain the codes to be deduced.

As the player sequentially enters the four colours forming a deduction, the codes generated by the RNG are called up for comparison with each of these entries as they appear. Note the necessity for each entry to be compared with all four internal codes, so that a total of 16 comparisons are made for each deduction entered.
The machine does not await the entry of all four colours of a particular deduction before computing the


Fig. 1.2. Block schematic diagram of game
score, but instead computes this as each colour is entered. Thus the score is updated as further colours are entered, with the final result being available for display following the entry of the fourth colour of the deduction. Mastermind thus has a serial mode of operation.

As each colour is entered the results of the comparisons then performed are operated on in accordance with the scoring rules of the game discussed earlier. This function is performed by the scoring logic, the operation of which is the most complex section of the machine to understand.

The entry section of the logic debounces the contacts of the coloured push-buttons, and also transforms the debounced signal so obtained into binary machine code.


Fig. 1.3. Showing peg box and switch panel assembly. No switch drilling details are given as this will depend on game options

Finally, the control, or, as it is sometimes called, the "Orchestrating Logic" serves to conduct the electronics and achieve the correct temporal balance in the machine's hierarchy of logical operations.

## PHYSICAL FEATURES

The majority of the logical circuitry is wired up on a single sheet of Veroboard, and you may, quite rightly, consider this to be a somewhat unusual format for a construction involving so many i.c.s. Admittedly, it is unusual and requires slightly more care regarding the routing of power supply lines and the provision of adequate decoupling for the circuits than for, say, a p.c. board system. There are, however, several advantages; for example, with only a single board there are obviously no interboard connections of noise sensitive logic signals. It is also cheaper.

## CONSTRUCTION OF THE CASING

In the prototype the casing was made from softwood, with dimensions as shown in Fig. 1.4. The power supply unit is not housed within this, and the 5 V supply lines enter via a two pin socket. It is advisable that you obtain such a socket prior to making the cut-out to accommodate it, so that the hole can then be made precisely to the dimensions of your particular socket.

When cutting the joints, work to the internal dimensions of the box, taking care to orientate the joints correctly. The wood should then be finished with a fine sandpaper before fixing the pieces together with wood adhesive.

The bottom of the casing is formed from light hardboard, suitably perforated to facilitate ventilation. The removable back of an old valve wireless set would be ideal for this purpose! This is fastened using four small wood screws passed through four rubber cabinet feet.

The top surface is in three parts: (a) the peg board, (b) the switch panel, and (c) the peg box. The peg board has dimensions as shown in Fig. I.4. This board is supported by four Perspex batons (or batons of a similar material) screwed to the woodwork.

The aluminium switch panel for the switches is detailed in Fig. 1.3, also showing the peg box. In the prototype, the latter is constructed using transparent Perspex. If the


## CUTTING LIST

Casing
Total length of softwood $1,448 \times 44.5 \times 9.5 \mathrm{~mm}$.
Perspex peg board $311 \times 209.5 \times 3 \mathrm{~mm}$ (opaque). Aluminium switch panel. $63.5 \times 209.5 \times 1.5 \mathrm{~m}$. Perspex peg box top $63.5 \times 209.5 \times 3 \mathrm{~mm}$ (transparent). 3 mm perforated base hardboard $438 \times$ 209.5

Fig. 1.4. Casing assembly details with cutting list

## POWER SUPPLY UNIT

Integrated Circuit
IC1 LM309K 5V Regulator
Capacitors
C1 $10,000 \mu \mathrm{~F} 20 \mathrm{~V}$ elect.
C2 $100 \mu \mathrm{~F} 10 \mathrm{~V}$ elect.
Bridge Rectifier
D1-D4 2A or 4A 500 V
full wave type


Transformer
T1 $10 \mathrm{~V}, 2 \mathrm{~A}$ secondary- 240 V primary with screen
Miscellaneous
LP1 mains neon, FS1-1.25A fuse and holder, two pole mains switch, tinned heat sink, chassis etc.

"four from ten colour" alternative is required, the switch panel must be re-organised to accommodate ten colour push-buttons.

All upper and lower surfaces must be easily removable, and for this reason six small fasteners formed from solder tags are used to secure the top peg board and the lid of the peg box. Those on the lid are loosely screwed to enable easy turning by hand when the lid is to be removed.

The cabinet is finally completed with a coat of paint, both inside and out.

## POWER SUPPLY

The power supply is built on a separate chassis with the connections to the game made using a two cored cable. This cable must have a current rating of at
least 5 A in order to avoid any significant voltage drop along it. It is also for this reason that its length must not exceed 2 metres.

A current of approximately $1 \cdot 1 \mathrm{~A}$ at 5 V is required for the game and this is obtained from a regulator chip of the LM309K variety, as shown in the circuit diagram above.

This regulator must be mounted on a heat sink and the secondary voltage of the mains transformer should not exceed 10 V in order that heat generation by the regulator be kept to a minimum.

No assembly details are given for this as construction is straightforward.

NEXT MONTH: Entry section of logic is described together with master clock and random colour generator.

## diadidut <br> A SELECTION FROM OUR POSTBAG

Readers requiring a reply to any letter must include a stamped addressed envelope. We regret that we cannot answer any technical queries on the telephone.

## ADJUSTABLE VICE

THere are on the market, miniature vices which can be set to grip at any angle. Obviously this is very useful when working on p.c.b.s, as the components are inserted from one side, and the board is then rotated to allow soldering.

I have designed a similar device (Fig. 1) which only cost me about three pounds to make. A hand vice (from any good tool shop), has one of
its limbs drilled and tapped to $\ddagger$ inch BSW; a local garage might do this at negligible cost if you do not have the tools, but a photographer should find it worthwhile to purchase them as $\frac{1}{4}$ BSW is the standard tripod thread. The vice can now be mounted on a ball and socket head. which is available at most photographic dealers. The tripod head is finally mounted on a substantial piece of timber, which in turn can be clamped to the work bench. The rotating ball and socket allows movement in any direction.
R. M. Henderson, Newcastle upon Tyne.

Fig. 1


50 CMOS IC PROJECTS<br>By R. A. Penfold<br>Published by Bernards (Publishers) Ltd.<br>102 pages, $108 \times 180 \mathrm{~mm}$. Price $\mathbf{£ 0 . 9 5}$

AFTER an introduction covering the more important characteristics of cmos devices, the chapters march soberly but thoroughly through monostable, bistable and astable multivibrators, amplifiers, oscillators, and Schmitt triggers. finalising with "Special Devices" featuring i.c.s of a range considered by the author to be less useful to the typical constructor.

A good introduction to cmos at a practical level, but watch out for confusing mistakes such as page 32, where a BCl 79 is shown as a $n p n$ transistor.
M.A.

## HAM RADIO

## By Kenneth Ullyett Published by David and Charles 163 pages, $216 \times 136 \mathrm{~mm}$. Price $£ 4.50$

|NTENDED as an introduction to amateur radio for the layman, or to give the experienced amateur a useful and up to date resume of his hobby. this book claims to be the first to cover the subject without the use of mathematics, circuits, block diagrams or formulae.

Subjects covered include the various communication modes, getting a transmitting licence, learning the Morse code, antennas (aerials), and equipment.

Unfortunately, the book is marred throughout by numerous inaccuracies, which will be as misleading for the newcomer as they are annoying for the experienced.
G.C.A.

## ROOMS FOR RECREATION

## By Euan Barty

Published by the Design Council
69 pages, $197 \times 208 \mathrm{~mm}$. Price $\mathbf{£ 1 . 9 5}$

ONe of a series of Design Centre publications, this book deals with a total of sixteen hobbies, among which electronics is conspicuous by its absence!

However, it contains a wealth of general information which will be of use to the electronics enthusiast. Subjects covered include workshop planning, furniture and fittings. lighting, heating, ventilation, wall and floor finishes, noise and safety.

## BEGINNER'S GUIDE TO AUDIO

## ByI.R. Sinclair <br> Published by Newnes Technical Books 184 pages, $119 \times 186 \mathrm{~mm}$. Price $£ 2.75$

THis book brushes on most aspects of audio, starting with the nature of sound, how it is picked up, amplified. recorded and reproduced, and gives a quick splash of room acoustics.

Being only a guide it is non mathematical where possible, and almost vague in places, but goes into detail you can get your teeth into when dealing with the "neat" electronics such as tone controls and output stages.

This would not be a bad starting point for the complete novice because of the book's broad base, although a little supplementary study might be required on the circuit theory chapters.

Up to date methods are not neglected, and descriptions of current dumping. quad. pseudo quad, and electrostatic techniques are given.
M.A.

A CATALOG OF OPERATIONAL TRANSFER FUNCTIONS

## By Don Watts

Published by Garland Publishing, Inc. 545 Madison Avenue, New York, N.Y. 10022 224 pages, $150 \times 222 \mathrm{~mm}$. Price $\$ 26$
This book is a complete single-source catalogue of electronic operational transfer functions giving s-plane pole and zero locations, gain and phase Bode plots, sketches of impulse and stop functions, responses (where applicable to linear circuits), actual circuit or block diagrams with reference designators, and pertinent design equations for each function. Sections are included on: Linear, amplitude, and frequency independent; non-linear amplitude dependent; linear single pole; tinear single zero; linear double zero; linear one pole/one zero; linear one pole/two zero: linear two pole; linear one zero/two pole: linear two pole/two zero; and multiple order functions. This format makes possible the selection of one of several possible solutions to the same function, which is the design engineer's job, and at the same time allows additional realisations to be added to the appropriate section if and when they become available.

An introductory chapter explains how the book may be used by practising engineers, scientists, and students. As an aid to the uninitiated, each modern function is preceded (at the section start) by the classical R-L.-C filter function before the introduction of realisations using only resistors and capacitors with integrated circuit operational amplifiers. Thus, the student can quickly learn the features of modern audio filter design, the latest outgrowth of analogue computer technology.

## FUNDAMENTAL ELECTRICAL TECHNOLOGY By Marvin H. Klayton <br> Published by Addison-Wesley Publishing Co. 710 pages, $242 \times 160 \mathrm{~mm}$. Price $\mathbf{£ 1 3 . 6 0}$

THIS is another book of American origin which provides a basic course in electrical engineering. Whilst it does not avowedly fulfil any set syllabus the content should adequately encompass the first two years of any UK electrical engineering course

The chapters cover basic electrical concepts, simple circuits, network solving, magnets and electromagnetism, a.c. circuits, polyphase circuits, transformers, resonance, special application networks, electrical signals and measurements and instruments.
The units and symbols are SI and there is a copious appendix so that one does not need to go outside the book for reference in studying.

Each chapter is backed with problems with solutions which makes it a good choice for self study, more so as it has an excellent 11 page index.

The review copy sent was in hard back.

## ELECTRONICS AND THE PHOTOGRAPHER <br> By T. D. Towers <br> Published by Focal Press <br> 316 pages, $216 \times \mathbf{1 3 7 m m}$. Price $\mathbf{5 6 . 5 0}$

AVERY comprehensive book, covering just about the entire field of applications of electronics (and, to some extent, simple electrics) to photography.

Subjects covered include: Batteries, Mains Power Supplies, Light Measurement, Exposure Meters, Semi- and Fully-automatic Cameras, Electronic Shutters, Electronic Focusing, Artificial Lighting-embracing Mains-driven Lighting, Expendable Flash Bulbs, and Electronic Flash, Remote Control, Electronic Timers, Print Exposure Control, Electronics in the Darkroom, Photographing TV and Oscilloscope Screens, Adding Sound to Photographs. Other chapters cover electronic fundamentals, and the history and future of electronics in photography.

Basically, this is a "how-it-works", rather than a "how-to-do-it" book. There are some circuits for the electronics enthusiast to play with in the later chapters, but these are very definitely not suitable for the novice.
G.C.A

# SETHENDUUTIOR UPDAIE 

## SWITCHER

Standard series voltage regulators are a real blessing but one thing they cannot claim to be is efficient because they must, by their very design dissipate a substantial amount of power, especially if the input/ output voltage differential is large.

It has long been recognised that a more efficient regulator design can be produced by employing the "switching" technique which relies not on controlling the effective resistance of a series regulator but on varying the pulse width or frequency of a "chop-ped-up' version of the input voltage which is then converted into an equivalent smoothed d.c. output with the aid of a simple LC filter. A switching regulator is efficient because the series pass transistor is always turned hard-on or hard-off and these of course are both low dissipation states. In the past, switching regulators have only been used where their high efficiency is particularly advantageous (generating 5 V from a 28 V battery supply for example) because they tended to be rather expensive to put together using discrete components.

Thanks to Texas instruments, the switching regulator can come in out of the cold and be used wherever its special characteristics are required, without worries about cost now that the TL 497 is available. This new device brings together in a tiddly fourteen pin d.i.l. all the active components required to build a variety of switching regulator circuits with 60 per cent or greater efficiencies, adjustable output voltages, and current limiting.

The TL 497 contains a 500 mA switching transistor, a control oscillator, current limiter, sense amplifier, 1.2 V reference and a commutating diode, and will operate over a range of frequencies from 10 kHz to above 50 kHz .

When you switch to "switchers" you don't only gain efficiency either. After being chopped up by the switching transistor the output is essentially an a.c. signal and so can
be used to provide voltage step-up or inversion with the aid of the inductor section of the l.c. filter.

## BUBBLE MEMORY

Microprocessors and also larger computers usually require two different kinds of read/write memory for efficient operation. First and foremost of course, they need a RAM array which can be accessed very rapidly (less than 1 microsecond) and which is used for the storage of programs and data required for immediate use.

This kind of memory, while being. the most versatile, is relatively expensive to provide and can be physically bulky. This creates a need tor a second kind of memory which can store very large quantities of data in a cheaper and more compact form such as magnetic tape or discs. When this kind of storage is available data can be transferred back and forth between it and the RAM so that the processor itself "sees" only high speed random access storage which apparently has a limitless capacity for data.

Due to their inherently sequential operation magnetic tape and disc systems have average access times considerably in excess of those exhibited by semiconductor RAM, but their cheapness and non-volatility still make them very attractive whenever bulk storage is required. Ever conscious of this requirement for cheap sequential access memory systems the semiconductor giants have long dreamed of producing a solid state equivalent which would remove the dependence on unreliable mechanical tape and disc drives, and on the face of it, Texas Instruments appear to be close to making these dreams come true with the TBM 0101 device.

The TBM 0101 is a "Bubble Memory" device which packs no fewer than 92 kilobits of data into a fourteen pin d.i.I. module which consumes less than 700 mW in continuous operation while providing an average access time of about 4 ms .
"Bubble" technology is unusual in that it marries the economy of magnetic storage with the high density of semiconductor fabrication techniques by building on to a semiconductor chip a sort of magnetic track along which tiny individual magnetic domains (only $5: / \mathrm{m}$ in diameter) are constrained to propagate.
Control functions such as "transfer", "replicate" and "annihilate" are implemented by providing current pulses through appropriate control elements on the chip which cause local alterations in the magnetic field. To detect the presence or absence of "bubbles" as the domains are called, two magneto-resistive elements are provided which can drive an external "sense" amplifier to produce TTL compatible data.

The TBM 0101 is a first step into this intriguing new technology, and no doubt further development will reduce the amount of external drive circuitry currently required. Who knows, perhaps one day we will see all solid state audio "tape recorders' using devices like these!

## BEEFY CMOS

The trouble with CMOS is that it can seem a bit puny when you want to use it to talk to the outside world where all the relays, lamps, and l.e.d.s live. If you have ever become a bit peeved about having to parallel umpteen CMOS gates together to get the sink current you needed you'll be pleased to hear about the CD 40107 BE from RCA which while being a fully paid up CMOS member can sink (wait for it!) no less than one hundred and twenty milliamps!

The CD 40107 BE contains two independent two-input NAND buffers with open drain $n$-channel output transistors, which means, of course, that it only sinks current and doesn't source it like standard CMOS. The bulging biceps of this new device are squeezed into an 8 pin Mini-d.i.p. package which has, I'm told, a built in hairy chest! design and develop hardware and processor based software for a Comprising three , Programmer and UV ment System, Prall component cost Eraser, the overall compoprocessor is around £200. Th 4040, and features used is the Intel 40t-based hexainclude calculatord and display, decimal keyboard aS RAM store, battery-powered crable input output and a

## Also in this issue...

##  

Using a 1 MHz crystal as the frequency standard, this instrument can count at up to 25 MHz , giving an accurate readout on a 5 -digit display. The four mode settings include TIME in milliseconds and COUNT PULSES.

## H.1.1. I E B M11 1 1 BEIG! ! I I

A simple unit which allows a conventional d.c.-coupled oscilloscope to be used to display the state of eight logic channels.


## BIGGER PAGES!

Starting next month the page size of PE is going up, giving more room for the growing technologies without neglecting simpler projects.

## PRACTICAL



Microprocessors do not just represent a new "ball game" for electronics enthusiasts, they make up a whole new "Olympiad", so full of new "events" that we need not blame ourselves for wondering, "Which shall we enter first?"

At one end of the arena we can expect to find simple dedicated applications which employ a small handful of chips in a low cost answer to an existing problem. At the other end we can already see the exciting prospect of truly useful home digital computers with language compilers, cassette storage, VDU displays and several kilobytes of RAM memory. This latter use of microprocessors will have the interesting effect of bringing new entrants into our hobby from the ranks of the "software-people" in rather the same way that our ranks are swelled by ever increasing numbers of "music lovers"!

If you are still sitting in the stands, wishing that you were down on the track, and wondering how on earth to get started, this concluding part of "Microprocessors Explained" is for you. The idea is to help you decide just what part of the arena you want to enter, and having sorted that out, to help you decide just what to spend your hard earned cash on. Oh-and if you are interested in gold medals, microprocessors may not be for you, all we can offer is a great deal of "toil, tears and sweat'", and a lot of fun!

WHAT do yot want to do with microprocessors? If they are a new and bewildering subject to you (and if you haven't had any association with digital computers in general, they probably are) you may answer that question by pointing out that you find the concept exciting but that you really don't feel ready to take a positive move towards any specific microprocessor-system until you have had time to learn a lot more about the subject.

If this is your reaction don't just say, "One of these days I'll get a book from the library." There has been enough meat in this series to get you out of the novice class and on to the nursery slopes, so dig out those back numbers and be advised that when you feel au fait with input/output ports, stacks, accumulators and hex code, you will be ready to put the L plates up and start the exciting business of building and/or operating a microprocessor system of your own!

Of course you could end up hating the sight of a hexadecimal keyboard, or even having ceremonial software-bonfires on your front lawn, so obviously you don't want to fork out a lot of money on your first cautious contact with this alien world. Fortunately, the microprocessor manufacturers are aware of your problems (well, some are!) and it should be possible to find a system to suit your neecis and your pocket, with the aid of Table 6.1. This "Consumer Guide" attempts to set down the salient features of the Evaluation Kits and Tutorial Systems now available.

## LOW COST INTRODUCTORY SYSTEMS

What manufacturers do to produce these low cost introductory systems is to put together on a p.c.b. a basic self-contained microprocessor system with clock facilities, a modicum of ram, and a rom full of "System

Monitor" programs. In general, no cabinets, power supplies or input/output terminals are provided, and all these are necessary to a greater or lesser degree. The System Monitor programs usually expect to speak to an AsCII coded, 10 character-a-second terminal, such as a Teletype ASR33, although in some cases a simple hex keyboard/display may be used, or may even be provided as part of the deal.

The idea is that the user can cook up small programs and enter these into his system's ram memory via an input device, and then run them and debug them using system-monitor commands. If you are asking "What kind of programs" then the answer must be "very simple ones" because (a) there won't be a lot of program space in the ram and (b) the input/output arrangements will be a limiting factor.

To start with, these basic systems act as tutorial systems, allowing the user to become familiar with the operation of a particular MPU chip and to develop his programming skills. As confidence is gained they take on the usefulness of development systems, where sections of a program can be checked out before they are transferred to a "homebrew" hardware system.

## EXPANSION

Very often the basic system can be expanded by adding extra memory and interface facilities, though in most cases the method of expansion is left very much to the user, and a good knowledge of system operation is required before this can be attempted.

## HARDWARE ORIENTATED

All this fussing about "evaluation" and "tutorials" may turn some of you off. You probably cut your teeth on a 7400 gate, back in the frontier days of logic. Since then you have dabbled with those cissy cmos gates and rode roughshod through board after board of "Manufacturers' TTL fall-outs", blazing a trail littered with pseudo-random lawn sprinklers, psychedelic door bells, and a host of other less well-known achievements. Now along come these microprocessors. They look as though they have some potential, particularly since you have just calculated that to build your latest U.F.O. detector you will need $428 \cdot 25$ tit packages which according to rule-of-thumb estimates will have the interesting effect of dimming the lights for miles around when switched on. Maybe, just maybe, it would be worth running your eye over a microprocessor workshop manual (it's only logic after all), putting a new point on your soldering iron, and sending off for a few Jiffy-bags full of MPU chips.

Exaggeration? Well, maybe a little, but you know the sort of hardware-orientated fanatics we are talking about, and with a maverick spirit like theirs they will get to grips with microprocessors in the end, never fear, even without our help! If anyone reading this identifies with them, and is casting his flinty gaze in the direction of microprocessors ("A man's gotta do ... etc.") we would point out that their interest is very close to our hearts; we too like to think of microprocessors doing something practical.

All we urge is that the hardware enthusiasts recognise that using these devices does require an investment in acquiring some new skills, and so a good place to start is with Table 6.1 so that a minimal system (let's call it a development system!) can be selected to start them on their way. (No doubt before they even get their system unpacked these guys will be thinking about expanding


| TABLE 6.2: HOME COMPUTERS |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| System | Manufacturer | MPU Chip | Description - | Available peripherals |
| 8800 | Altair | 8080 | The ALTAIR 8800 is a ready built cabinet mounted computer system which uses a versatile bus structure for easy plug-in expansion up to 16 boards. System monitor, resident assembler and BASIC interpreter are all available on paper tape or cassette. | TTY, VDU, Line printer. Floppy disc drive. |
| MP68 | Computer Workshop | M6800 | The MP68 is a cabinet mounted computer system which is available either as a kit or ready built. The basic system arrives with 2 K of RAM but this is expandable to 24 K by plugging in extra boards. Software available includes an assembler/editor, two versions of BASIC and a floating point math package. | VDU, Printer, Audio cassette interface, Graphics system. |
| PET | CBM | 6500 | The PET is a futuristically styled self contained home computer which includes a VDU, an ASCII keyboard, a cassette deck and 4K of RAM. Software is based on a powerful BASIC interpreter in ROM and memory expansion is possible up to 32 K . Plans are afoot to sell pre-recorded programs covering accounting and educational applications. (PET will be available in the Autumn.) | (Future) Printer, Modem. Floppy disc drive. |


|  |  |  |  |  |  | TABLE | 6．3 MPU C | HIPS |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Chip | Manfs． | Technology | Word length | On chip clock | Interrupt lines | Basic address－ ing range | Subroutine nesting levels | No．of instructions | Power supplies |  | 6u！fed，ןeכ！foedd， |  | Chip |
| 4004 | Intel National | PMOS | 4 bits | No | None | 4K | Three | 46 | $+5 \mathrm{~V}-10 \mathrm{~V}$ | 大 | 大 $大$ | $\star$ | 4004 |
| 4040 | Intel | PMOS | ＂ | No | One | 8K | Seven | 60 | $+5 \mathrm{~V}-10 \mathrm{~V}$ | $\star$＊ | ＊大 大 | ＊ | 4040 |
| 2650 | Signetics | NMOS | 8 bits | No | One | 32K | Eight | 75 | ＋5V | 大 | 大 $大$ 大 | ＊ | 2650 |
| 6500 （Family） | MOS <br> Technology | NMOS | ， | Yes | Two | 65K | Unlimited | 56 | $+5 \mathrm{~V}$ | 大 | 大 | ＊ | 6500 （Family） |
| $8080$ | Intel，Texas AMD <br> National | NMOS | ＂ | No | One | 65K | Unlimited | 74 | $5 V+12 V-5 V$ | 大 | $\star$ | $x+x$ | 8080 |
| CDP1802 | RCA | CMOS | ＂ |  | One | 65K | Unlimited | 91 | $+3 V$ to +12 V | ＊＊ | 大 | ＊＊ | CDP1802 |
| $F 8$ | Fairchild Mostek | NMOS | ＂ | Yes | System depen－ dent | 65K | System dependent | 62 | $+5 V+12 V$ | 大 | 大 $\boldsymbol{*}$ 大 | ＊ | $F 8$ |
| LP8000 | General Instrument | PMOS | ＇ | No | None | 16K | Unlimited | 48 | $+5 \mathrm{~V}-10 \mathrm{~V}$ | $\star$ | 大 | ＊ | LP8000 |
| M6800 | Motorola AMI | NMOS | ＂ | No | Two | 65 K | Unlimited | 72 | $+5 V$ | ＊＊ | 大 $大$ 大 | t＊ | M6800 |
| SC／MP | National | PMOS | ＂ | Yes | One | 4K | Zero （software expand－ able | 46 | $+5 \mathrm{~V}-7 \mathrm{~V}$ | 大 |  | $t>$ | SC／MP |
| 280 | Zilog Mostek | NMOS | ＂ | No | Two | 65K | Unlimited | 158 | $+5 \mathrm{~V}$ | 大 |  | $t \boldsymbol{*}$ | Z80 |
| IM6100 | Intersil Harris | CMOS | 12 bits | Yes | Two | 4K | Unlimited | Microcoded | +5 V to +10 V | ＊＊ | $\star$ | $\boldsymbol{*} \boldsymbol{*}$ | IM6100 |
| CP1600 | General Instrument | NMOS | 16 bits | No | One | 65K | Unlimited | $87$ | $+5 V+12 V-3 V$ | ＊ | $\star$ | ＊＊＊ | CP1600 |
| F100L | Ferranti | CDI | ＂ | No | One | 32K | Unlimited | 29 （BASIC） | $+5 V$ | $\star$ | $\star$ | 大 $大$＊ | F100L |
| PACE | National | NMOS | ＂ | No | One | 65K | Unlimited | 45 | $+5 \mathrm{~V}+8 \mathrm{~V}-12 \mathrm{~V}$ | ＊ | ＊ | ＊＊ | PACE |
| TMS9900 | Texas | NMOS | － | No | One | 65K | Unlimited | 69 | $+5 V+12 \mathrm{~V}-5 \mathrm{~V}$ | $\star$ | ＊ | $t \rightarrow t$ | TMS9900 |

[^1]what they have bought; extra memory, twin carburettors, U.F.O. field sensor peripherals, etc.!)

## SOFTWARE ORIENTATED

The next category of potential MPU user may not even read this magazine at all, or if they do, it's on microfiche which they absorb at 2.4 pages per second. These people wear hair shirts, and no shoes, and spend all their time thinking about normed vector spaces, transcendental equations and black holes. Normally they don't have to touch soldering irons, the required I.B.M. computers and graphics displays being pressed into their eager hands by a grateful public who feel they are getting good value for money.

These potential entrants into the microprocessor arena have their eyes fixed on the misty horizon (they have never grasped the concept of hard cash!)

Don't get us wrong, we need these fundamental research workers. They want Fortran compilers, floppy discs, 32-bit arithmetic and a vDu terminal. And after someone has got up a collection, they will show us all the way ahead.

If any of these boffin types are reading this, they can get help from the tables because although a minievaluation card may not interest them, the "Home Computer" market is starting to take off, particularly in the States and there are a few systems available on the UK market already, as shown in Table 6.2.

## HARD FACTS

We have painted a lurid picture of some potential "microprocessor-people" to emphasise the differences between these extremes, but of course in between lie all the shades of grey and we have no doubr that you won't fall immediately into one of our extreme categories. It is a fair bet however, that your main interest in microprocessors can be summed up in one of three basic ways:
(a) You want to learn all about these revolutionary devices.
(b) You want to do "practical" things like control your model train layout, your car ignition system, or your central heating.
(c) You want to build a "Home Computer" with extensive software and bulk storage facilities to crunch numbers and act as an intellectual challenge.
Our tables have been drawn up accordingly, with star ratings for each of these possible uses.

## CHOOSING A CHIP

You may have been surprised by the fact that we have put the choice of "system" before the choice of MPU "chip". Certainly the professionals would look hard at the choice of chips to start with, and worry about development systems and so on, later. In our case, however, the availability of an off-the-shelf get-you-started system, or even just a design tor one, is an important prerequisite to choosing a chip to do a job.

As an example, the Zilog Z80 is an extremely attractive eight-bit MPU chip with lots of Rolls-Royce features, but if you want to use it you will have to start from scratch because there is no low cost "basic system" to help you. In our book this rules it out for the present.

By way of contrast, the Intel 4040 is not the latest and the greatest microprocessor chip on the market,
but starting next month in these pages there will be a project covering the construction and use of a low cost development system based on this chip and this makes it a very attractive choice for amateur projects.

Even though the availability of hardware and software support (in our price range!) has a powerful influence on the MPU chip we eventually choose, there is bound to be a need for an objective chip comparison, and we have provided this in Table 6.3 which covers the more prominent MPU chips currently available. This comparison does not include the faster bipolar microprocessors because in our opinion they are out of our market and rather specialised in their application at present.

## CHOOSING A PERIPHERAL

The choice of an input/output device for use with general purpose microprocessor systems is a very difficult one for home constructors. Many "basic systems" cards expect to talk to a Teletype ASR33 terminal or similar, as mentioned earlier, and with prices starting at about $£ 500$ this is clearly out of the question for most of us.

Some manufacturers have recognised this problem and have come up with ingenious solutions. Simplest of all is a rewrite of system software to allow it to control a simple hexadecimal keyboard and sevensegment display via a few input/output ports. Hexadecimal peripherals of this sort can be made from small calculators and are consequently of low cost, but the problem is that unless you modify the system software yourself (not an easy job if you are a beginner) these devices are not compatible with most "ASCII-speak" systems.

A compromise between the Teletype and the hex keyboard, which overcomes the software compatibility problem of the latter, is possible if a system is built which "imitates" a Teletype in some way. You can patch up simple imitations with the help of a UART chip and some leDs and switches; however G.R. Electronics actually make a Teletype imitation which lives in a calculator case and does most things that a Teletype does at a fraction of the price! Needless to say, at the heart of this neat innovation is a microprocessor which has been programmed to believe it's a terminal!

## VIDEO DISPLAY UNIT

One very attractive way to talk to your microprocessor is via a VDU (Video Display Unit) which uses a TV screen as an output medium and is usually twinned with a full AsCII keyboard for input. vDus can be made fully compatible with existing system software and need not be as expensive as they sound if you can use an existing TV and can buy a ready-made, surplus ASCII keyboard.

If you are interested in the "Home Computer" side of microprocessors then a vDU is a natural choice, but if you want to build dedicated systems then a simple hex keyboard will be sufficient if your chosen system will drive it.

## THE END OF THE BEGINNING

That brings us to the end of this introductory series. Next month sees the start of an exciting microprocessor constructional project which will enable those readers who have developed all the symptoms of the micro-processor-bug to indulge themselves further!


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# SCRIUMPI KIT REVIEWED <br> By D.B.JOHNSON-DAVIES 



WITH all the articles on microprocessors that have been appearing recently there must be a number of readers who feel that they will never really understand micros until they have actually used and programmed one, and who are therefore wondering how they can get their hands on a system as cheaply as possible. Bywood's "Scrumpi" kit may be the answer as it provides a self-contained development system using the minimum of parts, and at $£ 55.56$ costs less than most other solutions.

## DESIGN CENTRE

It is designed around National Semiconductor's SC/MP, an 8-bit low cost micro which has a simplified instruction set and architecture in aid of economy. Its lack of sophistication means that most programs require more steps to achieve the same as a micro with a greater variety of instructions, registers, and addressing modes. On the other hand the chip provides a good selection of control inputs and outputs eliminating the need for $1 / 0$ devices in simple applications: three outputs, flags $F O$ -1 , and -2 , are controlled by bits in the status register and two inputs, SENSE-A and $-B$, set bits in the
status register. In addition, SENSE-A can optionally cause an interrupt. Serial $/ / 0$ can be performed via the SIN and SOUT pins using the extension register.

In "Scrumpi" the states of the twelve address lines and the eight data lines are displayed in binary form on l.e.d.s driven by CMOS buffers. The data lines can be taken to ground by eight programming switches. The memory consists of two $256 \times 4$ bit memory chips, providing 256 words of read/write memory. Two four-bit latches act as an eightbit 1/O port in which each set of four can be wired as either inputs or outputs. They are enabled by the highest address line, All, so that all addresses in the range X'800 to X'FFF (where the $X$ ' signifies hexadecimal notation) are mapped on to the one I/O port.

## FUNCTIONS

The various functions of the kit are controlled by a flip/flop, a 555 timer and some NAND gates, and are selected by a further eight toggle switches. These are: RESET, SLOW. STEP, RUN/HALT, PROTECT, SENSE-A, SENSE-B, and LOAD. The circuit of the. kit is shown in simplified form in Fig. 1.

All the components are mounted on the single-sided fibreglass printedcircuit board; twenty wire links are needed to complete the connections. A double-sided board would add little to the cost and it is difficult to see why one was not used. Apart from this inconvenience construction was straightforward. All the parts were supplied and sockets were provided for all the i.c.s. The switches are soldered to the board by their terminals, but the whole board could be mounted behind a suitably drilled panel to make a more robust unit. The circuit needs a power supply of +5 V and -7 V and these can be derived from a single 12 V supply with a 5 V Zener diode.

## HOW IT WORKS

"Scrumpi" gets away without the need for any monitor program in rom by making cunning use of the control signals provided by the MPU. The memory is programmed by a primitive form of DMA (direct memory access) by automatically stopping the MPU during each instruction cycle. All the instructions consist of at least one read cyclethe "instruction fetch" which gets the op-code from memory. For example, $S R$ (shift right) has only one cycle. For the two-word instructions there is a second read cycle to fetch the displacement or data; for example, LDI (load immediate) has a second read cycle to get the data from the next location. Store instructions obviously have an additional write cycle, and the two longest instructions ILD (increment and load) and DLD (decrement and load) consist of three read cycles and one write cycle.

The MPU is stopped by taking the NHOLD input low during the input or output cycle, and this extends the cycle indefinitely until NHOLD is


Fig. 1. Simplified circuit diagram of "Scrumpi". The eight function switches control the various modes of operation of the kit
returned high. In "Scrumpi" the NHOLD input is controlled by a D-type flip/flop, which is reset by the pulse on the NADS (address strobe) output occurring at the start of each input/output cycle; see Fig. 2. This puts NHOLD low extending the cycle until the flip/flop is clocked by a pulse from the output of the 555 timer; the MPU is then released to run until the next NADS pulse at the start of the next cycle.

In a read cycle the MPU is stopped with NRDS (read strobe) low which is used to enable the memory in write mode, causing the data in memory at the location addressed by the MPU to be put on to the data bus. In a write cycle the MPU is stopped with NWDS (write strobe) low which enables the memory to read data put on to the data bus by the MPU.
"Scrumpi" is programmed by stepping or running the MPU to the required address, putting the eight data switches to the required eightbit binary value, and then operating the LOAD switch. This switch puts the memory chips into read mode and so loads the value on the data bus into the memory location. Programming
can only be done during a read cycle since in a write cycle the MPU is putting data on to the data bus too.

## ENTERING A PROGRAM

To make this operation clear consider how one would enter the following program which uses the instruction ILD (increment and load) to increment the contents of location X'032 using program-counter-relative addressing. The program follows:

| Address: | Data: |  |
| :---: | :--- | :---: |
| 001 | A8 | ILD |
| 002 | 30 | disp. |
| 003 | next | instruction |
| $\cdot$ | $\vdots$ | $\vdots$ |
| 032 | 04 | data |

First all memory locations are set to X'00 by running the MPU while loading with the data switches set to 0 . Operating RESET now starts the MPU at address X'001 (Fig. 2 (a)). The required value, $X^{\prime} A 8$, is set on the data switches (as 10101000) and LOAD operated to store this to memory. The STEP switch will now
cause the 555 timer to deliver a pulse, releasing the MPU from hold state to execute the instruction. Since the ILD instruction is four cycles long (see Fig. 2) a further three operations of the STEP switch are needed to complete execution of it.

The second cycle fetches the displacement ( $X^{\prime} 30$ ); see Fig. 2 (b). This added to the program counter gives the effective address of the data: X'032. The third cycle fetches the data from this location. Finally the fourth cycle writes the incremented value back to the same location (Fig. 2 (d)).

- The kit could thus be said to provide a hardware trace facility by making use of the SC/MP's control signals; "Scrumpi" makes an asset out of economy and provides a graphic demonstration of how each instruction behaves in action.


## BREAKPOINTS

"Scrumpi" also provides a hardware breakpoint facility. The code $X^{\prime} 00$ is interpreted by the MPU as a HALT instruction; in fact execution of it will pulse the H -flag which is put out on line D7 when NADS is


Fig. 2. Timing diagram showing how the control input NHOLD is used to stop the MPU after each of the read/write cycles of the four-cycle instruction ILD (increment and load). The shading indicates that the outputs concerned are in high-impedance state

Iow. With the run/Halt switch in the correct position D7 is taken to the flip/flop, gated by NADS. A HALT instruction placed anywhere in a program will then act as a breakpoint; executing it will rest the flip/ flop and put the MPU into hold state.

It should be obvious from the foregoing description that programming is a tedious business; the data switches must be set for each instruction to be entered and although the conversion from hexadecimal to binary becomes automatic after a time, the process is error-prone and slow which discourages attempts at large programs; added to which is the knowledge that the program will evaporate on switching off the power.

## JUMP TO SUBROUTINE

A fair amount of ingenuity is needed to get some programs into memory, especially if they contain conditional jumps, as the only access to a location is by executing instructions which lead to it. It might therefore be prudent to leave the first seven locations free so they can be loaded with the following "jump to subroutine'":

Address: Data:
\(\left.\begin{array}{lll}001 \& C4 \& LDI <br>
002 \& 01 \& X'01^{\prime} <br>
003 \& 37 \& XPAH P3 <br>
004 \& C 4 \& LDI <br>
005 \& 23 \& X^{\prime} 23 <br>
006 \& 33 \& XPAL P3 <br>

007 \& 3 F \& XPPC P3\end{array}\right)\)|  |
| :--- |
| load |
| P3 |
| with |
| $X^{\prime} 0123$ |

Execution of this will cause a jump to X'0124. Any location can be reached by loading the correct address in $X^{\prime} 002$ and $X^{\prime} 005$.

## KIT DESIGN

One worrying aspect in the design of this kit is the way programming Is achieved by using the data switches to ground the data lines linking the MPU and memory. Suppose that $X^{\prime} F F$ is to be altered to $X^{\prime} 00$ at a certain location. In this case all eight outputs from the memory devices are, until the LOAD switch is operated, driving into a short-circuit. The "on" resistance of the outputs is about 30 ohms so dissipation under these conditions could reach 3
watts; the maximum recommended dissipation is 1 watt. This is one reason for the instruction to load the memory with $X^{\prime} 00$ before programming.
Operation of the LOAD switch was also somewhat erratic; it is surprising that the spare flip/flop was not used to eliminate contact-bounce. Mr. Miller-Kirkpatrick of Bywood is currently involved in designing a new version of the kit which may overcome these problems.

## CONCLUSION

Aspiring computer programmers who want to forget about the hardware the moment "Scrumpi" is working would be well advised not to spend their money on this kit; it is just not a practical proposition to write more than the simplest of programs on the system. To quote from the manual: 'You will very soon realise that "Scrumpi" is very limited as it stands because it does no more than light up l.e.ds." The constructor who is more interested in hardware than software, however, could use "Scrumpi" to form the base from which to build a more extensive microprocessor system.

## Fulfils the need for an accurate means of measuring

 capacitance and resistance over wide ranges

0NE of the most frustrating problems that the electronics enthusiast can encounter is to be faced with a capacitor of unknown value or a capacitor that is suspected of being faulty, without having available the appropriate test gear to perform the required measurement. Although a capacitance meter is likely to be required less often than the more important items of test gear, it can prove to be very useful and much used in the long term.

Problems can also arise when one wishes to make accurate resistance measurements, as many multimeters have only a couple of resistance ranges, and a logarithmic resistance scale that reads from right to left. Apart from being inconvenient to read, the accuracy on the resistance ranges of most multimeters is less than that obtained on the other ranges.

The device that forms the subject of this article has been designed to fill the need for a convenient and accurate way of measuring capacitance and resistance at low cost. Furthermore, it requires no external components for calibration, and the calibration process merely consists of adjusting four preset resistors (one for each range) for f.s.d. of the panel meter.

## RANGES

The circuit does not merely consist of separate resistance and capacitance measuring circuits with the same meter being used to indicate the measured value, but achieves maximum economy by using the same basic circuit for both types of test.

Eight ranges are covered, four of resistance and four of capacitance. These are as follows:

| Range | Resistance | Capacitance |
| :--- | :--- | :--- |
| 1 | $0-10 \mathrm{M} \Omega$ | $0-1 \mathrm{nF}$ |
| 2 | $0-1 \mathrm{M} \Omega$ | $0-10 \mathrm{nF}$ |
| 3 | $0-100 \mathrm{k} \Omega$ | $0-100 \mathrm{nF}$ |
| 4 | $0-10 \mathrm{k} \Omega$ | $0-1 \mu \mathrm{~F}$ |

These ranges permit the measurement of resistance between a few hundred ohms and 10 megohms, and capacitance between a few tens of picofarads and one

## COMPONENTS

## Resistors

| Resistors |  |  |
| :--- | :--- | :--- |
| R1 | $10 \mathrm{k} \Omega$ | R9 |
| R2 | $120 \mathrm{k} \Omega$ | $1 \mathrm{M} \Omega$ |
| R3 | $680 \Omega$ | R10 |
| R4 | $5.6 \mathrm{k} \Omega$ | R11 |
| R5 | $100 \mathrm{k} \Omega$ |  |
| R $\Omega$ | $4.7 \mathrm{k} \Omega$ | VR12 |
| R | $4.7 \mathrm{k} \Omega$ preset |  |
| R | $560 \Omega$ | VR13 |
| V | $4.7 \mathrm{k} \Omega$ preset |  |
|  | VR14 | $4.7 \mathrm{k} \Omega$ preset |
|  | VR15 | $4.7 \mathrm{k} \Omega$ preset |

R8 $10 \mathrm{M} \Omega$ (see text)
All metal oxide 1 or $2 \%$ except presets

## Capacitors

| C1 | $100 \mu \mathrm{~F}$ 10V elect. |
| :--- | :--- |
| C 2 | 220 nF type C280 |
| C 3 | 470 nF type C280 |
| C4 | $1 \mu \mathrm{~F}$ |
| C5 | 100 nF |
| C6 | 10 nF |
| C7 | 1 nF |

Semiconductors
$\begin{array}{ll}\text { IC1 } & \text { NE555V } \\ \text { IC2 } & \text { NE555V } \\ \text { TR1 } & \text { BC109 }\end{array}$

## Switches

> S1 D.p.d.t. toggle switch S2 D.p.d.t. toggle switch (used as s.p.d.t.) S3 4-way 3-pole standard wafer rotary switch S4 Push-to-make release-to-break push button switch

## Meter

ME1 1 mA f.s.d. moving coil panel meter

## Miscellaneous

Case about $205 \times 140 \times 75 \mathrm{~mm}$ (Verobox type 75-1411D or similar). 3.5 mm jack plug and socket, two crocodile clips or probe clips, materials to produce the p.c.b. PP7 battery and clips to suit, control knob, hardware.
microfarad. It thus covers by far the majority of values the amateur is likely to need to measure.

All ranges have a forward reading linear scale.

## OPERATION

The circuit is based on two NE555V timer i.c.s. Fig, 1 shows the complete circuit diagram of the unit.

IC1 is used in the astable mode, and C2 is continually being charged via R2 and discharged through R3. As R3 has a much lower value than R2, the discharge time is considerably shorter than the charge time.

The output of IC1 is developed across R4, and the voltage at pin 3 of IC1 is high while C2 is charging, and low while it is discharging. A series of very brief negative pulses are thus produced by IC1 and fed via C3 to the input of IC2. The astable operates at the fairly low frequency of about 50 Hz .

The meter circuit is not fed direct from the output of IC2, as the peak output voltage of this varies with fluctuations in the supply voltage. It is important in the interest of accuracy that the average output voltage across the meter is dependent upon the monostable pulse length, so R5, R6, R7, and TR1 are used to form a shunt regulation circuit, and they clip the output pulses at approximately +4 V . TR1 is used as an amplified diode, and this gives a much higher degree of stabilisation than using a low voltage Zener diode.

Varying the supply voltage from a little over 9 V to 7.5 V (the approximate range covered by a 9 V battery during its useful lifetime) was found to have a slight but insignificant effect upon the accuracy of the unit.

## CAPACITANCE MEASUREMENT

In the capacitance measuring role, $S 1$ connects a reference resistor into circuit and connects the test


IC2 is used in the monostable mode. Here the device produces a positive output pulse at pin 3 after a negative trigger pulse has been received at pin 2. The length of the pulse is determined by the values given to the timing resistor and capacitor. When the circuit is in the capacitance measuring mode the timing capacitor is the capacitor under test, and the timing resistor is an internal component of the device. When used to measure resistance the opposite is true.

## OUTPUT STABILISATION

There is a linear relationship between the length of the output pulse and the values of the timing components. The output of the meter circuit is fed to a meter which responds to the average output voltage of the monostable.
prods between the negative supply rail and pins 6-7 of IC2. There are actually four reference resistors (R8 to R11) giving four capacitance ranges, S 3 being used to switch in the resistor for the desired range.

With S3 in the position shown, R11 is switched into circuit and the unit has a range of $0-1$ microfarad. With a 1 microfarad capacitor connected across the test terminals each output pulse from the monostable ends shortly before the next pulse from the astable is received. This gives the astable and monostable output waveforms shown in Figs. 2(a) and 2(b) respectively. The meter circuit sensitivity is adjusted using VR12 to give f.s.d. of the meter with a 1 microfarad capacitor in circuit.

If a lower value capacitor, say $0 \cdot 5$ microfarad is now connected, the length of monostable output pulses will
be halved. This gives the output waveform shown in Fig. 2(c). The meter reads the average output voltage which will obviously be half its previous level.

It will be apparent from this that the meter reading is linearly proportional to the value of the test capacitance. Each time S3a is moved a position to the right the reference resistance is raised by a factor of ten times, and so only one tenth of the capacitance is required across the test terminals to provide f.s.d. of the meter. The unit thus obtains its four capacitance ranges of $0-1 \mathrm{nF}, 10 \mathrm{nF}, 100 \mathrm{nF}$, and $1 \mu \mathrm{~F}$.

## RESISTANCE RANGES

When used in the resistance mode the circuit operates in the same basic manner, except it is now the timing resistor that is the test component and the timing capacitor that is an internal part of the unit. S1 switches the reference resistors out of circuit and the reference capacitors into circuit, and switches one test prod from the negative to the positive supply.

As we have already seen, with a microfarad timing capacitor in circuit a timing resistance of 10 kilohm produces f.s.d. of the meter. Lowering the resistance across the test terminals reduces the monostable pulse length proportionately, and gives a lower reading on the meter. Again there is a linear relationship between the value of the test component and the meter reading, and of course the scale is forward reading. The same basic circuit can thus be used for the measurement of both resistance and capacitance. Four switched reference capacitors ( C 4 to C 7 ) provide four resistance ranges.

The power is not supplied to the circuit until S4 is depressed. A normal on/off switch is not used as when S1 is in the "Resistance" position and no resistor is connected across the test prods, the meter would be deflected beyond f.s.d. if the power was connected. This problem is solved by using a pushbutton for the on/off switch, as this is not closed until the component under test has been connected to the test prods.

## BATTERY CHECK

Current consumption is about 10 milliamps, but as power is only drawn while a reading is being taken, an ordinary 9 V radio type battery (PP7, etc.) can be used to power the unit and will have virtually its shelf life.

When the battery voltage does drop due to ageing, misleading results could be obtained and there is the danger of the battery leaking and damaging the unit. A

battery check circuit has therefore been included. This uses S2 and R1, and with S2 in the "Check" position the meter is connected across the supply rails via R 1 . The meter then has a f.s.d. sensitivity of about 10 V , and can be used to check that the loaded supply voltage is satisfactory.

## CONSTRUCTION

Many of the components are mounted on a printed circuit board that measures $86 \times 56 \mathrm{~mm}$. Details of this are shown in Fig. 3.

There is quite a large amount of point to point wiring to the components on the front panel. When this has been completed the p.c.b. is mounted on the base of the cabinet behind S1, S2 and S3 using three 6BA or M3 bolts, and spacers to hold it a little way clear of the bottom of the case.


Fig. 2. (a) Output from the astable circuit, brief negative pulses to trigger the monostable (b) the waveform across the meter at f.s.d. (c) the waveform across the meter at half f.s.d. (d) the waveform across the meter at ${ }_{1}^{1}$, th f.s.d.


Fig. 3. Printed board track pattern shown full size


Fig. 4. Board assembly and complete interwiring details for unit

## ADJUSTMENT AND USE

A set of test leads are required, and these consist of a couple of 100 mm lengths of insulated wire each terminated in a 3.5 mm jack plug at one end and a crocodile clip at the other.

At the outsetVR12-VR15 are all adjusted to insert maximum resistance into circuit (fully clockwise). Temporarily connect the centre tags of S3a and S3b together. Mechanically zero the meter, turn the unit on, and set S3 to position 1. The meter should give a large positive indication and then VR15 is adjusted to give precisely f.s.d. of the meter. Then switch S 3 to the other three switch positions, and use the appropriate preset resistor to produce f.s.d. of the meter in each switch position.

## COMPONENTS

This method of calibration uses the internal timing components as the calibration standards. It is therefore important that these components have close tolerances as it is the precision of their values that largely determines the accuracy of the finished unit.

The resistors should have tolerances of 1 or 2 per cent, and the capacitors tolerances of between 1 and 5 per cent, according to availability. The smaller the tolerance of these components the better.

R8 can be a 5 per cent type as this is the closest tolerance in which this value would seem to be available. Alternatively it can be made up from several 1 or 2 per cent types connected in series to provide the required value of 10 megohms.


## HIGH FLYING

At the end of his year of office as President of the Electronic Engineering Association. Peter Bates needs no excuses for pointing out how well the industry had performed in 1976 against a background of world recession, high interest rates, inflation and all other problems which affect us.

As I recorded month by month in this column last year the various successes in order intake, in deliveries. in exports, I remained optimistic while fellow commentators on other industries were almost universally full of gloom.

My optimism was apparently iustified. The 1976 figures now available show that the capital goods sector of the electronics industry increased its total output of $£ 1,400$ million, a gain of 28 per cent, and of the total 42 per cent was directly exported. There was a positive trade balance in Britain's favour of £206 million. an improvement of nearly 40 per cent over 1975.

If we exclude computers, where our imports are traditionally greater than our exports, the trade balance looks even better. This does not mean, however, that our own computer industry is in the doldrums. Total sales in 1976 were $£ 565$ million, a gain of 26 per cent, and 55 per cent went for export.

Well, these are iust the bare bones of a mass of statistics which confirm the trend towards even greater achievement. But the difficulties remain immense, not least being the handicap of a weak government which, while paying lip service to the need for incentives to greater efforts does precious little to provide them.

The successor as President to the EEA is Ronald Newham, an old hand at EMI (he has 40 years' service) and Director responsible for engineering and marketing at EMI Electronics. Amona the pressing problems he is now facing is forging a new relationship with the Society of British Aerospace ComDanies (SBAC) now that the maior airframe manufacturers, including the guided missile sectors which have strong direct electronic interests, have been nationalised.

Even today few people fully realise that one person out of every three of the working population is employed in the public sector. Think about it. Seven million Deople. The whole of the manufacturing sector of British industry, the wealth producing sector, only employs $7 \frac{1}{2}$ million.

## HAND-OUT?

As forecast in this column in our February issue, the Post Office had £100 million surplus and, under pressure, has agreed to return $£ 7$ per line to each telephone subscriber. My figures were exactly right but I was wrong in suggesting that the Post Office would not pay out. But they have done so grudgingly, with ill grace, and are clearly determined to claw it all back in an as yet unspecified manner.

But the Post Office still wins. £100 million invested at a modest 10 per cent over six manths still yields $£ 5$ million in interest, a handsome sum which, as it rightly belongs to the public, might well be used to buy 20 badly needed EMI Scanners for the Health Service.

Not content with piling up the profits in telecommunications, I note the postal side has been doing nicely with Jubilee stamps. The collectors' presentation pack of four stamps (face value $42 \frac{1}{2} p$ ) with 16 page booklet costs $£ 1 \cdot 20$. Without the booklet they are $52 \frac{1}{2} \mathrm{p}$, only 10 p more than buying them loose. And none will be used to post a letter. There's maximising profits for you.

## THREAT FROM THE EAST?

With a thousand square kilometres of land and a population barely more than half of Greater London, Hong Kong now has 700 electronics factories, 70,000 workers in the business and a total output of over $£ 500$ million a year, nearly all in consumer electronics. Add to this Japan with its population of twice that of the UK and
equaliy frantic activity in consumer electronics and it's enough to send a shiver down the spine. Not to mention increasing production in places like Korea, Taiwan and Singapore.

Not content with domestic production, leading Japanese companies are busy setting up plants in other areas, including the UK where, at the time of writing, Hitachi is trying to follow the example of Sony and Matsushita. Naturally enough, British manufacturers of domestic TV are regarding the Japanese invasion with distaste but the government view is that if such companies are coming to Europe they may as well come to Britain with their investment and their iobs, especially if they establish themselves in areas of high unemployment.

The Hitachi affair is interesting because that company has recently helped establish a TV picture tube manufacturing plant in Finland and hope to source tubes from there for their proposed TV assembly plant in the UK. But the British would be happier if they used the Mullard 20AX tube made in Durham. Unfortunately the Hitachi tube is of their own design and to switch to 20AX would mean a re-design of the TV set. The bargaining is still going on.

## INSTRUMENTS IMPROVE

Instrument manufacturers are doing a lot better than of late according to a survey by ICC Business Ratios. Covering 60 leading companies, the survey shows a 50 per cent growth over the past three years, export sales at 32 per cent of the total and an improvement on return on capital.

## HIGH TECHNOLOGY

Few people outside the industry appreciate the level of complexity of high technology products. When the Americans decided to buy the European designed Roland allweather short range air defence missile system they found they had to translate into English 90,000 engineering documents and, almost as confusing, the 25,000 drawings were in metric measure whereas the Americans, so forward in many areas, still work in inches.

The initial phase of US production, through to prototypes and some test firings, is costing 265 million dollars. But if the Americans wanted to develop such a system themselves the cost would have been a billion dollars according to the Brigadier General who is managing the project for US Army Missile Command.

# Rimaliont A SELECTION FROM OUR POSTBAG 

Readers requiring a reply to any letter must include a stamped addressed envelope. We regret that we cannot answer any technical queries on the telephone.

## A Hot Point

Sir-With reference to the letter from Mr Aylen Baker and Mr Wilkinson (June 1977), I wish to make a few points concerning solar heating systems as applied to domestic hot water systems.

Firstly, referring to Mr AylenBaker's point on polystyrene insulation, the temperature of solar heating systems rarely exceeds $60^{\circ} \mathrm{C}$, due to the high heat losses at these high temperatures, and the fact that with the systems used in this country, the quantity of water in the system is too much to be heated over $60^{\circ} \mathrm{C}$ in one day, even if no heat was extracted from the system to heat domestic water, etc. In fact, it is undesirable to raise the temperature too high, because weighed against the extra heat gain are the heavy losses. Thus polystyrene insulation for collectors is a cheap and effective way of cutting down heat losses.

The most efficient way of running a system is to have a fairly high flow rate through the collectors. This way the temperature rise across the collector is kept low and so are the heat losses from it. I would therefore recommend the pump to be switched on for long periods continuously, instead of short bursts as Mr AylenBaker suggests. This would lead to
water lying in the collector, and being heated to ridiculously high temperatures, leading to heavy heat losses.

On the other subject of angle of incidence, I confirm Mr Williams figures of $35-40^{\circ}$ as being the optimum for collection to be maximum all year round.
M. K. Berry,
Ramsgate.

## Bad Move?

Sir-I note with some disappointment that Practical Electronics is "following the crowd" in changing to a larger format.
In my opinion, to change the size at all is a bad move, but to make the change part way through a volume is ridiculous beyond words, and shows little consideration for your readers, especially those who have kept bound copies over the years.

What argument can be used in favour of enlarging the page size escapes me. The magazine is presently of a convenient size, and the argument of using International paper sizes (e.g. A4) does not hold water, since the present page-size is so very close to the I.S.O. size B5 (see British Standard 3176). I can only suppose that someone "on high" has decided that "bigger" equals "better", and has issued his
"fiat" accordingly. We poor down-to-earth readers are (as usual) not consulted, and just have to "lump it".
R. C. Fuller,

Middlesex.
It has for long been our intention to increase the page size of PE. We believe this to be in the interest of readers and likely to be generally welcomed.

This change involves the use of larger rolls of paper by our printers. Unfortunately, reordering of bulk paper supplies does not coincide with the beginning of the year (or volume), but has to be made in the autumn.-Editor.

## Cross-hutch Generator

Sir,-Constructors building the "Cross-hatch Generator", Practical Electronics, September 1976, may be interested in a modification to the generator which facilitates colour television receiver purity adjustment. The modification may also quite easily be incorporated into an existing unit, as it requires only minimal disturbance to the circuitry.

For colour television purity adjustments, an all white raster is required. This may be achieved simply by the addition of one single-pole, singlethrow switch, wired between VDD $(+9 \mathrm{~V})$ and pin 8 of IC6a (see Fig. 2 on page 710 ). With the switch open, the unit generates the cross-hatch pattern as before. When the switch is closed, the passage of video pulses through nor gate IC6a is inhibited. Pin 10 of IC6a is therefore held at zero volts, which is the required logic level to set the video component of the waveform at IC6d output to logic 1. Blanking pulses through IC6d remain unaltered. The resultant waveform generates an all white raster.
A. A. Birch,

Penrith, Cumbria.

We are pleased to announce that the Back Number Service has now been reinstated. This takes effect with the issue dated June 1977.

This and subsequent issues of Practical Electronics will be available at the inclusive price of $65 p$ per copy. (This includes Inland/Overseas postage and packing).
Orders should be addressed to :
Post Sales Department,
I.P.C. Magazines Ltd.,

Lavington House,
25 Lavington Street,
London SE1 OPF.
Cheques and Postal Orders should be made payable to I.P.C. Magazines Limited.

A limited supply of earlier back issues is also available. Requests, with appropriate remittance, should be sent to the above address.

In the event of non-availability, remittances will be refunded.


By E. B. EVES

Some integrated circuits for the construction of digital clocks are fitted with the facility to dim the display in the dark. The circuit in Fig. 1 allows this facility to be added to most other clocks, or indeed any circuits using l.e.d. displays. With some modification it can also be used to provide two brightness levels in filament lamps, as for instance in driving lights for usé in fog.

## THE CIRCUIT

The circuit uses a light dependent resistor as the detector, connected in series with a fixed resistor. The voltage across the l.d.r. depends upon the current through it, which in turn depends upon the level of incident light.

The voltage is applied to the inverting input of IC1, while the non-inverting input is connected to the slider of Trimpot VR1.

The operational amplifier works as a saturation switch which controls the base of TR1. At normal daytime light levels the output of the amplifier is low, and TR1 is held off. The voltage at point X, therefore, is the sum of the two Zener voltages, and the output
from the circuit is approximately 0.6 V below this. As the light level falls, the voltage at the inverting input of the amplifier falls and eventually "crosses over" the voltage of the non-inverting input. The amplifier switches, and saturates TR1, shorting out D1. The voltage at point $X$ is now approximately $0.6 \mathrm{~V}+\mathrm{D} 2$ voltage. Simultaneously the voltage at the output will be 0.6 V less than this ( $\mathrm{V}_{\mathrm{BE}} \mathrm{TR} 2$ ), or D 2 voltage.
In order to prevent too high a current through the diodes, R4 should be large, and drop a large proportion of the supply volts, so that in both states the current in the diodes is within the correct operating range. For this reason the supply voltage needs to be high compared with the required display supply, even in daylight conditions. In the clock to which the prototype was fitted, the CT7001 clock chip was used, which requires a supply of 17 V . After some experimentation it was found that supply levels of 10.9 volts and 2.7 volts to the l.e.d.s via suitable dropping resistances gave acceptable day and night brightness levels, hence 5.5 volts and 13.7 volts were dropped across R4 in the two states. The maximum current rating for continuous


Fig. 1. Basic voltage control circuit

## COMPONENTS <br> - -

| Resis | tors |
| :---: | :---: |
| R1 | ORP12 (light dependent resistor) |
| R2 | $6.8 \mathrm{k} \Omega$ |
| R3 | $4.7 \mathrm{k} \Omega$ |
| R4 | * |
| R5 | $4.7 \mathrm{k} \Omega$ |
|  | Watt 10\% carbon |

Potentiometer
VR1 $20 \mathrm{k} \Omega 20$ turn Trimpot

Capacitors
C1 $10 \mu \mathrm{~F} 16 \mathrm{~V}$ elect.

| Semiconductors |  |  |  |
| :--- | :--- | :---: | :---: |
| IC1 | $\mu A 741$ |  |  |
| TR1 | BC108, BC548 etc. |  |  |
| TR2 | BFX85 |  |  |
| D1-D2 | BZY88* |  |  |

## Miscellaneous

Veroboard, 8 pin d.i.l. integrated circuit holder, connecting wire.

* see text


Fig. 2. Component layout on 0.1in Veroboard


Fig. 3. Photocell location at display window


Fig. 4. (a) Common anode drive arrangement, such as DL707, etc. The existing display cathode resistors set the segment current. (b) Common cathode drive arrangement, such as DL704, DL33, etc. Here Rs sets the segment current
operation of the diodes used was 50 mA . A resistor of $1 \cdot 2 \mathrm{k} \Omega \mathrm{kept}$ the current within this limit, allowing a current of 5 mA in the "daylight" state, which was sufficient to operate the diode satisfactorily, and approximately 15 mA in the "dark" state.
The large difference in supply volts, especially when it is remembered that the l.e.d. typically drops $2 \cdot 1$ volts is due to the great change in sensitivity of the eye. The actual relative levels are a matter of personal preference, but care must be taken to stay within the current limits of the type of l.e.d. display used.

## CONSTRUCTION

The layout of the circuit will depend on whether it is constructed as a separate unit as shown in Fig. 2, or it may be incorporated as part of the clock control boards. If a suitable voltage is available from the clock supply this may be done, if not, a higher tapping on the transformer, and a simple rectifier and smoothing circuit must be used.

The l.d.r. must be mounted to receive light falling on the display, but not light from the display. If a filter is used, setting the display back from it improves visibility in daylight, and leaves room for the l.d.r. to be mounted as shown in Fig. 3.

Potentiometer VR1 should be positioned so that it can be adjusted through a hole in the back or the bottom of the box. It was found preferable to use a $20-$ turn Trimpot, as this gave greater ease of adjustment than a normal skeleton preset. The connections to common anode and common cathode l.e.d.s are shown in Figs. 4a and 4b.

## TESTING AND SETTING UP

When the circuit has been assembled, before connecting to the display, a $10 \mathrm{k} \Omega$ resistor should be placed across the output, and the voltage across this measured in full daylight. The display "window" should then be covered. After a short delay caused by Cl (which prevents transient light flashes or shadows switching the circuit), the voltage should fall. If it fails to switch, VR1 is probably set too low and should be adjusted until switching occurs at the required light level.

In order to assess the voltages required to give satisfactory outputs, D1 and D2 may initially be replaced with a variable resistor, connecting the centre tap to TR1 collector.

The setting up described above may be carried out in this way, then the l.e.d. displays connected and the clock started. The variable resistor can now be adjusted to give the required brightness initially in the dark, and then in daylight. It should be remembered to allow the eye to adapt for several minutes to the dark before deciding finally on the output level.

The voltage at point X can now either be measured or calculated for both states, and the correct values of Zener diodes put in place of the resistors. It is not advisable to use resistors permanently, because it was found that fluctuations in the current drawn by the display caused noticeable variation in intensity at night.

This circuit has been used successfully on a l.e.d. display digital clock. For other types of display this circuit may be suitable, although some modification of the output may be required, and certainly some experimentation to find the right voltage levels.

This circuit could be used to control the filament current in a phosphor diode display, and with a suitable output transistor it could also be used with incandescent lamps.

## Microprocessor Symposium

$\mathrm{R}^{\mathrm{E}}$EADERS involved in the application of microprocessors will be interested to learn of a forthcoming residential symposium organised by the Society of Electronic and Radio Technicians. Entitled "Microprocessor Systems and Software", it will be held at the University of Kent at Canterbury frons September 26-29.

This symposium comes just twelve months after the very successful "Microprocessors at Work". In the intervening period many more working systems have been built and much practical experience gained.

This year's symposium is intended to take delegates from basic principles through surveys of current devices, development systems, system testing and software documentation. Further sessions will describe actual working applications, including greenhouse monitoring, control of heating and ventilating systems. medical applications and graphics terminals.

Further details can be obtained from the Symposium Secretary (MPU), S.E.R.T., 8-10 Charing Cross Road, London WC2H OHP, telephone 01-240 1152.

## Build Your Own Computer

THIS one-day conference, the first of its kind in the UK, attracted some 400 delegates on a sunny Saturday in May. In fact, it proved so popular that people were being turned away at the door!

Following an introductory teach-in on digital circuitry and microprocessors. a fascinating address by Manfred Peschke, publisher of the American small computer systems magazine Byte, gave a picture of personal computing developments in the USA. Applications including colour displays, synthesised speech and music, and speech analysis were described. and finally some results from a sample readers' survey conducted by Byte were given.

These revealed that some 35 per cent of readers owned an operating home computer system, while 74 per cent had qualifications at least equivalent to a Bachelor's degree. The median salary of the sample was $\$ 20,000$, and most of them expected to spend about $\$ 2,000$ per annum on their hobby! It would be interesting to see results of a similar survey in the UK.

Others speakers described the various items of hardware and software of interest to the personal computing enthusiast, with special reference to input/output devices. Several users recounted their experiences in building and using small computers in various fields, including video synthesis and education. The final speaker indulged in a little crystal gazing on the future of the computer in the home, from appliance control to complete home information systems.

Twelve firms had stands in an associated exhibition, displaying a wide range of hardware and literature. Orte of these. Computer Workshop, announced at the conference a new, complete four-terninal, multi-user computer system running time-sharing BASIC (a simple high-level language) and priced at under $£ 3,000$ including a printer. Previously, a system offering such facilities would probably have cost over ten times this amount.
Online Conferences Ltd.. who organised the conference, plan to run a similar event next year. They can be contacted at Cleveland Road. Uxbridge UB8 2DD.

# MRRHE PLALE 

Items mentioned in this feature are usually available from electronic equipment and component retailers advertising in this magazine. However, where a full address is given, enquiries and orders should then be made direct to the firm concerned. All quoted prices are those at the time of going to press.

## TOUCH CONTROL KITS

Touch activated switching can now be employed by the hobbyist or evaluated by product designers with a new kit offered by AMI Microsystems.
Designated the TCK 100, the kit includes the first microcircuit available off-the-shelf for operation of touch activated (capacitance) control panels. Up to 16 touch switches can be operated with a single integrated circuit which can be interfaced, using the AMI kit, with virtually any electrically operated product or apparatus.
Included in the kit is a prewired control panel, an AMI S9263 integrated circuit and an instruction package. With the addition of a few readily available standard components such as l.e.d.s, a transformer, etc. the unit will conveniently demonstrate the many advantages of AMI's touch control switching, which has already been used in similar form in electronic cookers introduced in the United States by Frigidaire.

As well as offering greater reliability than conventional electromechanical switches, touch control switching panels are mechanically safe, since there are no protruding knobs, electrically safe because of the insulator layer separating the circuitry from the touch surface, and more easily cleaned, a feature of particular significance in the design of domestic appliances.

This form of switching can be used in computer control, television equipment, domestic appliances, power tools, games, industrial equipment, keyboards of all types, and many other consumer and industrial products.

Full technical details and further information on the AMI TCK100 Touch Control Kit can be obtained from AMI Microsystems Ltd., 108A Commercial Road, Swindon, Wilts.

## PROGRAMMABLE CALCULATORS

Claiming a major technological advance in handheld programmable calculators featuring pre-written solid state software libraries Texas Instruments have just announced their TI-58 and 59 models.

Both can use interchangeable prerecorded program libraries which range in content from applied statistics and surveying to aviation and marine navigation.

The module programs can be addressed repeatedly from the keyboard or be inserted as subroutines in other programs developed by the user. Module contents cannot be altered although users of the TI-59 can record up to 960 steps of any new program on two magnetic cards. (A module and an inserted card can be seen in the photograph.)

So that users can put their machines to work more quickly and obtain maximum benefits of programming, Tl has developed new instructional material in a "personal programming" book form to replace the traditional owner's manual. This learning guide comes with either calculator.

In step-by-step fashion, users can learn simple programming techniques in the book's first chapter. Then they can move on through a self-paced course in programming. A comprehensive selection of examples from a number of disciplines permit users to apply programming power to a particular personal, professional, or occupational interest.

Included with each calculator is a master library module and manual

Texas Tl-59 Calculator

covering 25 pre-written programs in engineering, mathematics, statistics and finance.

Backed by a one-year warranty the TI-58 will sell for $£ 99.95$ and the 59 for $£ 24.9$-95.

Lower priced at $£ 49.95$ is the TI-57 which should have special appeal for users wanting to learn programming fundamentals, like the 58 and 59 it has features to make it easy for users to edit or correct errors in programs. These include single-step and backstep keys to review programs and insert and delete keys so instructions can be added or removed at any time.

Besides its programming features the calculator has the normal facility for higher mathematical problem solutions.

The TI-57 comes with a charger, carrying case, owner's manual and program record forms. The same warranty as before applies.

## PRINTED CIRCUIT KIT

Amongst the many new items in the latest edition of Verospeed components catalogue is a complete copper etching kit that is claimed to be both clean and safe to use.

The Seno-GS system comes in a special pack and the chemicals/ powders are kept in sealed bags. One of the bags is used to "seal-in" the prepared board during the etching process. This is accomplished by using two slide-on clamps. Designed for quick production of prototype printed circuits from copper-clad blanks, the kit will remove the copper from up to 10 Eurocard-size boards.

Also contained in the kit is an etch resist pen, transfers and a copper cleaning block. Finally, a special neutraliser is included which ensures environmentally safe disposal once the kit is exhausted.

Further details and price of the Etching Kit are contained in the Verospeed Catalogue available from Verospeed Service, Unit 10, Barton Park Industrial Estate, Eastleigh, Hants, SO5 5RR.

Do not toy with the idea of road safety for children. Build this educational working model; for it's definitely no toy, but indeed could be a life-saver!

Practical demonstration is one of the most effective methods of teaching. Another, is learning by your mistakes; but there are occasions when the price of a mistake is too high! In these situations the written word gives way to the instructional model, where research has shown that practical demonstration of techniques and procedures eases assimilation of information, which might otherwise be highly indigestible.
It was this philosophy which resulted in the design of a small scale working model of a Pelican Crossing, primarily intended as a teaching aid for children and old people, but found in practice to capture the attention of other age groups from all walks of life.

The model had particular novelty value for children, who took delight in its operation, whilst being blissfully unaware that they were learning at the same time. Questioned afterwards, the children showed that they had grasped the essentials of using a Pelican Crossing, and their parents expressed greater peace of mind as a result.
The beneficial value of taking on a constructional project such as this for a local school or old people's home is considerable, but even at home in the lounge, the model makes an interesting conversation piece, and by its constant reminder to children, could one day save their lives.
giving fast switching edges suitable for driving TTL logic, even with a long time constant. The output pulses are fed to input a of IC2 (decade counter). The вСD output of IC2 is then fed to the input of the 74145 (IC3), which is a $B C D$ to decimal decoder/driver. Pushing S1 therefore, will cause IC3 to count through, operating RLA to RLE in turn. These relays are used for the various switching operations on the pelican crossing.

Steering diodes D7 to D18 ensure that only the correct lights operate. As well as triggering IC1, S1 also resets the decade counter IC2 to zero. The bleep is generated by IC5 modulated by IC4, the latter being a slow running multivibrator. The output from IC5 drives the loudspeaker via C8. Another slow running multivibrator is formed by IC6, which operates relay RLF. This in turn operates the flashing amber and green-man lights.

Various outputs of the 74145 have been strapped together to enable the timing cycle ratios of the lights necessary for realism. For instance, when S1 is pressed, the wait sign is illuminated, but the 74145 is allowed to count two pulses before the traffic lights change to amber.

A smooth 6.3 volt supply powers IC4, IC5 and IC6. This prevents buzzing in the loudspeaker, and relay "chatter" due to ripple. The other i.c.s are fed from a 5 volt stabilised supply, provided by IC7 and its associated components. The power supply circuits are shown in Fig. 2.

## THE CIRCUIT

The circuit is shown in Fig. 1, and when push button



Fig. 1. Pelican
circuit diagram


Fig. 2. Power supply unit. Two $6 \cdot 3 \mathrm{~V}$ lines are generated, one with additional smoothing (R5 and C14)


COMPONENTS
Resistors

| R1 | $2.7 \mathrm{k} \Omega$ | R4 | $100 \Omega$ |
| :--- | :--- | :--- | :--- |
| R2 | $1 \mathrm{k} \Omega$ | R5 | $470 \Omega$ |
| R3 | $1.8 \mathrm{k} \Omega$ |  |  |

All resistors $\frac{1}{4}$ W 5\% unless otherwise stated
Potentiometers
VR1 $100 \mathrm{k} \Omega$
VR2 $100 \mathrm{k} \Omega$
VR3 $2 \cdot 2 \mathrm{k} \Omega$
VR4 $47 \mathrm{k} \Omega$
VR5 $47 \mathrm{k} \Omega$
VR6 10k $\Omega$
VR7 $47 \mathrm{k} \Omega$
VR8 $100 \mathrm{k} \Omega$
All min horizontal skeleton presets

## Capacitors

| C 1 | $47 \mu \mathrm{~F}$ tantalum bead type |
| :--- | :--- |
| C 2 | $330 \mu \mathrm{~F}$ elect |
| C3 | $0.1 \mu \mathrm{~F}$ paper |
| C 4 | $22 \mu \mathrm{~F}$ tantalum bead type |
| C5 | $0.01 \mu \mathrm{~F}$ paper |
| C6 | $0.1 \mu \mathrm{~F}$ paper |
| C7 | $0.01 \mu \mathrm{~F}$ paper |
| C8 | $4.7 \mu \mathrm{~F}$ tantalum bead type |
| C9 | $22 \mu \mathrm{~F}$ tantalum bead type |
| C10 | $0.01 \mu \mathrm{~F}$ paper |
| C11 | $10,000 \mathrm{~F}$ elect |
| C12 | $0.22 \mu \mathrm{~F}$ paper |
| C13 | $0.47 \mu \mathrm{~F}$ paper |
| C14 | $470 \mu \mathrm{~F}$ elect |
| C15 | $1,000 \mu \mathrm{~F}$ elect |

Semiconductors

| D1-D6 | 1N4001 1A 50 V (6 off) |
| :--- | :--- |
| D7-D18 | 1N5401 3A 100V (12 off) |
| D19-D22 | REC 76 2A 200V (1 off) |
| D23-D24 | 1N5401 (2 off) |
| TR1 | BC108 |
| TR2 | BC108 |
| IC1 | 74121 |
| IC2 | 7490 |
| IC3 | 74145 |
| IC4-6 | NE555 (3 off) |
| IC7 | LM309K 5V regulator |

Miscellaneous
6 off reed relays (6-9 volt, $700 \Omega$ coil)
2 off SPDT push-buttons
Mains on/off switch
2 off 14 pin d.i.l. i.c. socket
3 off 8 pin d.i.l. socket
1 off 16 pin d.i.l. socket
Strip-board 91 by 204 mm ( 0.1 inch matrix)
Strip-board 95.5 by 50 mm ( 0.1 inch),
for power supply.
Miniature loudspeaker $35 \Omega$
Mains transformer 6.3V 5A
Fuse ( 1 amp ) and holder
Instrument type mains plug and socket
Mains neon lamp
Aluminium front plate
32 way edge connector
Up to 17 bulbs ( 6 volt 0.04 amp )
Tinplate for traffic lights
Coloured gel for lenses
Aluminium tubing
Con-Tact or Fablon

Fig. 3. Board layout.' Diode leads should be sleeved, and links shown beneath the relays should be made on the conductor side. Veropins may be used instead of a 32 way connector, and are used where several wires terminate at one hole

|  | RED | AMBER | FLASHING | green | RED | Green | $\begin{aligned} & \text { FLASHNG } \\ & \operatorname{GREEN} \end{aligned}$ | walt | bleeper |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\begin{gathered} \text { SEQUENCEE } \\ \text { NUMBER } \end{gathered}$ | $\bigcirc$ | $0$ |  | $0$ | $\hat{q}$ | so |  | WAIT | $6$ |
| 1 |  |  |  | $0$ | \％ |  |  |  |  |
| 2 | $]$ |  |  | $\bigcirc$ | $\mathrm{GH}_{3}$ |  |  | WAIT |  |
| 3 |  |  |  | $0$ | $\mathrm{GH}^{3}$ |  |  | WAIT |  |
| 4 |  | $0$ |  |  | 枵 |  |  | WAIT |  |
| 5 |  | $0$ |  |  | 枵边 |  |  | WAIT |  |
| 6 | $0$ |  |  |  |  | ns |  |  | $\cdots$ |
| 7 | $\bigcirc$ |  |  |  |  | $\mathrm{B}$ |  |  | $\sigma$ |
| 8 |  |  |  |  |  |  |  |  |  |
| 9 |  |  |  |  |  |  |  |  |  |
| 1 |  |  |  | $0$ | \％ |  |  |  |  |

Fig．4．Sequence of events at Pelican Crossings

## CONTROLS

（1）Adjustment of the transistor multivibrator pulse rate is made by VR1．
（2）The period of IC1 is controlled by VR2．Presets VR1 and VR2 need to be adjusted together to produce a full sequence of events．
（3）Adjustment of the bleep rate is set by VR3 and VR4．
（4）The actual bleep pitch is governed by VR5 and VR6．
（5）The amber light and green－man flashing rate can be set by VR7 and VR8．

The dormant state of the Pelican Crossing is when the pedestrian lights are at red，and the traffic lights are at green（see Fig．4）．When the push to cross button is pressed，the crossing system goes through five steps，starting with the WAIT signal illuminating， and finishing with the crossing back at the dormant state．The period of IC1 should be set by VR2，for the overall time taken by the crossing to complete its cycle of events．Then the multivibrator rate should be adjusted by VRI to provide enough pulses during that period to drive the crossing system through the com－ plete number of operations．


Fig．5．Power supply board layout．Nylon nuts and bolts should be used for mounting


Fig. 7. Complete wiring arrangement


Fig. 8. Real life dimensions of traffic and pedestrian lights, and control box (drawings not to scale)

## CONSTRUCTION

The main circuit is built on stripboard, the layout of which is illustrated in Fig. 3. A 32 way edge connector links this board to the rest of the circuit, including the separate power supply board of Fig. 5. The general layout of the whole unit is shown in Fig. 6.

The mains input socket, on/off switch, mains neon and fuse, are all mounted on a small aluminium front panel. Also, the transformer and Cl are mounted separately, connected by a wiring loom (Fig. 7).

Various full size dimensions of the traffic and pedestrian lights and control box are shown in Fig. 8. The dimensions are in millimetres and must be scaled down, dependent upon the required size of the model. The control box which would have contained the PUSH To cross button for maximum realism, was found to be too small on the prototype, and so a separate box on a larger scale was mounted on the side of the model. To alleviate detailed drawing and lettering on this larger control box, a photograph of the real thing was reduced to postcard size and mounted on the box facia.

The traffic light heads were built from thin tinplate suitably bent to shape, solder being used to fix the pieces together. Aluminium tubing was used to fabricate the traffic light columns. The lenses were constructed by placing red, amber or green gelatine over the light bulbs. In the case of the red-man and green-man. black Con-Tact was placed over the gelatine, and then cut out with a sharp knife, to give the outline of a man. The road itself is black Con-Tact, and the white lines are pieces of white Con-Tact.

The push buttons may have to be wired using screened cable due to the length of the looms, and the fact that TTL circuitry is susceptible to noise pick-up. It may also help to put suppressor capacitors across the push button contacts.

The loudspeaker is mounted on the side of the case, with small holes to allow for sound propagation.
The circuit does not show the push button on the other side of the road. However, the additional button is simply wired in parallel with S1. Likewise, all bulbs are not illustrated, and the extra repeater bulbs are wired parallel to the ones shown.


The prototype circuit board featured in the photograph differs from the diagrams, where the layouts were rearranged to accommodate point to point wiring on the component side

# PRTENTE RETUETMO. 

## COIN DETECTVE

An improved coin-operated timer capable of detecting fraud and suitable for a wide variety of uses, including parking meters and other pay-by-the-hour facilities, is claimed by Veritronics in BP 1464371.

The signal generator shown in the block diagram, Fig. 1, generates a 100 kHz waveform, which is applied to one plate of a capacitor, C1, through which an inserted coin passes. The current amplifier, connected to the other plate of the capacitor, amplifies the square wave of current induced to flow by the resultant voltage waveform, and the rectifier produces a proportional d.c. signal.

The rectifier output is fed to the "upper limit" and "lower limit" detectors. If both give a satisfactory output when the coin drops through the capacitor, an AND gate is activated to transmit a signal to a 10 ms delay which gives an output only if the capacitance remains with in the required limits for the 10 ms as the coin falls. This prevents the apparatus being operated by unauthorised large objects.

The delay output is fed to the monostable, which when triggered gives a delay of 100 ms . During this delay the circuitry is primed by causing diodes D1, D2 to conduct and at the same time a fixed voltage is applied to D1. At the end of the 100 ms delay, S1 stays on for 30 ms , to allow a controlled charge to flow into the timing capacitor C2 via diode D1. Thus for each genuine coin detected the timing capacitor voltage is raised by a fixed amount.

Meanwhile, a low frequency oscillator drives the 30 microsecond switch S3, which removes a controlled amount of charge from capacitor C2 every 1.5 seconds. When not being charged or discharged the capacitor is presented with the high reverse impedance of diodes D1, D2.

The meter ME1 is connected to a peak voltage sampler for capacitor C2 and indicates time paid which has not been used up. The zero level detector detects the end of time and switches off the equipment (such as games and appliances).

If the device is tampered with, the 60 ms delay operates, to indicate that a coin or other object has been in the chute for too long; a 60 second switch S2 then operates, to discharge the timing capacitor C2.


The obvious advantage of the invention is that it is purely electrical, and contains no mechanical or moving parts, except for the coin itself.

## FEEDBACK KILLER

In BP 1458 663, A.R.D. Anstalt, of Liechtenstein, proposes an apparently novel idea for killing acoustic feedback between the microphone and loudspeaker of a two-way communications system. The object is to reduce feedback risk, without recourse to voice-operated switching, even where the microphone and loudspeaker lie closely adjacent at each station.

The diagram Fig. 1 shows the circuit adopted at a station $F$, with a single loudspeaker and two microphones. The outputs from the two microphones are applied to an additive circuit $A$ and a subtractive circuit $S$, each circuit being generally a transformer or amplifier.

The additive and subtractive outputs are passed through phase and amplitude adjustment circuitry $P$ and summed in amplifier 2. This feeds the output line to remote stations and the loudspeaker LS1 of home station $F_{1}$ via hybrid $H$ and home amplifier 1. Automatic gain control (a.g.c.) is also incorporated in the circuit.

When a speaker talks directly into the two microphones this "wanted" sound produces effectively equal outputs from each. But each microphone output also inevitably contains a component due to "unwanted" sound emanating from the adjacent loudspeaker LS1. However, because LS1 is laterally offset with respect to the microphones by what is inevitably a different distance from each, the unwanted sound compqnents in the microphone outputs will differ in phase from each other and from the wanted component.

## BP 1458663



It is interesting to note that the American pop group, "The Grateful Dead", have experimented with a similar system to kill feedback from their stage PA system.


## SPECIAL SATELLITE

As part of the special research programme of the International Magnetospheric Study (IMS), two major satellite missions may be regarded as the heart of this project. Scheduled to run for the period 1976-1979, IMS is set to make a detailed study of the various mechanisms that have appeared as a result of the substantial data now available.

There are a number of important reasons for using the magnetosphere as a sort of laboratory. It provides an opportunity to study in a small scale the activities in the universe. A very large proportion of the matter in the universe consists of plasma interacting with a magnetic field. The process by which magnetic field energy is transferred to ionised particles becomes a matter of major significance.

Since this process occurs in the magnetosphere it is possible to study, on a small scale, the behaviour of matter in the universe. There are vital rewards from this for not only can it help to solve some of the meteorological problems (particularly the prediction of the weather) but also provide clues toward a better knowledge of the pulsar and nuclear fusion.

## THE TRAGEDY OF GEOS

The special satellite GEOS was designed to carry scientific equipment into a stationary orbit. This is the first satellite to be devoted to such a mission and it carried the hopes of many scientific groups. Unfortunately the launch was a partial failure. The full details of the failure are not yet known but the
consequence is that GEOS will not reach its planned stationary position of $36,000 \mathrm{~km}$ above the equator. An emergency decision at the time of the rocket motor failure put the GEOS into a highly elliptical orbit with a 12 hour period. This means two things: the first, only half the useful time each day will be available and the second that the satellite will pass through certain levels of the radiation belt which may so damage the basic electronics that its life may be limited to six months. The net result is that only a few per cent of the target hopes will be reached.

This is a salutary warning that in such important missions there should be a back-up system. The cost of the launcher is small when compared to the total cost. The tragedy is that all the costs of the launch have to be borne by ESA. Though some useful data will result it is a high cost, for more than 100 million dollars have been spent so far on GEOS.

## THE PLANNED MISSION

The satellite would have been stationary at $36,000 \mathrm{~km}$ above the equator. This position covers the region of the magnetosphere where disturbances take place due to dynamic processes. GEOS was to have been so positioned that the field lines joining the auroral zones would be observed both from the satellite and the ground stations. Thus the passage of particles back and forth along the field lines could be studied in great detail.

The satellite would have been in permanent view from a tracking station at Darmstadt in West Germany. One of the special problems with satellite experiments is the modification of local environments by the satellite. On some missions the spacecraft itself masks special phenomena. One of these is spacecraft charging and this can reach levels of 10 kV . To this end GEOS has been given an all metallic skin so that there is equipotential distribution. There are eight booms on which are mounted sensitive detectors. The booms carry these detectors some 20 metres away from the body of the satellite. It is an ambitious attempt to isolate the effects not only of the satellite itself but also the equipment within it. This very long boom system is unique and it is hoped that the sensitivity will remain unimpaired. One of these sensors is a detector set to react to a variation of the magnetic field as low as a thousand millionth of the Earth's field.

The experiments involve a number of very sensitive parameters. This is necessary for a proper understanding
of the interactions of a waveparticle nature. Four of the experiments will measure particle flux and the variations with direction and energy, over a wide range of thermal levels. There are also three experiments which will measure the effect of electromagnetic waves. A frequency of a range from zero to 77 kHz will be used for the electric fields and a frequency of zero to 20 kHz for the magnetic fields. Some of the experiments will be in duplicate to safeguard data.

The combined venture of NASA and the European Space Agency, which is the other major half of IMS, involves three satellites with a code name of International Sun Earth Explorer.

## CHANGE OF NAME

The Mariner spacecraft designated for the second flypast of Jupiter and the other outer planets have a new name. They are now to be called Voyager / and 2. They will start their journey with at least one new addition to the mission. It will be an opportunity to check at close quarters the new discovery of possible rings round Uranus. These rings would appear to contain lumps of dense material, probably rocks of the order of 100 km in diameter. This is something very exciting and susceptible to direct observation. The discovery was made a few weeks ago when an occulted star was found to have been eclipsed a number of times. This was witnessed by three independent teams of observers. It is possible that this confirmation may be available in about 1986.

It is interesting that Herschel who discovered Uranus did in fact note that there appeared to be a flattening of the poles. He wrote that there appeared to be double opposite points which might be rings! He gave this information to the Royal Society in 1796 and the details appear in the Philosophical Transactions of 1798. Later he decided that there were no rings. All this points to something peculiar about an already somewhat strange planet. However, considering the amount of observations over the 180 years that have elapsed since then it is surprising that no other references appear in the literature. It is of course possible that there is another explanation which may involve refractive layers round the planet. The correlation of three distinct and separated observing teams make a prima facie case for the solid or nearily solid occulting medium. Though it must also be said that for the occulted star to have appeared precisely in position on each side of the planet calls for very close observations.


A selection of readers' original circuit ideas. It should be emphasised that these designs have not been proven by us. They will at any rate stimulate further thought.

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# GUITAR frequency DOUBLER 



The circuit of Fig. 1 gives an output frequency which is twice that of the input. This is done quite simply by full-wave rectification. In rectifying the input signal, however, a good deal of distortion is produced at the output.

However, there are very few guitarists who do not thrive on distortion in some form and anyway the introduction of harmonics makes for a more interesting sound.

In Fig. I IC2 takes the negative half oif the input sinewave via D1 and inverts it to produce a positive output. The positive sinewave input via D2 is not inverted so that the end product is effectively frequency doubling.

ICl is required to produce an amplified version of the guitar output,
as normally this would not be sufficient to cause conduction in the diodes.

The preset VRI should be adjusted to provide 4 V r.m.s. at point A with either a guitar or audio generator, set at around 50 mV , connected to the input. This set-up should give around 180 mV at pin 6 of 1 C 2 which in turn is reduced by the potential divider R6 and VR2 to give a maximum output of about 80 mV .

If desired it is possible to get rid of a lot of the upper harmonics which contribute to the distortion by strapping a $0.022 \mu \mathrm{~F}$ capacitor across VR2.
P. G. Ludgate, High Wycombe, Bucks.



Fig. 1

## RANDOM LIGHT DISPLAY

Pulses from one half of the 7413 in Fig. 1 (dual Schmitt irigger used as oscillators), are fed to the clock pin of IC2 (sk flip-flop) via S4. The flip-flop is wired with the J and $K$ inputs at logic 1, thus each clock pulse causes the outputs $Q$ and $Q$ to swop states. The clock frequency is controlled by the value of VRI and associated capacitor. Values of $200 \mu \mathrm{~F}$ and $6,000 \mathrm{pF}$ were chosen to give slow and fast clocking frequencies, selectable by S3. The flip-flop outputs are used to drive lamps, via

TR1 and TR2. Releasing the pushbutton leaves one lamp or the other alight, providing a simple heads or tails circuit.

An added refinement to this circuit has been incorporated to widen the scope of its use. Here, the other half of the 7413, also connected as an oscillator, produces clock pulses which are fed to the SD and CD inputs of the flip-flop. These latter pulses take precedence over the pulses at the clock pin, consequently the output depends upon the pulses to the direct inputs together with the clock input. When the direct inputs are at logic 1 , a complement output is
obtained. Setting the direct inputs to logic 0 results in $Q$ and $Q$ being in the same state (logic 1). Thus by controlling the frequency of both oscillators, one may obtain an interesting variety of lighting effects from the lamps. The switches S1 and S2 may be closed, if it is desired to operate the circuit in some kind of permanent display. The number of lamps may be doubled by using the other half of the flip-flop, as it shares a common clock pulse with the first half.

$$
\begin{aligned}
& \text { P. R. G. Reynolds, } \\
& \text { Benfleet, } \\
& \text { Essex. }
\end{aligned}
$$



The circuit shown in Fig. 1, when wired up to the direction indicator system of a car or motor-cycle, will give an audible warning "bleep" whenever the indicators are activated. Unijunction TRI, together with R1 and C1, forms an audio oscillator whose output is amplified by TR2 and applied to a low impedance miniature loudspeaker or earpiece. Diodes D] and D2 maintain isolation between right- and left-hand indicator circuits. The unit is specially suited for motor-cycles, where the indicator units are not self-cancelling and visual indications are not sufficient reminder. 7. Najam,
Bedford.

## WARNING SYSTEM



## gUITAR TUNING REFERENCE

THE heart of the circuit in Fig. 1 is the popular 555 timer, which is utilised in the astable mode of operation to produce a reference note for tuning guitars.
The frequency produced is determined by the formula:

$\mathrm{f}=\frac{1 \cdot 44}{(\mathrm{VR} 1+2 \mathrm{VR} 2) \mathrm{C} 1}$<br>Or $\mathrm{Cl}=\frac{1.44}{(\mathrm{VRI}+2 \mathrm{VR2} 2) \mathrm{f}}$

where $\mathrm{f}=164.81 \mathrm{~Hz}$ for bottom E , and 659.78 Hz for top E .


Top E may be a better choice, as the harmonics would be out of the ear's range.

Taking the nominal value $30 \mathrm{k} \Omega$ for VR1 and VR2 to roughly give a centre setting, the value for Cl would be $24 \cdot 2 \mathrm{nF}$ for top E , and 97 nF for bottom E, using the above formula.

The unit is fairly stable, and will operate from a supply voltage ranging from 4 to 15 volts. With the timing controlled by VR1 and VR2, the unit can be calibrated using an oscilloscope or frequency counter looking across the terminals of LS1.


Fig. 1

## W. P. Bond,

 Cheltenham.0N adding the phasing unit (PE Sept 1973) to my PE Synthesiser, it was decided that the circuit should be voltage controlled to be in keeping with the rest of the synthesiser.

The circuit in Fig. 1a was developed for this purpose, and uses a bulb to control light sensitive resistors, which replace the dual-gang potentiometer of the phasing circuit.

With VR1 in its mid position, a negative going ramp (Fig. 1b) applied to the voltage control input. would cause the outputs of IC1 and


Fig. 1

IC2 to swing negative, causing TR2 to switch on, and light the bulb in accordance with the magnitude of the input voltage.
If, however, the wiper of the bias control VR 1 is set negative, the output of IC1 now sits positive (Fig. Ic). this switching TR2 off. and TR 1 on, causing the bulb to remain alight, and gradually dim on each ramp, returning suddenly to bright again.

The circuit has proved to be very versatile, in that with the adiustment of just one control (VRI)
it is capable of accepting both positive and negative control signals. and is able to reverse their growth if required.

A further use found for the circuit was to operate a waa-waa unit. and some interesting results can be obtained when used in conjunction with either a sample and hold, or an envelope shaper.
M. Whyte,

Merseyside.

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2N697 \& 0.30 \& $2 N 370$

 

2N696 \& 0.35 \& $2 N 3703$ <br>
2N697 \& 0.30 \& $2 N 3704$ <br>
2N698 \& 0.62 \& 2N3705
\end{tabular}

 \begin{tabular}{ll|l|}
\& $2 N 76 A$ \& 0.12 <br>
$2 N 3708$ <br>
$2 N 709$ \& 0.21 \& $2 N 3709$ <br>
\hline

 

2N709 \& 0.50 \& $2 N 3710$ <br>
2N718 \& 0.27 \& $2 N 3711$

 

2N718 \& 0.27 \& $2 N 3711$ <br>
$2 N 718 A$ \& 0.30 \& $2 N 3712$
\end{tabular}

$\qquad$ | 2NT20A | 0.80 | 2N3713 |
| :--- | :--- | :--- |
| 2N914 | 0.35 | 2N3714 |
| 2N916 | 0.30 | 2N371 | | 2N914 | 0.35 | 2 N3714 |
| :--- | :--- | :--- |
| 2N916 | 0.30 | $2 N 3715$ |
| $2 N$ |  |  | | 2N916 | 0.30 | 2N3715 |
| :--- | :--- | :--- |
| 2N918 | 0.38 | 2N3716 |
| 2N929 | 0.25 | 2N371 | 2N918

2N929
2N930

| 2N2218 | 0.60 | $2 N 3794$ |
| :--- | :--- | :--- | :--- |
| 2N2218A | 0.37 | $2 N 3819$ |
| 2N |  |  |

$\begin{array}{llll}\text { 2N2218A } & 0.37 & \text { 2N3820 } \\ \text { 2N2219 } & 0.30 & \text { 2N } 2323 \\ \text { 2N2219A } & 0.32 & \text { 2N3 }\end{array}$
$\begin{array}{llll}\text { 2N2219A } & 0.30 & 2 N 3823 \\ 2 N 22923\end{array}$

| 2N 2220 | 0.35 | $2 N 3906$ |
| :--- | :--- | :--- |
| 2N2221 | 0.22 | 2 N 4036 |


| 2N $22221 A$ | 0.22 | 2N4036 |
| :--- | :--- | :--- |
| 2N2222 | 0.25 | 2N 4037 |

$\begin{array}{llll}\text { 2N2222 } & 0.25 & \text { 2N4058 } & 0.2 \\ \text { 2N2222A } & 0.25 & \text { 2N4059 } & 0.20 \\ \text { 2N2328 } & 0.25 & \text { 2N }\end{array}$

| 2N2368 | 0.25 | 2N4060 | 0.20 |
| :--- | :--- | :--- | :--- |
| 2N2369 | 0.25 | 2N4061 | 0.17 |
| 2N2369A | 0 | 29 | 2N4062 |
| 2N2644 | 0.75 | 2N4U2 | 0.17 |


| 2N2369A | 0 | 29 |
| :--- | :--- | :--- |
| 2N4062 |  |  |
| 2N2646 | 0.75 | 2N4126 |
| 2N2647 | 1.40 | 2N4201 |

2N2647
2N2904

| 2N2905A | 0.37 | 2N4921 | 0 |
| :--- | :--- | :--- | :--- |
| 2N | 2N 4922 | 0. |  |
| 2N2906 | 0.28 | $2 N 4923$ | 0.70 |


| 2N2906 | 0.28 | $2 N 4923$ | 0.70 |
| :--- | :--- | :--- | :--- |
| 2N2906A | 0.25 | $2 N 5190$ | 0.21 |


| 2N 2906A | 0.25 | 2N5190 | 0 |
| :--- | :--- | :--- | :--- |
| 2N2907 | 0.21 | 2N5191 | 0 |
| 2N2007A | 0.22 | 2N5 | 0.75 |

$\begin{array}{lllll}\text { 2N2924 } & 0.15 & 2 N 5192 & \text { 2N5195 } & 0.90 \\ \text { 2N2925 } & 0.17 & \text { 2N5245 } & 0.35 \\ \text { 2N3019 } & 0.55 & 2 N 525 & 0.40\end{array}$

| 2N2925 | 0.17 | $2 N 5245$ | 0.35 |
| :--- | :--- | :--- | :--- |
| 2N3019 | 0.55 | $2 N 5294$ | 0.4 |
| 2N3053 | 0.30 | $2 N 5295$ | 0.40 |




2N3392
2N3393
$2 N 3394$
$2 N 3439$
$2 N 3439$
$2 N 3441$
$2 N$

| 2N3441 |
| :--- |
| 2N3442 |

2N3638
2N 3638 A
2N3638A
2N3639
2N3641
2N
2N3702

| 0.15 | $2 N 6126$ | 0 |
| :--- | :--- | :--- |
| .15 | 40361 | 0 |
| .15 | 40362 | 0 |
| .16 | 4035 |  |


| .15 | 40361 | 0 |
| :--- | :--- | :--- |
| 15 | 40362 | 0 |
| 16 | 40363 |  |
| .18 | 40406 | 0 |


| 16 | 40363 | 1 |
| :--- | :--- | :--- |
| 18 | 40406 | 0 |
| 16 | 40407 | 0 |
| 18 | 40408 | 0 |


|  | $B C 158$ |
| :--- | :--- | | BC159 |
| :--- | :--- |

$\qquad$

| BD11 | 1.29 | BFX85 |
| :--- | :--- | :--- |
| B0116 | 1.20 | BFXR7 |
| BD131 | 0.54 | BFX85 |

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| $\begin{aligned} & 0.40 \\ & 0.40 \\ & 0.41 \end{aligned}$ | INTEGRATED CIRCUITS |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 0.40 | CA3020 | 1.761 | LM1808 | 1.92 | TAA550 | 0.60 |
| 0.40 | CA3020A | $2 \cdot 29$ | LM1828 | 1.75 | TAAS60 | 1. 60 |
| 1.25 | CA3028B | 1.01 | LM3301N | 0.85 | TAA570 | $2 \cdot 30$ |
| 0.34 | CA3028A | $1 \cdot 29$ | LM3302N | 1.40 | TAA611B | 1.85 |
| 0.38 | CA3030 | $1 \cdot 24$ | LM3401 | 0.70 | TAA621 | $2 \cdot 15$ |
| 0.36 | CA3030A | $1 \cdot 89$ | LM3900 | 0.75 | TAA661a | 1.32 |
| 0.34 1.37 | CA3045 | 140 | LM3905 | 1.60 | TAA661B | 1.32 |
| 517 | CA3046 | 0.89 | LM3909 | 0.68 | TAA700 | 3.91 |
| 50 | CA3048 | 2.23 | MC1035 | 1.75 | TAA930A | 1.00 |
| 31 | CA3049 | 1.65 | MC1303 | 1.47 | TAA930B | 1.05 |
| 0.32 | GA9057 | 7.62 | MC1304 | 1.85 | TAD100 | 95 |
| 2.20 | CA3053 | 0.60 | MC1305 | 1.85 | TBA120 | 0.65 |
| 0.20 | CA3080A | 1.88 | MC1310 | 1.91 | TBA500 | 2 -21 |
| 0.15 | CA3086 | 0.51 | MC1312 | 1.98 | TBA5000 | 2.30 |
| 0.20 | CA3088 | 1.59 | MC1327 | 1.54 | TBA510 | 2.21 |
| 0.10 | CA3089 | 252 | MC1330 | 0.92 | TBA5100 | $2 \cdot 30$ |
| 0.10 | CA3090 | 3.80 | MC1350 | 0.75 | TBAS20 | 2.21 |
| 1.35 | CA3330 | 0.94 | MC1351 | 1.20 | T8A5200 | $2 \cdot 30$ |
| 1.55 | LM301A | 0.65 | MC1352 | 0.97 | TBA530 | 1.98 |
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| 1.45 | LM304 | 2.45 | MC1458 | 0.91 | TBA540 | $2 \cdot 21$ |
| 1.25 | LM307N | 0.65 | NE555 | 0.53 | TBA5400 | $2 \cdot 30$ |
| 0.58 | LM308C | 1.82 | NES56 | 1.05 | TBA550 | $3 \cdot 13$ |
| 0.58 | LM308N | 1-17 | NE565 | 1.30 | TBA5500 | $3 \cdot 22$ |
| 0.60 | LM309K | 2.10 | NE566 | 1.65 | TBA5600 | $3 \cdot 22$ |
| 0.45 | LM317K | 3.00 | NE567 | 1.80 | TBA570 | 1.29 |
| 0.65 | LM318N | 2.25 | SAS560 | 2.50 | TBA5700 | $1 \cdot 38$ |
| 1.40 | LM323K | 6.40 | SAS570 | 2. 50 | TBA641B | 2.50 |
| 0.85 | LM3399N | 1.75 | S042P | 2. 50 | TBA651 | 1.80 |
| 0.35 | LM348N | 1.91 | 76001 N | 1.57 | TBA700 | 1.52 |
| 0.40 | LM360N | 2.75 | 76003 N | 2.55 | TBA7000 | 1.61 |
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| 0.35 | LM374N | $2 \cdot 25$ | 76023 N | 1.70 | T8AB10 | 1.16 |
| 0.24 | LM377N | 1.75 | 76023ND | 1.57 | T8A820 | 1.03 |
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| 0.60 | LM382N | 1.25 | 76227 N | 1.51 | TCA280a | 1.30 |
| 0.49 | LM384N | 1.45 | 76228 N | 1.75 | ICAz90a | $3 \cdot 13$ |
| 0.65 | LM386N | 0.80 | 76530 N | 0.91 | TCA420A | 1.84 |
| 0.50 | LM387N | 1.05 | 76532 N | 1.50 | TCA730 | $3 \cdot 22$ |
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| 0.50 | LM389N | 1.00 | 76544 N | 1.44 | TCA750 | $2 \cdot 30$ |
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| 2.50 | LM723C | 0.85 | 76620 N | 1.10 |  |  |
| 3.35 | LM723N | 0.75 | 76650 N | $1 \cdot 10$ | SOCKE |  |
| 0.70 | LM741C | 0.65 | 76660 N | 0.60 | 8 PIN | 0.15 |
| 0.85 | LM741N | 0.50 | 76666 N | 0.92 | 14 PIN | 0. 16 |
| 0.80 | LM741-8 | 0.40 | Tha310A | 1.50 | ${ }_{16}$ PIN | 0.18 |
| 0.95 | LM747N | 0.90 | TAA320A | 1.15 | 22 PIN | 0. 30 |
| 0.65 | LM748-8 | 0.50 | taA350a | 2.48 | 24 PIN | 0.35 |
| 0.55 | LM748N | 0.50 | TAA521 | 1.00 | ${ }^{28}$ PIN | 0.45 |
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## SIMPLE LOGIG PROBE

T
THE circuit of Fig. I is a simple logic probe which can detect low, high and floating logic levels and also single short pulses and pulse trains. When the probe is connected to logic 0 then TR1 is turned off and the light emitting diode Dl does not glow. When the probe is at logic 1, TRI is turned hard on and Dl glows brightly. However, when the probe, or logic signal, is floating then a small current flowing out of the monostable input A2 turns TR1 on slightly, causing DI to glow dimly.

The second light emitting diode, D2, is on only when the monostable is triggered, which occurs on every 1 to 0 transition of the input signal. For a single pulse there is only one such edge and so D2 flashes once. For
a pulse train at the input, the monostable is constantly being retriggered and so D2 glows brightly. Note that the brighter the glow from D2, the higher the frequency of the input signal.

The logic probe has a loading effect of slightly more than one standard input. The unused inputs to ICI may be left unconnected. None of the component values is critical, but the value of RI must be adjusted so that D1 glows dimly when the input is floating, also Cl must be sufficiently large that a single pulse produces a visible flash.

S: G. Bailey,<br>Guildford.

Fig. 1


## LOW COST V.C.A.

Fig. 1


THE voltage controlled amplifier shown in Fig. 1 is comparatively much cheaper than MFC6040 but has a performance nearly equal to it. It is certainly far superior to the f.e.t. and diode v.c.a.s sometimes used but has only slightly greater complexity.

TR1 and TR2 form a differential pair with the current through them determined by the current source TR3. This current is controlled by the
voltage applied to the base in the usual exponential manner. The signal is applied to TRI base and extracted from TR2 collector. VR1 sets the voltage attenuation ratio of the circuit and is, in effect, the control input.
M. Bryant,

Calmore, Hants.

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