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	AYS it 495 1A TO220 Plastic Casing +ve -ve		OCKETS p	8p 25p	SPECTRUM
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BY127 14 2A/200V 40 Red/Green 100 OCP7 CR033 250 2A/400V 46 Green 100 OCP7 CR034 250 2A/400V 46 Green 100 OCP7	1 120 ICL7660 248 LM317K	28	l way Way Way	575p 695p 845p	IDC CONNECTORS (Speed block type)
OA47 12 6A/100V 83 0.2' Tri colour LEDs 2N577 OA70 12 6A/600V 125 Red/Green/Yellow 85 4N33 OA70 12 6A/600V 125 0.2' Bed High Bright 89	7 50 78H05 5V/5A 580 LM317P 135 78H12 12V/5A 640 LM323K	99 500 175	DIL PLUGS	(Headers)	PCB Male Female Female with latch Header Card-Edge
0A81 20 10A/200V 218 High Bright Green or 0A85 15 26A/200V 298 Yellow 100 Receiv	tit +24V 5A 599 LM723 rer 715 79HG -2.25V to TBA625B	30 14 75 16		sp 95p	2 rows Strt. Angle Socket Connector Pins Pins
OA90 8 25A/600V 395 TL32 infra Red (emit) 46 OA91 8 BY164 56 SEM/2016 (detate) 52 OPT1	LM309K 120 RC4194	375 24 160 28 40	4 88 8 185	ip 138p 5p 290p	10 way 90p 99p 85p 120p 16 way 130p 150p 110p 195p 20 way 145p 166p 125p 240p
0A200 8 TIL78 (detector) 55 SWI 0A202 8 TIL38 82 TIL00 90 Refle	ctive SWITCHES		BBON CAB	LE	26 way 175p 200p 150p 320p 34 way 205p 236p 169p 340p 40 way 220p 250p 190p 420p
IN916 5 Range 2V7 to 7 Slotte 1N4001/2 5 Jay 400mW 7 Segment Displays to RS	d similar SLIDE 250V TOGGLE	2A 250V 35 48 10	lays Gre		50 way 235p 270p 200p 470p
1N4003 6 8p each TIL321 5 C.An 140 ALU 1N4004/5 6 Range 3V3 to DI 704 2* C Cth 120 4x21	M.BOXES JA DP on/on/on 40 4 pole on x2 100		5 25p 30p	40p 50p	EURO FEMALE MALE CONNECTORS SOCKETS PLUGS Gold Rashed contacts Strt, Angle Strt, Angle
1N4148 4 1N5401 15 33V 13VV 1Sp each DL707 -3" C.Anod 125 FND357 or 500 130 5x49	21 120 PUSH BUTTON TOGGLE 27 105 Spring loaded SP change	2 amp 34	4 60p 70p	85p 90p	DIN 41612 2x32 way 275p — 220p 285p DIN 41612 2·3 x32 way 295p — 240p 300p DIN 41612 3 x32 way 360p 385p 260p 395p
1N5406 17 VARICAPS ±1 3' Red or Green 150 5x23: 1N5408 19 VARICAPS Bargraph 10 seg. Red 275 5x23:	x21 130 Momentary 6A SPST on SPDT c/o	f 58 04	100 CONNECT		TRANSFORMERS (mains Prim, 220-240V)
1544 9 BA102 30 Bargraph NSM3914 500 5x4a 15921 9 BB1058 40 FERRIC CHLORIDE 6x4x	21 120 DPDT c/over 200 DPDT 6 ta 27 120 DPDT c/over 200 DPDT 6 ta DPDT C/C	05 80 Plr		15 25 way way	37 30 3V, 6-0-6V 100mA; 9-0-9V 75mA, 12:0:12V 75mA; 15:0:15V 75mA 98p 6VA; 2x6V-5A; 2x9V-4A; 2x12V-0:3A;
6A/400V 50 Crystals 11b 7×5× 6A/800V 65 State 105 + 500 080	3" 180 Non Locking DPDT Bia 3" 210 Push to make 15p 4 point 3	ay 220 Sol	ALE Ider 80p	110p 160p	2x15V-25A 250p 240p 12VA: 2x4V5-13A; 2x6V-12A; 2x12V-5A;
TRIACS 3A/100V 48 OALO ETCH RESIST 10×7 12×5	×3 275 ×3 260 ROTARY: (Adjustable Stop T	ype)		210p 250p 160p 220p	355p 2x15V-4A 345p (35p p&p) 310p 24VA: 6V-1 5A 6V-1 5A; 9V-1 2A 9V-1 2A; 12V-1A 12V-1A; 15- 8A 15- 8A; 20V-6A
SCR's 3A/400V 56 Pen plus spare 1/p 100p 12×8 Thyristors 3A/800V 85 coppen cl ap 80 apps		3 pole/ FEI 48p Sol	MALE Ider 105p	160p 200p 215p 290p	20V- 6A 385p (60p p&p) 338p 50VA; 2×6V-4A; 2×9V-25A; 2×12V-2A, 2×15V-
5A/300V 38 8A/400V 69 Fibre Single Double- 5A/400V 40 8A/800V 115 Glass sided sided	SR8P 95"×85" ROTARY: Mains 250V AC, 4 Am	p 64p Str		200p 300p	420p 520p (60p p&p) 100VA: 2×12V-4A: 2×15V-3A: 2×20V-2 5A;
8A/300V 60 12A/400V 82 6×6 100p 125p 8A/600V 95 12A/800V 135 6×12 175p 225p	110p DIP SWITCHES: (SPST) 4 way 6 way 80p; 8 way 87p; 10 way (SPDT) 4 way 190p.	100m		g. 385p, Skt. 45	965p (60p p&p)
12A/400V 95 16A/400V 105 Clad Plain 'VQ' Board 12A/800V 188 16A/800V 220 21x3 ² 95 - DIP Board	180 AMPHENOL CONNECTORS	Solder			JUMPER LEADS Ribbon Cable Assembly DIL Plug (Headers) Single Ended Lead, 24" long
BT106 150 25A/400V 185 21×5* 110 Vero Strip BT116 180 25A/400V 295 31×3* 110 Vero Strip C106D 38 25A/100V 32×5* 125 95p PROTO-DE	144 24 way IEEE 475p 36 way Centronix 525p Cs 24 way Female 490p	470p 475p Si 450p Sc	IL	DGE CONNEC	TORS Length 14pin 16pin 24 pin 40 pin 24" 145p 165p 240p 325p
TIC44 24 30A/400V 525. 41×18" 420 275p Veroblock TIC45 29 30A/400V 525. 41×18" 590 - S-Dec	480 395 ASTEC UHF MODULATORS	375.0 20	1" 2> 0 way 2>	(22 way 215 (23 way 175	p 6' 185p 205p 300p 465p p 12' 198p 215p 315p 490p
TIC47 35 T2800D 125 Pitc of 100 pins 55p Eurobreadb 2N5064 38 Spot Face Cutter 150p Bimboard 1 2N4444 130 Pin Insertion Tool 185p Superstrip 5	695 8MHz Wideband	550p 32	2 way 2 x	(25 way 285 (28 way 190 (30 way 310	p 36° 230p 250p 375p 595p
OIAC SOLDERCON VERO WIRING PEN and Spool	380p C15W 535p Spare bi 380p G517W 545p Element nbs 6p G18W 550p Iron star	280p	2×	<36 way 360 <40 way 380 <43 way 450	p IDC FEMALE RECEPTACLE Jumper Leads 36" 20pin 26pin 34pin 40pin
ST2 25 500 370p Wire Wrapping Stakes 100	250p XS25W 560p Heat Shi		2>	(75 way 650	
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EPSON RX80 Printer £229 EPSON RX80 F/T Printer £245 EPSON FX80 Printer £316 EPSON FX100 Printer £435	DISC STORAGE CASES Holds ten 5½" Diskettes£2 DISC ALBUMS Attractively finished in beige leather-vinyl, these convenient ly store up to 20 discs. Each disc	32768KHz 100KHz 200KHz 455KHz 11MHz 120MHz 128MHz 15MHz 18MHz 18MHz 18MHz 20MHz 20MHz 26MHz 25576M 25576M 25576M 25576M 25576M 357954M	100 545 370 370 275 450 450 595 545 100 225 200 Kit, 225 200 Kit, 220 150 160 98 300	PECIAL OF e stock the f tsubishi), D ble, Dust Co rs, Connecto tter (Graph , Joysticks, achinecode ional Applic	FER THIS MONTHONLY £315 ull range of BBC Micro peripherals, Hard- re like, Disc Drives (Top quality Cumana & skettes, Printers, Printer Paper, Interface vers, Cassette Recorder & Cassettes, Mon- rs (Ready made Cables, Plugs & Sockets), ic Tablet) EPROM Programmer, Lightpen Sideways ROM Board, EPROM Eraser, ROM, The highly sophisticated Watford's , WORDWISE, BEEBCALC, Software (Edu- ation & Games), BOOKS, etc. etc. Please
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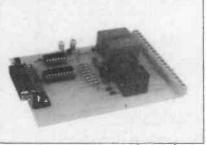
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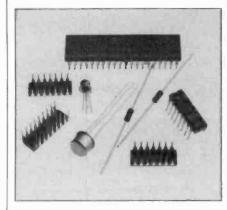
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Soldering equipment is well covered in this issue and, while we make no claim that the guide is complete, it will give readers a good basis on which to build a list of required equipment. No doubt many readers will find new companies and products which they were not aware of before. If you make an enquiry about any product we describe (especially those detailed in our buyer's guides) would you please mention PE as the source of the original information. This applies equally to educational establishments and industry or training departments etc., as such feedback to the suppliers assists us to give good coverage in future guides.

INDEX

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In extreme cases we can supply a photostat of the article from the editorial office but we do have to charge 75p for each article or part of a series.

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Mains' utilities Monitoring System

A new remote-monitoring system, designed for load-management of electricity, gas and water supplies, is presently undergoing trials in London and Milton Keynes. The Thorn/EMI system Mainsborne Telecontrol uses the electricity mains to carry bidirectional coded data between the central control and the consumer combining far more cost effective tariff structures with remote meter reading.

The purpose of the trials is to establish the long term levels of reliability of the unique spread spectrum bi-directional signalling system over the low voltage mains, a system adopted to overcome excessive noise problems. A spread spectrum signal essentially provides a continuous frequency over the band and is highly resistant to narrow band interference. It is also capable of signalling through a high noise level environment such as the mains and additionally provides high security.

The consumer is provided with a microprocessor controlled home unit and communications module. These carry out a number of tasks including accumulating signal pulses from electricity, gas and water meters; tariff switching; calculating actual and estimated costs incurred by the consumer; operating 80 amp and 25 amp contactors for space and water heating control and also for detecting any tampering with the meters. The communications module contains a clock/calendar synchronised to that of the central controller which is housed in the local C.E.G.B. transformer chamber. Non-volatile RAM storage of 1K byte permits a one day store of incremental

meter readings for each utility.

The central controller can communicate via the mains with up to 1,024 addresses. There is 500K bytes of non-volatile bubble memory capable of storing three days of 48 half-hourly meter readings from all three utilities as well as storing data retransmission upstream to Load Management Control or downstream to home units.

The customer display (pictured) has a 10 digit, alpha numeric, vacuum fluorescent display with 19 touch sensitive keys. A series of control and data display functions are available to the customer such as display of meter reading including multi-tariff metering; display of units consumed and cost so far since last bill; display of estimated cost of next bill; display of water and space heating on/off times; when not in use for any other reason the display shows the time.

Two other communication systems are presently being tested in the UK for remote-monitoring and load management; one uses an idle line facility on the standard telephone network, the other uses radio signals transmitted in the 200kHz band by the BBC.

NOV. SHOW CANCELLED

ahead with the show under these conditions.

Mr. Gordon Johns, Managing Director of Trident International, said "We are very disappointed. It was obvious from the reaction of the public that there is a very real demand for a show that reflects all aspects of the leisure industry but because of general market conditions many companies were unwilling to give the show their support."

Trident do hope however to stage the show as soon as market conditions improve.

TOP WOMEN?

Women in London will have the opportunity to train for top engineering jobs in new technology thanks to £350,000 in grants from the Greater London Enterprise Board, the Greater London Training Board and the European Commission.

The grants are in recognition of the work being done by the GLEB-sponsored London New Technology Network (LNTN) based in Camden and will finance training courses for women to be supervisors and trainers in micro-electronic engineering.

Enter MSX

The long awaited MSX computer range is finally finding its way into the UK shops. MSX is a standard system adopted by 15 multinational electronics companies, mainly based in Japan. The standard is based on the Z80 processor, a TI video display chip and a. General Instruments sound generator chip; all MSX machines and their peripherals are totally compatible. It is undoubtedly the hope of the conglomerate that their standard will eventually dominate the home-computer market.



Mitsubishi, Toshiba, Sony, Sanyo, JVC, Hitachi and Canon recently held a joint launch for their machines which all cost around the same—between £279 and £300. The software has been written by the American company Microsoft, it is a version of Basic. Around 75 general interest software packages are currently available. All the machines have a similar appearance and each possesses 64K bytes of user RAM with a separate 16K bytes of video RAM; the Basic is stored in a 32K byte ROM.

Peripherals available will include $3\frac{1}{2}$ in floppy discs, printers, data cassette recorders, joysticks, touchpads, tracker balls and communications adaptors. Mitsubishi are the only company to launch two machines, the cheaper version (pictured) is a 32K byte model ML F48 costing £249.



which was to be held at the Royal Hor-

ticultural Hall in London from November 8th to the 11th, has had to be cancelled by the show's organisers Trident International.

Despite the fact that the show was being sponsored by ten leading magazines, PE included, it did not receive the necessary backing from the electronics industry and the organisers felt it would not be possible to go

MARGE BLACE

WATFORD'S Computer MODEM 84

The Watford Modem 84 is a direct connect unit for use with BBC micros; it is a fully British Telecom approved device and is probably the cheapest way to connect to Prestel. The unit is supplied with or without a software ROM.



Having a full duplex capability the system can send and receive data at the same time. Only 1K byte of memory is used by the system for preparing mailboxes and system operation. User passwords can be optionally programmed into the ROM giving automatic log-on whilst still allowing you to change the personal passwords from the keyboard. The system has many special features. Modem 84 costs £97 including the software ROM. The two can be bought separately, Modem 84 alone £74, software alone £23. All prices include VAT and p&p. For further details and full specification contact: Watford Electronics, 33/35 Cardiff Road, Watford, Herts, WD1 8ED. (0923 40588).

look alikes

Systema has introduced a mini desk calculator together with an LCD clock both styled like modern computer terminals

The DC2 computer-calculator has a full working keyboard with memory, percentage and square root functions. Its eight digit display is fitted into the VDU cabinet. Overall measurements are 80 × 75 × 47mm.

The time and date are displayed alternately on the VDU of the CC1 computer-clock and its time keeping is accurate to within two seconds a day. Overall measurements are 52 × 45 × 41mm

Both items are fitted with long life batteries. Prices are £6.95 for the DC2 computer-calculator and £3.68 for the CC1 computer-clock, inc VAT and P & P. From, Systema (UK) Ltd., 72/74 South Street, Reading, Berks. RG1 4LG. (0734 586429).



Briefly...

Constructors please note: as part of a 'moving sale' the Midwich Computer Co. Ltd. is featuring many BBC products and compatible peripherals at low prices. Examples include a 100K, 40 track disc drive for £99.95, and the Uchida daisy wheel printer for £227.65. Prices exc VAT. All offers are while stocks last. New address: Gilray Road, Diss, Norfolk IP22 3EU. (0379 4131).



The Mitsubishi Electric Corporation has developed a method of access control that recognises personnel by their palm prints. Employees' palms are photographed and entered into a Charge Coupled Device (CCD) camera recognition system. Access to restricted areas is gained as the prospective entrant keys in a personal code and simultaneously presses his palm on a recognition plate; a positive palm print check coupled with the correct code number permits entry.

POINTS ARISING

PARALLEL/SERIAL CONVERTER September '84

The +ve connection of C2 should be connected to +5V and not to TR2 as printed. See page 18, Fig. 3 (circuit diagram).



Please check dates before setting out, as we cannot guarantee the accuracy of the information presented below. Note: some exhibitions may be trade only. If you are organising any electrical/electronics, radio or scientific event, big or small, we shall be glad to include it here. Address details to Mike Abbott.

Leisuretronics Cancelled (see report, far left.)

- Compec Nov. 13-16. Earl's Court. K2
- P.c.b. Manufacture & UV Box Construction (meeting) Nov. 17. Elec-
- tronic Organ Constructors Society, Y4
- Systems Security Nov. 19-20. Barbican Cntr., London. E Computers In The City Nov. 20-22. Barbican, London. O
- Data Security Nov. 20-22. Barbican, London. O
- Business & Data Processing Nov. 20-24. Kelvin Hall, Glasgow. M
- Northern Computer Fair Nov. 22-24. Belle Vue, Manchester. K2
- Northern Energy Manager Nov. 27-28. Lancashire County Cricket Club, Old Trafford, Manchester. W3
- Transducer Tempcon Nov. 27-29. Harrogate Exhibition Centre, Yorkshire. T

Electronic Displays Nov. 28-30. Kensington Ex. Cntr., London. D4 Electron & BBC Micro User Dec. 6-9. New Horticultural Hall, London. L

Amusement Trades International Dec. 18-Jan. 14 '85. Olympia. E5 Your Computer Xmas Fair Dec. 30-Jan. 2 '85. Olympia. K2 International Light Show Jan. 14-28 '85. Olympia. E6 British Toy & Hobby Fair Jan. 18-Feb. 2 '85. Olympia. D6

- Network & 0280 815226 **D4**
- **D6** 6 01-701 7127
- Evan Steadman & 0799 26699 E
- \$ 01-228 4107 **E**5
- **E6** £ 058 84 658
- Reed Exhibitions, Surrey Ho., 1 Throwley Way, Sutton, Surrey **K2**
- Database & 061-456 8383 L
- Montbuild & 01-486 1951 M
- Online & 01-868 4466 0
- Trident Exhibitions & 0822 4671 Т
- T1 Cahners & 0483 38085
- MCM & 01-231 1481 W3
- Percy Vickery & 0202 423863 Y4



HOW MUCH DOES IT REALLY COST TO USE YOUR CAR?

CNDON, Geneva, Frankfurt, Paris... hardly a motor show goes by without a major car manufacturer introducing a new top-of-the-range car fitted with either a trip computer or a digital m.p.g. readout integrated into the instrument panel. Now here is an opportunity to bring your car right up to date with one of the new generation of car computers. These devices are designed primarily to add interest to your motoring, but they can save you money as well.

There is plenty of money to be saved. The average motorist drives 9000 miles each year, which given a typical 30m.p.g. means consuming 300 gallons per annum, costing £540 at £1.80 a gallon. Next year petrol is likely to go above £2 a gallon if only to keep pace with inflation and the weak pound. But this is only half the story because, as a rule of thumb, for every pound spent on petrol another pound is spent in total on oil, tyres, maintenance costs and car depreciation due to additional miles on the clock.

The Outricer car computer has been developed after the first wave of car computer novelties, and we are able to build on their experience. We have concentrated on achieving a high standard of design so that the Outrider will be simple and safe to use whilst driving, easy to fit to the car and easy to refit if you change cars. But most important of all we have developed a high quality digital m.p.g. readout which is stable under continuous driving conditions, such as on a motorway, yet is also responsive to acceleration, and responsive too, when easing off the accelerator pedal. It is not necessary to fit sensors to the accelerator incidentally!

When the car is stationary other features of the computer can be accessed. For example, the computer will *predict* the gallons of fuel required for a trip and the trip cost before you travel. To do this simply enter onto the computer the distance you expect to travel and view the appropriate functions. Precisely how data is entered is dealt with later, but it is worth noting at this stage that, as a safety measure, cata can only be entered into the computer when the car is stationary.

Another important computer feature is the calculation of long term average m.p.g. which provides the information you require to decide whether the car needs servicing.

CAR SHARING

Displaying the trip cost can be an incentive for car sharing. Not only do *you* become fully aware of the cost of car travel, which may encourage you to share, but so do *your passengers!*

The Outrider automatically displays the cost of the trip (petrol cost plus a mileage charge) when you switch off the ignition. This is shown for ten seconds before the computer switches off the display.

BUSINESS TRAVEL

Many of us have to record our mileage in order to reclaim expenses from our employers. The trip distance key is invaluable for this as we can easily underestimate the true length of trips when claiming expenses. Self-employed people and company managers may also benefit from knowing the cost of car operation for particular business trips.

CAR OPERATING COSTS

So how should we evaluate the cost of travelling by car, and at what value should the mileage charge within the computer be set? To work out the cost of operating your car fill-in Balance Sheet 1.

The most obvious cost of motoring is fuel cost and if the mileage charge is set to zero then just the fuel cost of a trip will be displayed by the COST key.



However, each time you drive you incur additional costs. When you have estimated these and set the mileage charge to cover these costs then the trip cost displayed by the COST key will be the sum of the fuel cost and the extra costs that you incur having driven the distance of your trip. If you had stayed at home this is the total amount that you would have saved. If you use the trip cost prediction facility you would have an estimate of the trip cost that you could directly compare to the rail fare or a telephone call. Setting the mileage charge to cover these additional costs is the most useful setting for household owned cars.

Business users, the self employed and car sharers may however want to add the fixed car costs into the figure, not just those additional costs which are incurred at the time of driving.

MAIN BOARD

The heart of the circuit is a Motorola 6803 single chip microcomputer. This chip contains the processor, 128 bytes of RAM, a programmable timer and 13 input/output lines. The program for the microcomputer resides in a 2K byte EPROM. A 74LS373 is used to latch the low order address lines from the 8 bit multiplexed address/data bus. Timing information is derived from the 3 579545 MHz crystal. This part is commonly available as it is used in colour t.v. sets.

KEYBOARD AND DISPLAY

Each of the four l.e.d. digits and the function indicator l.e.d.s are controlled by a transistor driven from one of five output lines of the 6803. Rapid scanning of the digits synchronising with the data to switch segments on or off, which is held in the 74LS374 latch, gives the impression of all digits being on simultaneously but with great economy in wiring and components.

The 7-segment l.e.d.s were selected for both their appearance and their high brightness. The latter is particularly important for car instruments which must be readable in all light conditions

The five digit lines are also used to scan the five input keys, which have a common line of input to the processor. The processor detects a key press when this line goes high.

The Outrider contains calibration and trip information which must not be lost when the ignition is switched off. 64 bytes of the 6803 memory can be retained even when the processor is turned off, through the Vcc standby pin. This pin is connected directly to the 5V regulator which is in turn connected directly to the 12V car battery. The power to the rest of the circuit is switched by a transistor which is turned on either by the ignition line or an output line from the 6803. This enables the 6803 to hold the power on to enter a defined power down sequence and to display trip cost information for a 10 second period after ignition is turned off.

TRANSDUCER

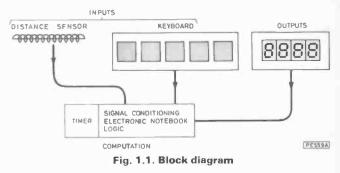
This is a high inductance coil mounted as close as possible to the rotating magnet found in all conventional speedometers. Normally it is sufficient to place the coil tight up against the back of the speedometer case and mounted at right angles to the axis of the drive cable. Exceptionally it is necessary to drill through the speedometer side or back and locate the transducer internally.

Each time a pole of the speedometer magnet passes the sensor coil a voltage is induced which is then converted to a digital signal by a Schmitt trigger on the main circuit board. A twisted pair connects the sensor coil to the main board to remove any electrical noise problems coming from the car's ignition circuit.

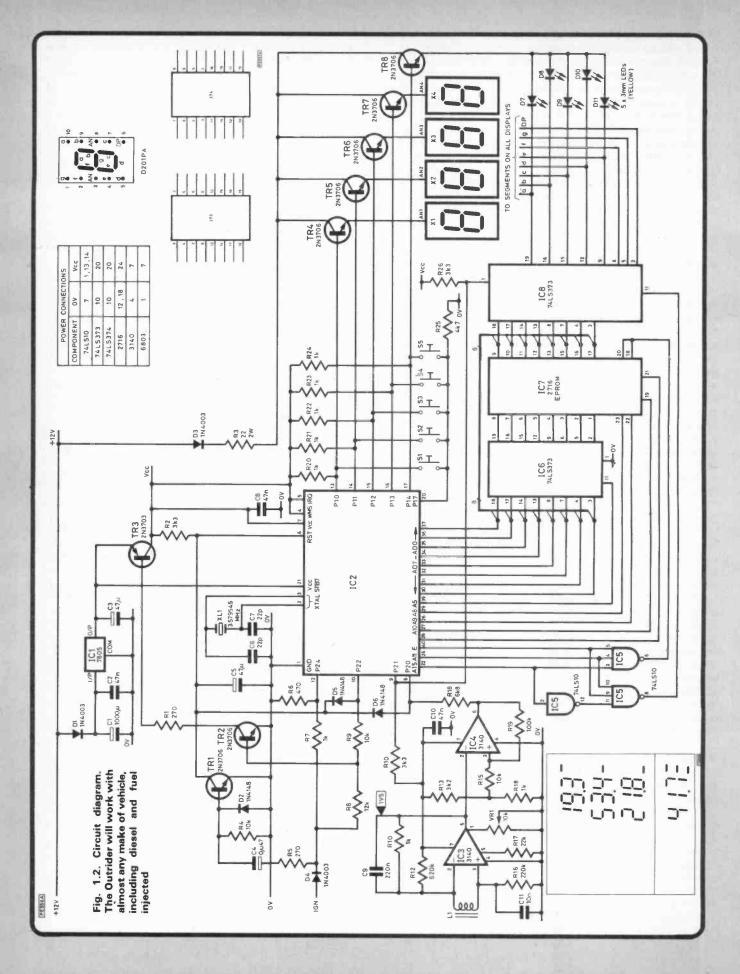


Fixed costs £ Annual Vehicle Tax Insurance Finance charges Half annual depreciation Other, e.g. AA/RAC membership	£ £ £ £ £
	Total, B = £
$\begin{array}{l} Mileage charge in pence covering nil non-petrol costs is given $100 \times (A+B)$ annual mileage $$ $$ $$ $$ $$ $$ $$ $$ $$ $$ $$ $$ $$$	
Just for interest sake we can now calculate the cost of a year annual mileage average MPG × price per ga	's fuel, this is given by: $Ilon = f_1 \dots = C$

A computer model eliminates the need for a fuel flow meter



THE C	OMPUTER OUTPUTS
The k	keys display these functions
Keys	Functions
DIST	Distance travelled on trip
FUEL	Fuel used on trip
MPG	Instantaneous m.p.g. (above 20m.p.h.)
	Trip average m.p.g. (below 20m.p.h.)
COST	Trip cost in £.p
SET	Display brightness



12

HOW THE COMPUTER WORKS

The overall configuration of the computer is shown in Fig. 1.2. Distance travelled is detected by a sensor coil located behind the speedometer. Fuel consumption information is fed into the computer via the computer keyboard. This is done by entering into the computer's electronic notebook the amount, in pounds and pence, spent when fuel is purchased. The computer then displays the price of a gallon of petrol the previous time it was bought to check that this is still the current price. The amount spent on fuel purchase is then divided by the price of a gallon of petrol to give the volume of petrol purchased. The volume of fuel purchased is not displayed but is retained in the computer and ultimately divided by the total distance travelled when the user requests a calculation of long term average m.p.g. This calculation is best made when the tank is completely filled and gives a highly accurate result. Long term average m.p.g. may be displayed at any time by pressing the MPG key when the computer is in calibration mode.

The distance sensor controls the dynamic behaviour of the computer outputs in conjunction with the crystal oscillator which provides a time source. Together the distance and time inputs are used to determine distance travelled, cold starts, vehicle speed, vehicle acceleration and vehicle braking. These factors are used in the computer model to determine instantaneous m.p.g. using the long term average m.p.g. to calibrate the model itself. Readers will recognise this kinetic method of calculating instantaneous m.p.g. as being very similar to the approach taken in the standard EEC driving cycle for new cars. Here m.p.g.s are given according to the speed at which they are driven (56m.p.h. and 75m.p.h.) and for an urban driving cycle involving acceleration and braking.

There are advantages to this kinetic method of determining instantaneous m.p.g. Flow meters in the fuel line can be inaccurate or expensive and there is always a slight additional risk of a petrol leak if a meter is introduced into the fuel line. Fuel flows reflect the characteristics of the carburettor float chamber as well as driver behaviour and the combination can be confusing to the driver particularly during continuous, e.g. motorway driving. By contrast the kinetic method can give a high quality signal to the driver.

Trip m.p.g. is the average of the instantaneous values over the period since the trip data were reset to zero. Fuel consumed on trip and trip cost are also calculated from the trip average m.p.g. figure. In the latter case the cost of fuel used and distance times the mileage charge are also used to determine the total trip cost. Users determine an appropriate charge per mile to reflect an average of all non-petrol costs or those costs less the truly fixed costs (tax, insurance, etc.).

Provided that the power supply to the computer is maintained, all trip and calibration data are retained when the computer display is off and the car is not in use.

THE M.P.G. READOUT

There has been considerable debate about whether a digital or analogue readout is best for car instrumentation in general and for the m.p.g. display in particular. Our display (Fig. 1.2) is something of a hybrid using three of the 7segment l.e.d.s to display digital m.p.g. while the fourth l.e.d. is effectively an analogue display summarising how the car is being driven. This horizontal bar can be usefully positioned at the edge of the driver's vision when the driver is at the wheel and looking straight ahead.

DATA ENTRY

Always begin data entry by pressing the SET key (for two seconds until SET is displayed and the light above the SET key flashes slowly). Next choose the function key that you wish to tell the computer about, e.g. COST. Now any number between 00.00 and 99.99 can be entered as follows. Each of the four left-hand keys rolls a separate digit on the display. "Roll-up" your desired number (the price of a gallon of petrol, say 1.75 in this case) then press the SET key to finish.

This is the sequence keys you will press to enter data FUNCTION SET NUMBER SET (2 secs)

Remember the car must be stationary and the SET key pressed for two seconds.

trip

miles long

Data entries are as follows for each function key:

- SET DIST 0000

SET DIST NUMBER SET- Predicts the fuel required and



SET MPG tF.OO



SET FUEL NUMBER SET Enter the amount spent on fuel each time you buy petrol. The NUMBER is the total cost of fuel which you have just purchased. The computer also needs to know the cost per gallon of the fuel and to help here the computer automatically goes over to the cost function and displays the cost per gallon last used. Change if necessary. Press SET to return to normal computer operation.

SET- Resets all trip data to zero, e.g. fuel used on trip, distance

travelled on trip and cost of

cost of a trip a NUMBER

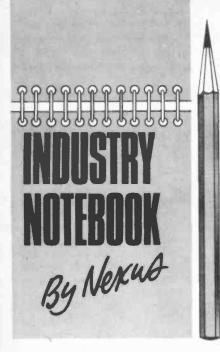
SET- Calculates your average m.p.g. over the last 6 to 10 gallons*. tF stands for "tank Fraction". Enter here the position of the needle on your petrol gauge. If it is reading half full enter tF.50: if it is a quarter full enter tF-25 etc. To make a completely accurate result fill the tank and enter fuel purchases in the usual way (above) then the tF-99 should be entered. After pressing SET, average m.p.g. will be displayed. Press SET to return to normal computer operation.

SET COST NUMBER SET Enter here the cost of a gallon *Equal to the volume of your petrol tank, see calibration section.

The Outrider car computer is available from: Mark Space Enterprises, 11 Church Green Rd., Bletchley, Milton Keynes MK3 6BJ. The complete kit costs £59.95 inclusive of carriage and VAT. A kit comprising those parts not generally available, i.e. case, label, 5 x front panel switches, p.c.b.s, 4 x 7-seg. displays, coil, programmed ROM is available for £29.95-most useful for the constructor with a well endowed spares box!

When ordering, please specify the make and model of the vehicle if known.

NEXT MONTH: Construction, installation and setting up.



Breezy Business

Economic predictions are notoriously unreliable. The experts are often proved wrong, not by failure to correctly analyse and marshal data from which to fashion a conclusion, but by unforeseen events. Pity the poor economist whose job is to predict in an unpredictable world.

Energy demand and resource forecasting should have a much better record if only because it is a narrower field. But it has been no more reliable. In the 1950s we were all worrying that the world would soon run out of oil. In the 1960s there was an enormous surplus. In the 1970s the price explosion forced us to think about alternative sources as well as renewing fears that oil would soon run out. Today, nearing the mid-80s, we have another glut of oil not to mention more coal in the ground than has ever been mined, natural gas in abundance and an increasing capacity in atomic energy.

Of course nobody in the 1950s imagined that in the 1980s the family car would travel 50 miles to the gallon instead of 30, or that all-solid-state TVs would consume only 30 percent of the energy of their valved predecessors, or that double glazing and loft insulation would cut domestic heating demand by 10 percent.

Abundance of supply plus more efficient usage has eased our fears and almost, but not quite, extinguished popular enthusiasm for the alternative energy sources so keenly debated in the late 70s. Whatever happened to tidal power, wind power and solar energy?

Wind power, I am happy to report, is alive and well. The Wind Energy Group, a consortium of GEC Energy Systems, British Aerospace and Taylor Woodrow Construction, has identifed a world market for wind turbine generators estimated to be in tens of thousands of installations. The demonstration model is now under construction on a hill at llfracombe, Devon, and should be operational by the end of the year. It has a three-bladed rotor 25m in diameter and will generate up to 250kW, The project is financially aided by the EEC and by the Department of Trade and Industry. Naturally, the control system is electronic and one of the more interesting features is that microprocessor technology is employed for remote control over a telephone link from WEG's offices in Greenford, Middlesex, together with feedback of operating data for analysis.

The windmills of the 80s and 90s will help conserve oil resources but that is not the main sales message. The real truth behind WEG's optimistic sales forecast is that by clever engineering wind energy is now price-competitive with conventional diesel generated electricity in small communities.

Turnabout

Conventional wisdom has it that the requirements of national defence and, in particular, a fighting war, is the forcing ground for truly innovative developments. The most notable example is radar which made enormous progress during World War 2 and didn't come into civil use on any scale until after the conflict.

An offshoot of radar was microwave technology which, again, only surfaced for civil communications use after the war. Today we still hear stories that defence R & D operates on the very frontiers of knowledge and with large budgets, especially in the United States, there is still an element of truth in such assertions.

But curiously, the most revolutionary innovations of all time, the integrated circuit and later the microprocessor, had their origins in the civil market and were adopted only with some reluctance by the military.

The first simple i.c.s were the brainchildren of Jack Kilby of Texas Instruments and Bob Noyes of Fairchild Semiconductor in 1958 and 1959. Acceptance by the industry was painfully slow until ten years later Intel was formed (in 1968) as a specialist i.c. company with its first product a 64-bit solid state memory.

In 1969 a Japanese company, Busicom, asked Intel to help produce a set of i.c.s for a projected range of high performance calculators. Busicom's ideas were very good but in the opinion of Intel's project engineer, Dr Marcian E. Hoff Jr, they could be made more cost-effective by adopting a systems approach using a general purpose central processor and using peripheral memory chips to achieve the calculator functions.

Hoff had previously worked on the PDP-8 computer at Digital Equipment and was systems-trained. He also foresaw many applications other than calculators where a CPU could be of value. This was the beginning of the MPU as we know it today and what brought Intel to its present impressive status in the industry.

Hoff's ideas came to the open market in November 1971 with the 4004 CPU chip

followed by the 8008, the first of the 8-bit chips and a succession of more powerful devices. By 1975 the new innovation was gaining ground with Intel and its competitors shipping 1 million chips.

Five years later the annual shipments had topped 50 million. Ironically, Busicom who had started the bandwagon rolling went out of business. Note, too, that crossfertilisation of TI and Fairchild's original work plus Digital Equipment system experience plus market demand (Busicom) combined with Intel's own expertise to generate something entirely new.

The military are not entirely to blame for tardiness in adopting the new technology. They were apprehensive over reliability in view of earlier experience with transistors which were sensitive to breakdown through power line surges and spikes. Additionally there were storage and other military specifications to be met. Many of today's operational devices for the military demand Mil Spec qualification in both production and testing but in non-operational or sheltered environments good commercial quality is good enough.

We see this also in certain items of equipment. Naval minesweepers and patrol vessels, for example, frequently use good commercial quality radio equipment originally designed for the trawler market. Financial constraints even led to the idea of VTOL Harriers and helicopters operating from converted merchant ships instead of conventional aircraft carriers which are very much more expensive.

There are many areas of course where military R & D is essential and money well spent. But it is good to see that many products pioneered in the civil field for civil application are no longer sneered at by the military establishment.

Self-Help

One of the Government objectives has been to encourage self-reliance as being far more virtuous than outright dependence on the nanny-state. Cut-backs in central funding have forced many organisations to seek internal economies and, in some cases, to search for alternative sources of revenue.

University R & D is a case in point. It has been traditional for research departments to take out patents and enjoy the benefits of royalties but there are now signs that our academics are becoming far more business-minded. They are actually forming separate companies to sell their services. Thus we see Salford's Industrial Centre, Unisheff Ventures owned by Sheffield University and Unived Technology set up by Edinburgh University.

There are others and the trend is likely to continue. As private companies they can raise their own funds in the commercial market and even conceivably go beyond R & D and consultancy work through to manufacture and marketing, either independently or in joint ventures with existing companies.

How refreshing to see people rising to a challenge instead of indulging in perpetual self-pity.

The Latest In High-Tech Micronta[™] Multimeters!

Folding LCD Multimeter With Auto-Range



- Automatic Polarity Reverse
- Automatic Power-Off When The Case Is Shut!
- Extra Large Non-Glare Display
- "Beep" Indicator For Quick Continuity Testing

It's the DVM that thinks! YOU select the function and IT selects the proper range automatically. Features extra large 11/16" non-glare display with adjustable viewing angle, a built-in compartment for its test leads, "beep" indicator for quick continuity testing, range-hold switch to override autoranging, diode/semiconductor junction check function and zero-ohms adjust for super-accurate low resistance measurements. DC volts to 1000. AC volts to 500. Up to 10 amps AC and DC. Resistance to 2 megohms. Open: 107/8 x 55/8 x 11/2". Requires two "AA" batteries. 22-193 £64.95



Snaps Shut When Not In Use!

Compact 25-Range, 20,000 Ohms/Volt Folding Multitester

95 90-120-150-180° Hold-Position Hinge Fuse and Surge-Absorber Protection

"Big meter" features include fuse and surge-absorber protection, 4" threecolour mirrored meter with automatic shunt protection (when shut) 20,000 ohms per volt DC sensitivity. DC Volts: 0 to 1200, 7 ranges. AC Volts: 0 to 1200, 5 ranges. DC Current: $0-6\mu$ A, 3-30-300mA, 4 ranges. Resistance: 0-2-20-200k-2 megohms (centre scale 24), dB: -20to +63 dB, 5 ranges. Accuracy: $\pm 3\%$ DC, $\pm 4\%$ AC. Measures Open: $71/4 \times 4^{5}/1e \times 11/4$ ". Requires "AA" battery. **22-211**

Folds Up For Travel!

Take A Look At Tandy, Today! Visit your local store or dealer and see this and many more sale bargains. We service what we sell - over 2,700 exclusive lines!

See Yellow Pages For Address Of Store Nearest You

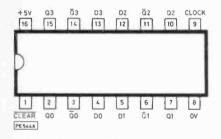
OVER 9,000 STORES & DEALERSHIPS WORLDWIDE Prior to this advertisement, all quoted regular prices have been charged during the last six months at the Tandy Store, Tameway Tower, Bridge Street, Walsall, West Midlands. WS1 1LA. Known As Radio Shack in The U.S.A. Prices may vary at Dealers Offers subject to availability

Sequential Logic Techniques Part 3

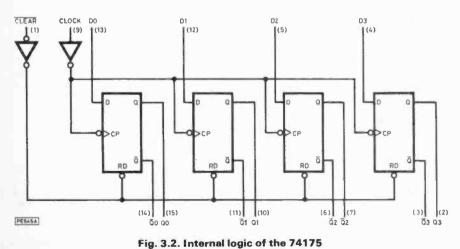
M.TOOLEY BA and D.WHITFIELD MAMSc CEng MIEE

W E have already made some use of a simple data latch configured around the four D-type bistable stages of a 7475. Data latches are ideal for the temporary storage of data and several enhanced devices have appeared. One such device, the 74175, is a quad edge-triggered bistable which can be used both as a conventional parallel type latch having separate data input and output lines from each bistable, and as a serial type shift register where data is fed from one bistable stage to the next.

The 74175 is housed in a 16-pin d.i.l. package, the pin connections of which are shown in Fig. 3.1. The internal logic of the device is shown in Fig. 3.2. A common clock input is applied to each of the bistable stages and data present on the D inputs is transferred to the Q outputs on the positive-going edge of the clock pulse. It should be noted that clock triggering occurs at a



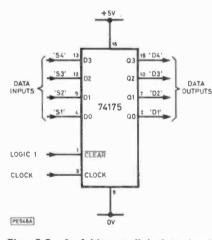


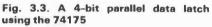


particular voltage level present at the clock input and is not directly related to the transition time of the positivegoing pulse. A common master reset input is also provided which, when taken low, asynchronously clears all of the Q outputs.

74175 4-BÌT PARALLEL DATA LATCH

We shall start our investigation of the 74175 by showing how it can be used as a 4-bit parallel data latch in the arrangement shown in Fig. 3.3. The four incoming data lines are connected to the four D inputs whilst the four out-





put lines are derived from the four Q outputs. The latch enable signal is applied to the clock input whereas the clear signal is applied to the master reset pin.

The 74175 should be inserted into socket C of the Logic Tutor ensuring, as usual, that pin 1 aligns with C1. The following connections should be made:

C1	to logic 1	(active low clear)
C2	to D1	(D1 shows state of Q0)
C4	to S1	(data input, D0)
C5	to S2	(data input, D1)
C7	to D2	(D2 shows state of Q1)
C8	to OV	(common)
C9	to clock	(latch enable)
C10	to D3	(D3 shows state of Q2)
C12	to S3	(data input, D2)
C13	to S4	(data input, D3)
C15	to D4	(D4 shows state of Q3)
C16	to +5∨	(supply)

Adjust S1 to S4 to produce logic 0 on all input lines. Check the state of the output lines (QO to Q3) by examining the state of D1 to D4 and verify that these all go to logic 0 (and stay at logic 0) after the first rising clock edge. Now wait until the clock goes low and depress S4 so that logic 1 appears on this input. The corresponding output (indicated by D4) should remain at logic 0 until the clock next goes high, at which point the output should change to logic 1.

Readers should experiment with various combinations of data input obtained by appropriate adjustment of S1 to S4. It should be noted that, in all cases, data is only latched on a positve going clock edge (i.e. as the clock l.e.d. becomes illuminated). Furthermore, after the clock transition has occurred, the 74175 ignores any further changes on its data inputs until the next rising clock edge arrives. In practice, this means that input data MUST remain stable for the duration of the rising latch enable input signal since this is the only time at which the input data is valid.

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The clear input may now be tested by first loading data into the latch, disconnecting the link from C1 to +5V(the input pin of the 74175 remains at logic 1 during this process) and then connecting the link from C1 to logic 0. All four outputs should then immediately go to logic 0 and no further data will be latched until the clear input is returned to logic 1 (or left to float high again).

A SIMPLE LOGIC ANALYSER

Here is an example of the use of the data latch. One of the primary functions of a logic analyser is that of "capturing" transient data present in a microprocessor based system and "freezing" it so that it can be displayed and examined at leisure.

The point at which data is captured is defined by a "trigger event". This usually takes the form of a particular set of logic states present on the control bus. When this pre-set bit pattern occurs, data is latched into the memory of the logic analyser, decoded and displayed in binary or hexadecimal form.

A very simple form of logic analyser is shown in Fig. 3.4. Two 4-bit data latches are used to capture the lower nibble (present on data lines D0 to D3) and the upper nibble (present on data lines D4 to D7). The clock and clear lines of all eight bistable stages are common; the clock being driven from the trigger event decoding logic (which is arranged to produce a 0 to 1 transition when the trigger event occurs) whilst the clear line is connected to a push-button 'reset' switch.

Whilst this simple arrangement is only capable of capturing a single byte (i.e. the eight data bits present on the bus) at a time, most logic analysers provide a memory of 1K bytes (or

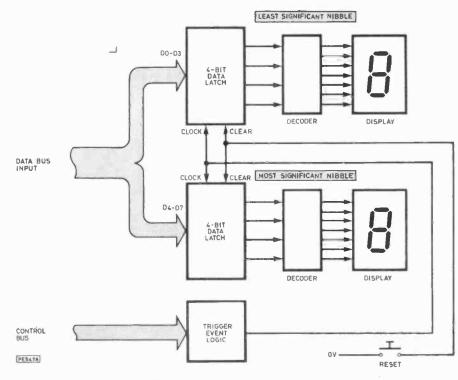
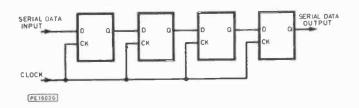


Fig. 3.4. A simple logic analyser





more) and, in addition, will offer the facility to capture data both before and after the pre-determined trigger event.

SHIFT REGISTERS

Shift registers essentially provide a form of serial memory (as opposed to the parallel form of data latch which we looked at last month) in which data can be synchronously clocked from one stage to the next. In a simple shift register made from D-type bistables this means that the Q output of the first stage must be taken to the D input of the next stage, and so on. A register of this type is shown in Fig. 3.5 and is known as a serial-input/serial-output (SISO) shift register.

Instead of using D-type bistable elements in the SISO shift register we could, of course, use J-K bistables. The equivalent of Fig. 3.5 using J-K bistables is shown in Fig. 3.6. Both of these arrangements use four cascaded bistable elements, thus four clock pulses will be needed in order to shift data right through the shift register.

At this point it is probably worthwhile making a clear distinction between the SISO shift register and the simple binary counter. In the former case, the clock line is common to every bistable element whereas, in the latter, the Q output from the first stage feeds the clock input of the next stage, and so on.

On its own, the SISO type of shift register is only capable of providing a delay equivalent to the length of the shift register in clock pulses. Hence an 8-stage SISO shift register will, for example, impose a delay of eight clock pulses between serial data entering the register and that leaving it.

Other types of shift register do, however, have a seemingly endless variety of applications arguably the most important of which involves the conversion of serial data to parallel data and vice versa. In such cases the basic SISO register arrangement must be modified so that parallel data access (input and/or output) is possible. One

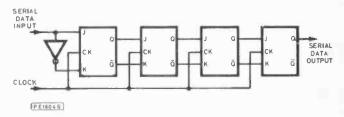


Fig. 3.6. A 4-bit SISO shift register using J-K bistables

obvious possibility is that of making each Q output of the SISO shift register available. Such an arrangement is shown in the serialinput/parallel-output (SIPO) register of Fig. 3.7. Here serial data is synchronously clocked into the register and, after the requisite number of clock pulses (four in this particular case), the register is completely loaded and the data is then available in parallel form from the four Q outputs.

Another possibility is that of parallel loading the data into a shift register using the PRESET and PRECLEAR inputs of the bistable elements. Data may then be shifted out in serial form. Such an arrangement is known as a parallelinput/serial-output (PISO) shift register, a simplified form of which is shown in Fig. 3.8. The data on the parallel inputs should not, of course, be allowed to affect the state of the bistable elements during the shifting

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process and hence the PISO shift register must have additional logic to control the parallel loading of data. Most, if not all, PISO shift registers also provide a serial data input and thus can be also used for serial-toparallel data conversion.

A further refinement is that of providing both parallel data input and parallel data output from the shift register. Such an arrangement is shown in Fig. 3.9 and is known as a parallel-input/parallel-output (PIPO) shift register. This arrangement, which is sometimes also referred to as a "universal shift register", is very similar to that of the PISO, the only difference being the addition of data output lines derived from the Q outputs of each bistable stage.

We shall begin our practical investigation of shift registers showing how a simple SIPO arrangement can be built using the four D-type bistables of a 74175 quad data latch. The 74175 was introduced last month and hence we will not repeat the pin-out or internal logic schematic of the device.

SIPO SHIFT REGISTER USING THE 74175

The circuit diagram of a simple SIPO shift register based on the 74175 is shown in Fig. 3.10. It should be noted that the clock input (pin-9) is common to each D-type bistable. The active-low clear input is taken to logic 1 and the serial data input (derived from push button S1) is taken to the D input of the first bistable stage. The four I.e.d. indicators of the Logic Tutor are used to monitor the state of the Q outputs. With the 74175 in socket C (and with pin-1 aligned with C1), make the following connections on the Logic Tutor:—

C1 to logic 1 (active low clear) C2 to D1

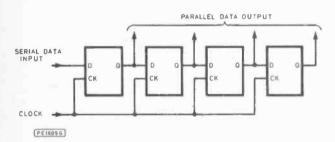


Fig. 3.7. A 4-bit SIPO shift register using D-type bistables

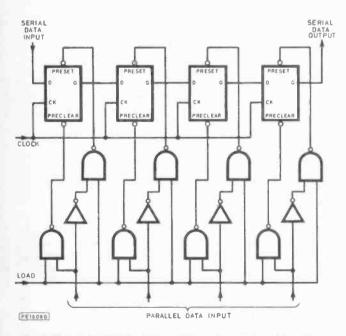
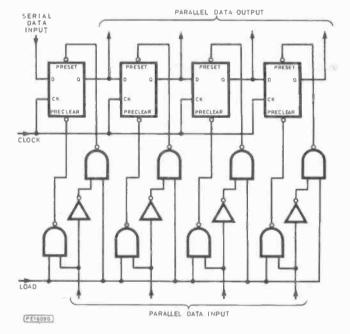


Fig. 3.8. A 4-bit PISO shift register using D-type bistables





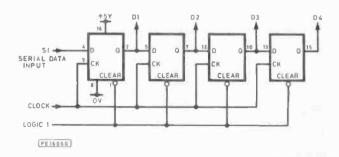


Fig. 3.10. A SIPO shift register based on a 74175 quad data latch

C4 to S1	(data input)
C5 to C2	
C7 to D2	
C8 to OV	(common)
C9 to clock	
C10 to D3	
C12 to C7	
C13 to D10	
C15 to D4	
C16 to +5V	(supply)

Outputs (indicated by D1 to D4) of the shift register should initially all be zero. With S1 providing a logic 0 data input, the outputs should remain at zero as the logic 0 is effectively shifted into the register during subsequent clock cycles. Now wait for the clock to go low and then depress S1. Nothing should happen until the clock next goes high, at which point D1 will become illuminated indicating that the logic 1 has been loaded into the first stage of the shift register. Now release S1 to produce a logic 0 input. On the next rising clock edge D2 should become illuminated and D1 should become extinguished. This shows that the logic 1 has now shifted into the next stage of the shift register whilst the subsequent logic 0 input has moved to replace the initial logic 1 output of the first stage. The subsequent movement of the logic 1 should be observed as it shifts right through the register. Operation should conform to the timing diagram shown in Fig. 3.11.

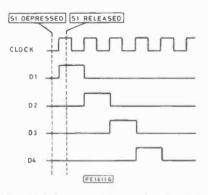


Fig. 3.11. Timing diagram for the shift register of Fig. 3.10

It should be noted that, had we stopped the clock after four complete clock cycles, we would have produced parallel output data on the four Q output lines that was equivalent to the serial data input (i.e. 1000).

It is now suggested that readers repeat the foregoing exercise holding S1 down for the first four clock cycles. A logic 1 will then be seen to load into the register, apparently filling the register from left to right. After the fourth clock cycle, S1 should then be released and the register will empty as the logic 1's are successively replaced with logic 0's. After observing this process for one or two further periods of four clock cycles, readers should be reasonably familiar with the operation of this simple form of 4-bit SIPO shift register.

It should be noted that the outputs of the 74175 shift register can be simultaneously set to logic 0 by taking the active-low clear input to logic 0. Readers may wish to confirm that this is so. counter using a 74175 is shown in Fig. 3.12, the $\overline{\Omega}$ output of the last stage is fed back to the D input of the first stage. Since all four Q outputs start at zero, a logic 1 is fed to the D input and the first stage changes state as soon as the clock input goes high. The register thus first fills with logic 1's but, when the fifth clock pulse arrives the logic 0 present on the $\overline{\Omega}$ output of the first stage and hence the register then begins to fill with logic 0's. The timing diagram for the four stage walking ring counter is shown in Fig. 3.13.

To convert the arrangement used in

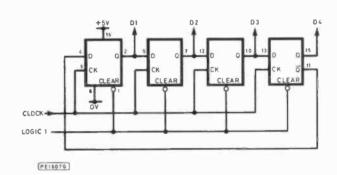
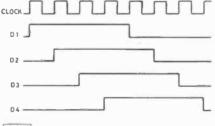


Fig. 3.12. A 4-bit walking ring counter based on a 74175 quad data latch



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Fig. 3.13. Timing diagram for the walking ring counter of Fig. 3.12

THE WALKING RING COUNTER

We shall now digress slightly from our main theme of shift registers to take a look at an interesting application of the SIPO shift register. If the complementary output from the last stage of a shift register is fed back to the input of the first stage, we can produce a counter which has twice as many states in its counting sequence as there are stages in the shift register. Such an arrangement is known as a "walking ring counter" or "Johnson counter". In this type of counter, as compared with a conventional binary counter, only one stage changes state at a time.

A practical four stage walking ring

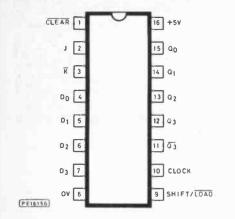
Fig. 3.10 to that in Fig. 3.12 it is only necessary to disconnect the link from C4 to S1 and reconnect C4 to C14. When power is applied the register will successively fill with 1's and then 0's, taking eight clock cycles for the complete sequence.

THE 74195

The 74195 is a versatile 4-bit parallel access PIPO shift register. The device is useful in a wide variety of shifting, counting and storage applications. The 74195 is particularly suited to high speed serial-to-parallel and parallel-to-serial data conversion. The device is housed in a 16-pin d.i.l. package, the pin connections of which are shown in Fig. 3.14. The internal logic of the device is shown in Fig. 3.15.

The 74195 has two distinct modes of operation: shift right and parallel load. These modes are selected by means of a SHIFT/LOAD input. In the shift mode the SHIFT/LOAD control input is taken to logic 1 and serial data enters the first bistable via the J and K inputs when the SHIFT/LOAD input is at logic 1. Data is then shifted through the register one bit (in the direction Q0

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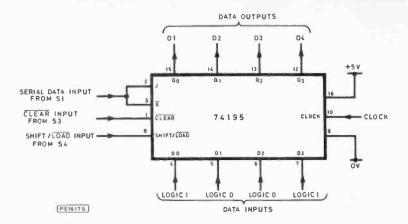


Fig. 3.14. Pin connections for the 74195

Fig. 3.16. Parallel 4-bit PIPO shift register using the 74195

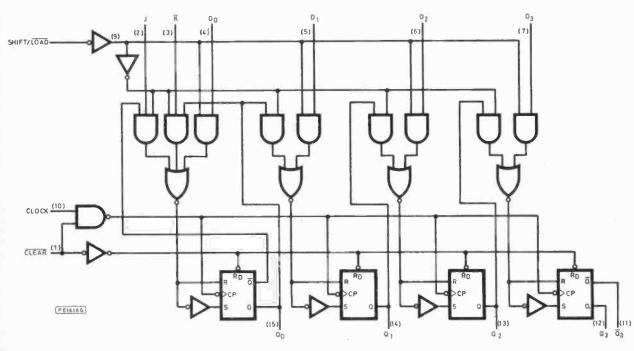


Fig. 3.15. Internal logic of the 74195

 \rightarrow Q1 \rightarrow Q2 \rightarrow Q3) following each low-to-high clock transition.

In the parallel load mode the SHIFT/LOAD control input is taken to logic 0 and data on the four parallel data inputs (D0 to D3) is transferred to the respective bistable outputs (Q0 to Q3) at the next low-to-high clock transition.

Shift left operation $(Q3 \rightarrow Q2 \rightarrow Q1)$ \rightarrow Q0) can also be achieved by tying the Q_n outputs to the D_{n-1} inputs (i.e. Q3 to D2, Q2 to D1, Q1 to D0) and holding the SHIFT/LOAD control input at logic 0.

It should be noted that all parallel and serial data transfers are synchronous and occur after each positive clock edge is received. Furthermore, by virtue of the edge triggered characteristic, there is no restriction on the activity on the J, K, D and SHIFT/LOAD inputs other than that associated with set-up and release.

The 74195 also has an active low clear input which sets all Q outputs low independent of any other input condition. It should be noted that, since the clear and clock inputs are internally gated, to avoid false clocking a low-tohigh transition on the clear input should only be permitted during the period for which the clock is low.

The 74195 should be inserted into socket B of the Logic Tutor ensuring, as usual, that pin-1 aligns with B1. The following connections should then be made:---

B1 to S3	(active low clear)
B2 to B3	
B3 to S1	(serial data input)
B4 to logic 1	(parallel data input D0)
B5 to logic 0	(parallel data input D1)
B6 to logic 1	(parallel data input D2)
B7 to logic 0	(parallel data input D3)
B8 to OV	(common)
B9 to S4	(shift/load control)
B10 to clock	(clock input)
B12 to D4	(D4 shows state of Q3)
B13 to D3	(D3 shows state of Q2)
B14 to D2	(D2 shows state of Q1)
B15 to D1	(D1 shows state of Q0)
B16 to +5V	(supply)
(A total of 15 links)	

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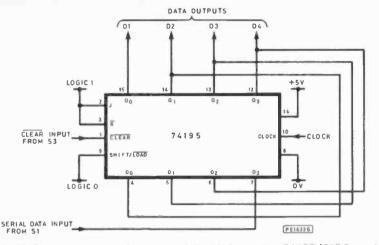


Fig. 3.17. Arrangement used to permit left shifting of the 74195 (SIPO model)

The Logic Tutor arrangement conforms to the circuit shown in Fig. 3.16. The shift register should be tested by first adjusting S3 to produce a logic O input on the clear line. All l.e.d. indicators should immediately become extinguished as all the Q outputs go low. Note also that S1 and S4 have no effect whilst the clear line is held low. Now adjust S4 to produce a logic 1 on the SHIFT/LOAD input and then operate S3 to generate a logic 1 on the clear line. The register will now commence loading from the serial input but, since S1 is producing logic 0, there will be no change in any of the Q outputs and the l.e.d. indicators will remain extinguished.

Now wait for the clock to go low (D9 extinguished) and depress S1. This places a logic 1 on the serial input. Notice how, as the clock next goes high, this logic 1 is transferred into the first stage of the shift register and D1 becomes illuminated as the first bistable changes state and QO goes high. Now release S1 and notice how a logic 0 loads into the first stage on the next rising clock edge whilst the previous logic 0 is transferred into the next stage. Readers may now like to experiment with serial loading of the shift register, inputting data manually by means of S1.

Readers should also confirm that S3 can be used to clear the register on the next rising clock transition after S3 has been set to produce a logic 0.

The parallel load mode can be selected by adjusting S4 to produce a logic 0 input, checking first that the clear input is at logic 1. On the next rising clock transition the data (1010) that has been hard-wired on the data inputs will be transferred to the Q outputs. The data remains static as long as the SHIFT/LCAD control input is held low but as soon as S4 is set to produce a logic 1 normal shift right operation is restored.

SHIFT LEFT OPERATION

In order to obtain shift left operation, the 74195 should be left in socket B and the links re-arranged as follows:----

B1	to	S3	(active low clear)
B2	to	B3	
B3	to	logic 1	
B4	to	B14	
B5	to	B13	
B6	to	B12	
B7	to	S1	(serial data input)
B8	to	OV	(common)
B9	to	logic 0	
		clock	(clock input)
B12	to	D4	(D4 shows state of Q3)
B13	to	D3	(D3 shows state of Q2)
B14	to	D2	(D2 shows state of Q1)
B15	to	D1	(D1 shows state of Q0)
B16	to	+5V	(supply)
(A to	tal	of 15 lin	ks)

This arrangement conforms to the circuit diagram shown in Fig. 3.17. Serial data can be fed into the register by means of S1 and cleared by means of S3 (a logic 0 being required to clear the register). Now wait for the clock togo low and then press S1 to generate a logic 1. D4 will become illuminated when the next rising clock edge occurs and data will then be subsequently transferred from Q3 to Q2 (D4 to D3) on the next rising clock edge, and so on. To clear the register S3 should be adjusted for logic 0 and all Q outputs will then go low regardless of the state of the clock.

NEXT MONTH: Pseudo random numbers and universal shift registers

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	TF 25		1000 mf 63V Ax Elect	£2.00
	TF 26		.022 mf 400V RAD POLY	
TF 27 10 100 mf 250V Ax £2.00	TF 27		100 mf 250V Ax	
TF 28 10 2.2 mf 160V Polyester RAD £2.00 TF 29 10 VDR's £2.00	TE 20			
TF 210 10 Mixed TTL (74 series) £2.00	TF 210			
TF 211 4 2N 3055 H(RCA) £2.00	TF 211			
TF 213 200 IN 4151 Diodes £2.00	TF 213	200	IN 4151 Diodes	£2.00
TF 214 200 IN 4148 Diodes £2.00	TF 214	200	IN 4148 Diodes	£2.00
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TF 53 2 10,000 mf 63V Comp. grade £5.00				
TF 54 JKG Reel 22g ersin multicore £5.00	TF 54	₿KG	Reel 22g ersin multicore	
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TF 56 5 0CP71 £5.00	IF 56			£5.00
TF 57 3 10,000 mf 40V Comp. grade elect £5.00	11 3/	3		65.00
TF 58 720 JW Carbon film 1Ω-10MΩ ten	TF 58	720		1.3.00
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ALL PRICES INCLUDE VAT - ALL GOODS BRAND	ALL PR	ICES INC	LUDE VAT - ALL GOODS BRA	ND

ALL PRICES INCLUDE VAT – ALL GOODS BRAND NEW & NORMALLY DESPATCHED BY RETURN POST. TERMS: Cash with order. POST & PACKING: Please

add 75p to total order.





AND OTHER SUPER SOUND KITS!

STD BOX BLK BOX

P.E. MONO-STEREO ECHO-REVERB (SEP84) 200ms echo, lengthy reverb, multi-tracking kit as published – BLK box: SET 218 £55.66

CODE

PANEL CONTROLLED SUPERKITS

BASS BOOST: Increases volume of lower octaves	SET 138B	£8.46	£11.46
BLOW BOX: Voice operated VCF & VCA for fascinating effects	SET 214	£24.33	£28.33
CHORUS UNIT: A solo voice or instrument sounds like more!	SET 162	£31.40	£34.90
COMPRESSOR: Limits & levels maximum signal strength	SET 133	£10.86	£13.86
DIGITAL TO ANALOGUE: 8-bit binary to Lin & Log voltage	SET 176	£23.96	£26.96
ENVELOPE SHAPER: Note triggered ADSR unit with VCA	SET 174	£17.15	£20.65
EQUALISER: Variable combinations of Low, Mid, Top & Notch	SET 217	£72.33	£25.83
EQUALISER: 10 Channels fully variable	SET 134	£37.83	E41.83
FADER: Voice operated with 5 response controls	SET 167	£14.21	£17.21
FILTER: For voice bandwidth enhancement over background	SET 142	£9.23	£12.23
FLANGER: Fascinating music effects, plus phasing	SET 153	£22.74	£26.24
FREQUENCY CHANGER: Tunable note & waveform modifier	SET 172	£34.46	£37.96
FREQUENCY DOUBLER: Guitar octave raiser & tone changer	SET 98	£9.80	£12.80
FREQUENCY GENERATOR: Multiwaveform, 0.4Hz-470KHz	SET 128	£19.04	£22.54
FUNKY-WOBULO: Modulates a singing voice	SET 149	£12.40	£15.40
FUZZ: Smooth distortion, retains attack & decay	SET 91	£10.57	£13.57
GUITAR OVERDRIVE: Heavy fuzz with selectable qualities	SET 56	£19.73	EZ3 23
GUITAR SUSTAIN: Extends note decay time, with noise gate	SET 222	£22.81	£25.31
GUITAR TO SYNTH INTERFACE: With voltage & trig outputs	SET 173	£32,87	£36.37
HAND CLAPPER: Auto & manual variable clap effects	SET 197	£22.69	£25.65
HEADPHONE AMP: 2 watts into phones or speaker, variable	SET 156M	£12.03	£15.53
HUM CUT: Tunable mains hum cut filter	SET 141	£11.26	£14.26
JABBERVOX: Voice disguiser with reverb & tremolo	SET 150	£23.84	£27.34
METRONOME: With audio output & visual beat & downbeat	SET 143	£13.81	£16.81
MIC PRE-AMP: Variable again & switched tone response	SET 147	£7.13	£10.13
MIXERS: Several in catalogue			
MOCK STEREO: Splits mono signal into stereo simulation	SET 213	£19.07	EZ3.37
MULTIPROCESSOR: Ang, Rvb, Faze, Fuzz, Wah, Trem, Vib	SET 189	£57.14	£61.14
MULTIWAVEFORM VCD: Log voltage to frequency, switchable	SET 177	£16.98	£20.48
MUSIC MODULO: 8 variable tremolo & wah guitar effects	SET 196	£18.79	£21.79
MUSICAL CALL SIGN: Programmed call sign generator	SET 121	£12.91	£16.41
NOISE GATE: Reduces tape & system noise	SET 145	£9.97	£12.97
PHASER (SIMPLE): Auto & manual rate & depth controls	SET 164	£18.40	£21.90
REVERB: (SIMPLE) Mono/stereo, variable depth & delay	SET 203	£25.54	£29.54
RHYTHM GENERATOR: Computer driven, 9 drum effects	SET 185	£30.64	E34.64
RHYTHM GENERATOR: 15 pre-programmed rhythms, 9 effects	SET 170	£35.64	£39.14

BLK BOX - steel & all, black plastic finish. STD BOX - plain aluminium, lipped lid. DC BOX - robust diecast. SET codes include PCBs, parts, instructions, boxes, wire, solder. More details & kits in catalogue - send S.A.E. (Overseas £1 or 5 IRC's). P.E. RING MODULATOR (NOV84). With multi-waveform VCO, Noise Gate & Auto-level Control. Kit as published – BLK box: SET 231 £73 99

ROBOVOX: Versatile robot type voice modifier ROGER CALL SIGN: Twin gongs. auto triggered ROGER CALL SIGN: Single tone auto triggered SIRENS: Auto triggered by sound or pulse SIRENS: Manually controlled, constantly varying SIRENS: Manually controlled, constantly varying SIRENS: Manually controlled, constantly varying SPECH PROCESSOR: Clearer speech and level control STORMS EFFCTS: Auto Sine wave 20th-15KHz, variable SYNTHESISER: 2-oct push sw. variable ES, Freq. Shape, Span TOM-TOM SYNTH: Sound triggered, multivariable TOME CONTROL: Bass, mid. treble, gain & cut TREBLE BOOST: Increases votume of upper octaves TREMDLO: Mono variable rate & depth modulation TUNING FORX: 98 note audio/visual tuning aid VOLTAGE CONTROLLED FLTER: 12dB, variable modes	SET 165 SET 126 SET 127 SET 199 SET 151 SET 146 SET 169 SET 169 SET 182 SET 130 SET 130 SET 138 SET 136 SET 138 SET 136 SET 136 SET 177	621.03 61.38 69.04 619.99 613.19 69.96 615.05 615.05 615.05 615.05 615.05 615.05 615.05 615.05 613.17 61.13 69.71 61.70	224.53 £14.30 £12.04 £12.04 £12.60 £12.60 £19.36 £19.36 £19.36 £19.37 £18.05 £16.67 £11.13 £12.71 £12.70 £12.70 £12.70
	SET 178 SET 137	£17.02 £23.94	£20.52 £27.44
VIBRATO: Variable rate & depth of freq shift VOCODAVOX; Modular vocoder, 7 chans, extendable	SET 152	254.31	£68.31
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T this point it is worthwhile looking at some oscillograms of simple waveform combinations, produced with the unit described in Part 1. Table 1 shows a schematic representation of the control settings used.

Fig.	S7	S 4	S 6	S5	S 3	52	VR1	VR5	VR4	VR6	S1
3а	+	+	+	+	X	-	X		1	x	-
Зb	+	+	+	+	X	-	X	1	1	X	
3c	+	+	+	+	X	-	x		X	х	1
4	+	ł	ł	+	X	-	X			x	-
5a	+		+	x	X	X	X		х	X	X
5b	+	+	ł	X	X	X	X	X	X	X	x
5c	+	+	ł	X	X		x	1	x	X	x
5d	+	+	+	Х	X	1	X	1	х	x	x
6a	+	+	+	ł	X	-	X	X	1	X	
6b	+	+	+	ł	Х	-	X	X	1	X	1
6c	+	+	+	ł	X	+	X	X	X	X	1
6d	+	+	1	+	х	-	X	X	1	X	
6e		+		+	x	-	х	1	1	X	-
6f	+	+	ł	+	Х	+	X	1	1	х	X
6g	+	+	+	+	X	-	X	1	1	Х	1
6h	+	+	+	+	x	-	X	1	1	X	
6i	+	+	ŧ	+	X	-	X	X	1	X	-
6j	+	+	+	+	Х	+	Х	X	1	X	1
6k	+	+	1	+	X	-	X	X	1	х	1
61		1	+	+	Х	-	X	X	1	X	
7a	+	1	+	Х	X	1	X	X	Х	Х	X
7Ь	+	1	+	х	X		X	Х	Х	X	Х
7c		1	+	X	X	1	X	X	X	X	Х
7d	+	1	+	X	X	1	X	X	Х	X	x

WAVEFORM OSCILLOGRAMS

In Fig. 3, a triangle wave of 3kHz is being used as the carrier signal going to one input, though its actual waveshape is irrelevant at this moment. This is being modulated by a second waveform, on the other input, at a much lower frequency of about 150Hz. The two frequencies shown are widely diverse for illustrative convenience but a similar principle applies to other spacings. The modulating waveform is shown in the upper trace, and the composite output in the lower, with the higher frequency inside the 'bubble' envelope. It will be immediately apparent that the unit in the mode selected has doubled the frequency of the sine and triangle modulators to 300Hz. Although the ramp modulator appears not to be doubled, close examination of the scope reveals that each diamond 'bubble' is in fact two. with a very fast change at the centre, coinciding with the ramp trailing edge. The effect of modulating by square wave is not shown as the edges are too fast to produce a readily visible effect, though it can just be discerned audibly. What is not apparent in the oscillogram is that the carrier within the 'bubble' now consists of 3kHz-300Hz = 2700Hz, and 3kHz + 300Hz = 3300Hz and on the oscilloscope the carrier in the alternate 'bubbles' is seen to be phase shifted by 180°, with a slight vibrato on its edges. Although the full harmonic content of the product signal is not seen in the oscillograms, the differentials can be heard clearly through an amplifier. Using two signal generators with one supplying a fixed frequency, the other is slowly swept across the audio spectrum. When the sweep frequency is rising but below the fixed one, two notes are heard at the output, one rising, the other falling. Close to frequency equality the lower note becomes progressively deeper until at the balance point the low frequency is totally cancelled and only a doubling of the fundamental is heard. As the sweep progresses upwards so two frequencies are again heard, but each rising, one more slowly than the other. The very low frequency heard near the balance point is clearly shown in Fig. 4. The high frequency within the shape is at twice the original. This then is the sum of the two, and the difference. The frequency doubling can also be verified without a scope by taking both the final signal and one of the originals into a mixer and alternating the pan control between both, whereupon the octave differential will be heard. The same can be repeated when a low frequency note modulates a high frequency one. If the high frequency is out of audio range, the low frequency modulator will be heard to be one octave lower than the bass content of the output.

FREQUENCY DOUBLING

Fig. 5 shows the effect of deliberately feeding the same signal in to both the modulating and carrier inputs. From these photos it will be clear that frequency doubling occurs with each of the four different, original waveforms, but also that the output does not have the shape of the original. In the photographs of the ramp and squarewaves, both original waveforms have been intentionally slightly distorted so that the very fast intermediate peaks are more clearly seen. In all cases the frequency doubled waveform takes on a much



spikier shape, even for the sine wave. Indeed in other frequency doubling methods known to the author, a similar degree of sharpening is experienced. Any frequency doubled signal is thus bound to be a harsher note one octave higher, especially so with the ramp, and from an audio position, frequency doubling of a square wave will only produce objectionable clicks as the mark-space ratio is so wide. Doubling still takes on a harsher sound when introducing a time delay, which produces a phase shift, to one of two identical signals. (Interesting sounds are produced though, if the complete ring modulator is preceded or followed by echo, or reverb units). The doubling effectiveness is also subject to the complexity of the original signal. For simple monotonic frequencies, the effect is quite usable. However, if used when the signal is complex, like trying to double the pitch of a chord or of multiple instruments, the effect is cacophony. Speech too takes on extremely odd noises if octave raising in a ring modulator is attempted. For true frequency doubling with waveform retention, much more complex equipment is needed.

MUSIC PROJECT

and interesting effects. When using a low frequency simple waveform with a higher frequency source such as speech or music, tremolo can be given to the music, and speech can be made to sound robot-like. In creating Dalek voices, one of the essentials is to modulate the voice at a low frequency, though other factors are involved in the production of the authentic Dalek sound. Tremolo and vibrato modulation are both effects that when used with discretion can greatly enhance the interest of many sounds.

For music the speed of modulation is at its most satisfying if the rate is in the region of 6.5Hz. In an analysis some years ago of the modulation given by violin players and opera singers, there was remarkable consistency between all of them, modulating their pitch within about 1 cycle of 6.5Hz. The author is involved in many types of effects units and finds that when testing rhythmic sources the most satisfying, and eventually the most hypnotic rates, are those within this same range. It is an interesting speculation that there might be a correlation between this frequency and the brain wave frequencies associated with Alpha and Theta

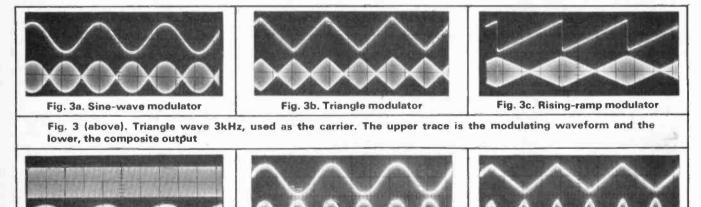


Fig. 5a. Sine-wave

Fig. 4 (above). High frequency modulator, with modulating carrier at almost same frequency showing effective slow-beat frequency

Fig. 5 (right). Effects of using the same signal in both modulating and carrier inputs

Fig. 5c. Rising ramp-wave

TRACKED RING MODULATING

Another option available is to use the inputs with two accurately controlled waveforms of different frequencies. The most usual source of these is two oscillators of a synthesiser, with the frequencies tracking identically, and exactly spaced so that the correct harmonic relationship is the same throughout the range. The complex chording structure is then simpler to achieve. Referring back to the frequency example earlier, 'G' at 396Hz is 1.5 times the frequency of 'C' at 264Hz. Similar relationships between two original signals will produce other equivalent composites, though not necessarily of a concordant nature.

FIXED CARRIER

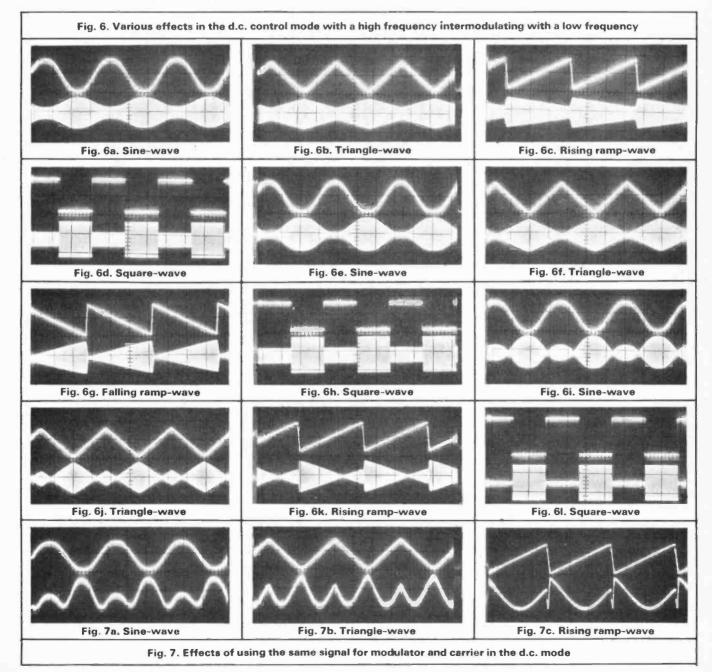
It is not necessary for the carrier frequency to be shifting in sympathy with the modulator and in many cases it is better to keep the carrier static for the production of some weird rhythms. Theta rhythms are those with a frequency of about 4Hz to 7Hz, and which appear to be connected with mood. Alpha rhythms occur between 8Hz and 12Hz and occur most often in relaxed meditation. It is possibly no coincidence that the most satisfying tremolo and vibrato rates lie between the two.

Fig. 5b. Triangle-wave

Proper vibrato cannot be readily produced by a ring modulator as it involves true frequency shifting without additional tone generation, but tremolo with a trace of vibrato is easily obtained through low frequency amplitude modulation around 6.5Hz. Voice modulation for Dalek-type production lies in the range of about 15Hz to 30Hz. At frequencies lower than this the modulation tends to be lost amongst the consonant peaks of speech as the vowels are usually much shorter than in singing. As the modulation rate increases between 400Hz and 1kHz so a metallic quality is acquired by the voice, becoming more pronounced but less intelligible as the carrier goes beyond 1kHz. If music is modulated with a fixed carrier in the higher frequency ranges extremely uncanny effects are produced. This is particularly true of carriers between 3kHz and 6kHz, when music can take on an almost nightmarish quality. When working on the prototype, the author found that 10 or 15 minutes of this upper frequency modulation was all that was tolerable at one time. Using low frequency ramps and squarewaves to modulate complex higher frequency sounds usually results in objectionable clicks. However, these clicks can be made use of if the carrier is a single high frequency or white noise source. With the former 'pinging' sounds can be produced and with the white noise, a variety of steam engine effects can be created. Using a white noise generator in conjunction with an external pulse source can also produce sounds akin to gun shots.

Fig. 6 shows the effect of VR5 in the d.c. control mode at three settings when a high frequency intermodulates with a

low frequency. As will be seen some quite distinctive waveform shapes appear, all of which have their own unique effect upon the final output quality. When bypassing the ALC control a 180° phase change occurs and the ramp waveform changes to the opposite slope introducing a fifth set of variations. Full modulation in the d.c. mode is intentionally inhibited as the increased level required would inherently raise the carrier breakthrough level. Full carrier modulation is of course provided when S4 is in the a.c. mode. When switching between both modes, the nature of the circuitry produces a cross-fade effect between them. This is due to the relative d.c. levels applied to C21 changing. at a rate determined by the biasing resistors and provides a smooth changeover. A similar effect is produced when S7 is switched in and out. Fig. 7 shows the effect of using the same signal as both modulator and carrier in the d.c. mode. Frequency doubling still occurs but with a changed amplitude relationship.



ASSEMBLY

The p.c.b. layout is shown in Fig. 8 and the wiring diagram in Fig. 10. It is easiest to assemble the printed circuit board in order of component size, leaving the insertion of i.c.s into their sockets until last. The short wire links on the p.c.b. can be shaped from. resistor offcut leads. It also helps with subsequent checking if the components are mounted with their identities readily visible. Diodes, electrolytic capacitors and i.c.s must only be inserted the correct way round as shown. At the very least the circuit will not work properly if this is ignored, at worst components could suffer extinction. Wiring to the controls must be tackled neatly and methodically to avoid the unit looking like a pig's nest! Make the connections between the panel components first, then wire up to the p.c.b., keeping all wires coming round the edges of the p.c.b.-coming over it makes it messy. They should be reasonably short, but long enough to enable the p.c.b. to be turned over for track side examination. Try to get it right first time as the box space is tight and it is tricky to manoeuvre the p.c.b. between the various panel parts. Ticking off components and wires as they are connected minimises errors, as does closely studying solder joints with a magnifying glass. These should look round and shiny, covering all round the solder pad on the p.c.b. If they look dull, crazed or daylight can be seen through them, then they are not good joints. Neither is one that has the solder spread across adjacent tracks. The author sometimes sees home assembled projects returned for servicing, and in practically all cases the only reason for malfunction is that improper attention has been given to soldering. It is very rate for modern components to be the cause of any misbehaviour.

SETTING UP

Alignment of the three presets is quite straightforward and no specialised equipment is needed. First, VR1 min, VR2 and 3 fully right, VR4 midway, VR5 min, VR6 max, VR7 midway, S1 position 1 (sinewave), S2 position 1 (internal VCO), S3 up (gate off), S4 off (d.c. coupled), S5 up (slow VCO), S6 up (ALC bypass), S7 up (Rmod override). If a signal generator is not available it is preferable to use a pre-recorded speech or music track from a cassette recorder or similar during testing. Plug this in to the high input jack socket, and the output jack socket into the main amplifier. Check that the cassette signal reaches the amplifier, if necessary increasing VR1

Fig. 8. The p.c.b. layout of the Ring Modulator

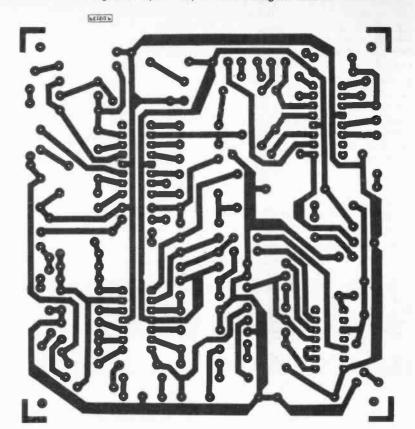
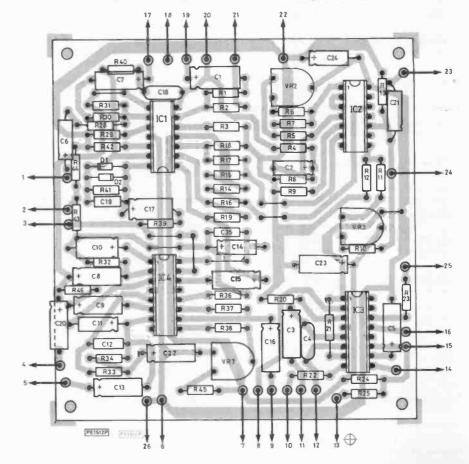


Fig. 9. The component layout of the Ring Modulator



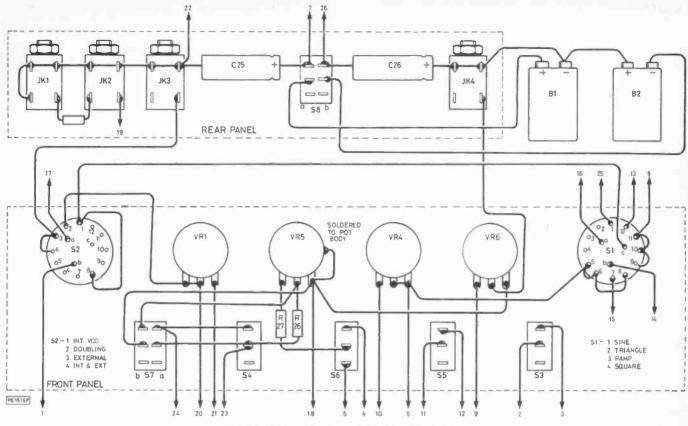


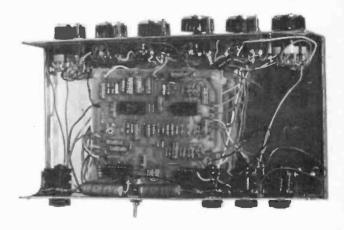
Fig. 10. Wiring diagram of the Ring Modulator

to a suitable level without distortion. Switch on S7 and a similar output level should remain. Switch on S4 to a.c. mode whereupon the sound should diminish. Slowly adjust VR3 around its midway point until the minimum level is heard. Bringing up VR5 to maximum, a slow modulation of the sound should be heard. Varying VR4 the modulation rate will change. Switching on S5 the rate will increase dramatically, and the guality of the audio output change with it. Remove audio source from the input leaving only the carrier signal. Switch S1 through its four settings and note that changes in carrier quality are evident. The carrier level will also change with the settings of S1. With S6 switched on to ALC mode the variations will be less pronounced and on an amplifier VU meter, the levels will appear practically the same. Maximise VR5 and carefully adjust VR2 around its midway point until carrier breakthrough is as low as possible. Again apply audio source from cassette. Switching on S7, carrier modulation will cease leaving only the original sound. Now align VR7 adjusting it around its midway point until minimum waveform distortion is heard at higher amplitudes. If no difference is noticeable, leave midway and ignore, though the correct setting will be obvious on an oscilloscope if a triangle waveform from a signal generator is used. Finally again remove the cassette, switch off S7, set VR5 max, VR4 and S5 set for maximum frequency. Increase amplifier volume until carrier breakthrough is audible. Switching on S3 the noise gate should come into operation and close down the output, eliminating all but the very smallest trace of crosstalk within IC4. Experiment with all the controls and switches with various types of speech and music inputs until familiar with the operation of the unit. If making minute adjustments to VR2 and VR3 allow the unit to be switched on first for a few minutes in case of a slight temperature sensitivity, although this was not apparent in the author's model.

The basic functions of a ring modulator have been covered



Photographs illustrating the internal and external assembly of the complete unit



in the introduction. The use of the unit is otherwise subject only to the imagination of the user. It will soon become familiar knowledge which settings are best for which type of input, whether for trains or tremolo, musical effects, or for modifying speech for robot and Dalek type vocalisations.

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		34.80	TL487-CP 0.59	78HGASC 9.95 78L05 0.30	170 100 100 100 100 100 100 100 100 100
		25LS2521PC 3.28	TL494-CN 199	78L12 0.30	74HC163N 1.51 74LS166 1.95 4015 0.65
		25LS2538PC 2.72	T1496-CP 0.59	78L15 0.30	74HC163N 1.51 74L5166 1.95 4015 0.65 74HC164N 0.95 74L5173 1.13 4016 0.46 ZIF SOCKETS
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y	1.70	1.40 0	0.95	26 way	3.75	6' cable 12'cable 1	8'cable	BBC315P BBC31DP	Single 100k TEC (expandable to dual)40 track with P.S.U. Dual (2 × 100k)TEC 40 track singleside with P.S.U.	£15 £25
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SOLDERING is one area of electronics where many constructors seem to give little thought to either the equipment they use or the methods they adopt. This often causes damage to components and p.c.b. tracks and can result in many unnecessary hours of fault finding once the project has been assembled. These problems can be easily overcome by using a suitable soldering iron and following the few simple rules set out below.

The basic requirement of a soldered joint is to provide an electrically conductive path with a secondary consideration being the mechanical strength of the joint.

Before soldering it is essential that the surfaces to be soldered are clean and free from any dirt or grease. If solder is to be applied to any heat-sensitive components then a suitably sized heat-shunt should be used. These are normally in the form of specially designed tweezers, although many people prefer to use a small pair of pliers.

The most important part of the soldering iron is the 'tip' or 'bit'. This is the part of the iron which stores the heat ready for passing onto the joint. The size of the bit and the power rating of the iron will determine the amount of heat that is supplied by the iron to the work and also the rate at which the work can be carried out. the entire joint. The solder should be removed first and then the iron. After the joint has cooled it should then be checked. Remember, to ensure a good joint, never blow or move a soldered joint before it has set!

A good joint should have a smooth, shiny appearance with no pitting, spikey or dull parts; and should of course be mechanically sound.

It doesn't take long to realise that when you are soldering you always seem to need an extra pair of hands to hold either the work or the component. If you are working on a p.c.b. then it is best to use a p.c.b. holder which will allow you easy access to both sides of the board and hold it steady whilst you are soldering.

Before you start soldering ensure that all the components you require are laid out in the order in which they are to be soldered. An ideal method of storing components prior to soldering is to use a polystyrene block; note though that MOS components should be kept in their packages until you are ready to solder them into place.

You should also ensure you have plenty of light over your workplace and that you have a comfortable sitting position.

Always replace your iron in its holder when you have finished with it. Never leave an iron on your workbench.

SOLDSRING INSTRUMENT BUYER'S GUIDE



If the temperature of the bit is incorrect it can lead to a number of problems. Too low a temperature can result in the insufficient activation of the flux, poor solder flow and therefore dry joints. If a joint is dry it will exhibit a high resistance which can be very difficult to trace. It should be noted that the majority of dry joints will only become dry after a period of time.

When the temperature setting is too high the flux will be vaporised, causing the solder to oxidise, resulting in poor quality joints and perhaps damage to any heat-sensitive components.

Soldering iron bits are usually made from copper to provide the maximum heat transfer at low cost. Because the copper soon becomes eroded many bits are coated with either nickel or chromium on their non-soldering surfaces to prevent oxidising whilst the tip can be coated with iron to increase its operating life.

The surface of the bit should be clean and free from any pits, burrs or indentations. To enable the smooth flow of heat from the surface of the bit to the joint a small amount of solder is placed on the bit prior to soldering each joint, this is called 'tinning'. After each soldering operation the bit should be cleaned with either a damp cloth or sponge and re-tinned if another joint is to be made.

When making a soldered joint the pre-tinned bit of the iron should be held against the joint and the flux-cored solder applied; the solder should flow immediately covering When choosing a soldering iron for your particular needs you must take into account all the applications for which it will be used. Soldering irons come in a variety of wattages, bit sizes and operating voltages. Some irons come as part of a soldering station and include a holder, sponge tray and a temperature adjustment to set the operating temperature of the bit.

In this buyer's guide we have tried to show the wide range of soldering irons currently available and have also included some of the soldering aids that can be used.

The prices shown include VAT but not post and packing except where stated.

PLEASE NOTE

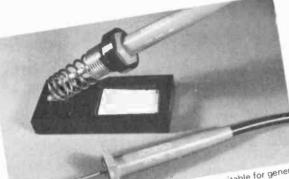
We would like to point out that readers buying from the guide are not protected by the Mail Order Protection Scheme unless the company concerned have advertised the product in a display advertisement in this issue.

The guide is designed as an aid to the purchaser and makes no recommendations.

SOLDERING INSTRUMENT **BUYER'S GUIDE**

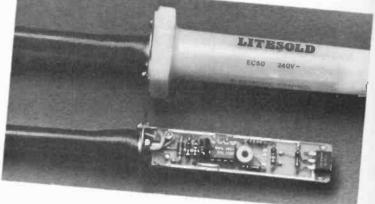
The Oryx M3 iron is rated at 17 watts and has a normal operating temperature of 380 degrees centigrade. It is supplied complete with a replaceable push-on tip and storage hook. The M3 is available in 12V, 110V and 210/240V versions with the 12V model fitted with a cigar-lighter plug for car repair work. Priced at £6.85 it is available from Greenwood Electronics, Portman Road, Reading, Berks (0734 595844.

The Litesold EC50 has an electronic temperature control which can be easily adjusted via an aperture in the handle. The temperature can be adjusted between 280 and 400 degrees centigrade. The bits are iron coated copper for long life and are retained by circlips to prevent sticking. The 50 watt iron is priced at £2B.00 and is available from Light Soldering Developments Limited, Spencer Place, 97/99 Gloucester Road, Croydon & 01-689 0574



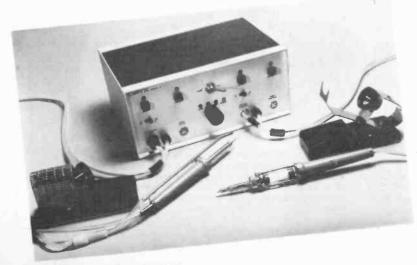
The LC18 from Litesold is a high efficiency iron suitable for general The LC18 from Litesold is a high efficiency iron suitable for general electronic assembly and servicing work. It can be either mains or fow voltage powered and takes a wide range of bits. The LA12 is imilar to the LC19, but is rated at 12 wate and is mainly intended Iow voltage powered and takes a wide range of bits. Ine LATZ is similar to the LC18 but is rated at 12 watts and is mainly intended for smaller work. The LATZ is priced at 67.25 and the LC19 is similar to the LC18 but is rated at 12 watts and is mainly intended for smaller work. The LA12 is priced at £7.25 and the LC18 is priced at £7.31. Light Soldering Developments Ltd. ¢01-689 0574

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The Weller WM12D weighs just 7 ounces and is the smallest iron in their range. It is rated at 12 watts and develops a tip temperature of 425 degrees centigrade. There is a choice of three tips which can be easily interchanged. The WM12D which is priced at £6.58 is also available in kit form together with two spare tips, a pair of tweezers and a supply of resin cored solder. Cooper Tools Limited, Sedling Road Wear, Washington, Tyne & Wear (091 416 6062

The new portable butane powered soldering iron from Oryx is only slightly bigger than a felt-tip pen. There is no flame during use, the chemical energy of the gas is converted into heat by means of a catalytic converter in the bit. The iron delivers the equivalent of 60 watts with the tip temperature being variable between 250 and 450 degrees centigrade. The iron will run for 60 minutes on its gas supply. The Oryx Portasol is available from Greenwood Electronics and is priced at £17.25. (0734 595844.



The totally self-contained Oryx HSR1 requires no external air or vacuum supply lines. It consists of five units: the TC84 temor vacuum supply lines. It consists or rive units: the 1084 tem-perature controlled iron, the SR84 vacuum solder removing iron, p.s.u. and two magnetic base safety stands for the Irons. The TC84 is fitted with a fume extractor which can be switched from the TCB4 to the SR84 as required. The workstation is priced at £570.00. Greenwood Electronics (*0734 595844.

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The Litesold ETC-4/FXc soldering station has a built in fume extractor (see Inset), and fully variable electronic temperature control with digital readout. The 40 watt iron uses a thermocouple sensor, is "spike" and "r.f.i." free, and also free of static and leakage. Price £299 + p&p from Light Soldering Developments Ltd., Spencer Place, 97/99 Gloucester Rd., Croydon, Surrey CRO 2DN. \$ 01-689 0574.





P.c.b. Track Repair Kit includes master frames with tracks, P.c.b. Track Repair Kit includes master trames with tracks, fingers, pads, elbows and flatpack pads, eyelets and funnelets plus the setting tools includes enough the electric enotities abreein tingers, pads, elbows and tlatpack pads, eyelets and tunnelets plus the setting tools. Includes epoxy, flux, cleaner, spatulas, abrasive eticke tweezere element where the economy version is also the setting tools. Includes epoxy, flux, cleaner, spatulas, abrasive sticks, tweezers, clamps and knives. An economy version is also available. Price, standard £145.90. Economy £72. **OK Industries UK Ltd.**, Dutton Lane, Eastleigh, Hants SO5 4AA. ¢ 0703 619841

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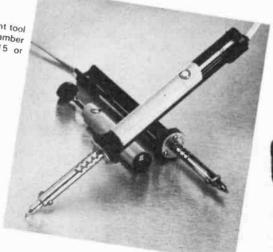
SA-6 desoldering iron is a powerful lightweight tool combining heating and suction. The suction chamber combining heating and suction. The suction champer is easily removed for cleaning. Available 115 or 230V. Price £19:19 from **OK Industries**.

Adcola 101 electronic controlled soldering iron is production orientated. Features r.f.l. free temperature control, zero leakage, open cir-

tateo. reatures r.t.i. tree temperature control, zero teakage, open cir-cuit failsafe protection, I.e.d. temp. indicator, and lockable temp. dial (120–420 deg. C) Bifilar wound to prevent magnetic effects. Price 778 40. (1) 56. consistent temp. Advete Readers and the second (120–420 deg. C) Biniar wound to prevent magnetic effects. Price **278.40** (+ **£6** carriage) from **Adcola Products Ltd.**, Adcola House, Gauden Rd., London SW4 6LH. **6** 01-622 0291.

The TCSU-D is a 60-watt soldering station with an electronic temperature control range of ambient to 495 deg. C. The iron itself works on 24V stepped down from mains to safety and isolation. Price £80.81 from Antex (Electronics) Mayflower House, Plymouth, Ltd., Devon. & 0752 667377.





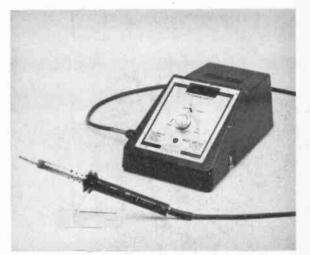
SA-8 series industrial grade soldering irons, available in 371 deg. C (SA-8-15) and 427 deg. C (SA-8-20), heat-up in two minutes using ceramic elements. They may be used with static-sensitive components without earthing. Tips are corrosion resistant. Available in 115 and 230V versions. Price £21.50, from OK Industries.

SOLDERING INSTRUMENT **BUYER'S GUIDE**

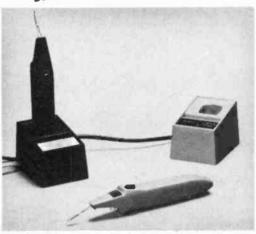
The Weller SI-25 25 watt iron, along with the Weller SI-15, SI-40 and WH1/2 hobby kits are claimed to be the only soldering related products on the market entitled to display the BEAB seal of approval for safety. Price £7-80 from Coopers Tools Ltd., Sedling Rd., Wear, Washington, Tyne & Wear NE38 9BZ. \$ 091 416 6062







Solon-Electrex DS400 Station supplied with TC24 Iron (50 watt) is thermostatically controlled from 150-400 deg. C (1% precision) with digital readout. The iron takes the 'Duratyp' range of bits. Price £138-69 from GEC-Henley, Gravesend, Kent DA11 9DA. \$ 0474 64466.



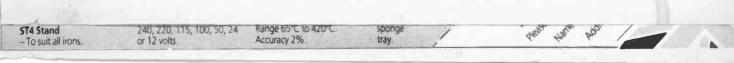
Oryx ISO-TIP series irons are cordless (rechargeable) for complete mains isolation. The iron automatically recharges itself when placed in its base, and has a built in spotlight. The 50 watt iron can solder 100 joints between charges. Tip temperature is 370°C. Also takes a drill attachment. Price £41 from Greenwood Electronics, Portman Road, Reading, Berks RG3 1NE. & 0734 595844.

Solon-Electrex 325 is a 25 watt mains 'instant' heat soldering iron of maximum temperature 380 deg. C. It uses C25 tip or TP30. Price £10.79 from GEC-Henley.

Adamin Model 15 miniature iton is available in 240V or 12V (12 watts). One of the smallest irons in the world and takes a range of hits from 1.2mm Adamin Model 15 miniature iron is available in 240V or 12V (12 watts). One of the smallest irons in the world and takes a range of bits from 1.2 watts). Developments Ltd., Spencer Place, 97/99 Gloucester Road, Croydon, Surrey CRO 2DN. © 01-689 0574.

Terter merte

Practical Electronics December 1984





GLORIOUS PAST—BRIGHT FUTURE

A rather sad note has been struck. The Carnegie Institute of Washington, which operates the observatory on Mount Wilson in California, has announced that during 1985 the great 100-inch Hooker reflector there will be "mothballed", and support for the two famous solar tower telescopes, the 150-foot and the 60-foot, will be gradually withdrawn. The other large instrument, the 60-inch reflector, will remain in service, but, as one commentator has said, the decision "sounds the deathknell" for Mount Wilson as a leading astronomical institution.

The trouble is due to the continued spread of the city of Los Angeles, which has become not only larger, but also brighter and more polluted. From the city, the summit of Mount Wilson can be clearly seen, and from the top of the mountain the sky is not nearly so dark as it was in 1917, when the 100-inch reflector came into operation. This means that many branches of research can no longer be carried out there.

The 100-inch has a glorious history. When it was completed, it was not only the world's largest telescope, but it was in a class of its own, and it remained so until 1948, when it was surpassed by the Palomar 200-inch.

Using the Mount Wilson reflector in the 1920s, Edwin Hubble proved that the objects then called "spiral nebulae" were in fact independent galaxies, far beyond the limits of our own Milky Way system. Hubble achieved this by studying the behaviour of certain variable stars inside the spirals. These variables, known as Cepheids, "give away" their real luminosities—and hence their distances—by the way in which they brighten and fade. In Hubble's day, only the 100-inch was powerful enough to be used in studying them.

The telescope itself is as good today as it ever was, but by modern standards it is oldfashioned, and there is no thought of moving it to a better site. So its story may be coming to a close, even though it is still capable of carrying out really valuable research. At any rate, its place in history is assured, and mercifully there has been no suggestion as yet that it will be dismantled, so that hope remains.

Two major telescopes are being planned elsewhere: one in Hiroshima in Japan, and the other to be set up at the observatory on La Palma, in the Canary Islands, where the I.N.T. or Isaac Newton Telescope is now in full operation. (While I was there, a few months ago, we used the I.N.T. to obtain a colour video picture of the Ring Nebula in Lyra—the first time that this had been achieved for an object beyond the Solar System.) The projected new telescope is a 100inch reflector, and will be a joint venture by Norway, Sweden, Denmark and Finland. The optics will be made at Turku in Finland.

Halley's Comet is now under regular observation, and continues to brighten slowly as it draws in toward the Sun, but it will not come within the range of average-sized telescopes until the middle of next year. Unfortunately, this is a poor return, and the comet will be not nearly so conspicuous as it was in 1910 or in 1835, though with luck it will be easily visible with the naked eye towards the end of 1985.

THE SUN

On 22–23 November there will be a total eclipse of the Sun. The path of totality begins in the Molucca Islands and then crosses New Guinea, passing north of New Zealand and ending in the South Pacific. The maximum length of totality is exactly two minutes.

The partial phase will be seen from the Philippines, parts of Australia and New Zealand, and also from part of Antarctica, but of course the eclipse will be invisible from Europe, as it occurs during European night. The next total eclipse to be seen from anywhere in Britain will be that of 11 August 1999, when the track of totality will cross Cornwall.

Despite *Skylab* and other space-stations, total solar eclipses are still of tremendous importance, because it is only when the Sun is completely covered by the Moon that ground-based observers can see the corona in its full glory. At this month's eclipse the corona will be of the "minimum" type, because the Sun is now approaching the lowest point of its 11-year cycle of activity. There have already been several spotless periods this year, when the solar disc has been entirely blank.

The solar cycle is not perfectly regular, and the usually quoted figure of 11 years between successive maxima is only an average. Moreover, it may well be that we know less about the Sun than we used to believe. Even the cycle may not be permanent; there is excellent evidence that between 1645 and 1715 there were almost no sunspots at all—a period now generally known as the Maunder Minimum, since attention was first drawn to it by the British astronomer E. W. Maunder (and, independently, by Spörer in Germany).

SPACE MINE

Very important studies are being carried out from what is undoubtedly one of the strangest observatories in the world! It is situated a mile below ground, at Homestake Mine in South Dakota. This is the country of the gunslingers; little more than a century ago it was the home of colourful characters such as Calamity Jane, Wild Bill Hickok and "Doc" Holliday.

It is also a gold-mining area. Today the gunslingers have gone, but the gold is still there, and Homestake Mine is the largest in the whole of the United States. The solar observatory has been set up in a special chamber, or rather pair of chambers, hollowed-out specially for the purpose.

The Sun is radiating by nuclear transformations taking place near its core. Basically, hydrogen nuclei are combining to form nuclei of helium, with release of energy and loss of mass. (The mass-loss amounts to 4,000,000 tons per second, though by solar standards this is not very much, and there is no reason to suppose that the Sun will change dramatically

THE SKY THIS MONTH

This is not a particularly good month from the viewpoint of planetary enthusiasts. Mercury, Venus, Mars and Jupiter are all technically evening objects, but Mercury is well south of the celestial equator and is badly placed from Britain, even when at its greatest eastern elongation on 25 November.

Mars and Jupiter are also in the southern hemisphere of the sky, and set not long after the Sun; moreover Mars is now so far away that its magnitude has faded to 0.8, about the same as that of Aldebaran, and even large telescopes will show little upon its disc. Saturn is in conjunction with the Sun on November 11, and is therefore out of view altogether.

It may be worth noting that Ceres, the largest and

first-discovered of the minor planets or asteroids, comes to opposition on November 10. It is in Taurus, near the stars Xi and Omicron Tauri, but its magnitude is only 6.9, so that it is too faint to be seen with the naked eye. Binocalars will show it, though of course it looks exactly like a star. Its diameter is rather over 620 miles, and it is much the most massive member of the asteroid swarm.

Plans for asteroid probes are now being made, and should be put into practice by the mid-1990s, but it seems likely that the first target will be not Ceres, but Vesta—the brightest member of the group—which is smaller than Ceres, but considerably closer-in to the Sun. for at least 5,000 million years in the future.) Theorists also calculate that there should be the emission of neutrinos, which are particles with no electrical charge and virtually no mass—so that they are extremely hard to detect, since they can pass through the Sun and also through the Earth without being checked.

The only way to catch them is by making them interact with atoms of chlorine. If a neutrino hits a chlorine atom, the result will be an atom of Argon-37, which is radioactive and is therefore comparatively easy to track down.

THINK TANK

In Homestake Mine, Dr. Ray Davies and his colleagues have set up a large tank containing 100,000 gallons of perchloroethylene, which contains a great deal of chlorine and is nothing more nor less than cleaning fluid! The procedure is to leave the tank for a period of around eight weeks, and then carry out tests to see how many atoms of Argon-37 have been produced.

Since there will not be more than about a dozen of them, and the whole tank contains about a thousand million million million atoms of various kinds, the tests are far from straightforward. But for the radioactive qualities of Argon-37, there would be no hope at all.

Why "go underground"? The answer is simple. Cosmic rays from space will affect the chlorine in exactly the same way as neutrinos. But cosmic rays cannot penetrate a mile of solid rock—at least, not easily; a few can get through, but these can be allowed for. This is why Homestake is so suitable for this particular experiment.

The solar observatory has now been in action ever since the 1960s, and the results have been startling. Apparently the Sun is sending out only about a quarter as many neutrinos as in theory it ought to do. Unless there is something wrong with the experiment, which seems unlikely, or else there is a defect in our theories—or even the possibility that at the present epoch the Sun is behaving abnormally.

Neutrino emission is very sensitive to temperature. It has been calculated that the temperature at the core of the Sun is about 15,500,000 degrees Centigrade. If we reduce this by a million degrees, the neutrino results fall neatly into place, but such a fall in calculated temperature would raise other theoretical problems. Is it possible that neutrinos are more complicated than we think, so that they could break up or become modified during their 93,000,000-mile journey from the Sun to the Earth?

As yet we do not know. The Homestake results seem reliable enough, and are confirmed by a similar experiment being carried out in the Soviet Union. So we must do some re-thinking, and it is not surprising that solar physicists are placing great importance on the behaviour of a tank of cleaning fluid deep in a South Dakota gold-mine.

PRACTICAL ELECTRONICS PRINTED CIRCUIT BOARD SERVICE

Printed circuit boards for certain PE constructional projects are now available from the PE PCB Service, see list. They are fully drilled and roller tinned. All prices include VAT and postage and packing. Add £1 per board for overseas airmail. Remittances should be sent to: **PE PCB Service, Practical Electronics Editorial Offices, Westover House, West Quay Road, Poole, Dorset BH15 JJG.** Cheques should be crossed and made payable to IPC Magazines Ltd.

Please note that when ordering it is important to give project order code and the quantity. Please print name and address in Block Caps. Do not send any other correspondence with your order.

Readers are advised to check with prices appearing in the current issue before ordering.

NOTE: Please allow 28 days for delivery. We can only supply boards listed here or in the November issue.

PROJECT TITLE	Order Code	Cost
SEPT '81		
Horologicum	109-01	£3.16
	109-02	£3.11
	109-03	£2.97
Analogue Frequency Meter	109-04	£2.87
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APRIL '82		
Med. Resolution Equaliser (UK 101)	204-01	£1.73
Enlarger Timer	204-02	£4.02
AUG 82		
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Home Alarm	20802	£3.21
SEPT '82		
Waveform Digitiser	209-01	£8.24
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Accessory PSU	303-02	£1.35
41 Digit Frequency Meter	30303	£3.69
APRIL '83		
Phaser	304-01	£3.41
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Program Conditioner	30601	£2.30

PROJECT TITLE	Order Code	Cost
SEPT '83		
Guitar Active Tone Control	309-01	£2.27
Ground Communication System	309-02	£2.13
	309-03	£2.31
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Expanding the Vic 20	312-01	£5.18
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JULY '84	400-01	10.02
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EPROM Duplicator	407-02	£3.74
Alarm System	407-03	£3.19
Oscilloscope Calibrator	407-04	£4.23
AUG '84		1
Comm. 64 RS232C Interface	408-01	£3.02
Field Measurement	408-02	£3.19
11	408-03	£2.76
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Simple Logic Analyser	408-05	£2.93
Alarm System	408-06	£4.24
	408-07	£3.14
и	408-08	£3.23
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Parallel to Serial Converter	409-01	£2.92
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	409-03	£2.71
OCT '84	410-01	£1.90
Logic Probe NOV '84	410-01	L1.30
Computer DFM Adaptor	411-01	£2.76
DEC '84	411-01	12.70
Ni-Cad Charger	412-01	£2.40
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MICHAEL TOOLEY BA DAVID WHITFIELD MA MSc CEng MIEE

INLIKE their lead acid counterparts, nickel cadmium cells require a constant current rather than a constant voltage charging source. Furthermore, comparable sintered and mass plate cells have different charge rate requirements and this must also be taken into account.

The maximum indefinite charge rate for a sintered cell is usually taken as C/8, i.e. for a 1Ah battery the maximum indefinite charging current is 125mA. Cells may, however, be charged at higher rates provided care is taken to avoid overcharging which can permanently damage the cells. The maximum charge rate for a sintered cell is usually assumed to be 10C but, before attempting to 'fast-charge' a nickel cadmium cell of any variety, it is essential to ensure that the

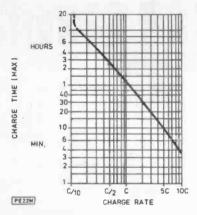


Fig. 1. Charge time versus charge rate for nickel cadmium cells

cell is initially fully discharged. Using the same 1Ah battery, for example, a 2C charge would be achieved by charging at 2A for 30 minutes.

The relationship between charge period and charge rate for nickel cadmium cells is illustrated in Fig. 1. To ensure a long cell life and a maximum number of charge/discharge cycles, charge rates in excess of C should be avoided if at all possible. Mass plate cells cannot, by virture of their construction, be charged at the high rates associated with sintered cells. The maximum charge rate for such cells is usually C/10 for 14 hours whilst the maximum indefinite charging current is often no more than C/100. Hence, for a 110mAh PP3 battery, the recommended charging current is 11mA for a period of 14 hours. Furthermore, if this type of battery is to be left on-charge indefinitely, the charging current should not be allowed to exceed 1.1mA. Table 1 shows the recommended charge rate for a number of popular nickel cadmium batteries.

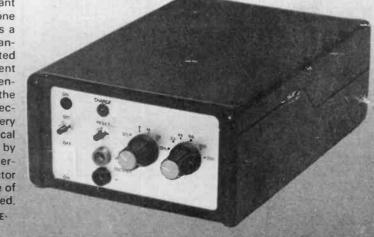
Battery type	Max. indefinite charging curren (mA)		
AAA	20		
AA	66		
С	250		
D	500		
PP3	1-1		
PP9	100		

Table 1. Maximum indefinite charge rates for various nickel cadmium batteries

We shall now describe a simple automatic charger which provides a total of 24 different charge rate options.

COD CHARGER

The easiest method of obtaining a reliable constant current source involves nothing more than a d.c. supply, one transistor and just three other components. Fig. 2 shows a typical family of output (collector) characteristics for a transistor operated in common emitter mode. It should be noted that, for a fixed value of base current, the collector current remains substantially constant and is reasonably independent of the value of collector-emitter voltage. Hence, if the battery to be charged is connected as the load in the collector circuit, and the base voltage held constant, the battery will receive a reasonably constant current charge. A typical circuit is shown in Fig. 3. The base voltage is stabilised by means of the Zener diode, D, and the base current is determined by the value of emitter resistor, R_E. Since the collector current is equal to the base current multiplied by the value of common emitter current gain for the transistor concerned, the charging current can be set by appropriate choice of R_E.



HOME PROJECT

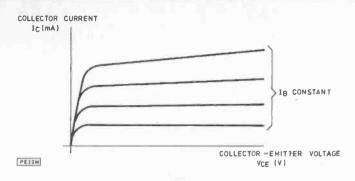


Fig. 2. Typical output characteristics for a transistor connected in common emitter mode

Another simple constant current charging source is shown in Fig. 4. Here a conventional monolithic three-terminal regulator is connected in what, at first sight, may appear to be a strange arrangement. The normal regulator output is, in fact, developed across Rs and since this voltage remains substantially constant, the current flowing through it will also be constant. The output current is given by:—

 $I_0 = \frac{5}{R_S} + I$ where I ≈ 5 mA

The value of R_S can be varied so that different charge rates can be catered for, as shown in Table 2.

With the two previous circuits, the charge rates may be easily selected by changing just one component. In practice, different charge rates can be obtained either by switching resistors or by incorporating a variable resistor into the circuit. There is, however, one problem; it is all too easy to connect the charger to a battery, switch 'on', and then forget it! Whilst this may be of little consequence when charge rates of C/10 are concerned, serious damage may be done if a fast charge rate is selected and thus, ideally, a charger should incorporate a facility which will discontinue the charge after a pre-determined time interval has elapsed. Hence, if separate controls for charging current and charging time are provided, a wide variety of charge rates can be accommodated to suit almost any type of cell which may be encountered. Furthermore, the user can rest assured that no harm will be done if the unit is left connected for an indefinite period!

AN AUTOMATIC CHARGER

The complete circuit of the automatic charger is shown in Fig. 5. A conventional mains transformer, T1, and bridge rectifier, REC1, provides an unregulated supply rail of approximately 17V. A programmable timer, IC1, is used to provide accurate monostable timing periods which are derived from a timebase and eight-stage binary counter. The two fundamental timing components are C2 and R2 and only the last four divider outputs are employed. A miniature normally-open push-button switch, S4, is used to initialise the timing sequence whilst a similar switch, S3, is used to re-set the timer, aborting the current timing period and discontinuing the charge.

The time period is selected by a rotary switch, S1, and the voltage at this point goes low for the duration of the timing period removing the bias from TR1 and switching the transistor 'off'. In this condition, R5, D1 and D2 supply bias for the constant current source, TR2. At the end of the monostable period the base voltage of TR1 rises and TR1 conducts. In this condition, D1 no longer achieves its Zener voltage and no base current is supplied to TR2. The output

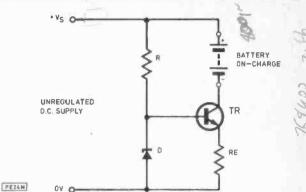


Fig. 3. A simple charger using a transistor connected as a constant current source

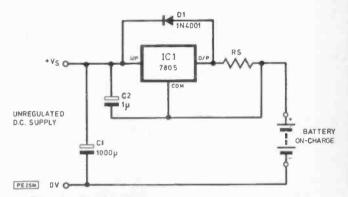


Fig. 4. Simple charger using a monolithic threeterminal voltage regulator

R (ohm)		Charge current (mA)
820	0.5	11
470	0.5	15
330	0-5	20
220	0-5	28
82	0.5	66
56	1	100
22	2.5	250
10	4	500

Table 2. Relationship between ${\rm R}_{\rm s}$ and charge current for the circuit of Fig. 4

current, which is selected by means of rotary switch S2, thus falls to zero and remains in this state until another charging period is initialised by the user.

CONSTRUCTION

The majority of the components for the automatic charger are mounted on a single sided p.c.b. measuring approximately 53 x 104mm, the design of which is shown in Fig. 6. The corresponding component layout is shown in Fig. 7. The recommended sequence for mounting components is: terminal pins, d.i.l. socket, resistors, diodes, transistor, bridge rectifier, and capacitors.

The underside of the completed p.c.b. should be carefully checked for solder bridges and dry joints whilst the component side should be examined paying particular attention to the placement and orientation of the polarised components. The remainder of the components, excluding the mains transformer, fuseholder, and mains connector, are all mounted on the aluminium front panel. The wiring of the front panel should follow the layout given in Fig. 8 and connec-

COMPONENTS ...

Resistors

R1	47k	R8,R9	5Ω6 (2 off)	
R2	2M2	R10	10	
R3	100	R11	15	
R4	10k	R12	1	
R5	1k	R13	22k	
R6	56	R14	1k	
R7	2Ω2			
R1 to R5	B13 and B1.	4 are 11/ 5% car	hon	

R to R5, R13 and R14 are $\frac{1}{4}$ v 5% carbon R6 to R12 are $\frac{1}{2}$ W 10% carbon

Capacitors

C1	1000µ 16V p.c. electrolytic
C2	220µ 16∨ p.c. electrolytic
C3	100n polyester

Semiconductors

D1	BZY88 C4V7 Zener
D2	Green 0-2in I.e.d.
D3,D4	IN4148 (2 off)
D5	Red 0-2in I.e.d.
TR1	BC108
TR2	TIP32A
IC1	2240
REC1	50V 1A p.c. mounting bridge rectifier

Miscellaneous

- T1 6VA mains transformer with 2 x 6V or 1 x 12V
- secondary winding rated at 500mA 16-pin d.i.l. socket TO220 heatsink (see text)
- S1 3P 4W rotary switch (1 pole only used)
- S2 2P 6W rotary switch (1 pole only used)

tions to the p.c.b. may be most conveniently made using short lengths of ribbon cable.

Where less than six series connected cells (or a battery of equivalent voltage) is to be charged at currents of 100mA, or more, the series transistor, TR2, MUST be mounted on a heatsink. This should be a TO220 variety of 10.5°C/W, or better. The front panel should be labelled as shown in the photograph.

INITIAL TESTS

When all wiring is complete, a final check should be made before inserting IC1 into the d.i.l. socket (taking care to observe the correct orientation). The mains supply should then be connected and the charger switched 'on'. The red I.e.d., D5, should immediately become illuminated whilst the green I.e.d., D2, should remain extinguished. At this point it is worth checking the positive supply rail voltage which appears at pin 16 of IC1. This should be $17V \pm 1.5V$. If this is not the case, connections to the mains switch, fuse, and transformer should be carefully checked.

The charge period switch, S1, should then be switched to give the shortest time (3.5 hours) whilst the charge current switch, S2, should be switched to the lowest current setting (12.5mA). S4 should now be momentarily pressed at which point the green I.e.d., D2, should become illuminated. Now momentarily depress S3 to reset the charger. As S3 is released, D2 should become extinguished. This set/reset procedure should be repeated a few times until the user is familiar with its operation. If no charge is apparent, readers

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50.6

R10

10

D11

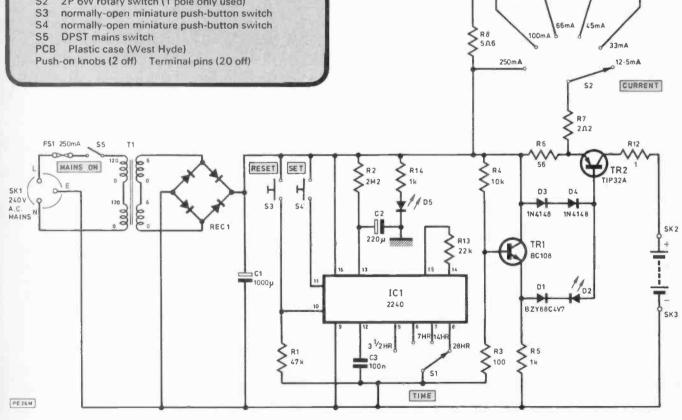
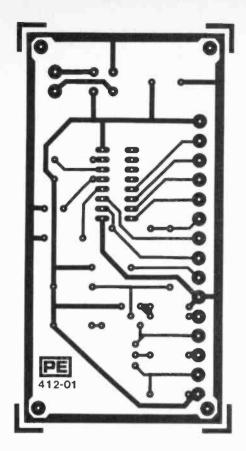


Fig. 5. Complete circuit diagram of the automatic charger



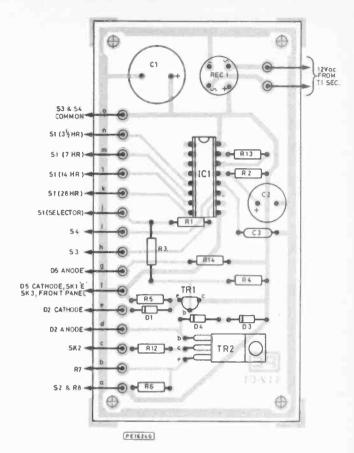


Fig. 6. P.c.b. design

Fig. 7. P.c.b. component layout

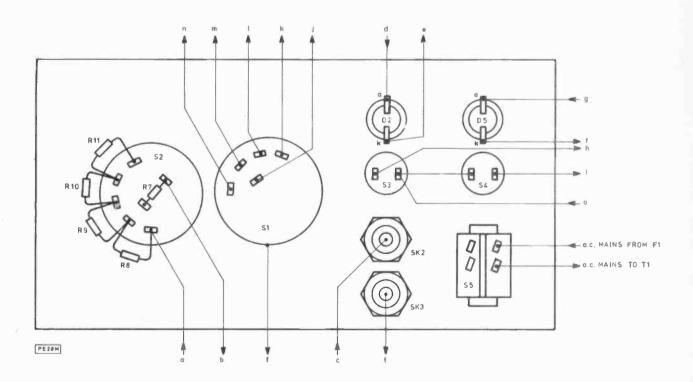


Fig. 8. Front panel wiring

	Voltage		
Test point	Reset	Set	
IC1 pin-16	17.0	17.0	
(0	17.0	17.0	
TR1 (b	14.0	1.0	
le	13.4	8.8	
(0	0	16.6	
TR2 { b	16.8	16.0	
le	17.0	16.7	
Output (Sk2)	0	16.6	

All voltages measured with a 20kohm/V multimeter

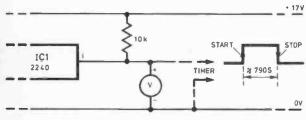
Table 3. Test voltages

should refer to the test voltages provided in Table 3. If the supply voltage is correct but neither l.e.d. is illuminated, it is worth checking the polarity of D2 and D5.

Having confirmed that the set/reset switching is functional, the next stage is to check the charge time. Since it would be somewhat tedious to wait for even the shortest charge period (approx. 3.5 hours) it is more expedient to employ one of the unused shorter time periods available from the programmable timer, IC1. The shortest of these is available at pin 1 and readers should refer to the test circuit of Fig. 9a. The time period for a 'high' output should be measured using the following procedure:—

- Connect the test circuit using either a voltmeter or digital timer. The voltmeter should be switched to the 20V, 25V or 30V range whilst the timer should be adjusted to 'start' on a positive edge and 'stop' on a negative edge.
- Depress S4. The voltage at pin-1 should be 'low' for several minutes and then it will go 'high'. At this point timing should commence (using a stopwatch or the digital timer).
- 3. After approximately 13 minutes the voltage should go 'low' again. At this point timing should stop.

The time period at pin-1 is 1/16 of the nominal 3.5 hour period. However, due to component tolerances (particularly that associated with C2), the time period will seldom be exact. Where a precise time interval is desired, R2 may be



PE29M

Fig. 9a. Arrangement for testing the charge time

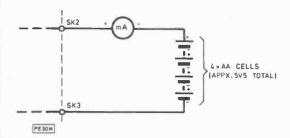


Fig. 9b. Arrangement for testing the charge current

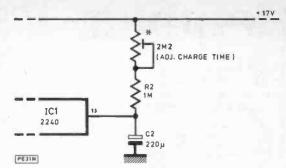


Fig. 10. Circuit modification for precise time period adjustment.

replaced with a 2M2 pre-set resistor in series with a 1M fixed resistor, as shown in Fig. 10. The pre-set resistor is then adjusted for a pin-1 high output time of approximately 790 seconds.

Finally, the charge current should be checked. This is accomplished using a typical battery pack consisting of four series connected AA cells and measuring the current delivered with the charger 'set'. The required circuit arrangement is shown in Fig. 9b. The charge current on each range of S2 should be measured and this should be within approximately 10% of that marked. If this is not the case, minor ad-

		Charge Time (hours)			
		3.5	7	14	28
	12.5	30	60	90 (PP3)	180 (AAA)
ant	33	80	160 (AAA)	320	640
Curre	45	110 (PP3)	220	440 (AA)	880
Charge Current (mA)	66	160 (AAA)	320	640	1280 (PP9)
Ċ	100	250	500 (AA)	1000 (PP9)	2000 (C)
ŝ	250	612	1200 (PP9)	2400 (C)	4800 (D)

(Note: Battery capacity is shown in mAh)

Table 4. Recommended charge rates for the automatic charger

justment to the appropriate resistor (R7 to R11) may be made simply by connecting larger resistance values in parallel and trimming for the exact current desired.

This completes the initial tests and the automatic charger is now ready for use. Table 4 provides a guide as to the charge rate obtained with various combinations of S1 and S2. Recommended rates for charging various common types of nickel cadmium battery are also shown. Note that only cells of similar type should be charged at any time and they should ALWAYS be connected in series. Users should equip themselves with a range of battery holders to suit the cells currently in use. Such holders are readily available from most electronic component suppliers.

Finally, care should be taken to ensure correct polarity of cells connected to the charger. Failure to observe this precaution may result in damage to both the cells themselves and to the charger!

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HERE'S A QUESTION: just how many companies in the components business, do you think, started out as a display of valves in the side window of a butcher's shop? I know of only one. That's the firm of A. F. Bulgin—a household name in the industry for more than half a century.

The founder was one Arthur Bulgin whose father was a respected purveyor of highquality meats to the gentry (this was the grand title, or something similar, that graced his letterheads). And although he was probably disappointed that his son showed no inclination to follow the tradition of victualling the well-to-do, he gave the lad every encouragement in a venture which was, on the face of it, somewhat dicey. So the side window was donated willingly, if, I suspect, with not a little trepidation. What the squire and other distinguished beef-champers thought about it is not recorded.

Arthur justified his father's confidence when he set up a business proper in April 1923. This was at a time when the British Broadcasting Company had just launched the first public radio service in the UK and the future was rich in golden opportunity.

"The first product . . . was a light-emitting escutcheon"

Long before he went in for manufacturing, Arthur traded as a distributor—he was totally dedicated to this method of marketing. His family claims—and who shall blame them—that he was one of the first to operate in this now firmly established branch of the component sector.

The first product to come out of Bulgin's modest stable was described as a lightemitting escutcheon. Or, to put it more simply, a signal window. It was set in the cabinet of a radio receiver and a quick gander through it told you if your valves were alive and well. A comforting glow meant you were in business. An inky blackness spelt trouble.

By 1948—and we're having to skip a bit of history here—Bulgin had become a limited company employing 250 people and turning out what the Americans call panel hardware: passives, switches, connectors, fuses, fuseholders, signal lamps, clips and so on. And in spite of a considerable broadening of the range over the years, such "nuts and bolts" remain the firm's bread and butter.

Arthur's four brothers emulated their father's farsighted support of his meat-shy son and one-by-one they came into the business, leading it on to bigger and better things and consolidating the high reputation it continues to enjoy. Today the family tide runs even more strongly. Ronnie Bulgin, son of the founder, is chairman and MD of what has become a group rather than a single company. His cousin Robert is his deputy. Ronnie's son Richard (25) bowler-hatted his way through the Stock Exchange for three years before joining the fold in 1980 and is now involved in selling and marketing with a new Bulgin venture—of which more anon. Robert's son Clifford (23) is equally active on the production side. And I'm told there is a reserve stock of other little Bulgins—too young yet to get in on the family act—watching out for their cues.

I met Ronnie and Robert the other day. Ronnie is shrewd and softly-spoken. Robert is a burly, jovial extrovert. That's understandable. Before joining the firm he was in advertising for a spell. It's the group's good fortune that he eventually decided to mend his ways. Between them, the cousins house a lot of ability and a treasury of enthusiasm.

"Let's tell you about our exciting new operation," said Ronnie. "Some months ago we consolidated Ambit International, Solent Component Supplies, Broxlea and Projex Distribution—which represent Bulgin's distribution and custom manufacturing activities—into a single company called Cirkit. It's the first stage of a major development programme.

"Cirkit, far from taking a 'me too' stance in the distributor ranks by stocking all and sundry, is going for the specialist route. It is building an inventory of essential popular components from the top manufacturers with an emphasis on exclusivity. Among the lines for which it is sole stockist are Alp's (the world's largest manufacturer of electromechanical devices) and Toko (tops in wound components). Additionally, we're appointed distributors in the South of England for Cooper Tools/Weller, brand leaders in soldering and desoldering equipment. And we're not stopping there. We plan to secure other important franchises, with the accent on high-volume components and high technology, in such key areas as telecoms, defence, control and computers."

Cirkit is also stepping up its operations in the home-user and hobbyist market, through both mail order and across-the-counter outlets. New kits coming on to the market include, in the 'expert' category, a 20W, 144MHz linear power amplifier for boosting the output of hand-held and transportable transceivers, but also, in the 'enthusiast' bracket, a universal audio function generator with on-board mains PSU.

The aspiring 'student' class is not forgotten either. Kits in this area range from a universal temperature sensor which can be used as a frost warning, deep freeze alarm, greenhouse temperature alarm, etc., to a locomotive sound generator with whistle—a boon and a blessing for the rabid thwarted railwayman who has turned his attic into a miniature Clapham Junction.

While Ronnie and Robert Bulgin keep a watchful eye on Cirkit as non-executive members of the board, they have mustered a whole new team to steer the company on its adventurous path. Spearheading the team is Chief Executive Christopher Sawyer, a born leader who has achieved a particularly successful track record with such giants as BMW, Smedleys and the Ross Group. Alongside him is Financial Director, Ronald McKellar CA, who like Sawyer is in his late 30's and has an equally impressive business background.

Chairman of Cirkit is Alistair McDonald, who brings not only a wealth of experience in top-level financial-type appointments, but also, I suggest, an odour of sanctity. He spent many years with the Church Commissioners, juggling, as investment secretary, with assets in excess of £1,000 million. Another stalwart is ex-sailor Ken Hollingsworth. He came into electronic component distribution in 1963 and has become rich in the knowledge of the game. Then there's Jonathan Burchell, who is master-minding the introduction of Cirkit's new range of kits. Richard Bulgin (Consumer Services) and Clifford Bulgin (Manufacturing Services) represent the third generation of the founding family.

"We have tried," said Ronnie Bulgin, "to bring a real breath of fresh air into the business of electronic component distribution. This is typified by the appointment of Alistair McDonald, who, in spite of coming into this sector for the first time, is generating enormous enthusiasm.

"When you think about it, this kind of consolidation of several distribution companies into one is plain commercial common sense. Promoting each of them individually, with their own advertising and catalogues, as well as premises, is not only hideously expensive, but inefficient."

There's one point the Bulgins underline in bold capitals: In spite of its expansion in the distribution area, it remains firmly in the business of manufacturing, with a main factory at Barking (Essex) and a satellite at Broxbourne (Herts). In fact, in the current year the ratio will be 40 per cent distribution to 60 per cent production, turning out components for internationally-known customers with products ranging, as Robert Bulgin said, from toasters to telecoms, robots to oil rigs.

With an eye, no doubt, on future manpower needs, Cirkit has put $\pounds 100,000$ into an electronics award scheme for young people at schools and colleges. They must make and design an electronic device with a viable application in everyday life—for business or leisure. Any design taken up will attract royalities and, what's more, the company will help with patenting.

Arthur Bulgin would, I'm sure, be delighted to see what that valve display, cheek-by-jowl with the lamb chops, has led to in the space of one man's lifetime. But would he, like me, have some doubts about that name Cirkit? It's certainly ingenious. But, because I've that sort of mind, I can't for the life of me resist adding 'and see'.

And I'm sure we shall.

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B

In April 1982 PE, in conjunction with RT-VC, published the PE Quasar Stereo Cassette Deck design. The design provides some outstanding features, including variable recording bias and a gate noise reduction system. As a result of this successful design we are now pleased to be able to offer kits to PE readers at this exclusive price. The kits, including a wrap round simulated wood finish case, will be accompanied with a reprint of the PE articles which fully describe the unit and its construction.

The offer is for a limited period only and a coupon should be sent to the address shown. The specification of the made-up unit is given below:

SPECIFICATION

Case size 2B5 x 260 x 90mm approx.
Mechanism with automatic stop and tape counter with reset
button.
Tape Speed: 4.76cm/sec. (17in/sec.).
Wow & Flutter: Typically 0.1%.
Drive Motor: 12V d.c. with electrical governor.
Play Torque: 40-75g/cm (DYNAMIC).
Rewind & Fast Forward Torque: 60-140g/cm (STATIC).
Rewind & Forward Time: Less than 100 sec. for C60 tapes.
Blas/Erase Oscillator: Externally variable, frequency
90-100kHz.
Output: (Adjustable) Up to 1 volt r.m.s.
Mic. Sensitivity: 1mV @ 47k.
DIN Sensitivity: 30mV @ 47k.
Frequency Response; 30Hz-12.5kHz (-3dB).
Signal to Noise Ratio.
Noise reduction OFF -50dB
Noise reduction H.F56dB
Noise reduction FLAT -70dB
Cross Talk: Typically –50dB.

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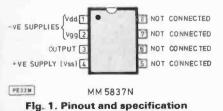
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SEMICONDUCTOR CIRCUITS TOM GASKELL BA (Hons) C Eng MIEE

DIGITAL NOISE SOURCE (MM5837N)

AT first sight it might seem a little odd that someone should produce a special i.c. to generate noise. After all, noise is something that we usually strive to get rid of in electronics! Indeed, there are very few areas of the business where noise doesn't regularly cause problems. Video noise results in the familiar 'snow' on pictures, digital noise (especially when carried along power supply tracks) can cause spurious changes in logic states, corrupting data or even causing serious malfunction of equipment. Audio engineers face a continuous battle against noise in microphone amplifiers, record deck pickup cartridge pre-amps, and most of all in cassette and reel-to-reel tape recorders. where noise reduction systems such as Dolby or dbx are often used specifically to try to overcome limitations of the medium.

So why should anybody actually want to produce noise? One reason is connected with the sort of problems that have just been described. In order to test out designs of noise reduction equipment, and to test the ability of circuits to reject noise or remain unaffected by it, we need to have a reliable source of noise, probably at a relatively high level initially so that it can be reduced and controlled to produce exactly the effect that we require. Noise is, by definition, a random or semirandom type of signal, and the very unpredictability of it can prove useful in generating random numbers in digital systems, or random voltages in analogue systems. Many digital communication links are tested with digital noise sources to simulate a wide range of signal types. Analogue noise has applications in sound synthesis, forming the basis for many types of sound or sound effect. It's used in speech synthesis to model the hissing sounds made by the human mouth and vocal tract, and it's also used extensively in audio systems to allow the precise adjustment of tone controls or graphic equalisers when optimising the response of a sound system in any given room or building.



GENERATING NOISE

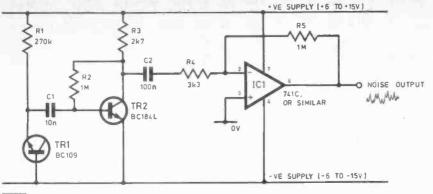
We've already considered the fact that noise is a random type of signal. It contains no steady frequency components at all which are discernible; in fact, a perfect broadband noise signal can be considered to simultaneously contain all possible frequencies from d.c. up to infinity, the net result of which is a wavepattern in which no repetitive waveforms or tones can be distinguished. This is the type of signal produced by natural phenomena within all normal semiconductor materials, albeit at an extremely low level. In very high gain amplifiers this noise is amplified along with the signal, resulting in unwanted background 'hiss'.

One technique for generating noise uses exactly this effect, and is shown in Fig. 2. TR1 is connected as a reverse biased diode with its collector open circuit. The tiny noise current generated by this is amplified, first by TR2 then by the op-amp IC1. This technique is simple, and usually works fairly well, although different types of noise, i.e. noise signals with different amplitudes at different parts of the spectrum; a lack of low frequency components in the noise, an excess of mid-band components, or similar. A more consistent performance can be obtained by using a special noise generating Zener diode, such as the Z5J, with a suitable high gain amplifier configuration, although these tend to be quite expensive. There can also be problems with mains hum and interference, or instability, caused by the very high gains of the circuitry involved, so struction of this type of system.

A DIGITAL NOISE SOURCE

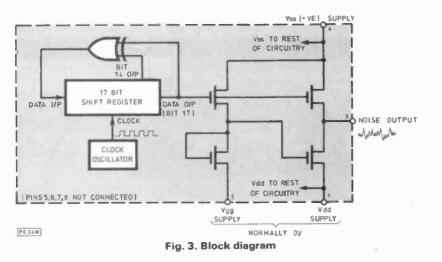
An alternative way of generating noise uses an 'artificial', rather than an amplified natural effect, and this is the basis for the i.c. featured this month, the MM5837N from National Semiconductor. It's a digital technique using a clocked shift register with multiple feedback paths. Fig. 3 shows the block diagram of the i.c. An internal high frequency clock oscillator is used to continuously clock a 17 bit shift register. The shift register acts as a 17 bit 'first

Characteristic	Notes	Minimum Value	Typically	Maximum Value	Units
V _{dd} supply ∨oltage	With respect to +ve supply pin, V _{s9} (pin 4).	$V_{ss}-25$	V _{ss} -14	V _{ss} +0·3	V
V _{gg} supply voltage	(absolute maxima)	$V_{ss}-33$	$V_{ss}-27$	$V_{ss} + 0.3$	V
+ve supply V _{ss} for normal operation	V _{dd} and V _{gg} connected to OV	+8.9*	+14	+25	v
Temperature range		0		+70	°C
For all following spec's	$V_{gg} = -14V, V_{dd} = -14V, V_{gg} = -27V$				
Quiescent current, V _{dd} (pin 1) Quiescent current, V _{gg} (pin 2)	No output load	3		8	mA mA
Half power point		24		56	kHz
Cycle time		1.1		2.4	S
Output voltages:	(pin 3 loaded with 20k to V_{ss}) and 20k to V_{dd}				
		V _{ss} -1·5 V _{dd}		V _{ss} V _{dd} +1·5 V _{dd} +3·5	V



PE33M

Fig. 2. Analogue noise generator



in first out' storage device. Any data bit present at its input when the clock is pulsed will emerge at the data output pin exactly 17 clock cycles later. In this respect, the action of the shift register can be likened to pushing coloured balls into a length of tubing one by one; they emerge at the other end, one by one, a fixed time later, in exactly the same order that they were pushed in. If the output of the register is connected back to its data input, whatever pattern of logic 1's and 0's is present within the register at that time will be 'rotated' continuously round the register without changing.

If we look at other points within the shift register, such as the 14th bit rather than the output (the 17th bit), we see the state that the input was at 14 clock cycles earlier, rather than 17 cycles earlier as would be the case when looking at the output. If the 14th and 17th bits are combined together with some logic gating, such as an exclusive-OR gate, we can feed a rather oddly derived signal back to the data input of the shift register. The mathematics of the resultant effect are very complex, but the practical end result is an almost purely random digital signal, known as a 'pseudo-random' signal. In fact, it's not perfectly random; it does have a pattern which repeats, but only every 1.1 to 2.4 seconds, which only affects very low frequency applications of the noise source. Various different lengths of shift register, and various combinations of output bits in multiple feedback paths

could be used, but the arrangement employed by the MM5837N is quite satisfactory for most applications.

OUTPUTS FROM THE I.C.

The i.c. is fabricated using PMOS technology, hence the unusual approach of calling the positive supply V_{ss}, and the negative supply V_{dd}. A third, optional supply Vgg can be biased somewhat more negatively than V_{dd} if required, which will slightly improve the output drive voltage available from the MOSFET output buffer stage connected to pin 3. The manufacturer's specifications are based on using the i.c. in a positive earth arrangement, with OV connected to pin 4, -14V connected to pin 1, and -27V connected to pin 2. This would cause considerable headaches in most potential applications, so it is quite acceptable to simply connect pin 4 to the positive supply rail (+8.9 to +25V) and connect both pins 1 and 2 to 0V. This will give an acceptable output voltage swing for most purposes. Note that the lower voltage limit given in the specification, Fig. 1, is derived from experimentation on the prototype applications circuit, not by the i.c. manufacturers, who don't give a lower limit figure. As the voltage drops to around this figure, the noise spectrum becomes somewhat irregular, and below this voltage fixed frequency tones start to become discernible within the noise.

The clock oscillator is completely selfcontained, so no external timing components are needed. This is a high frequency oscillator, and it totally defines the maximum high frequency limit of the output noise spectrum. The 'half power' (i.e. the -3dB) point is specified as between 24 and 56kHz, inferring that the clock frequency must be around 100kHz or more.

NOISE COLOUR

The spectral content of a noise signal is referred to as its colour. There are actually many colours of noise: red, blue, etc., but the two types most commonly used are white and pink.

White noise is characterised by having equal energy per constant bandwidth in the spectrum. For example, the energy content in the region between 1kHz and 2kHz will be the same as that between 2kHz and 3kHz, because in each case the bandwidth is 1kHz. The energy content will rise by 3dB per octave, however, with an octave being defined as a doubling of frequency, so the energy contained in the region 2kHz to 4kHz will be 3dB higher than that in the region 1kHz to 2kHz. The converse is true for pink noise, which has an equal energy level per octave, i.e. a 3dB loss in amplitude per constant bandwidth. For example, the energy content in the region 2kHz to 4kHz will be 3dB LOWER than that in the region 1kHz to 2kHz. Put very simply, white noise is a very hissy sound with high frequency content, whereas pink noise is a duller 'wooshing' low frequency content sound.

For audio use, especially in the setting up of graphic equalisers and sound systems, the use of pink noise is widespread. Graphic equalisers are usually calibrated in octaves, with each control cutting or boosting the audio signal in a narrow octave wide, or even one third of an octave wide band. Using pink noise ensures that the mean signal amplitude will be the same in each band of the equaliser, allowing the spectral response of the system to be tailored precisely.

CONVERTING WHITE NOISE TO PINK

The MM5837N produces only white noise, so to obtain a pink noise signal we must include filtering to roll off the amplitude at the rate of 3dB per octave. Unfortunately, even a simple resistor/capacitor network has a 6dB

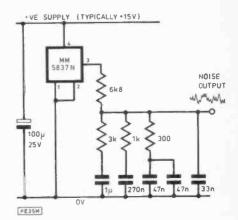


Fig. 4. Simple pink noise generator

per octave slope, and most active filters are designed for 12, 18, or more dB's per octave. The easy solution is to cascade several resistor/capacitor networks together, each set to different turnover frequencies in such a way that they combine to produce an overall slope of only 3dB per octave. National Semiconductor's recommended network to do this for the MM5837N is shown in Fig. 4. Note that the output of this circuit has a fairly small amplitude for the actual noise signal (only about 1V peak-to-peak) but this rides on a d.c. level of just over half the supply rail voltage, and as a result will normally need a series decoupling capacitor prior to feeding into any other circuitry.

APPLICATIONS

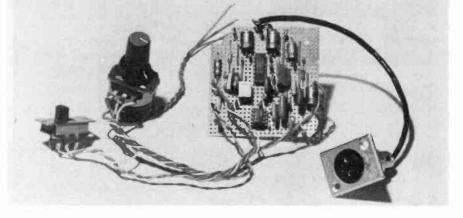
The i.c. can be used either as a purely digital noise source, or with suitable filtering or amplification as an analogue noise source. Consider using the i.c. output to feed into a CMOS serial in/parallel out register's data input, then clock that register with a regular slow clock pulse. The output from the register will be a pseudo-random digital number. If fed into a digital to analogue converter, this would produce a regularly changing random analogue voltage, ideal for use in experimental sound or music synthesis. Other potential uses for a noise generator have already been discussed, but perhaps the most obvious is the basis for this month's applications project.

The MM5837N can be obtained from Alpha Electronics, 66 Wilbury Way, Hitchin, Herts, SG4 0TT.



T HE circuit diagram for a useful audio noise generator is shown in Fig. 5. It can produce either white or pink noise, and has a variable level control to adjust output amplitude. The output is electronically balanced, making it ideal for feeding into balanced inputs to audio equipment, although it can equally well be used with unbalanced systems.

One of the attractions of the MM5837N is that it is very compact, replacing several larger i.c.s with an 8-pin d.i.l. package. The circuit, and its Veroboard layout, have been designed to take advantage of the small size of the device, and the result is a very small assembly, ideal for building into a hand-held case. R1, R4, R5, R6, R7, R8, C2, C3, C4, and C5 form the passive network which derives pink noise from the i.c. output. The more difficult to obtain values in Fig. 4 have been replaced by different quantities of other values of component to make it easier to build with full accuracy. The pink noise is amplified up to a reasonably high level by IC2a, then fed to the selector switch S1. The white noise feed to S1 comes from the output of IC1, decoupled by C1, attenuated and biased to 0V by R3, and with a high frequency roll-off provided by R2 and C7 to help reduce high frequency components in the waveform caused by the sharp edges of the digital output of IC1. IC2d buffers the output of the level control VR1, and feeds into the two halves of the balanced output, IC2b and IC2c. These are connected to give EXACTLY the same gain, one with signal inversion, and the other without. The two outputs are thus symmetrical, i.e. mirror images, about 0 volts. This



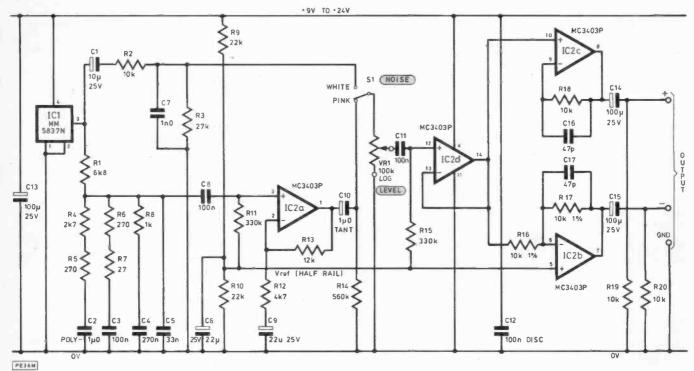


Fig. 5. The Audio Noise Generator

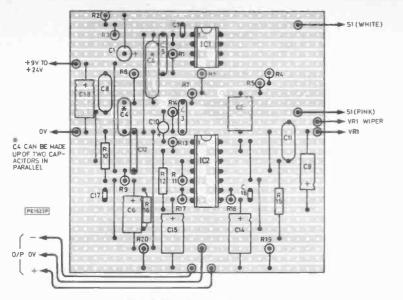


Fig. 6. Veroboard layout

can directly drive into balanced inputs of audio equipment, while for conventional unbalanced systems, either output, positive or negative, can be used, with the common 0V or ground connected as the return. C16 and C17 roll off the response of the amplifiers at high frequencies to keep them stable, and R18

helps to match the response of IC2c as closely as possible to that of IC2b. Ideally, the values of R16 and R17 should be within 0.1% of *each other*, although the actual value itself is not as critical. Practically, 1% is adequate for most systems, and even 5% resistors can be used if non-critical uses are anticipated.

BIAS POINT

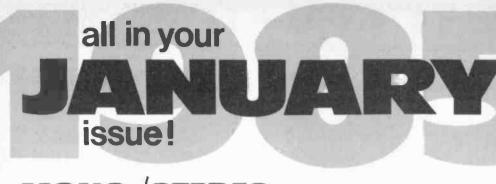
Components R9, R10, and C6 provide a half-rail bias point for the op-amps, C12 and C13 decouple the supply, C14 and C15 decouple the outputs to block d.c. from the opamps, and R19 and R20 bias the outputs to OV. The output impedance is very low indeed, so if a 600 ohm output impedance is required, then a pair of 300 ohm resistors, again matched to within 0.1% or 1% of each other, should be added in series with the +ve and -ve outputs. The relative amplitudes of pink and white noise can be adjusted by varying the gain of IC2a (change R12 or R13) or the attenuation of the white noise source (change R3), although care must be taken to avoid clipping of the waveform, which could be checked for on an oscilloscope. Many quad op-amp packages could be used for IC2, but some do not work well on low supply voltages in the voltage follower mode. The MC3403P does work well under these conditions, although if supplies of 18V or more (or +/-9V) are used the TL074 offers better high frequency performance, and is to be preferred.

The noise generator can be used for testing noise rejection or reduction systems, for setting up graphic equalisers or room equalisers, or even for generating special effects. Its compactness is largely due to the diminutive MM5837N, and shows a typical use for this digital noise source i.c.

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RICHARD B. H. BECKER — SYSTEM DESIGN AND MECHANICAL ENGINEERING. TIM ORR — COMPUTER INTERFACE AND CONTROL ELECTRONICS.

neptune and mentor

THIS SERIES is concerned in part four with the control electronics and mechanical construction of the MENTOR robot. Like the NEPTUNES, MENTOR interfaces directly to any one of three popular home computers and provides the facility for control either from the keyboard, or by making use of the "learning arm".

PRINCIPLES OF OPERATION

MENTOR is a 6-axis servo controlled electric robot (Fig. 4.1). Whilst the NEPTUNEs are designed for industrial use, MENTOR is primarily intended for educational purposes but as can be seen from the specification (Table 1) it too has the repeatability for many commercial applications, where the load is light. Each of the axes is powered by a small d.c. servo motor with integral gearbox with a large reduction ratio.

Axis \emptyset is the centre column which rotates in a nylon bearing in the top plate of the base of the robot and is powered by the motor through an additional pair of gears (Fig. 4.2). The radial position of the column is sensed by a conductive polymer potentiometer fitted to the underside of it. The column is hollow to enable the cables to pass down it to the computer interface board.

PART FOUR

Axis 1 is the lower section of the arm which rotates about an axle fitted to the centre column (Fig. 4.3). Again a pair of gears is used to transfer torque from the motor to the axle and a potentiometer provides the feedback information. The axle is hollow for the cables to pass through it.

Axis 2, the fore arm is driven in the same manner as axis 1. A large piece of steel at the back end of the lower arm section counterbalances the weight of this section of the arm making the robot's position stable when there is no power applied to it;

AXISØ (centre column)	Angular movement 210°. Axle centre 170mm above top of base.	CONTROL SYSTEM	All axes serve controlled with servoing performed by the control electronics
AXIS 1 (shoulder)	Angular movement 180°. Arm length between axle centres 165mm.		Position defined by 8 data bits giving angular resolution of 0.4%.
AXIS 2 (elbow)	Angular movement 230°. Arm length between axle centres 150mm.	COMPUTER INTERFACE	Parallel. Robot addressed as if part of computer memory. Connects to expansion port
AXIS 3* (left wrist axle)	Angular movement 320°.		(1MHz bus on BBC).
AXIS 4* (right wrist axle)	Angular movement 320°.	SOFTWARE	Accepts commands in BASIC
WRIST PITCH	Angular movement 140°.		or machine code.
WRIST ROTATION	Angular movement 320°.	SOFTWARE PROVIDED	Extensive package of BASIC
AXIS 5 (gripper)	Jaw opening 30mm. Jaw		programs including direct
13. FF	pressure 10 newton. Distance		control from computer
	from end of jaws and axis 3		keyboard, control by
	and 4 axles 105mm.		simulator, sequence storing,
REPEATABILITY	2mm.		replay, sequence editing,
LIFTING CAPACITY	300gm.		sequence storage on disc or
REACH (from axis 1 axle	420mm.		tape, multi-speed control and
centre)			graphical illustration of robot
BASE DIMENSIONS	Overall width 320mm.		dynamics.
	Overall depth 270mm.	*AXIS 3, 4 movements are	combined to provide wrist pitch
	Overall height 189mm.	and wrist rotation.	

Table 1. MENTOR SPECIFICATION

ROBOTICS PROJECT

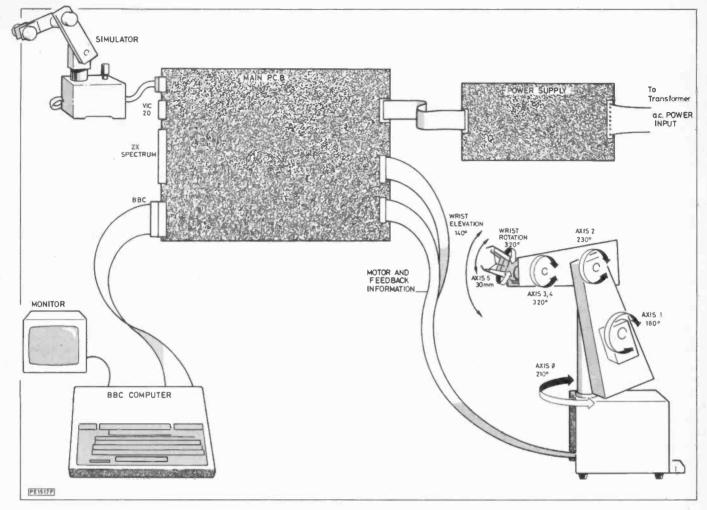


Fig. 4.1. Exploded view of the complete MENTOR system

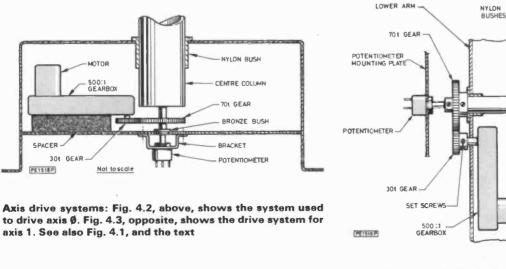
therefore the drive motor does not have to be continuously on to keep the arm in position. There are also counterbalance weights on the fore arm to balance out the wrist motors.

Axes 3 and 4 are the wrist drive motors. The movements are combined to provide wrist elevation and wrist rotation by means of a set of bevel gears. When axes 3 and 4 advance together the

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MOTOR

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wrist rises. When the axes move in equal and opposite directions the elevation remains constant but the wrist rotates, as shown in Figs. 4.4 and 4.5.

Axis 5 is the gripper which is also servo controlled. To keep down the weight at the front end of the fore arm the drive motor for this is fitted to the lower arm with the power applied to the jaws via a flexible cable consisting of a nylon-coated multistrand steel wire inside a spiral wound PVC-coated conduit. This is similar to the brake cable of a bicycle. Torsion springs open the jaws when tension is released on the cable. Being servo controlled it is possible to program different degrees of jaw closure. If the programmed position is for a gap between the jaws of less than the size of the object to be handled then the grip will depend on how much further the servo system is trying to move the jaws.

THE MENTOR CONTROL SYSTEM

The MENTOR, like the NEPTUNE, can be connected to one of three popular computers, the Commodore VIC20, the Sinclair ZX Spectrum and the BBC. When the computer sends the robot new data it responds by moving to a position where the feedback information from the potentiometers exactly matches the information from a DAC (digital-to-analog converter) controlled by the computer. This process is called servoing. Each of the 6 axes of movement can be defined with 8-bit (1 part in 256 or 0.4%) resolution.

ROBOT CONTROL

To move each of the axes the computer sends (WRITEs) 6 bytes of data to the robot, one to each axis. The servoing is performed by dedicated hardware on the computer interface board of the robot. In a typical movement of the robot, the computer

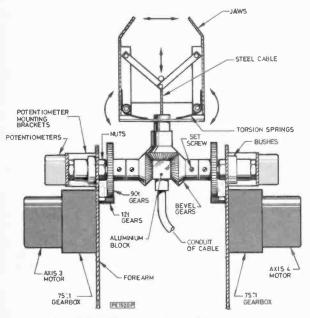


Fig. 4.4. Wrist and gripper drive system

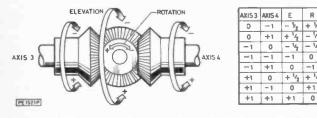


Fig. 4.5. Wrist operation showing effect of one-bit change on axes 3 and 4

sends 6 new bytes of data to the robot. The computer is then writing to what it "thinks" is just another area of its memory. The interface board then stores these 6 bytes of information and converts them to 6 position control voltages. The analog electronics on the interface board then compares these voltages with the feedback voltages. The difference between these two voltages is delivered to high power operational amplifiers which drive the motors in the direction which reduces the difference to zero. This is the servoing process.

FEEDBACK

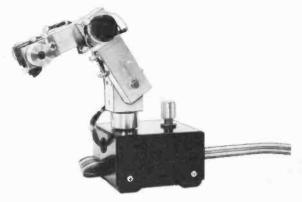
The computer is not involved in the servoing, it is merely generating data and relying on the robot to follow the instructions correctly. However the computer can perform a READ of any or all of the 6 feedback voltages and can thus be aware of all the movements of the arm. This process can be used to delay sending the next set of co-ordinates until the previous set of coordinates have been reached or for varying the speed of the robot during playback of a sequence. It is also easy to produce a graphical display of the movements and observe the effect of varying loads and of alterations to the components determining the characteristics of the servo system.

The computer can also READ the position of the simulator, which is a small hand operated model of the robot which can be plugged into the learn axis input CN400. The computer READS the position of the simulator and then WRITES this data into the robot. The arm follows the real-time motion of the simulator. The movements are stored in the memory of the computer and can be transferred to tape or disc for later use.

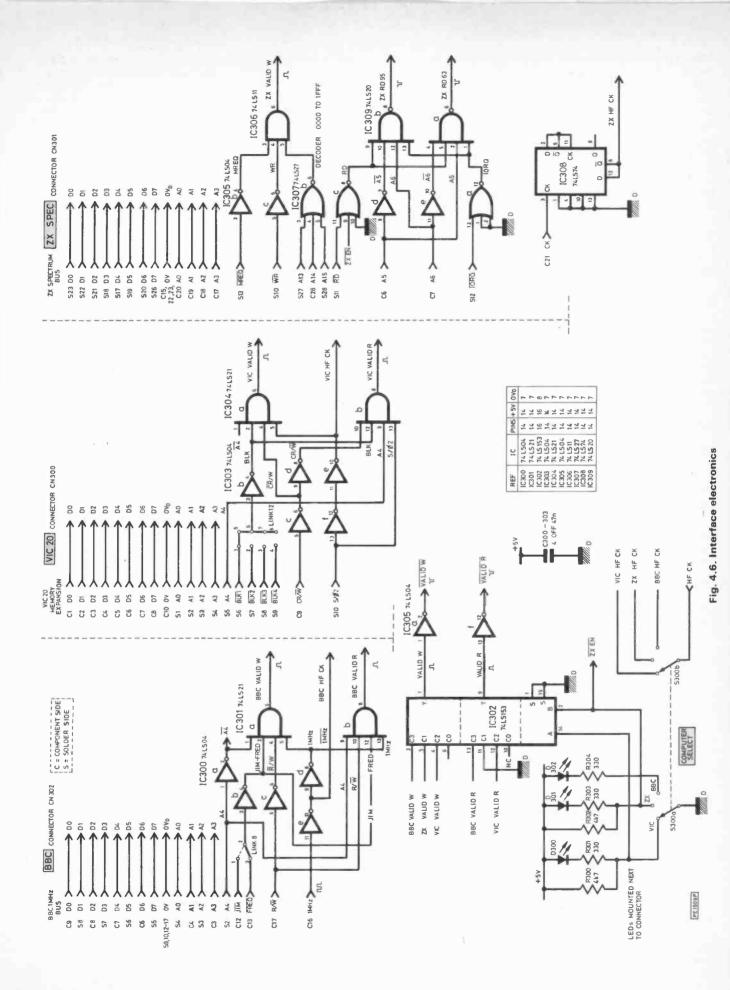
COMPUTER INTERFACES

All three computers make available various signals for external use. See Fig. 4.6. These include the data bus $D\emptyset$ to D7, some of the lower address lines, READ and WRITE signals, the system clock and on the VIC20 and BBC a memory block decode. The BBC and VIC20 both use the 6502 microprocessor, so have relatively similar decoding electronics. The Spectrum uses a Z80. This together with some unusual practices in the Spectrum design makes interfacing somewhat different.

All the data and address lines are hard wired together so if two computers were simultaneously connected a bus clash would occur. However there is no reason why two or more robots should not be daisy-chained together either to perform the same task or completely different tasks if a different address is used. For example, using a BBC Computer one robot could be addressed with the link-selected "FRED" decode (FCØØ-FCFF) whilst the other is addressed with the link-selected "JIM" decode (FEØØ-FEFF). All three interfaces generate a VALID WRITE signal. The Spectrum WRITES to the block ØØØØ-IFFF which is where the ROM resides and as ROMs are not normally written to no conflict occurs. The other computers WRITE to nonexistent areas of RAM. IC302 is a multiplexer which selects which interface will generate the READ and WRITE signals. The Spectrum interface generates two READ signals (ZXRD63 and ZXRD95). These are not passed through the multiplexer but are used later on in the electronics to perform direct READs.



The MENTOR simulator (learning arm)



55

WRITING

Data is sent to the robot as 6 bytes, each with a different address. See Fig. 4.7. Six latches are used to store this data (IC104, 107, 109, 112, 114, 117). These are individually clocked with the address decodes from IC102, which is driven by the lowest three address lines. A typical WRITE is as follows. In one computer instruction cycle, the data is set up on the data bus, a VALID WRITE signal is generated and the LSBs of the address bus are set to the correct axis address. When the VALID WRITE signal goes high the data is written into the latch corresponding to that axis. The other axes are then written to similarly. The data held in the latches is converted into position control voltages by the DACs (IC105, 108, 110, 113, 115, 118). All the data passes through IC 100 which is a 74LS245 bus transceiver. When the DIRection signal is low, data passes from B to A (WRITE mode). When the DIR signal is high, data passes in the other direction from A to B (READ mode).

READING

There are two ADCs in the system, IC400 and 402 as shown in Fig. 4.8. These are 8-bit devices with integral 8-way analog multiplexers and integral 3-bit address latches. IC400 is used to look at the feedback voltages VFBØ to VFB5 and the other looks at the learn axis (simulator) signals VLØ to VL5. The input voltages to the analog multiplexer must not exceed the ADC

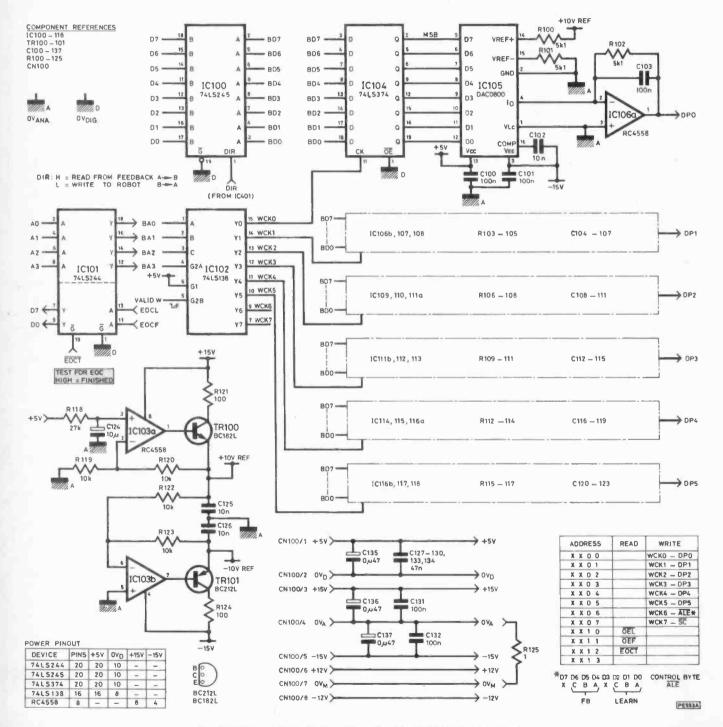


Fig. 4.7. The electronics concerned with a "Write" operation

supply rails. To prevent this from occurring diode and transistor clamps are used wherever higher voltages could possibly arise.

A READ is performed as follows. The axis channel is selected by writing a 3-bit code ($BD\emptyset$, 1, 2, and BD4, 5, 6) into the ADCs. The ALE (Address Latch Enable) signal latches these codes into the ADC internal registers. These codes select the multiplexer position. A start conversion signal is then generated. The ADC takes about 100 microseconds to perform the conversion. A test can be made for the end of conversion (EOC). By generating the EOCT signal both EOCs can be tested as both ADCs convert at the same time. A high indicates end of conversion (see IC101). When the conversion is complete then one or both of the ADCs can be READ. The ADCs have tri-state outputs which are enabled by the OEF and OEL signals (active high). The READ is then performed by generating a VALID READ signal and an address code to select one of the two ADCs. This enables the tri-state output of the selected ADC and also generates a DIR signal which sends data out from the interface board onto the computer's data bus. The computer can then READ the axis data.

NEXT MONTH: MENTOR construction

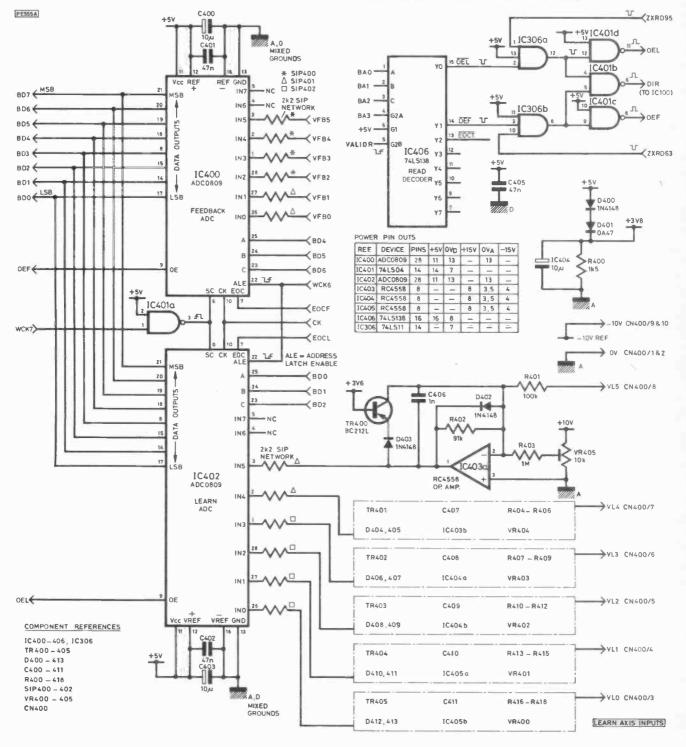
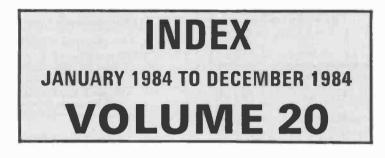


Fig. 4.8. Analog-to-digital conversion for a "Read" operation





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	NSI6032June Z800July
Robotics by Tom Iva	allNov

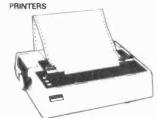
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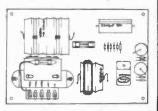
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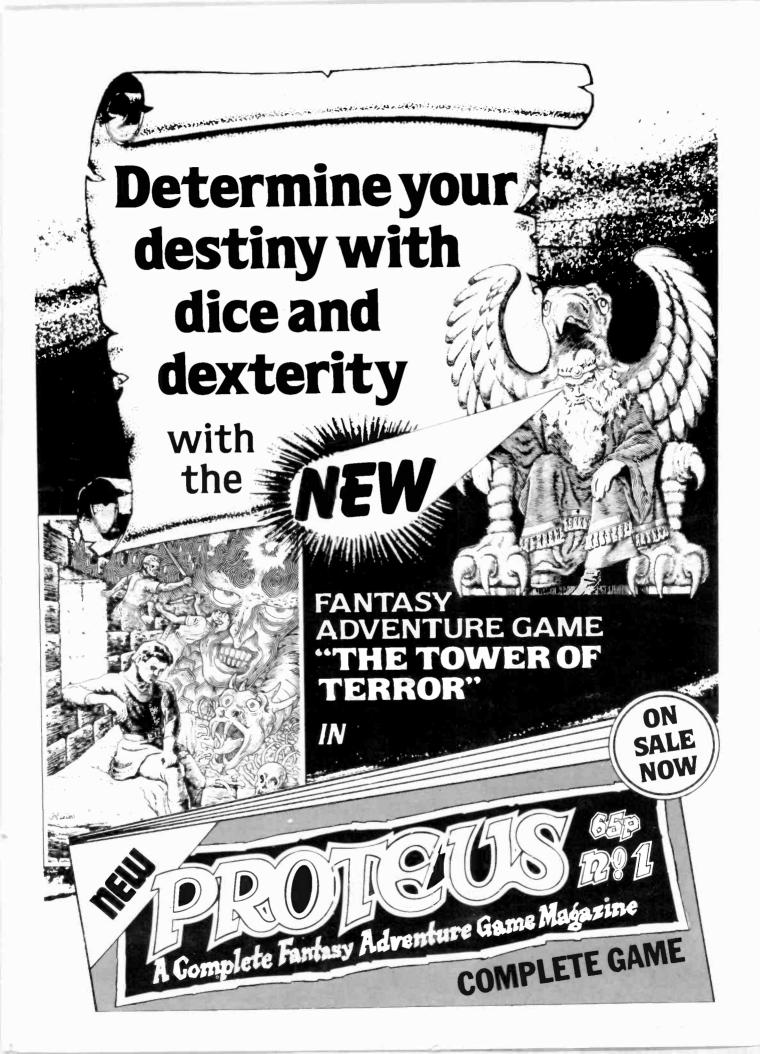
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