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### DECEMBER 1976

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270 MEDIUM WAVE DX SUPERHET-Part 1 **DECEMBER 1976** by A. P. Roberts Volume 30 No. 5 WIDE BANGE A.C. MILLIVOLTMETER 276 by B. S. Wolfenden Published Monthly (1st of Month) First Published 1947 280 NEWS AND COMMENT 282 OCTAL AND BINARY Incorporating The Radio Amateur by D. Sheffield Editorial and Advertising Offices MAINS CURRENT MONITOR 284 57 MAIDA VALE LONDON W9 1SN (Suggested Circuit 313) by G. A. French 285 TRADE NOTE --- New Compact Telegrams Databux, London Satellite Navigator © Data Publications Ltd., 1976. Contents SHORT WAVE NEWS-For DX Listeners 286 may only be reproduced after obtaining by Frank A. Baldwin prior permission from the Editor, Short abstracts or references are allowable 288 CONSTANT CURRENT AUDIO AMPLIFIER provided acknowledgement of source is -A Quality Battery Design by R. A. Penfold Annual Subscription: £5.00 (U.S.A. and Canada \$11.00) including postage. Remit-292 NOVEL L.E.D. STEREO BEACON tances should be made payable to "Data by R. N. Soar Publications Ltd". Overseas readers please pay by cheque or International Money 294 THE CA3130 COS/MOS OP-AMP by J. B. Dance Technical Queries. We regret that we are unable to answer queries other than 297 PHASE LOCKED LOOP F.M. TUNER --- Part 2 those arising from articles appearing in by R. A. Penfold this magazine nor can we advise on modifications to equipment described. 300 IN YOUR WORKSHOP-ELECTRONIC DICE We regret that such queries cannot be answered over the telephone; they must be submitted in writing and NOTES FOR NEWCOMERS-PINS AND LEAD-OUTS 306 accompanied by a stamped addressed by F. T. Jones 308 WORKSHOP AIDS Correspondence should be addressed to the Editor, Advertising Manager, Sub-MOMENTARY POWER FAILURE INDICATOR 309 scription Manager or the Publishers as by J. Knapp 312 **RADIO TOPICS** Opinions expressed by contributors are by Recorder not necessarily those of the Editor or iii ELECTRONICS DATA-No. 17 (For the Beginner-Germanium and Silicon Production .---- Web Dffset.

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Goods sent at customer s risk, unless suficient payment for rep post) or compensation fee (parcel post) included.	gistration (1 st class letter	JAP 4 gang min. sealed, tuning condensers New 35p
VALVE BASES Printed circuit B9A-B7G 5p Chassis B7-B7G 9p Shrouded chassis B7G-B9A-B8A 10p B12A tube 9p Speaker 6" x 4" 5 ohm ideal for car radiof1.25 TAG STRIP - 6 way 3p 5 x 50pF or 2 x 220pF 9 way 5p Single 1p trimmers 20p BOXES - Grey polystyrene 61 x 112 x 31mi self tapping screws 321p Clear perspex sliding lid, 46 x 39 x 2 ABS, ribbed inside 5mm centres for P.C.B., 1 screw down lid, 50 x 100 x 25mm orange 48p black 70p; 109 x 185 x 60mm black £1,10	24mm <b>10p</b> brass corner inserts,	ELECTROLYTICS MFD/VOLT. Many others in stockin stock70-200-300-450- Up to 10V 25V 50V 75V 100V 250V 350V 500VMFD104p5p6p8p10p12p16p20p254p5p6p8p10p15p18p20p504p5p6p9p13p18p25p-1005p6p10p12p19p20p2509p10p11p17p28p-85p£150010p11p17p24p45p200023p37p45pAs total values are too numerous to list, use this price guide to work out your actual requirements
ALUMINIUM         8 $3'' \times 2'' \times 1''$ <b>39</b> $4'' \times 2\frac{1}{2}'' \times 2''$ <b>44</b> 10 $2\frac{3}{4}'' \times 5\frac{1}{4}'' \times 1\frac{1}{2}''$ <b>45</b> $4'' \times 5\frac{1}{4}'' \times 1\frac{1}{2}'''$ <b>45 65</b> 10	"x6"x3" £1.02 "x4 <u>4</u> "x3" £1.02 "x5"x3" £1.02 "x7"x3" £1.20 "x7"x3" £1.22 "x8"x3" £1.50	8/20. 10/20, 12/20 Tubular tantalum 20p each 16-32/275V. 100-100/150V. 100-100/275V 30p; 50-50/385V. 12,000/12V, 32-32-50/300V, 20- 20-20/350V 60p; 700 mfd/200V £1.00; 100-100- 100-150-150/320V £2.00.
SWITCHES	RESISTORS	RS 100–0–100 micro amp null indicator Approx. 2" x $\frac{3}{4}$ " x $\frac{3}{4}$ " £1.50
PoleWayType42Sub, Min. Slide18p62Slide20p22Slide14p1313 amp rotary6p	1 watt 2p Up to 15 watt wire wound 6p	INDICATORS Bulgin D676 red, takes M.E.S. bulb 30p 12 volt or Mains neon, red pushfit 18p R.S. Scale Print, pressure transfer sheet 10p
2 2 Locking with 2 to 3 keys £2.00 2 1 2 Amp 250V A.C. rotary 28p Wafer Rotary, all types 30p S.P.S.T. 10 amp 240v. white rocker switch with neon. 1" square flush panel fitting 46p S.P.S.T. dot 13 amp, oblong, push-fit, rocker 20p AUDIO LEADS 5 pin din plug 180° both ends 1½ Mtr., 80p 3 pin din to open end, 1½ yd twin screened 35p	Cinch 8 way std 0.15 pitch edge connector20p Semiconductor Data Book 263 pages. Covers 2 N 2 1 through to 2 N 5558 plus some 3 N's. Type/connection/ parameter details £1.50 No VAT POTS	CAPACITOR GUIDE – maximum 500V Up to .01 ceramic 3p. Up to .01 poly 4p. .013 up to .1 poly etc. 5p12 up to .68 poly etc. 6p. Silver mica up to 360pF 8p, then to 2,200pF 11p, then to .01 mfd 18p. 8p1/600: 12p01/1000, 1/350, 8/20, .1/900, .22/900, 4/1625/250 AC (600vDC) .1/1500 .40p. 5/150, 9/275AC, 10/150, 40/150. Many others and high voltage in stock.
Phono to Phono plug, 6ft.       35p         COMPUTER AND AUDIO BOARDS         VARYING PANELS WITH ZENER, GOLD BOND,         SILICON, GERMANIUM, LOW AND HIGH POWER         TRANSISTORS AND DIODES, HI STAB RESISTORS,         CAPACITORS, ELECTROLYTICS, TRIMPOTS, POT         CORES, CHOKES ETC.         3lb for 85p + 85p post and packing         7lb for £1.95 + £1.20 post and packing         Skeleton Presets         Slider, horizontal or verti- calstandard or suffiging for         1" Terry Clips 4p	Log or Lin carbon 16p Switched 31p Dual Pots 45p Dual & switch 60p Lin wirewound 25p Slider Pot 35p Dual Slider 50p 1.5m Edgetype 8p THERMISTORS VA1008, VA1034, VA1039, VA1040, 100	FORDYCE DELAY UNIT         240 volt A.C./D.C. Will hold relay, etc., for approx.         15 secs after power off. Ideal for alarm circuits,         etc.       £1         CONNECTOR STRIP         Belling Lee L1469, 4 way polythene. 6p each         1 iglass fuses 250 m/a or 3 amp (box of 12)         Bulgin, 5mm Jack plug and switched socket (pair)         30p         Reed Switch 28mm. body length       5p         MAINS DROPPERS         36+79 ohm       25p
12 Volt Solenoid 30p         KNOBS         SILVER METAL PUSH ON WITH POINTER, OR         VHITE PLASTIC, GRUB SCREW WITH GOLD         CENTRE 8p EACH         ENM Ltd. cased 7-digit counter $2\frac{1}{4} \times 1\frac{3}{4} \times 1\frac{1}{4}$ "         Approx. 12V d.c. (48 a.c.) or mains 75p         ZM1162A INDICATOR TUBE         0-9 Inline End View. Rectangular Envelope 170V         2:5M/A         REGULATED TAPE MOTOR         9v d.c. nominal approx $1\frac{1}{4}$ " diameter         GOP	VA1055, VA1066, VA VA1082, VA1100 VA1077, VA1005, VA1026 15p RELAYS 12 volt S.P.C.O octal mercury wetted high speed 75p P.O. 30:00 type, 1,000. OHM coil, 4 pole c/o 60p Mains or 12v d.p.c.o heavy duty octal £1	$\begin{array}{c} 66+66+158 \text{ ohm, } 66+66+137 \text{ ohm} \\ 17+14+6 \text{ ohm, } 266+14+193 \text{ ohm} \\ 50+40+1k5 \text{ ohm} \\ 285+575+148+35 \text{ ohm} \\ 25+35+97+59+30 \text{ ohm} \end{array} \begin{array}{c} 30p \\ 40p \\ 25+35+97+59+30 \text{ ohm} \end{array}$
12v 8 amp Transformer £2.50 (p&p 85p) Ferric Chloride, Anhydrous mil. spec. 11b bag 40p	Boxed GEC KT88 valve £2	Sub-miniature Transistor Type 25p Valve type, 40p
THE RADIO SHA 161 ST. JOHNS HILL, BATTERSEA, LC Open 10 a.m. till 7 p.m. Monday to Saturday. V/	ACK	Transformers 6 volt $\frac{1}{2}$ A25pWhiteley Stentorian 3 ohm constant impedance volume control way below trade at 80pRS Yellow Wander Plug Box of 1225p18 SWG multicore solder2 $\frac{1}{2}$ p foot

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			20	2904/5/6/7/7A 15p 3053 14p	1N4148 2p BA14514p
AC107 AC128/176 8p BC557.'^/9 BCX32/'	12p   BFV	V30	£1 2N	3055 R.C.A. 50p	Centercel 24p BZY61/BA148 10p
ACY28 19p BCY40 AD149 40p BCY70/1/2	50p BFV 12p BF>		200 2N	3133 20p	BB103/110 Varicap 15p
AD161/2 33p BD112/3 AF116 161p BD115/6	50p BF)	(84/88/89		15036 (Plastic 2N3055)	BB113 Triple Varicap37p
AF124/6/7 25p BD131/2/3 AF139 20p BD135/7/9	35p 8F) 30p 8R	/90	50p 25	30p 5A141/2/360 31p	BA182 13p OA5/7/10 15p
AF178/80/81 30p BD142	30p BR	<b>/39/5</b> 6	260 25	5B135/6/457 20p 0250 (2N3054) 30p	BZY88 Up to 33 volt 7p
AF239 30p BD201/2/3/4 ASY27/73 30p BD232/4/5 BD232/4/5	49p 851	/79/80 F.E.T.s	80p		BZX61 11 volt 15p BR100 Diac 15p
BC107/8/9 + A/B/C 6p BDX77 BC147/8/9 + A/B/C/S 6p BF115/167/	173 15p BS	x20/21	14PI co	NEW B.V.A. VALVES	INTEGRATED CIRCUITS
BC157/8/9         +         A/B/C         6p         BF178/9           BC178A/B,         179B         12p         BF180/1/2/3           BC184C/LC         9p         BF194/5/6/7	/4/5 15p BS	195A	12P FC	391 34p CH81 34p	TAA700 £2.00
BC186/7 20p   BF194A, 19		7042 (0C41/44	SUD EC	LBO 36p 80 34p	723 reg (TO99) 45p 741 8 pin d.i.l. op.
BC213L/214B 10p BF200, 258 BC261B 8p BF202/3	20p / 30p GE	ASY63) T111	AOD EF	183 34p /86/7 34p	Amp TAD100 AMRF £1
BC327/8, 337/8 8p BF336 BC547/8/8A 10p BF528 Dual	27p OC Mosfet £1 ON	35 222	45P PC	286 53p	CA3001 R.F. Amp 50p
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Amp Volt BRIDGE RECTIFIE	HS ZT	(300/341	7P PC	CC84 34p CC89 45p	NE555V Timer 35p TAA550 Y or G 22p
1 140 OSH01-200	26p 2N	456A	50p PC	C189 45p CF80 34p	TAA263 Amp
1.4 42 BY164 0.6 110 EC433	40p 2N 15p 2N	987	4UP PC	CF82 34p CF801 46p	7400/10 9p 7402/4/20/3012p
5 400 Texas			15P PC	.82 34p	7414 56p 7438/74/86 24p
RECTIFIERS	BPX40 50p	LECTRONICS Photo transist	PY	7500A 80p 781/800 38p	7483 69p
Amp Volt	BPX42 80p	BPX29 8	30p . R2	20/U26 50p	LM300, 2-20 volt £1 74154 90p
IN4004/5/6 1 4/6/800 5p IN4007/BYX94 1 1250 5p	BPY10 80p (VOLTIAC)	and the second se	4p An		ISTORS
BY103 1 1,500 181p	BPY68 ]	2v 50m/A m	nax.	240 BTX18-200 400 BTX18-300	··· ·· ·· 30p ··· ·· 35p
SR100         1.5         100         7p           SR400         1.5         400         8p	BPY69 80p	ORANGE 1	Op 1 7p 15	240 BTX30-200 500 BT107	
REC53A         1.5         1,250         14p           LT102         2         30         10p	BPY77 J Diodes		4p 6.5	5 500 BT101-500R	
BYX38-300R 2.5 300 40p	TIL209 Red 10p		2p 6.5		£1,00 £3,00
BYX38-600 2.5 600 45p BYX38-900 2.5 900 50p		ON CONTROLL PNPN 10 amp £1	ED 15	800 BTX95-800R P	ulse Modulated £8.00
BYX38-1200 2.5 .1,200 55p	.3" red. 7 segme			1000 28T10 (Less N LOCK CONDENSER	ut) £3.00 I Push-to-Break or
BYX49-600 2.5 600 35p	D.I.L. 0-9+D.P. 10m/a segment	display 1.9v 0.25	5MFD	800 volt 30p	Push-to-Make Panel Switch
BYX49-900 2.5 900 40p BYX49-1200 2.5 1,200 52p	anode	61p 2040		250 volt 15p 250 volt 20p	
BYX48-3UUR 6 300 40p	DL747.6" Minitron 3" 3015	E filament 10N	1FD	500 volt 80p	SWG. PER YD.
BYX48-600 6 600 50p BYX48-900 6 900 60p		£1.10 4MF		250 yolt 20p on and insertion	20-24 <b>3p</b> 26+42 <b>2.5p</b>
BYX48-1200R 6 1,200 80p BYX72-150R 10 150 35p	CQY11B I Infra red transm	.E.U.		32p	GARRARD
BYX72-300R 10 300 45p	One fifth of			SSIS SOCKETS 9p, Coax 3p, 5 pin	GCS23T or GP93/1
BYX72-500R 10 500 55p BYX42-300 10 300 30p	Plastic, Transist	or or Dioue 180	0° 9p,	5 or 6 pln 240° din	Crystal Stereo Cart- ridge £1.50
BYX42-600 10 600 65p	Holder Transistor or Die	1p 6p. ode Pad 1p 3.5	, speak	er din switched 5p, switched 5p, stereo	HANDLES
BYX42-900 10 900 80p BYX42-1200 10 1,200 95p	Holdersorpads		jack en	closed 10p.	Rigid light blue nylon
BYX46-300* 15 300 £1.00 BYX46-400* 15 400 £1.50	Philips Iron There	mostat 1	15p		61 " with secret fitting
BYX46-500* 15 500 £1.75		8 way edge plug	10p	CRAZY OFFERS	Screws 8p
BYX46-600* 15 600 £2.00 BYX20-200 25 200 60p		HEATSINK		4700 mfd. 40v 35p	Belling Lee white plastic surface coax
BYX52-300 40 300 £1.75 BYX52-1200 40 1,200 £2.50		03B individual 'cu type. Ready drilled	120	2500 mfd. 40v <b>30p</b> 2200 mfd, 25v <b>30p</b>	outlet box 35p
•Avalanche type	Tested unm	arked, or marked		2200 mfd. 64v 40p	Miniature Axial Lead Ferrite Choke formers
Amp Volt TRIACS	ample lead e ACY17-20 8	x new equipmen b   0C71/2	50	0000 mfd. 15v 12p	Perrite Choke formers 2p
6 800 Plastic RCA £1:20	ASZ20 8	OC200-5	20p	1250 mfd. 35v 10p	RS 10 Turn Pot 1%.
25 900 BTX94-900 £4.00 25 1200 BTX94-1200 £6.00	ASZ21 300 BC186 110			6800 mfd. 10v 6p 2+32 mfd.275v 8p	250, 500 Ω, 1K,
12-0-12 50M/A Min. Txfmr. 90p	BCY30-34 10	2G302 1	15p 16	6+32 mfd. 350v 12p	50K £1 Copper coated board
RS 2mm Terminals Blue & Black	BCY70/1/2 80 BF115 100	2N711	15p 25p	8+8 mfd. 350v 8p	10" x 9" approx 25p
Blue & Black 5 for 40p Chrome Car Radio facia 15p	BY126/7 4	21698/9		hilips electronic eng-	TIE CLIPS
Rubber Car. Radio gasket 5p	HG1005 10 HG5009 3	2N1091	8p 10	neer kits add on series, 1004 75p each	Nylon self locking 7"
DLI Pal Delayline 50p	HG5079 3	2N1907	£1	i.E.C. 5% Hi-stab	or 3½" 2p
Relay socket 25p Take miniature 2PCO relay	L78/9 31 M3 10		128 08	apacitors .073, .056,	Geared Knob 8-1 ratio 1 <sup>7</sup> / <sub>8</sub> " diam,
B7G or B9A valve can 5p	OA81 3	p in 1" sq. heat si	ink) 0	061, .066, .069, .075, .08, .089, .095, .1 2p each	black 70p
0-30, or 0-15, black pvc, 360° dial, silver digits, self adhesive,	OA47 3 OA200-2 3	P GET872	25p 12p 3.	.5mm metal stereo plug	11b Mixed bolts; nuts,
41" dia	0C23 20	1 =00=00	30p	. 20p	washers etc. 45p
SMALL ORDERS, ENCLOSE	SUITABLE N	AIL ORE	DER	CUSTOMER	S ONLY ADD
STAMPED ADDRESSED EN LARGE ORDERS, ADD SUFFI	CIENT FOR 8	% VAT-I F	PAY	BALANCE O	N121% ITEMS
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# MEDIUM

Medium wave DX listening requires a receiver having a high selectivity, and in this superhet design the selectivity is achieved by the use of a narrow band mechanical filter in the i.f. amplifier stages. The present article describes the circuit and gives details of the construction of the case and chassis. Next month's concluding article will complete the constructional details and will then deal with the simple alignment procedure employed.

Receiver designs for the medium wave DX enthusiast are something of a rarity, and the number of medium wave DX receiver articles which have been published in the past is extremely small. In consequence, anyone wishing to embark on this form of reception usually has to use either one of the more expensive transistor portables or a communications receiver which has the medium wave band included in its range.

Neither of these alternatives is completely satisfactory. Transistor portable receivers are intended for entertainment use, and even the better of these do not have the level of sensitivity and selectivity which is needed for serious DX listening. A communications receiver is likely to give better results, especially if some form of directional aerial is employed. It is unnecessarily expensive, though, and has some features, such as a b.f.o., which are not required.

This article describes a receiver which has been designed specifically for medium wave DX reception and which is capable of an extremely high performance level. A later article will describe a preselector which is intended for use with the receiver. The preselector has an integral ferrite aerial and no other aerial, or an earth connection, are required.

Although these two items of equipment have been designed to work together, the receiver can be used on its own with other types of aerial, such as a long wire. Similarly, the preselector can be used to feed a communications receiver that is being employed for medium wave DX reception.

### **DESIGN BASICS**

A block diagram showing the basic stages and the semiconductor complement of the receiver is given in Fig. 1. It will be seen from this that the receiver uses five bipolar transistors, a dual gate MOSFET, a diode and an integrated circuit a.f. amplifier. The preselector uses a dual gate MOSFET as its only active device.

As with any high performance set, cross modulation

is one of the main problems with this type of receiver. In consequence, MOSFET's are used in the mixer and preselector stages since these have a better performance than bipolar devices in this respect. It is found also that the receiver has a lower noise level and higher gain than would be the case if a bipolar mixer were used.

Good selectivity is obviously as important a requirement in this type of receiver as it is in an ordinary communications set. A mechanical i.f. filter is employed and this ensures excellent selectivity. Indeed, it gives a level of selectivity which is too high for normal domestic listening, with the treble and high middle frequencies of an accurately tuned transmission being virtually eliminated. The bandwidth is adequate for perfectly intelligible speech, however, and its narrowness gives a good signal to noise ratio and a low level of adjacent channel interference.

The circuit after the mechanical filter is quite conventional except for the inclusion of the S-meter. This can be omitted if it is not required.





# WAVE DX SUPERHET

Part 1

COMP	DNENTS
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Resistors R1 560kΩ R2 2.2kΩ R3 12kΩ R4 10kΩ  $R5 100 \Omega$ R6 390Ω R7 56Ω R8 2.7kΩ R9 120kΩ R10, 39kΩ R11 680Ω R12 390Ω R13 470kΩ R14 680 Ω R15 1.8kΩ **R16 270**Ω R17 1k Ω pre-set potentiometer, standard skeleton, horizontal R18 5.6kΩ R19 56kΩ R20 220kΩ R21 1kΩ R22 2.2 Ω R23 27kΩ R24 68k Ω VR1 5k  $\Omega$  potentiometer, log, with switch S1 VR2 5k  $\Omega$  potentiometer, linear

### Capacitors

apacutors C1 10-40pF ceramic trimmer (see text) C2 125 $\mu$ F electrolytic, 10 V. Wkg. C3 47pF polystyrene C4 0.047 $\mu$ F type C280 (Mullard) C5 0.1 $\mu$ F type C280 (Mullard) C6 0.047 $\mu$ F type C280 (Mullard) C7 0.022 $\mu$ F type C280 (Mullard) C8 350pF silvered mica or polystyrene C8 350pF silvered mica or polystyrene (see text) C9 10-40pF ceramic trimmer (see text) C9 10-40pr ceramic trimmer (see the C10  $5\mu$ F electrolytic, 10 V. Wkg. C11  $0.1\mu$ F type C280 (Mullard) C12  $0.01\mu$ F type C280 (Mullard) C13  $0.022\mu$ F type C280 (Mullard) C14  $0.01\mu$ F type C280 (Mullard) C15  $0.1\mu$ F type C280 (Mullard) C16 82pF ceramic or silvered mica C17  $0.1\nu$ F type C280 (Mullard) C17 0.1µF type C280 (Mullard)

C18 200µF electrolytic, 10 V. Wkg. C19  $200\mu$ F electrolytic, 10 V. Wkg. C20 470pF polystyrene VC1,2 365+365pF, variable 2-gang type 'O' (Jackson)

### Inductors

L1 Transistor tuning coil, Blue, Range 2T (Denco) L2 Transistor tuning coil, Red, Range 2T (Denco) IFT1 I.F. transformer type IFT.13 (Denco) IFT2 I.F. transformer type IFT.14 (Denco)

Filter

MF1 Mechanical filter type MFH41-T (Ambit)

Semiconductors **TR1 40673 TR2 2N706 TR3 BF195 TR4 BF184** TR5 BC169C TR6 2N2926Y **IC1 MC1306P** D1 0A91

Meter

M1 S-meter, 1mA f.s.d. (see text)

Switch S1 s.p.s.t. (part of VR1)

Sockets SK1 3.5mm. jack socket SK2 Coaxial socket

Miscellaneous Tuning reduction drive, type T.502 (Eagle) 2-off B9A valveholders 2-off Knobs 9 volt battery (see text) Battery connector Loudspeaker or headphones (see text) Plain perforated board, 0.1 in. matrix Plain board, 0.15 in. matrix Veropins (see text) Materials for case (see text) Wire, nuts, bolts, etc.

### CIRCUIT

The complete circuit diagram of the receiver appears in Fig. 2. The aerial is coupled by the low impedance primary winding of L1 to the tuned winding, this connecting to g1 of TR1. The tuned winding also provides the gate bias for TR1. L1 has a third winding (not shown in the diagram) which is intended for coupling to the base of a transistor, but in the present circuit it is the tuned winding which is coupled to the gate of the transistor. This does not impair the input selectivity as TR1 imparts a very low level of loading on the tuned circuit. R2 and C4 are the source bias resistor and bypass capacitor, and the g2 of TR1 is held slightly positive of the negative supply rail by be-

ing connected to the source via R1. The oscillator transistor, TR2, is used in the grounded base mode, and positive feedback is provided between its collector and emitter by L2. VC2 is the oscillator tuning capacitor and is ganged with VC1, the tuning capacitor for the aerial tuned circuit.

Oscillator harmonics can be troublesome in any receiver, but tend to be especially so in a sensitive medium wave design. The practical result of these harmonics is the breakthrough of short wave transmissions with consequent heterodynes on medium wave stations. The basic oscillator design showed a very high harmonic content when the output waveform was viewed on an oscilloscope. In consequence, R6 and R7 have been included and they very greatly reduce the harmonic content.

C3 couples the output of the oscillator to g2 of TR1. The voltage at g2 controls the gain at g1, and the oscillator signal thus modulates the aerial signal, producing the required mixing action. The difference signal is at the intermediate frequency of 455kHz, and is coupled to the primary of the input i.f. transformer of the mechanical filter. No integral tuning capacitor is included for this winding of the filter, and C20 is required to perform this function. The manufacturer's data for the filter specifies the tuning capacitance needed as 500pF total, and the 470pF capacitor employed for C20 was found to be adequate

4kHz at the -6dB points, and less than 10kHz at the

in practice. The mechanical filter has a typical bandwidth of

-40dB points. The result is a very noticeably sharper bandpass than can be achieved using ordinary i.f. transformers at the same frequency, and the bandwidth is really about as narrow as can be employed for normal a.m. reception.

A high gain two stage i.f. amplifier is used, and this is entirely conventional. Single-tuned i.f. transformers are employed, as there is little point in using double-tuned ones when the selectivity is mainly determined by the mechanical filter. With the very narrow bandwidth of the mechanical filter, having a subsequent i.f. amplifier with a relatively wide bandwidth helps to keep i.f. alignment simple and straightforward. The i.f. transformers have a nominal frequency of 470kHz, but they will tune down to 455kHz (and lower) comfortably.

D1 is the detector diode, and C13, R14 and C14 are i.f. filter components. VR1 is the volume control, and from here the signal is fed to the audio amplifier. This uses a single integrated circuit type MC1306P, which is capable of offering 0.5 watt into an  $8 \Omega$  load.

R19 and R20 bias an internal pre-amplifier stage in the i.c. and set its voltage gain and input impedance at 12dB and 56K $\Omega$  respectively. C16 rolls off the high frequency response and helps to maintain good stability. R21 couples the output of the pre-amplifier to the input of the i.c. power amplifier stage, and sets the voltage gain of the latter at 20dB. R22 and C17 form an output Zobel network. Typical total har-monic distortion is only 0.5% at 250mW output.

Turning to components, the mechanical i.f. filter type MFH41-T is available from Ambit Inter-national, 25 High Street, Brentwood, Essex. The oscillator padding capacitor, C8, requires a value of 350pF in silvered mica or polystyrene. If difficulty is experienced in obtaining a single component in this value, two capacitors may be connected in parallel. In the prototype the author employed a 200pF capacitor in parallel with a 150pF component, both polystyrene. An alternative combination could consist of two 100pF and a 150pF capacitor. The ceramic trimmers, C1 and C9, are specified at 10-40pF. If this value cannot be obtained it is in order to employ 10-60pF trimmers, which are available from Doram Electronics.



The use of separate component boards for the i.f. and a.f. amplifiers results in a neat layout with adequate spacing

### S-METER AND A.G.C.

The S-meter circuit is fed from the a.g.c. voltage applied to the first i.f. amplifier transistor. An a.g.c. bias is produced across VR1 and is the d.c. component of the received transmission. Its amplitude is proportional to the strength of the received signal.

TR3 receives its base bias current, via R10 and the output winding of the mechanical i.f. filter, from a potential divider consisting of R9, R23 and VR1. When a weak signal is received very little bias is produced across VR1, and the biasing of TR3 is not significantly affected. On stronger signals, however, the negative d.c. bias due to the signal reduces the positive voltage across VR1, and on very strong signals a negative voltage of about 0.5 volt is produced across VR1. In consequence the bias current for TR3 is reduced.

This reduction in bias current causes the gain of TR3 to fall, and thus the transistor has a lower gain on strong signals than it has on weak ones. In consequence an automatic gain control system is set up which has the beneficial effects of producing similar





The mixer and oscillator components are wired up below the chassis

volumes from signals of differing strength, preventing overloading of the detector on strong signals and minimising the noticeable effects of fading. C10 bypasses the audio signal which would otherwise be present on the a.g.c bias.

TR5 is the S-meter amplifier, and is fed from the junction of R9, R10 and R23. The emitter resistor, R16, gives TR5 a high input impedance and low voltage gain. The high input impedance ensures an insignificant level of loading on the a.g.c circuit. TR6 is an emitter follower, and VR2 is adjusted so

TR6 is an emitter follower, and VR2 is adjusted so that the voltage at TR6 emitter is identical to that at TR5 collector under no-signal conditions. VR2 thus acts as the S-meter set zero control.

When a signal is received the voltage at TR5 base will be reduced in proportion to the strength of the received signal. This causes the voltage at TR5 collector to increase and so produce a forward deflection of the meter needle. The level of deflection is, of course, proportional to the amplitude of the received signal. R17 enables the sensitivity of the S-meter to be adjusted.

The S-meter has the same size as panel meters in the Henelec '38 Series', with a square front measuring 42 by 42mm. It has a full-scale deflection of 1mA. As an alternative a standard 0-1mA panel meter may be employed, this acting as a tuning meter as well as giving comparative indications of signal strength.

### **CASE AND CHASSIS**

The construction of the case and chassis is very simple but is nevertheless effective. The front and rear panels consist of two pieces of 18 s.w.g. aluminium sheet measuring 10 by  $3\frac{1}{3}$  in. The sides of the case consist of two pieces of timber  $\frac{3}{4}$  in. thick measuring 6 by  $3\frac{3}{4}$  in. The front and rear panels are fixed to the timber sides by two woodscrews near each edge. Two further pieces of 18 s.w.g. aluminium, each measuring 10 by  $6\frac{1}{4}$  in., form the top and the bottom, and are similarly secured to the timber sides with woodscrews.

Fig. 3 shows the drilling required for the front panel. The two small holes marked 'X' are for the tuning drive and are not drilled at this stage. The large hole for the S-meter may be initially cut out by means of a fretsaw. The meter itself can then be used for marking out the four small holes around the large hole.



Fig. 3. Drilling details for the front panel and the chassis. The front panel holes for the meter and VR2 are not required if the S-meter facility is to be omitted

Another view of the completed receiver. The legends on the front panel are taken from 'Panel Signs' Set No. 4, available from the publishers of this journal



be judged from the accompanying photographs. When the drilling, apart from the two 'X' holes, has been completed, the front and rear panels are mounted on the side panels, using four woodscrews for each panel. The top and bottom panels are then temporarily mounted in the same way to check that the case fits together properly. Although it is not essential for the top and bottom panels to be in electrical contact with the front and rear panels, it should in practice be found an easy matter to have the edges of the top and bottom touch the edges of the front and rear panels at several points. If desired, four small rubber feet may be fitted near the corners of the bottom panel.

The chassis is made up from a piece of 18 s.w.g. aluminium measuring  $7\frac{3}{4}$  by  $5\frac{3}{4}$  in. This is also shown in Fig. 3. The two holes marked 'Y' are for the 2-gang tuning capacitor and they correspond with two 4BA tapped holes in the bottom of the tuning capacitor frame. As these tapped holes do not appear in the normal specification of the capacitor listed, it is advisable to check the positions of the chassis holes with the capacitor itself before drilling. The capacitor should take up a position which will allow its spindle to engage correctly with the tuning drive on the front panel.

L1 and L2 are fitted in B9A valveholders. Each valveholder has two solder tags secured below the chassis under its mounting nuts. Both valveholders are mounted with pins 1 and 9 nearer the front. The mounting holes for the i.f. panel and the a.f. panel are not drilled yet. There are three further holes with no apparent purpose as yet, and these will later allow the passage of wires through the chassis. Their precise positioning is not important, and they should be fitted with grommets.

When the chassis drilling as so far described has been completed and the 90 degree bend has been made, the chassis is mounted as low down as possible on the right hand side of the case by means of two woodscrews. The  $\frac{3}{4}$ in. flange is below the mains chassis surface.

The tuning capacitor is next mounted to the chassis by means of two  $\frac{3}{8}$  in. 4BA bolts. Several 2BA nuts, or similar, are used to space the capacitor slightly away from the chassis. If this is not done, projecting parts below the capacitor frame will bear against the chassis; also the screws would pass too far through the capacitor frame with the risk of consequent damage to its vanes.

The tuning drive can then be placed in position on the front panel. The mounting screw which is supplied with the drive and which fits into the hole at its top rear has been found to be unnecessary, and is discarded. With the drive in position it is possible to use the drive itself as a template to find the positions of the two small mounting holes marked 'X' in Fig. 3. The tuning capacitor is then removed so that these holes can be drilled. After this, the capacitor and tuning drive are finally mounted. Two 1in. 6BA roundhead screws with nuts are used to mount the drive at the holes 'X'.

### NEXT MONTH

In next month's concluding article, details will be given of the remaining constructional steps required, together with the alignment. Any queries concerning components or parts will be cleared up in the concluding article.

(To be concluded)

### WE WISH ALL OUR READERS — A VERY HAPPY CHRISTMAS —

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DECEMBER 1976

# Wide range A.C. millivoltmeter

By B. S. Wolfenden

This simple but effective design requires only one operational amplifier i.c. and two transistors, and it draws an extremely low current from its battery supply.

A very useful piece of apparatus when dealing with audio equipment is undoubtedly an a.c. millivoltmeter. The design to be described can claim no great advance in performance, but it is very simple and the performance it has is very acceptable. An a.f. oscillator or signal generator is required for setting it up.

### **CIRCUIT DETAILS**

The circuit, which is shown in Fig. 1, can be split into two parts. The meter drive section incorporating

### SPECIFICATION

- Range: From 1mV f.s.d. to 300V f.s.d. in 10dB steps (12 ranges).
- Frequency Response: Within 1dB from less than 10Hz to approx. 250kHz on the 1mV range, falling to approx. 100kHz on the 100mV range.
- Linearity: Within approx. 0.5% of f.s.d. (as checked against a digital voltmeter).
- Input Impedance:  $150k \Omega$  typ. on mV scale,  $10M \Omega$  approx. on V scale.

TR1 and TR2 is a simple design following normal practice. The meter and rectifiers are included in the feedback loop and the resistor, R11, in the emitter of the first transistor determines the input sensitivity for full-scale deflection on the meter. In this case f.s.d. is approximately 20mV.

approximately 20mV. D1 and D2 are gold bonded germanium diodes which are specially suitable for high speed switching applications. Since the 0-100 $\mu$ A meter is included in the feedback circuit its resistance can have any value within reason. The meter employed in the prototype had a resistance of 675  $\Omega$ 

In order to produce an instrument with a sensitivity of 1mV f.s.d., and also to provide a reasonably high input impedance, the driver stage is preceded by a pre-amplifier making use of the 709 integrated circuit. The output from this stage is attenuated by the resistors R7, R8 and R9 to give a X1, X10 and X100 scale. For maximum accuracy these resistors should be as accurate as possible and with the prototype were selected by measurement on a universal bridge. R9 was given by a parallel combination of a 27  $\Omega$  and a 10 $\Omega$  resistor.

In order to save complicated switching and produce 10dB (3.162 voltage ratio) steps between obtainable scales, the gain of the first stage is adjusted by means of RV1, RV2 and S2 to obtain 1mV and 3.162mV f.s.d. on the meter. In the prototype the meter only had a 0-10 scale and therefore a suitable 0-3 scale was added. The correlation between this scale and the 0-10 scale to obtain the required 10dB step is given in



Fig. 1. The circuit of the wide range a.c. millivoltmeter. This has 12 ranges, from 0-1mV to 0-300V

COMPONENTS

Resistors C7 100µF electrolytic, 15 V. Wkg. (all fixed values 1/4 watt 5% unless otherwise stated) C8 80µF electrolytic, 10 V. Wkg. R1 10m  $\Omega$  high stability (see text) C9 250µF electrolytic, 6 V. Wkg. C10 22 $\mu$ F electrolytic, 15 V. Wkg. C11 22 $\mu$ F electrolytic, 15 V. Wkg. C12 20 $\mu$ F electrolytic, 15 V. Wkg. C12 50 $\mu$ F electrolytic, 25 V. Wkg. C13 100 $\mu$ F electrolytic, 25 V. Wkg. TC1 20pF trimmer R2 10k  $\Omega$  high stability (see text) R3 470k Ω R4 470k Ω R5 1.5k Ω R6 22k Ω **R7** 680  $\Omega$  high stability (see text) R8 68  $\Omega$  high stability (see text) Semiconductors R9 7.55  $\Omega$  high stability (27  $\Omega$  and 10  $\Omega$  in parallel IC1 709, in round TO99 case see text) **TR1 BC108** R10 1k Ω **TR2 BC108** R11 100 Ω **D1 AAZ13** R12 10k 0 **D2 AAZ13** R13 47k Ω R14 1.8k Ω Meter R15 15kΩ M1.0-100µA, moving-coil R16 1.8k Ω RV1 4.7k  $\Omega$  pre-set potentiometer, miniature Switches skeleton 0.1 watt, horizontal S1(a)(b) 2-pole 2-way, toggle or rotary RV2  $4.7k_{\Omega}$  pre-set potentiometer, miniature S2 s.p.d.t., toggle or rotary skeleton 0.1 watt, horizontal S3(a)(b) 2-pole 4-way, rotary Capacitors Miscellaneous C1 0.0068µF plastic foil Input socket or input terminals C2 1 $\mu$ F plastic foil C3 100 $\mu$ F electrolytic, 15 V. Wkg. Metal case (see text) 2-off 9V batteries with connectors C4 470pF silvered mica or polystyrene Perforated s.r.b.p. board, 0.1in. matrix C5 10pF silvered mica or ceramic Control knob or knobs (as required) C6  $0.1\mu$ F ceramic

### TABLE 1 0-3 Scale Calibration

Reading on 0-3 scale	Corresponding reading on 0-10 scale	
0.2	0.64	
0.6 0.8	1.27 1.90 2.52	
1.0	3.15	
1.2	3.78	
1.4	4.42	
1.6	5.05	
1.8	5.68	
2.0	6.32	
2.2	6.95	
2.4	7.58	
2.6	8.22	
2.8	8,85	
3.0	9.50	

Table 1. A dB scale was also included and a similar correlation for this cale is given in Table 2. This table is shows the dB calibration points against the 0-10 markings on the original scale. The reference level is taken as 1 mW into  $600 \Omega$ , corresponding to 775 mV.

### TABLE 2

dB Scale Calibration

Reading on dB scale	Corresponding reading on 0-10 scale	
+2	9.75	
+1	8.69	
0	7.75	
-1	6.90	
-2	6.15	
-3	5.46	
-4	4.89	
-5	4.36	
-6	3.88	
-7	3.46	
-8	3.08	
-9	2.75	
10	2.45	
11	2.18	
12	1.95	

An input attenuator employing S1, R1 and R2 to enable the unit to measure high level inputs is also incorporated. The value of TC1 is adjusted to compensate for the self-capacitance of the input resistors. As with R7, R8 and R9, the input resistors R1 and R2 should have values which are as accurate as possible. (In the absence of a suitable resistance bridge, the resistors required for R2, R7, R8 and R9 are listed in 1% tolerance by Home Radio. The 10M  $\Omega$  resistor, R1, is difficult to obtain in close tolerance as a single component, but it could consist of four 2.2M  $\Omega$ resistors and a 1.2M  $\Omega$  resistor in series, these values being also listed by Home Radio in 1% tolerance. — Editor.)

The total current consumption from the 18 volt supply is  $2\frac{1}{4}$ mA only.

### CONSTRUCTION

Constructional details are left to the reader but the following points should be of assistance. The size of the case required for the instrument will depend largely on the dimensions of the meter and the switches, and the prototype was built into a diecast box measuring  $7\frac{1}{4}$  by  $4\frac{1}{2}$  by  $2\frac{1}{4}$  in. S1, S2, S3 and the meter are mounted on the front panel. S3 is specified as 2-cole 4-way, but could be 3-pole 4-way with the third pole unused. Two PP3 batteries inside the box provide power.

R1, R2, R7, R8 and R9 are wired on the appropriate attenuator switches, as also are C1 and TC1. The remaining components are assembled on a plain perforated s.r.b.p. board of 0.1in. matrix having 17 by 42 holes. This is illustrated in Fig. 2. The board is mounted directly to the meter terminals whose centres, with the author's meter, are spaced by 1.3in. Other 0-100 $\mu$ A meters will probably have different terminal spacing, but the general wiring layout of Fig. 2 can still be followed. Meter terminal spacing may also necessitate the use of a larger board than that shown in the diagram.

### CALIBRATION

If no better alternative is available the unit may be calibrated with the aid of a mains transformer having a low voltage secondary and an ordinary moving-coil multimeter set to an alternating voltage range. Arrange the transformer, a 5k  $\Omega$  potentiometer and the multimeter as in Fig. 3. Adjust the potentiometer so that it offers a low voltage. Next adjust RV1 and RV2 so that they insert maximum resistance into cir-cuit, set the millivoltmeter controls to correspond to an f.s.d. of 3 volts and connect it in parallel with the multimeter. Adjust the potentiometer so that the multimeter reads 3 volts and then adjust RV1 so that the millivoltmeter gives f.s.d. on the 0-3 scale. Reduce the voltage so that the multimeter reads 1 volt and, with the millivoltmeter now set to read 1 volt full-scale, adjust RV2 to give f.s.d. The millivoltmeter is now calibrated for all ranges to an accuracy dependent upon that of the multimeter and the attentuators in the millivoltmeter.

Whatever method is used to calibrate the millivoltmeter, the X3 scale must be calibrated before the X1 scale. This is because RV1 determines the gain of the pre-amplifier in the X3 position, whilst the parallel combination of RV1 and RV2 determines the gain in the X1 position.



Fig. 2. Layout of the component board employed in the prototype. Two holes are drilled in this to accomodate the meter terminals. The layout is modified as required to suit the terminal spacing of the partticular meter used



Fig. 3: A simple circuit which may be used for setting up RV1 and RV2

To adjust TC1 an audio oscillator having a constant or monitored output is required. Set the oscillator to a low frequency (say 500Hz) and adjust the output to give f.s.d. on the 1 volt range of the millivoltmeter. Reset the oscillator to 100kHz and adjust TC1 to give f.s.d. again. Check that the response is reasonably flat over the frequency range and if necessary readjust TC1 for maximal flatness.

### CONCLUSION

Despite the fact that this millivoltmeter employs only two transistors and one operational amplifier the performance it offers is very adequate for the testing of audio equipment. The current consumption is low, enabling small batteries to be used for a power supply and allowing the complete instrument to be made up as a small, easily portable unit.

### **BACK NUMBERS**

For the benefit of new readers we would draw attention to our back number service.

We retain past issues for a period of two years and we can, occasionally, supply copies more than two years old. The cost is the cover price stated on the issue, plus 11p postage.

Before undertaking any constructional project described in a back issue, it must be borne in mind that components readily available at the time of publication may no longer be so.

We regret that we are unable to supply photo copies of articles where an issue is not available. Libraries and members of local radio clubs can often be very helpful where an issue is not available for sale.

### NEWS . . . AND

### Big screen TV sales reach 1000



Crown Cassette Communications recently announced the sale of their 1000th VideoBeam projection television unit. This big screen television is able to display a picture almost 6 feet wide.

Our photo shows Henry Oliver, Marketing Director of Crown Cassette Communications, making a special presentation to Edward Gillespe, Racecourse Manager of United Racecourses, for the purchase of the 1000th machine. He presented, on behalf of Crown Cassette Communications, a Philips N1501 VCR in front of a large crowd in the Grandstand at Sandown's prestige Variety Club meeting.

United Racecourses purchased the VideoBeam system to replace a number of smaller monitors at both Kempton and Sandown Park racecourses. This is the second time VideoBeam projection television from CCC has been seen by Sandown Park's regular racing enthusiasts. In June a large crowd watched live transmission of the Wimbledon tennis finals in between races.

### Loctite Super Glue-3 introduction

Loctite Super Glue-3 is a type of adhesive virtually unknown, until recently, outside of industry for which it was especially developed to meet the most stringent bonding needs. Ordinary adhesives require solvents, catalysts and a lengthy period of time before the treated parts can be handled. Loctite Super Glue-3 claims it has none of these disadvantages.

Fast action allows almost instant bonding of a wide variety of materials with non-porous surfaces: rubbers, metals, ceramics and most plastics. This enables the making and repair of many items which were previously either very difficult and timeconsuming or virtually impossible. It is ideal for repairing plastic toys, mending and making jewellery, restoring and repairing ceramic items such as ornaments and fine display china, and for many other tasks.

Loctite Super Glue-3 is very economical to use, only the smallest drop is needed to make a repair or join with a virtually invisible bondline. Applying too much of this adhesive will actually retard its highspeed action. There is need for neither the mixing of resins and hardeners nor the use of clamps. A small droplet of Loctite Super Glue-3 and firm fingerpressure are all that is needed to make an amazingly strong joint in seconds.

Loctite Super Glue-3 is obtainable from Woolworths, W. H. Smith, Halfords and most hardware and DIY departments and stores. Each 3 gramme tube contains approximately 190 drops and retails at a recommended price of 99p, including VAT. Due to its speed of action and strength the product should not be sold to or used by children. Each tube of Loctite Super Glue-3 is fitted with a special applicator nozzle to ensure accurate and controlled use of the adhesive.



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### COMMENT

### **Oracle Teletext specification**

A new edition of "Broadcast Teletext Specification" - the document that describes the technical parameters of the Teletext signals transmitted in the United Kingdom by Independent Television and the BBC — has recently been published jointly by the IBA, BBC and BREMA.

This edition, dated September 1976, includes a number of minor changes, designed primarily to make the displayed pages of information visually more attractive by the use of double height characters where required and providing more flexible use of colour backgrounds ("contiguous colour background"). These new facilities have been incorporated in the system in such a way that the transmissions will remain suitable for Teletext decoders based on the earlier Specification issued in January 1976, although such decoders will not, of course, display the informa-tion in exactly the same way. The ORACLE teletext transmissions put out by Independent Television already incorporate the new facilities.

### **Professional drilling machine**

P.B. Electronics of 57 High Street, Saffron Walden, Essex, has introduced a new drilling machine called the P.B. SuperDrill. It is British made and has been designed specifically for the professional drilling of PCBs. It is ideal for prototype and medium production use.

The case is of solid, all steel construction and the P.B. SuperDrill has a throat clearance of 150mm. Height under the

chuck is a big 44mm. Maximum chuck capacity is 4mm. The motor is rated at 100W and the P.B. SuperDrill runs at 18,000 rpm off load.

The unit has additional features, such as fan-assisted swarf removal and a high intensity lamp to illuminate the drilling area without discomfort to the operator - even after long periods of use. The P.B. SuperDrill also has a spring plunger which makes stack drilling easy and untiring, even on medium production runs. The P.B. SuperDrill is available in the U.K. at a special in-

troductory price of £125 complete with stand.

### **Tape gifts**



A thousand Scotch High Energy cassettes were given away by 3M at Audio 76 in Harrogate. Scotch cassette girls toured the show distributing lucky number cards, the recipients of which were invited to the 3M exhibition room where those with the lucky numbers received a free Scotch High Energy cassette — 3M's new small-particle "super ferric" product that is compatible with all cassette machines and yet can show an improvement of up to 9dB in performance over some standard ferric tapes it is claimed.

### Death of radio pioneer

It was with great sadness that The Marconi Company recently announced the death of Mr. E. Green M.Sc, MIEE, at the age of 86. Mr. Green was one of the pioneers of transmitter engineering,

was one of the pioneers of transmitter engineering, responsible for many developments in this field. From 1919-1929 he assisted C. S. Franklin in the development of the Marconi Short Wave Beam System, which led to the establishment of the Imperial Wireless Scheme. Under his supervi-sion the world's first 100kW Short Wave Broadcast sound transmitter was developed for the BBC.

In the field of television, Mr. Green was respon-sible for the design of the high-definition vision transmitter installed at Alexandra Palace in 1935.

He had more than fifty patents to his credit and was author of numerous technical articles.



# **OCTAL AND BINARY**

by

### **D. Sheffield**

One of the first numbering systems encountered when starting work with digital logic is binary notation. This is normally followed by the octal numbering system.

Binary is easy to understand because all that has to be remembered is that it has no digit higher than 1. If we add 1 to binary 1 we get binary 10. With octal there is no digit higher than 7. Adding 1 to octal 7 gives us octal 10.

These relationships fit readily into place when we remember that in our familiar decimal notation there is no digit higher than 9. When we add 1 to decimal 9 we get decimal 10.

A table giving binary and octal equivalents of decimal numbers up to decimal 16 is given in Fig. 1.

### CONVERTING NUMBERS

Converting binary or octal to decimal is not difficult, but can be rather time-consuming. However, the newcomer to binary and octal can be encouraged by the fact that converting binary to octal, and vice versa, is delightfully easy. Fig. 2(a) shows the binary number 10111001. To convert this to octal, first space out the binary digits in groups of three starting from the right, as illustrated in Fig. 2(b). When necessary.

### The conversion of binary to octal, or octal to binary, is a very simple process.

add a 0 or 0's at the extreme left to complete the left hand group of three. Then, as in Fig. 2(c), write down the octal (or decimal) equivalent of each group of three binary digits. The resulting number, shown in Fig. 2(d), is the octal equivalent of the binary number.

The reason for this ease of conversion can be inferred by examining the table of Fig. 1. Here, the first seven binary numbers have three digits or less. On proceeding to the eighth number, the binary numbers hop from the three digit 111 to the four digit 1000, and the octal numbers hop from the single digit 7 to the two digit 10. The binary numbers lower down in the table can then be converted to octal by the process shown in Fig. 2. Binary 1001 is the same as octal 11, binary 1010 is the same as octal 12, and so on. If the table progressed to seven digit binary numbers, the hop from six to seven digits in binary would be accompanied by a hop from two to three digits in octal.

### **BINARY POINT**

The decimal point becomes a binary point with binary notation and an octal point with octal notation. The same method of conversion is employed

DECIMAL	BINARY	ÓĊTAL
I I		
2	10	2
3	11	3
4	100	4
5	101	5
6	110	6
7	111	7
8	1000	10
9	1001	H L
10	1010	12
	1011	13
12	1100	14
13	1101	15
14	1110	16
15		17
16	10000	20

Fig. 1. Table showing the binary and octal equivalents for decimal numbers from 1 to 16

	(a)	10111001	(BINARY)
Fig. 2(a). A binary number which is to be con- verted to octal (b). The binary number is split into groups of three digits	(b)	010 111 00	
(c). The groups are converted to octal digits (d). The final answer in octal	(c)	2 7	
	(b)	271	(OCTAL)



Fig. 4(a). An octal number for conversion (b). The equivalent for each octal digit is written in groups of three binary digits (c). The binary number finally obtained



with binary expressions which include the binary point, as is illustrated in Fig. 3. First, break up the binary number into groups of three digits to the left and right of the binary point. If necessary, add a 0 or 0's at the ends to complete the outside groups. Then write down the octal equivalents of each group. The result is the octal version, complete with octal point, of the binary number. The reverse process is carried out when converting octal into binary. Fig 4 shows the procedure. Each octal digit is converted into a group of three binary digits, and the outcome is the binary equivalent. If required, a 0 or 0's is inserted in each group to make it up to three digits. Any 0's appearing at the extreme ends of the binary number can be discarded.

**DECEMBER 1976** 

283



Many experimenters and service engineers have quite a wide array of mains operated test equipment on their benches and it is quite possible, after an intensive session of work, to accidentally leave at least one item switched on after finishing. The mains current monitor described in this article is intended to cater for this situation; it is inserted in the mains supply to the work bench and it causes an l.e.d. to light up when even a very small mains current is being drawn. The l.e.d. will only extinguish when all items drawing current from the mains have been switched off.

### MONITOR CIRCUIT

The circuit of the monitor appears in the accompanying diagram. The primary of the transformer T1 is connected permanently across the mains supply and its secondary offers an r.m.s. voltage of 6.3 volts. The windings are phased such that, on halfcycles which cause the upper end of the primary to go positive the upper end of the secondary goes positive also.

The mains current to the supplied equipment on the bench passes through diodes D2 and D3 on halfcycles when the live mains input is positive, and through D4 on halfcycles when the live mains input is negative. These three diodes are all silicon rectifiers, and cause a voltage drop of about 1.3 volts when D2 and D3 conduct and a voltage drop of about 0.65 volt when D4 conducts. These low voltage drops will have no effect on normal mains equipment.

Let us assume next that a load is connected across the output of the circuit at the right, causing current to pass through D2 to D4. On halfcycles when the live input is negative the circuit does not operate. The upper end of the 6.3 volt transformer secondary is negative as well, and no current flows through D1. At the same time the base of TR1 is taken slightly negative of its emitter by way of R3, and the transistor is non-conductive. All that happens is that the load current passes through D4. On half-cycles when the live input is

On half-cycles when the live input is positive so also is the upper end of the transformer secondary, and D1 is able to conduct. The mains current flows through D2 and D3, with the anode of D3 becoming approximately 1.3 volts positive of the emitter of TR1. Base current flows via R3, TR1 is turned on and the l.e.d. lights up, l.e.d. current being limited by resistor R1.

Thus, as soon as a load is connected across the output of the circuit the l.e.d. is illuminated on the half-cycles when the live input is positive. Persistence of vision in the eye then gives the impression that the l.e.d. is glowing continuously.

### **COMPONENT VALUES**

Mains transformer T1 is a small heater transformer having a 6.3 volt



The circuit of the mains monitor. This is extremely sensitive, and the I.e.d. glows for mains currents which are lower than 1mA

secondary. Most transformers of this type have a secondary current rating of 0.5 amp or more, which is well in excess of the current requirements in the circuit.

Rectifiers D2, D3 and D4 require a forward current rating which is adequate for the mains driven equipment to be supplied. Rectifiers rated at 3 amps will, for instance, be adequate for loads up to 720 watts. They can have low inverse voltage ratings since the reverse voltages applied across them are very small.

The l.e.d. requires a maximum forward current rating of at least 60mA and that employed by the author was a red l.e.d. Type 4, available from Doram Electronics, which is rated at 80mA. The maximum current flows in the l.e.d. when the voltage at the upper end of the transformer secondary is at its positive peak value of 6.3 multiplied by 1.414, or 8.9 volts. Assuming a total voltage drop of 2.5 volts in D1, the l.e.d. and TR1, the remaining 6.4 volts appears across R1. The consequent current is then 53mA. This current only flows, of course, at one instant in the a.c. cycle, and the intensity of illumination in the l.e.d. is about the same as would be given if a direct current of around 17mA flowed

The circuit is very sensitive and, indeed, R2 is connected between the base and emitter of TR1 to keep the sensitivity at a reasonable level. In the author's circuit the l.e.d. lit up when a load of 1M  $\Omega$  was connected across the output points, and gave a glow which was just 'discernible with a load of 2M  $\Omega$ . A load of 1M  $\Omega$  corresponds to a current of 0.24mA, and so the circuit is capable of detecting insulation leakage in the bench wiring and mains connectors. The sensitivity will vary somewhat in different units made up to the circuit, because of gain spread in TR1, but it should still be of the same order as that obtained with the prototype.

As has already been mentioned, the mains transformer primary is connected permanently across the mains supply, and it will draw a negligibly low current. If, as is probable, the phase relationship between the transformer primary and secondary is not known it will be necessary to find the correct method of connection experimentally. The circuit should be initially checked out with a load connected, after which the mains should be turned off, the connections to the secondary transposed and the circuit checked again. The incorrect method of connection is that in which either the l.e.d. does not light up or it glows at very reduced brilliance.

The circuit should be assembled in a completely enclosed box with only the l.e.d. visible, and all precautions against accidental shock must be observed. If a metal case is employed this must be reliably connected to the mains earth.

### TRADE NOTE New compact satellite navigator



The MX 1102, a new precise positioning satellite navigator, introduced by S. G. Brown

S. G. Brown of Greycaine Road, Watford, Herts, a Hawker Siddeley company, and principal European agents for Magnavox, announce the introduction of the MX 1102, a new precise positioning satellite navigator.

The MX 1102 is the first satellite navigation system incorporating a microprocessor in place of a separate minicomputer. With its advanced, permanently stored programme, the MX 1102 is easy to use and requires only a few hours training for operation and maintenance. The receiver, microprocessor and CRT data display are housed in a

The receiver, microprocessor and CRT data display are housed in a single unit which is no larger than a portable TV set, and may be chart table, bulkhead or deckhead mounted. The only other item, a small antenna/preamp, is mast mounted in any relatively unobstructed location.

Continuous navigation information is clearly displayed, requiring no special charts or manual computations. Latitude, longitude, and Greenwich Mean Time (GMT), are supplemented by such information as distance travelled, heading to steer, Great Circle and Rhumb Line courses, distance to destination, and time of next (and future) satellite fixes, all of which may be displayed upon operator command. A feature never before offered in shipboard satellite navigation systems is Programmed Tracking. This feature enables the new system to distinguish between different satellites, and to only lock on signals from the satellite offering the best navigation fix. This results in more usable fixes than available from any other equipment. Between fixes, the system automatically dead reckons and compensates for set and drift. Specifications for Class Nav N certification by Det Norske Veritas have been met in the design and testing of the MX 1102. These include

Specifications for Class Nav N certification by Det Norske Veritas have been met in the design and testing of the MX 1102. These include demonstrated accuracy — static accuracy to 0.05 nautical miles (rms), and underway to 0.1 nautical mile (rms) — comprehensive environmental testing, failure mode effects and reliability analysis. In addition, the system selftests every two hours and, in the event of failure, identifies the easily replaced module responsible for the malfunction. With the exception of the microprocessor and mechanical packaging, the MX 1102 utilises circuits proven in service on over 700 ships, ensuring the highest system reliability.

As the result of a decade of satellite systems development, the MX 1102 with its reliability, simplicity of operation and ease of maintenance should prove attractive to shipowners and operators.



By Frank A. Baldwin

Times = GMT

Frequencies = kHz

Short wave listeners on the higher frequencies may find some of the following transmissions of interest to them — and we commence with two clandestine loggings.

loggings. "Radio Espana Independiente" on a measured 14479 at 1915, YL and OM alternate in Catalan with news and a talk on Catalan affairs; also heard in parallel on 12140. These pro-communist, anti-Spanish Government programmes are radiated from Bucharest and, possibly, Sofia. The programme logged here is in the schedule 1800 to 2245 on 10110, 12140 and 14485. These are all Bucharest frequencies but they are, as you may have already noted from the above, subject to variation (10kHz in the present case). Most broadcasts are in Spanish but Galician is included on Wednesdays, Basque on. Fridays and irregularly in Catalan.

"Voice of the Communist Party of Turkey" on 6200 at 0810 when opening with a choral version of the Turkish party anthem, repeated identification by OM in Turkish, then anti-Turkish Government propaganda. Closes at 0840 with a choral version of the "Internationale". At the time of writing this programme is radiated on Sundays and Tuesdays only.

ly. "Radio Andorra" on 6230 at 0800, identification by OM then a programme of U.K. pop records.

Jerusalem on a measured 15512 at 1910, YL in Hebrew in a relay of the Domestic 2nd Service to Europe and the Middle East, the schedule being from 0600 to 1800 and from 1830 to 2000 on this channel.

### CURRENT SCHEDULES

Whilst the schedules published here are correct at the time of writing, readers are reminded that some of them are subject to alteration at short notice, either with respect to frequencies, times, or both.

### VATICAN CITY

"Vatican Radio" has an External Service in which English transmissions are directed to the U.K. and Eire from 1445 to 1500 on 6190, 7250, 9645 and on 11740; from 2030 to 2045 on the first three channels mentioned above.

### • INDIA

"All India Radio", Delhi, in the Domestic Service, presents a series of five minute news bulletins in English at various times throughout the day. Two popular times for listeners here in the U.K. would probably be as follows — from 1430 to 1435 on 3255, 3925, 4860, 6015, 7125, 7195, 9645, 9950, 10335 and 11840; from 1530 to 1545 on all the foregoing channels with the addition of 9705. • IRAN

"Radio Iran", Tehran, has a Domestic Service First Programme which, of course, operates throughout the day. Listeners here in the U.K. may care to listen on 9022, 9765 or on 15085 from 2030 to 2100 when a newscast in Farsi (Persian) is radiated.

### • ALGERIA

"Radio Algiers" operates an External Service in English from 1900 to 2000 on 7245 (also announced on 9610 and 15420 but not heard by the writer on these channels despite many attempts). According to the BBC Monitoring Service, the following transmissions are also made from Algiers — from 1800 to 1900 in Arabic, "Voice of Palestine, Voice of the Palestine Revolution," presented by the Palestine Liberation Organisation on 6145, 6160, 7195, 7245, 9685, 11810, 15160 and on 17825. From 2100 to 2200 in Arabic/Spanish and/or French or vernaculars, "Voice of the Free Sahara" presented by the Polisario Front, the Popular Front for the Liberation of the Sahara and Rio de Oro. From 2200 to 2300 in Spanish, "Voice of the Free Canary Islands" presented by the Movement for the Selfdetermination and Independence of the Canaries Archipelago.

### **AROUND THE DIAL**

#### • ZAMBIA

Lusaka on 4911 at 1755, OM in vernacular then station identification and a newscast in English at 1800. This is the Home Service which operates in both English and vernaculars from 1400 to 2105 weekdays (2nd transmission period) and from 1400 to 2005 on Sundays, the power being 50kW.

#### SAUDI ARABIA

Riyadh on 15245 at 1845, chants from the Quran in a transmission from the "Holy Quran Station" to North and Central Africa, the schedule of this programme being from 1700 to 2000 daily.

#### • U.S.S.R.

Baku on 4785 at 0203, music in the local style, OM in dialect. This is the Baku Relay in Azerbaijan S.S.R. which relays Baku 1 from 0157 to 2200 in Azerbaijani/Armenian, this transmission period also including a relay of Moscow 1 from 2100 to 2200. Baku is a port of the Caspian Sea and is noted for its oil wells. The power is 50kW and the relay may also be heard in parallel on 9840.

Yerevan on 4990 at 2036, YL in Armenian in a

programme directed to the Near and Middle East, the schedule of which is from 1900 to 2100. The full schedule of this Armenian S.S.R. transmitter is as follows — from 1300 to 1630 and 1730 to 2200 with the Foreign Service, from 2200 to 0100 relaying the Moscow 2 programme, from 0200 to 1300 with the Home Service 1.

#### CHAD

Radio Ndjamana on **4904.5** at 0426, interval signal on a Balafon (native musical instrument) until identification and a newscast in French at 0430. This is a transmission in the Home Service which has a schedule from 0430 to 0630 and from 1740 to 2200 (Saturdays 2300) and the power is 100kW, making it quite the easiest African to receive on the 60 metre band. Which reminds me, why do some short wave listener journals published in the U.K. consistently print metre as meter — the latter being the spelling used in the U.S.A.? Perhaps tonite my S-Metre will liven-up on 60 meters. Oh dear!

#### • NORTH YEMEN

Sana'a on a measured 4853 at 1752, OM in Arabic with a programme for the local police force. This transmission is in the Domestic Service which is also radiated on the parallel channels of 7235 and on 9780. The schedule is from 0300 to 0700 (on Fridays to 1000 approx.), from 1100 to 2015 on 4853, 7235 and 9780 (the 4853 channel signs off at 0700 except on Fridays). From 2015 to 2200 sign-off on 7235 and on 9780. Sana'a is the capital city of North Yemen, it is walled and is 7,270ft above sea-level.

#### TANZANIA

Dar-es-Salaam on **5050** at 1848, when radiating a programme of local orchestral music with announcements in Swahili. This transmitter operates the Commercial Service in Swahili and the schedule is from 0300 to 0500 and from 1400 to 2015, the power being 10kW. Dar-es-Salaam is the seaport capital of Tanzania, situated on the shores of the Indian Ocean, the main activity is that of oil refining.

### CENTRAL AFRICAN REPUBLIC

Bangui on a measured 5038 at 1852, local pops on records, announcements in French. Schedule is from 0430 to 0730 and from 1630 to 2300, the power is 100kW.-Bangui is on the banks of the river Ubangi near the border with Zaire (formerly Republic of the Congo).

### MALAWI

Blantyre on **3380** at 1828, OM and YL alternate with a local newscast in English. Schedule is from 0257 to 0520 and from 1750 to 2215 (from April to September the first period extends to 1100 and the latter commences at 1300); the power is 100kW. Blantyre is in the Shire Highlands and is the capital city of Malawi. Being linked to Beira (Mozambique) by rail, Blantyre is the commercial centre of the country, tobacco is one of the main exports.

### PAKISTAN

Islamabad on a measured 4737.5 at 0118, YL with a mournful song in vernacular, no accompaniment all very sorrowful! This is Islamabad relaying PBC Rawalpindi Home Service from 1300 to 1810 from November to February according to the published schedule — obviously now very much amended. Islamabad is the capital of Pakistan and lies below the Himalayas, just to the north of Rawalpindi, it boasts a nuclear power station.

### DECEMBER 1976

### • CHINA

Radio Peking on 4460 at 2014, YL with a talk in Standard Chinese in a programme of the 1st Domestic Service. The schedule is from 2000 to 2335 and is also in parallel on 7935.

Radio Peking on **4500** at 2025, OM in Russian to the U.S.S.R., jammed by a relay of Moscow 1. Schedule is from 2000 to 2055.

Radio Peking on 4800 at 2050, YL with songs in Chinese in the 1st Domestic Service, schedule from 2000 to 0100.

Radio Peking on 9020 at 2118, orchestra and chorus with local music and songs in the 2nd Domestic Programme, schedule from 2100 to 1600.

Radio Beijing on **3920** at 2008, YL with a talk in Chinese in the 1st Domestic Programme, schedule from 1100 to 1735 and from 2000 to 0100.

PLA Fuzhou on **3900** at 1820, programme of Chinese orchestral music. This is the Fukien Front station with Network II programmes in Amoy scheduled from 1115 to 1900. The programmes are intended for the offshore islands and Taiwan. Be careful however when listening on this frequency, the Radio Peking Foreign Service in Mongolian is relayed by Hailar from 1400 to 1500 as is the Radio Peking Domestic Service in minority languages (in this case Mongolian) from 2230 to 2325.

#### AUSTRALIA

ABC Brisbane on **4920** at 1907, local and U.K. pop records, OM announcer. The schedule is from 1900 to 1402 (Sundays from 1930) and the power is 10kW. The town of Brisbane is a port and the capital of Queensland, it has a university and extensive docks, main exports being meat, wool, hides and skins. Oil refining is another commercial venture.

### EQUATORIAL GUINEA

Radio Equatorial, Bata, on a measured **4926** at 2050, guitar and drums, OM's in chorus. The schedule of this one is from 0430 to 0630, 1000 to 1600 and from 1700 to 2140, the power is 5kW. Sometimes identifying as "La Voz del Partido", the station has been reported closing as late as 2300 at weekends.

#### ALBANIA

Gjirocaster on a measured 5057 at 0436, a programme of typical local music in a relay of the Home Service. The schedule is from 0430 to 1830 and the power is 50kW.

#### • SWAZILAND

TWR (Trans-World Radio) Mpangela on **3240** at 1829, U.K. pop records programme with annoucements in English. This station broadcasts in English and Afrikaans according to the following schedule — 0515 to 0700 (Mondays and Wednesdays from 0445; Thursdays and Sundays from 0500); in local vernaculars from 1700 to 1830 (Sundays from 1800); in English from 1800 to 2100 and the power is 30kW.

### NOW HEAR THIS

#### **COLOMBIA**

La Voz del Caqueta, Florencia on 5035 at 0445, local music programme, OM with song in Spanish, three long drawn-out chimes followed by a chord on a Hawaiian guitar before and after identification with echo-effect at 0452, followed by announcements with several mentions of Bogota then off after a trumpet fanfare at 0457, without National Anthem.

# CONSTANT CLAUDIO AMPL A quality battery

The output quality of many battery operated audio power amplifiers is not very high, and often the level of distortion is so great that anyone using the amplifier is very much aware of its presence. Some simple output stages have total harmonic distortion levels of up to about 50% at high outputs.

The distortion is due to the simple designs employed in most of these circuits, which are usually kept fairly basic in order to keep their cost low. It is not necessary to tolerate such poor quality though, and it is possible to produce a reasonably simple design that is capable of quite acceptable output quality.

Such an amplifier forms the subject of this article. The amplifier was intended as a simple add-on unit for an f.m. tuner (such as the 'Phase Locked Loop F.M. Tuner' described in this and last month's issues. — Editor) but it will have many other applications. It has an output power of about 300mW r.m.s. at 1kHz into a 15 $\Omega$  speaker, and the t.h.d. level is no more than a few per cent for output powers up to this level. At higher outputs the signal is clipped, and in consequence distortion rises rapidly. Satisfactory operation can be obtained using any speaker impedance in the range of 8 to 80 $\Omega$ . The maximum output power will be significantly reduced, however, if a load of more than 25 $\Omega$  impedance is used. An input level of approximately 30mV r.m.s. is required for full output. The input impedance of the amplifier varies with changes in the setting of the volume control, but is generally in the region of 10k $\Omega$ .

A simple top-cut tone control is incorporated in the circuit.

Provided the amplifier is used with a reasonably good speaker the sound quality obtained is very pleasant, and the unit certainly achieves its main design aim.

### CONSTANT CURRENT LOAD

Normally an audio power amplifier relies upon the use of quite large amounts of negative feedback to reduce distortion. Simple battery powered circuits do not always employ much feedback, and can use a configuration like that shown in the skeleton circuit of

### This battery operated a.f. aroli current load for the output stae proved quality (r

Fig. 1(a). This is really just an ordinary common emitter amplifier driving a complementary emitter follower stage.

There is another method of obtaining a low level of distortion from an amplifier, although this technique is mainly encountered in high quality stereo amplifiers. This second approach consists of using constant current loads in voltage amplifier stages. In the present amplifier the only voltage amplifying stage is the driver, and the general scheme of things is shown in Fig. 1(b).

In this diagram the silicon transistor TRA provides



Fig. 1(a). Skeleton audio amplifier circuit representative of a simple driver and output stage (b). Here, the collector load for the driver transistor is a constant current source

# URRENT IFIER y design

oplifier incorporates a constant ge driver transistor to give imof reproduction.

> the constant current. A stabilized voltage of about 1volt negative of the positive supply rail is applied to its base, whereupon the voltage at its emitter is positive of this by about 0.65 volt. In consequence, what is effectively a stabilized voltage appears across the emitter resistor RA. Since the value of this resistor is constant, the current flowing in it will be constant as well. The emitter and collector currents of a high gain transistor are virtually identical, the only difference being that that the proportionally very small base current flows additionally in the emitter. Thus, the collector current of TRA can also be considered as a constant current, and this current will flow in the collector load if the latter has a sufficiently low impedance.

In Fig. 1(b) the load is the common emitter amplifier, TRB. RB biases this transistor so that its collector voltage is mid-way between the negative rail and the emitter of TRA.

Input signal currents at the base of TRB are amplified and fed to the bases of the output stage transistors. There are consequent voltage excursions at TRB collector but the standing collector current is unaffected by these, whereas the standing collector current of the driver transistor in Fig. 1(a) varies with voltage since the collector load is a fixed resistor.

The fact that TRB operates with a constant current source as load allows its hfe figure to remain unaltered. The hfe of a transistor varies with collector current and, in Fig. 1(a), the fixed resistor load causes such changes and hence introduces distortion.

In a practical version of the circuit of Fig. 1(b) there is still a degree of distortion, because no constant current generator is perfect and because the output transistors will themselves introduce some distortion. Nevertheless, amplifiers incorporating a constant current load give appreciably lower levels of distortion than do amplifiers with simple resistive load circuits.



### By R. A. Penfold

### COMPONENTS

Resistors

(All fixed values  $\frac{1}{4}$  watt 5% unless otherwise stated) R1 5.6k  $\Omega$ 

R2 4.7k Ω

R3 10k Ω

R4 22k Ω

R5 220 Ω

R6 220  $\Omega$  pre-set potentiometer, 0.1 watt horizontal

 $\begin{array}{c} \mathbf{R7} \ 100 \mathbf{k} \ \mathbf{\hat{\Omega}} \\ \mathbf{R8} \ 2.2 \ \mathbf{\Omega} \ \frac{1}{2} \ \text{watt} \end{array}$ 

R9 2.2  $\Omega$   $\frac{1}{2}$  watt

VR1 22k  $\Omega$  potentiometer, log

VR2 1M  $\Omega$  potentiometer, log

Capacitors

C1 100#F electrolytic, 10 V. Wkg. C2 10#F electrolytic, 10 V. Wkg. C3 470#F electrolytic, 10 V. Wkg. C4 0.001#F polystyrene

Semiconductors TR1 2N3703

TR2 2N3703 TR3 BC109C TR4 AC176 TR5 AC128

Sockets SK1 3.5mm. jack socket (see text)

SK2 3.5mm. jack socket (see text)

Switch

S1 s.p.s.t. rotary (see text)

Miscellaneous Instrument case type BV1 (Bi-Pak) Veroboard, 0.15in. matrix Veropins, 0.15in. 3 control knobs 9-volt battery (see text) Battery connector Nuts, bolts, wire, etc.



The controls and input and output sockets are mounted on the front panel. All the remaining components are assembled as a Veroboard module



Fig. 2. The circuit of the constant current audio amplifier. TR2 provides the constant current collector load for TR3

### PRACTICAL CIRCUIT

The complete circuit of the 'Constant Current Audio Amplifier' is shown in Fig. 2. The input signal is applied to the volume control, VR1, and from here it is fed to the base of the driver transistor, TR3, via d.c. blocking capacitor C2. TR2 is the constant current source transistor, and its base is stabilized at about 1.2 volts below the positive supply rail by the circuit incorporating R1, R2, R3 and TR1. TR1 is employed as an amplified diode. It is necessary to have a low voltage here since the greater the voltage across the emitter resistor, R5, the less the maximum peak-topeak output voltage available from the amplifier.

R6 provides the usual small forward bias to the output transistor bases in order to reduce crossover distortion to an insignificant level. Using a resistor to provide this bias voltage in a circuit with a resistive load can result in quite large variations in the standing current in the output transistors, as the bias voltage tends to change with variations in the battery voltage. This does not occur here because the resistor appears in a constant current circuit, and the voltage dropped across it is therefore stabilized.

TR4 and TR5 are the complementary emitter follower output transistors. Germanium devices are used here as these have lower base-emitter threshold voltages than have silicon types, and they thus provide a greater peak-to-peak output voltage swing. R8 and R9 are current limiting resistors and help to guard against thermal runaway in the output transistors. C3 is the output d.c. blocking capacitor.

R7 and R4 bias the amplifier and also provide negative feedback. If VR2 is adjusted to insert minimum resistance into circuit, C4 is effectively shunted across R7. The reactance of C4 becomes significant at the higher audio frequencies and it reduces as frequency increases. In consequence, C4 causes negative feedback to increase and amplifier gain to reduce at these frequencies, producing a treble cut. C4 has an increasingly reduced effect as VR2 is adjusted to insert more resistance into circuit, and it has no effect at all when VR2 inserts maximum resistance. VR2 thus acts as a simple top-cut tone control.

S1 is a rotary on-off switch, and C1 is the only supply bypass capacitor used in the circuit.



The Veroboard panel mounted in position on the bottom of the case


Fig. 3. Component assembly on the Veroboard and the wiring to the front panel controls and sockets

### CONSTRUCTION

Apart from those which are mounted on the front panel, the components are assembled on a piece of Veroboard of 0.15in. matrix having 15 holes by 24 strips. Details of this board are given in Fig. 3.

First cut out the board from a larger piece, then drill out the two 6BA clear mounting holes. A single break in the strips is required and this is made with the aid of a Vero spot face cutter or a small twist drill.

The components and link wires are then fitted and soldered into circuit. 0.15in. Veropins are employed at the points where external connections are made to the board. R6 is a miniature pre-set potentiometer having 0.2in. spacing between track tags, and 0.4in. spacing between the slider tag and the track tags. These tags will need to be spread out slightly to fit into the holes in the board.

The amplifier is housed in a case type BV1, available from Bi-Pak. This measures approximately 8 by 5<sup>‡</sup> by 2in. (203 by 133 by 51mm.) and the drilling required in its front panel is shown in Fig. 4. As may be seen from the photograph of the case interior, the component board is mounted inside the case to the left, as seen from the front. The two 6BA mounting screws are near the centre of the case, and ‡in. spacers are fitted to ensure that the underside of the Veroboard is well clear of the case bottom.

The front panel components are next fitted. SK1 and SK2 are jack sockets of open construction, giving an automatic chassis connection to the sleeve contact by way of their mounting bushes. S1 can be any 2-way rotary switch. The author used a multi-pole switch with no connections made to the unused poles.

Wiring between the Veroboard and the front panel components is now completed. Thin flexible insulated wire is employed and there is no need to use screened leads anywhere.

There is plenty of space for the battery on the right hand side of the case, and in the interests of good economy it is advisable to use a high capacity type. The author employed six HP7 cells in series, these being contained in a plastic battery holder. However, any fairly large 9 volt battery, such as the PP6 or PP7, can be used. The battery may be secured in place with a simple clamp. Alternatively, a piece of plastic foam may be glued to the lid of the case over the battery position, and this will hold the battery in place when the lid is fitted.







This view shows the front panel wiring as seen from the rear

### ADJUSTMENT

The only adjustment that is required before the completed amplifier is ready for use is to set up R6 to give the requisite quiescent current in the output transistors. Initially, R6 is set to insert minimum resistance into circuit, with its slider turned fully anti-clockwise. No input is applied to the amplifier.

Connect a testmeter switched to a high current range in series with the positive battery lead. If there have been no mistakes in the wiring the current drawn by the amplifier will be of the order of 3mA. Should a much higher reading be given, switch off at once and check for wiring errors.

If the reading on the high current range indicates that it is safe to do so, switch the testmeter to a lower current range which will enable currents of around 10mA to be measured.

The quiescent current required in the output transistors is about 4mA. R6 is next slowly adjusted to insert increasing resistance into circuit until the meter gives an indication which is 4mA greater than the in-itial reading. Thus, if the first reading was 3mA, R6 is adjusted for a current of 7mA. Avoid setting R6 for current readings above this level as this will result in a shortened battery life. There could even be a risk of thermal runaway in the output transistors if the potentiometer were grossly over-adjusted.



An alternative view of the completed amplifier, emphasising the neat appearance imparted by the instrument case in which it is assembled

After R6 has been set up the testmeter is removed and the normal battery connections completed. The amplifier is then ready for use.

# NOVEL STEREO

### An ingenious application for a 7segment l.e.d. display.

Many stereo f.m. tuners use an MC1310P phase locked loop i.c. as the stereo decoder, with a lightemitting diode as the stereo beacon. It occurred to the writer that a more striking effect could be obtained by using a 7-segment l.e.d. display as the stereo beacon. The figure 5 when displayed looks more like a letter "S", giving "S" for Stereo! The display used was an 0.3in. red common anode type, such as the DL707, SLA7 or equivalent.



#### Fig. 1. Connecting up the 7-segment display to function as a stereo beacon

# L.E.D. BEACON

### By R. N. Soar

proximately 10mA to flow in each segment and this is sufficient to give a bright display. For a 9 volt supply the resistor values may be reduced to 680  $\Omega$ . Incidentally, the maximum stereo beacon current rating for the MC1310P is 75mA.

If the display is a TIL302 or equivalent it is necessary to connect pins 3 and 9 to the positive supply in addition to pin 14. This is because the TIL302 has three common anodes. For the record, Fig. 2 shows the pin connections for a TIL303, which has the decimal point on the right.

In the author's tuner a rectangular hole was filed out in the metal front panel, and the display was glued in place with the pins and connecting wires hidden behind the panel.

SLA7



Fig. 2. Pin allocations for the TIL303 7segment common anode display

### D.I.L. PACKAGE

The 7-segment display is housed in a modified 14 pin d.i.l. package with unused pins omitted. The remaining pins are numbered to correspond with a normal 14 pin d.i.l. package. Fig. 1 shows a top view with the pins underneath.

Pin 14 is for the common anode and is connected to the positive supply. Pins 1, 2, 8, 10 and 11 are taken to pin 6 of the MC1310P via current limiting resistors R2 to R6. The decimal point, pin 6 of the display, is taken to chassis, the negative rail, by way of R1.

When the tuner is switched on and is receiving a mono signal the decimal point is illuminated: When a stereo signal is being received the "S" display lights up. The series resistors allow a current of ap-





An alternative method of presentation could consist of having segments A., G and D light up when the tuner is switched on and receiving a mono signal. Segments F and C are coupled to pin 6 of the MC1310P, as in Fig. 3. No connection is made to the decimal point. Three horizontal bars are then given for a mono signal, these changing into the letter "S" when a stereo signal is tuned in.

# The CA3130 COS/MOS OP-AMP

### By J. B. Dance

Currently available on the home-constructor market is the RCA COS/MOS linear operational amplifier type CA3130. Our contributor describes its performance and then gives working circuits for two voltage followers and a wide range pulse generator in which the CA3130 can be employed.

The CA3130 is a fairly new operational amplifier i.c. manufactured by RCA. One of its main advantages is that it is one of the few high impedance, high gain amplifiers which is cheap and readily available. It has an input impedance of about  $1\frac{1}{2}$  million megohms; if one connects its input to any circuit the current taken from that circuit is therefore very small indeed. A typical input current is only 5pA (five millionths of a microamp).

#### COS/MOS

This very high input impedance is obtained by the use of COS/MOS techniques; that is, Complementary Metal Oxide Silicon field-effect transistors. One can connect the inputs of this device into almost any circuit without loading the latter appreciably. Although COS/MOS devices are used in the input

Although COS/MOS devices are used in the input stage the second stage of the i.c. is a high gain circuit employing conventional bipolar transistors. However, COS/MOS devices are used in the third, output, stage and these enable the output voltage of the i.c. to swing to within a few mV of the potential of either supply line if the load impedance is fairly high.

The first stage provides a voltage gain of only 5 times, and acts as an impedance transformer by providing an output of low impedance. The second stage provides a voltage gain of 6,000, whilst the output stage gives a typical gain of about 30 times. Thus, the total gain is well over 100dB.

### CONNECTIONS

The CA3130 is available only in circular metal packages with eight leads, the connections being as shown in Fig. 1. The package is similar to a TO-5 type transistor encapsulation. The CA3130T has straight leads, whilst the CA3130S has its leads formed into a dual-in-line pattern so that it can fit into an 8-pin dual-in-line socket. More expensive types are available which have the suffix 'A' or 'B' in their type number. These have more closely controlled input circuit specifications, but the normal CA3130T is suitable for most purposes. The CA3130 has the normal inverting and noninverting connections of a conventional operational amplifier. Any increase in the potential of the noninverting input produces an increase in the output potential, whilst an increase in the potential of the inverting input produces a decrease in the output potential. Thus the inverting input is used for the application of negative feedback.

### **VOLTAGE FOLLOWER**

High input impedance devices such as the CA3130 are very useful as voltage followers; that is, they are employed in circuits in which the output voltage 'follows' the input voltage, the impedance at the output being much lower than that at the input. One can



Fig. 1. Pin connections for the CA3130

therefore measure the voltage at the input by connecting the output to a voltmeter or other circuit which requires an appreciable current. The CA3130 output can supply or accept a current of up to 20mA, this being enormously greater than the input current required by the device.



Fig. 2. A voltage follower circuit incorporating the CA3130

A typical voltage follower circuit is shown in Fig. 2. The non-inverting input of the CA3130 is connected via a protective  $10k\Omega$  resistor to the source of voltage. A 56pF capacitor connected between pins 1 and 8 gives frequency compensation and prevents possible instability at high frequencies. Negative feedback is taken from the output at pin 6 through R2 to the inverting input. Since the full output voltage is fed back the gain is unity, and the output follows the input voltage. The capacitor C4 prevents excessive overshoot on transients.

The power supply shown in Fig. 2 is 7.5 volts positive and negative of ground, this being slightly less than the maximum permissable voltage for the device of 8 volts positive and negative. Although the device will operate with positive and negative supplies as low as 2.5 volts, the use of a supply voltage near the maximum enables a fairly wide range of input voltages to be catered for. If the output is not delivering a current, the power supply current required is typically 10mA when the output is near ground potential or 2mA when the output is near the potential of either supply line. C1 and C3 are included to prevent possible instability.

In the circuit shown, the input may be connected to any voltage to be measured which has a value between the two supply line potentials. A voltmeter connected between the output of the device and ground will then read the input voltage.

### SINGLE SUPPLY

A somewhat similar circuit is shown in Fig. 3, in which a single supply line is used instead of the split positive and negative supply lines of Fig. 2. The potentiometer VR1 connected between pins 1 and 5 enables the offset voltage to be adjusted. Setting up VR1 enables the output voltage to be made equal to the input voltage. The maximum permissable supply voltage is 16 volts and the input voltage should be kept between zero volts and the positive supply voltage.



Fig. 3. Another voltage follower with a single supply rail

### PULSE GENERATOR

The exceptionally high input resistance of the CA3130 is an attractive feature for pulse generator circuit design because it permits the use of high values of resistors and, therefore, correspondingly low values of capacitors. One cannot use electrolytic capacitors if one requires a stable frequency, so the use of a high input impedance device enables reasonably small capacitors to be employed for generating low frequency outputs.

A CA3130 square wave generator circuit is shown in Fig. 4. Four frequency ranges are provided and are selected by S1. When C2 is switched into circuit the pulses have a duration of  $4\mu$ S to 1mS according to the settings of VR1 and VR2. The pulse duration with C3 is  $40\mu$ S to 10mS, with C4  $400\mu$ S to 100mS and with C5 4mS to 1S.

The times during which the output pulses are in their high and low voltage states (the 'duty cycle') can be independently controlled by VR1 and VR2. At a time when the output voltage is high, the potential at the inverting input will be low and the capacitor selected by S1 will be charging through VR1, R4 and D1, the charging rate being controlled by the setting of VR1.

When the potential at pin 2 rises above that at pin 3, the voltage at the output will suddenly be switched to a low value. The same timing capacitor now discharges through VR2, the rate of discharge being



Fig. 4. Wide range pulse generator in which the length of positive and negative pulses can be adjusted independently

determined by the setting of this potentiometer.

After a time the circuit will switch back to its former state in which the output is high and the capacitor selected by S1 will commence to charge again. The polarity of the diodes D1 and D2 determines the direction of the flow of current through VR1 and VR2.

An output attenuator, VR3, may be included if required. It should be noted that the output potential is not centred about ground; a capacitor in series with the output may be used if necessary. The maximum output amplitude is approximately equal to the supply voltage.

### PRECAUTIONS

The very high input impedance of all COS/MOS devices renders them liable to damage by electrical transient pulses. Although the CA3130 is protected by means of internal zener diodes which become conductive when the input voltage exceeds a certain limit, the manufacturers recommend that reasonable precautions should still be taken. In particular, all soldering iron tips used for soldering the CA3130 connections should be earthed.

It is wise not to solder the devices whilst power is

being applied to them. No input signal should be applied unless the power supply is connected, and the possible input current should be limited to 1mA by a suitable series resistor.

#### STROBING

Apart from its use for frequency compensation, pin 8 of the CA3130 can also be used for strobing the amplifier. When this pin is connected to the negative supply line at pin 4, the output potential at pin 6 rises to a value which is very close to the positive supply at pin 7. The amplifier remains in this quiescent state for as long as the potential of pin 8 is kept low.

This strobing of the amplifier into the off state can be effected by a mechanical switch or by electronic means. The strobing pulses may be synchronised with changes in the input circuit, such as input switching. Alternatively, the strobing facility may be used as an On-Off facility.

On-Off facility. When the CA3130 is strobed into the quiescent state, a condition of almost zero current drain can be attained if the ohmic load resistance presented to the amplifier output is very high, as occurs, for example, when it is used to drive COS/MOS digital circuits in comparator applications.

### **Mail Order Protection Scheme**

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# PHASE LOCKED LOOP F.M. TUNER

VR, (chassis)

Part 2 By R. A. Penfold

In this concluding article, outstanding details of construction are dealt with, after which a description is given of the single setting up operation that is required to bring the tuner into operational order.

The article which appeared in last month's issue described the circuit functioning of this rather unusual f.m. tuner. Also dealt with were the drilling of the receiver chassis and the assembly of the main component board, these being illustrated by Figs. 5 and 6, both of which were published last month. We now carry on to the mounting of the board in the case.

### FITTING THE BOARD

S1, VR1 and sockets SK1 and SK2 are fitted to the chassis as shown in Fig. 5. The main component board is next mounted in the approximate position indicated in that diagram. It is fitted by means of two  $1\frac{1}{2}$ in. 6BA bolts with nuts, metal spacers being used to hold the board about 1in. clear of the bottom of the case.

The board is then wired to the external components by means of ordinary thin flexible p.v.c. covered wire. The lead from the aerial socket centre conductor passes direct to the tap in L1, and does not require coaxial cable. The outer conductor of the aerial socket connects to chassis via its own mounting. The lead from the board to SK2, VR1 and battery negative connects first to the tag of SK2 which, by way of its mounting bush, is common with chassis. The appropriate socket tag may be identified by visual inspection or by means of a continuity tester or ohmmeter. A further lead then travels from this tag to the tag of VR1 which is at the track end corresponding to maximum anti-clockwise rotation of the potentiometer spindle. Also connected to this tag of VR1 are the negative battery lead and, later, the earthy lead from the oscillator board. The connection to the main component board from the oscillator board is made when the latter has been assembled and mounted in position inside the case.

### OSCILLATOR BOARD

The oscillator transistor and its associated components are mounted on another plain perforated panel of 0.1in. matrix, this having 23 by 17 holes. Details are given in Fig. 7.

This board is cut out and assembled in a similar manner to the main component board. Coil L2 is identical with L1, the only exception being that there is no tap on L2. The positions of the connection points for trimmer TC1 may vary slightly from those shown in Fig. 7 according to the dimensions of the actual





trimmer employed. When mounted, the board is spaced off from the case bottom in the same way as is the main component board. The approximate positioning of the board is shown in Fig. 5.

The oscillator board obtains its positive supply from the same tag of S1 which supplies the main component board. The positive battery lead then connects to the remaining tag of S1. As a final touch, four rubber feet are secured to the underside of the case near the corners, either with adhesive or by means of nuts and bolts passed through holes already drilled for them.

All the wiring should next be given a thorough visual check. When all is ready, the conductive foam or metal foil may be taken from the pins of the integrated circuit and it is then carefully plugged into the i.c. holder. Ensure that it is fitted right way round. It should be remembered that the i.c. used in the tuner is a COS/MOS device which can be damaged by high static voltages. As a precaution, it can be removed from the holder if any subsequent soldering work is carried out on the tuner.

There is plenty of room for the PP3 battery to be fitted vertically in the space behind SK2 and VR1. A piece of foam rubber or a similar material can be glued to the underside of the case lid above the battery position, so that the battery is held firmly in place when the lid is screwed on.

### ADJUSTMENT

One single adjustment has to be carried out before the lid can be finally fitted. This adjustment consists of setting up TC1 for correct frequency coverage.

During the initial testing and adjustment an aerial of some sort must be connected to SK1 and the output from SK2 applied to an amplifier and speaker or to a crystal earphone or crystal headphones. A few feet of connecting wire terminated in a coaxial plug will make a suitable aerial in areas where reception is good or fair, but a proper outdoor or loft aerial will be required in poor reception areas When using a simple wire aerial, the positioning of the aerial inside the room will greatly influence results. It will be



Illustrating the components mounted on the oscillator board



A view showing the front panel components from the rear

necessary to experiment a little with the placing of the aerial in order to find the position that corresponds to the strongest signal. This point should be kept in mind when the following adjustment is being carried out.

Initially set TC1 well towards minimum capacitance, and then switch the tuner on. The characteristic high level of background noise should be produced by the tuner when it is not tuned to a transmission. Adjusting VR1 should enable a few stations to be heard, but if none can be picked up TC1 should be adjusted for increased capacitance in order to increase the frequency coverage of the tuner. Also, L2 can be slightly stretched out or compressed to shift the tuning range slightly.

TC1 is given a setting that provides coverage of the required stations. Adjusting TC1 for increased capacitance greatly increases the low frequency range, but at the expense of the high frequency coverage. If adjusting TC1 for sufficient tuning range causes stations to be lost at the high frequency end of the band, L2 can be slightly stretched out. This will shift the frequency coverage towards the high frequency end of the band, and bring the lost stations back into the range of the tuner. Do not adjust TC1 for greater coverage than is really needed, as this will only make the tuning unnecessarily sharp.

It will be possible to tune each station twice, once with the oscillator frequency just above the signal frequency, and once with it just below. F.M. stations are well spaced out in terms of frequency and so this does not have any undesirable effects. One simply tunes to whichever tuning position is reached first.

In a normal superhet the r.f. tuned circuit would be used to accept one of these responses and reject the other. This is not feasible here as the very low i.f. brings the responses too close together for the r.f. tuned circuit to reject one and yet still accept the other one. In consequence, a broadband r.f. tuned circuit is used, which considerably simplifies the setting up of the tuner. Theoretically, L1 could be stretched out or compressed to peak the r.f. response at the centre of the tuning range. However, in practice this tuned circuit has a very wide bandwidth, and the inductance of L1 is not at all critical.

As is the case with any f.m. tuner, the stronger the aerial signal the lower the noise level that is obtained. It is therefore important that the aerial be orientated for the best signal, and hence also for the maximum signal to noise ratio.



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In this introduction to semiconductor devices, the author provides a comprehensive survey of modern active and non-active semiconductor technology. Without leaning too heavily on device physics, he explains device functions and then illustrates their use with typical circuits and applications.

Following a summary of the physical basis of semiconductor elements in non-mathematical terms — a study of bipolar and field-effect transistors leads to considerations of monolithic integrated circuits. More advanced charge-coupled devices, semiconductor memories and optoelectronic devices are studied in some detail.

### CONTENTS

- 1. Semiconductors
- 2. Basic Semiconductor Devices

3. Semiconductor Diodes and the Unijunction Transistor

- 4. Bipolar Junction Transistors, Amplifiers & Logic Gates
  - 5. Field-Effect Transistors, Amplifiers and Logic Gates
    - 6. Monolithic Integrated Circuits
      - 7. Charge-coupled Devices
      - 8. Semiconductor Memories

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This month we find Dick and Smithy at a Christmas Eve when, for once, they are not engulfed in an ocean of urgent repairs. Smithy takes advantage of the situation to demonstrate to his assistant the operation of his latest creation, an electronic dice.

"Christmas," grunted Dick. "Huh!" The Serviceman's assistant flopped,

scowling, on his stool. "Dear, oh dear," said Smithy from his bench; "what's wrong with you? This is Christmas Eve and you're sup-posed to be happy. What's upset you now?" "Everything!"

"Such as?"

"Well, for a start, we're usually dead busy on Christmas Eve. You feel really ready for Christmas when you've cleared out a good bit of work

"We were lucky this year," stated Smithy. "We got everything cleared up nice and early. In fact, there's a good three hours to go before we officially pack in for the day."

"Huh!"

"Dash it all," remonstrated Smithy, "tomorrow's Christmas Day. Aren't you even looking forward to your Christmas dinner?"

"No."

### **DINNER AT EFF'S**

Smithy sighed and glanced at his assistant's brooding features.

"When you get the blues," he remarked irritably, "everybody else has got to get out and push. Where will you be having your Christmas dinner?"

"That's part of the trouble," replied Dick morosely. "I've got to have it at my Auntie Eff's. Ye gods, I'm dreading it. I bet she'll have her cats walking all over the kitchen table when she's stuffing the turkey."

A gleam came into his eve. "She's so short-sighted that one day she might stuff one of the cats instead. Hah!'

"Oh, come on," snorted Smithy. "As it happens, I'd been looking forward to this quiet period just before Christmas to show off my latest electronic gadget to you. But you're so darned grouchy that I don't think you'll even appreciate it."

"Gadget? What gadget?"

"It's a gadget I've been making up in the evening as a little exercise in logic." "Oh yes?"

It was obvious that Dick was already beginning to forget his qualms about the morrow. Whenever his avid curiosity concerning electronic matters was aroused all other emotions faded into limbo.

"It's an electronic dice," stated Smithy proudly. "When you turn a switch it displays any number from 1 to 6."

Smithy got off his stool, reached inside the cupboard under his bench and produced a small aluminium box. As he placed it on the bench Dick walked

over eagerly to examine it. "Now, let's have a look at this," he remarked keenly. "There don't seem to be many things on the top panel." "There aren't," said Smithy.

"There's just an on-off toggle switch, a two-way rotary switch and a TIL302 seven-segment l.e.d. display. I'll turn, on the toggle switch."

Smithy clicked the switch on. At once, all the segments of the l.e.d. display lit up, to indicate the figure 8. There was a slight but just noticeable flicker in the segments.

"Turn that rotary switch to the right," said Smithy. Dick reached forward and turned

the switch. The flickering 8 gave way to a steadily illuminated figure 4. He turned the switch back, to produce the 8 again, then turned it to the right again. This time the display showed the number 2.

"Hey," he chuckled, "you can get hooked on this thing. Let's try it again.'

He operated the switch back and forth once more. On this occasion the l.e.d. display extinguished completely.

"What's happened here?" queried

"I'know," grinned Smithy. "I should have told you that the random generator circuit in this gubbins of mine offers eight options instead of six. Six of the options give the figures 1 to 6 and the other two cause the l.e.d. segments to extinguish. It's rather the same as you get when you're playing with an ordinary dice and it hops off

"How," asked Dick, switching off the dice, "do you get eight options?" "By combining the outputs of three

50:50 multivibrators," replied Smithy. "Bring your stool over and make yourself comfortable, and then I'll explain it all to you."

As Dick walked back to his bench, Smithy pulled his note-pad towards him and started to make up a sketch.

(Fig. 1). "Now," said Smithy, pointing to his note-pad after Dick had returned and settled himself on his stool. "Here are the three multivibrators, and I've drawn them in block form. The first multivib has two outputs which I've labelled T and V. When output T is at a low voltage output V is at a high voltage, and vice versa. We want the multivib outputs to light up individual segments in a common anode l.e.d. dis-

play, and they are able to do this when they are in the low state." "So output T is only of use," questioned Dick, "when it is in the low condition?"

"That's right," confirmed Smithy. "We'll be using some of the outputs when they're in the high state later, but for the time being we are only interested in a multivibrator output when it's low."

"Fair enough. Do the three multivibrators run at different frequen-cies?"

"They do."

"And what are those disabling switches?"

"They disable the multivibrators when they're opened, whereupon each multivib remains in the state it had at the instant of opening. The three switches are all ganged together in a single 3-pole component so that, when this is operated, all three mul-



Fig. 1. The random factor in Smithy's electronic dice is provided by three multivibrators running at different frequencies

		٦
4	TWY	
	TWZ	
	TXY	
	тхг	
	VWY	1
	VWZ	
	VXY	
	VXZ	
		1

#### Fig. 2. The three multivibrators offer eight output combinations

tivibrators stop running at the same time. Since they're 50:50 mul-tivibrators, the three sets of outputs can then produce, in completely random manner, one of eight combinations."

"Eight combinations, eh," repeated Dick thoughtfully. "Well, for a kick-off you could have T, W and Y in the

low state after the switch opens. After which you could have T, W and Z." "You've got the idea," stated Smithy, pleased with Dick's im-mediate grasp of the situation. "Let's write down all the switch. write down all the possible com-binations. You've already given me two, and these can be followed by TXY and TXZ. The next two can be VWY and VWZ."

"And," chimed in Dick, "the last two would then be VXY and VXZ. And that's the lot." "It is, indeed," confirmed Smithy,

writing down the last two combinations.

The pair looked at the completed list. (Fig. 2).

### SEGMENT SWITCHING

"We now," said Smithy, "come to what proved, so far as I was concerned, to be the hardest part of the design. I had to dream up a method of having six of the combinations light up the segments to form the numbers 1 to 6. I accepted the fact that two of the combinations would be redundant, and that it would be a very simple matter to have them operate a NAND gate which would extinguish the display. I could have routed the remaining six combinations through standard gates to the various l.e.d. segments but I wanted to see if I could control the segments with nothing more com-plicated that simple diodes. As it happened, I was able to work out a method which enabled the combinations to directly control the numbers 2 to 6, and I only had to call in another NAND gate to give me the figure 1."

Smithy reached in his cupboard again and produced a sheet of paper on which he had jotted down several columns of letters as well as the lettered outline of a seven-segment display. (Fig. 3). "Let's take a look at the display

first," he went on. "This is pretty wellknown, of course, with each segment being identified in clockwise order by the letters A to F. The middle segment is then identified as G." "That explains something that's

been puzzling me a little." "What's that?"

"Why you chose to give the mul-tivibrator outputs letters which are at the end of the alphabet. You didn't want to get these confused with the segment letters.'

"You're really with it today," com-mended Smithy. "If you're as bright as this today, your Christmas dinner tomorrow should be no trouble at all."

Dick's brow furrowed as he con-templated his immediate future.

"Christmas Day is going to be bad enough," he said morosely. "What I'm really dreading is Boxing Day. That's going to be murder."

Smithy's eyebrows rose.

"What's happening on Boxing Day?" "I don't," said Dick firmly, "want

to even talk about it. Let's get back to this gadget of yours." "Okay," said Smithy, allowing his interest in Dick's Yuletide activities to abate for the moment. "Where were we?"

"We'd got to the point where you'd "We'd got to the point the end of the used letters from the end of the alphabet to identify the multivibrator outputs. Incidentally, why didn't you include the letter U?" "Because," replied Smithy, "it

tends to get muddled with letter V if you're trying to work out com-binations. Now this is the final solution I arrived at for having the combinations turn on the segments.'



Fig. 3. The segments of a seven-segment display with identifying letters





(a)

Fig. 4(a). The multivibrator outputs turn on the segments in the manner shown here (b). Six of the multivibrator output combinations light up segments in the form of figures

(c). The figures produced by the combinations

Smithy pointed to a column of letters in the centre of the piece of paper he had taken from his cupboard. (Fig. 4(a).)

"I don't quite get this," commented

Dick, puzzled. "You'll see the idea soon," replied Smithy. "First of all, let's go down the column. When multivibrator output T is low, segments C, F and G are lit up. Similarly, when V is low, it is segments A, D and G which turn on. Output W A, D and G which tim on. Output w turns on segment C, output X turns on segment E and output Y turns on seg-ment B. The last output, Z, causes segments A and D to light up." "Some of the segment letters," ob-jected Dick, "appear opposite more than one output."

than one output.

"That doesn't matter," said Smithy. "A segment can be lit up by one output, or by two outputs if the segment is controlled by both."

He pulled his note-pad towards him and proceeded to add further letters to the column of multivibrator output combinations he had compiled earlier.

(Fig. 4(b).) "Right," he said briskly, "as you can now see, combination TWY causes segments B. C. F and G to light up. You'll notice that segment C appears into hot as L said just now, this twice but, as I said just now, this doesn't matter."

"Segments B, C, F and G," repeated Dick slowly. "What does that give us, Smithy?"

TWY		BCFG		4
TWZ		ACDFG		5
ТХҮ	-	BCEFG (Inhibit EFG)		1
TXZ		ACDEFG	<u> </u>	6
VWY		ABCDG	-	3
VWZ		INHIBIT ALL		
VXY		ABDEG		2
VXZ		INHIBIT ALL		

(b) ΤW TXY TW7 TXZ VWY VXY

#### (c)

"Draw them out on my pad," invited Smithy, handing Dick his pen.

Dick drew the segments in their ap-

propriate positions. (Fig. 4(c).) "Why," he exclaimed, "it's the

"It is," confirmed Smithy. "Let's get on to the next combination, which is TWZ. This gives us segments A. C.

"Sure," said Dick, drawing out the segments. "Well, these segments cor-

"That's right," agreed Smithy. "Now, we'll skip the next combination for the moment and proceed

to TXZ. This gives us segments A. C. D. E. F and G. Quite a lot this time." "They're all there except B" stated Dick. "And, ah yes, they form the number 6."

"Good show. They are followed by VWY, and this lights up segments A, B, C, D and G." "Just a minute," remarked Dick,

busy with Smithy's pen. "This one is

"Correct," stated Smithy. "Now VWZ is next and this doesn't form a recognisable number. So it inhibits all the segments and turns them off." "The following combination is

VXY," said Dick excitedly. "This will turn on, let me see now, segments A, B, D, E and G. And it's quite easy to see that these make up the number 2." "Very good," stated Smithy. "The

last output combination, VXZ, is another redundant one, and this also turns off all the segments."

"That's the lot, then," remarked Dick cheerfully. "No, it's not! We haven't done TXY yet." "Ah yes," said Smithy. "This gives figure 1. TXY produces B, C, E, F and G, and I had to add a NAND gate and external transistor to inhibit E. F and G. leaving B and C to form the figure 1. This is the only number which raises the control circuitry above simple diode level."

#### **MULTIVIBRATORS**

"Well," said Dick, "you definitely did a man-sized job sorting out those combinations. Now, how about those 50:50 multivibrators which provide the outputs? Do these use two transistors with cross-coupling capacitors in the usual multivibrator configuration?"

"They could do," said Smithy, "if they had a disabling switch circuit added. However, I felt it would be easier and that there'd be a small sav-ing in components if I used 555 integrated circuits instead. Here's the circuit of one of the multivibrators."

Smithy quickly sketched out the multivibrator circuit on his note-pad.

(Fig. 5.) "Apart from the disabling switch and the external transistor," com-mented Dick, gazing at the circuit, "that seems to be pretty standard to me.'

"It is quite standard," agreed Smithy, "when the disabling switch is closed the circuit runs as a standard 555 multivibrator. The capacitor C1 charges via R1 and R2 and discharges via R2, with the output voltage, at pin 3, going high when the capacitor charges and low when it discharges." "If," said Dick critically, "the

capacitor charges via two resistors and discharges via only one resistor, the output can't be a true 50:50 waveform, can it?"

"It can be as near to 50:50 as dam-mit," retorted Smithy. "The value of R2 is  $1M\Omega$  whilst R1 is only  $1k\Omega$ , which is one-thousandth of 1M So the charge and discharge paths for the capacitor are virtually identical in terms of resistance."

"Yes, I see that now," remarked Dick. "What frequency does the mul-tivibrator run at?"

"At a little under 100 Hertz."

"Blimey, that's low, isn't it?" "It's high enough for the present job. The other two multivibrators run at even lower frequencies.

"Why's that, Smithy?"

"Because," replied the Serviceman, "there's no point in having the multivibrators run at unnecessarily high frequencies. With frequencies as low as I've used here the risks of capacitive couplings between circuits are lower and you don't have to worry too much about general layout. The only high impedances in the circuit are at the disabling switch, and you do, in fact,

have to be a bit careful with the wiring there."

"What," asked Dick, "does the dis-abling switch do?"

"It disconnects the capacitor from the comparators inside the 555," ex-plained Smithy. "If the capacitor happens to be charging when the switch is opened it continues to charge and the 555 output stays high. Similarly, if the capacitor is discharging when the switch opens it continues to discharge and the output stays low. The 555 will only start oscillating again when the disabling switch is closed."

"I'm with it," said Dick. "Why is the output of the 555 coupled to the ex-ternal transistor?"

"To give you two outputs of opposite polarity. When the 555 output is high the transistor is turned on and its collector voltage is low. And when the 555 output is low the transistor is turn-ed off and its collector voltage is high. The circuit I've just drawn is the one which gives the T and V outputs in the complete circuit." "Ah," said Dick eagerly, "you

haven't shown me that yet.'

A glint appeared in Smithy's eye. "All in good time," he said. "But first of all you must tell me what it is that you're worried about on Boxing

Day." "Oh, come on, Smithy. I'm doing my best to forget about it." "Boxing Day," stated Smithy firm-ly, "or no circuit." "Hey," snorted Dick indignantly, "this is blackmail." Smithy looked at him impassively.

Smithy looked at him impassively. "Oh all right then," stated Dick reluctantly, "I'll tell you. It's all to do with the local T.C.P."

"The local what?"

"The local T.C.P.," repeated Dick. "That's the Thespians and Casual Players, and they're putting on an amateur pantomime on Boxing Day. It's going to be Cinderella." "How does that affect you?"

"They're making me do one of the Ugly Sisters," wailed Dick. "A right nit I'll feel out there dressed in drag, and with the blokes in the audience all whistling at me." "I didn't even know you had any

#### R Ikn 4 T n R<sub>2</sub> 555 3 Outputs IMa Disablina switch R<sub>3</sub> Iko TR O.OIµF

Fig. 5. One of the multivibrators in the electronic dice

**DECEMBER 1976** 

aspirations towards acting." "I haven't," said Dick aggrievedly. "I've just been going along with them as a sort of stage electrician and scene shifter. Last night they had their first proper rehearsal for the pantomime and it resulted in them calling me in to replace the chap who was going to do one of the Ugly Sisters." "What happened?"

"Well, you know the scene near the end where the Prince, and he's a woman if you get me, tries to fit the glass slipper on the Ugly Sisters' feet." "Go on."

"Well, the Prince came to this chap who I'm going to replace, and he put out his foot." "Yes?"

"How can I put this?" said Dick, dubiously. "Well, now, have you seen that TV ad where the bloke takes off his shoes and everybody falls down?" "I've seen it several times," said

Smithy. "In fact I've been trying to buy a pair of shoes like those in the ad. but whenever I go into a shoe shop and ask about them they all carry on as though I'm a nut."

Dick looked suspiciously at the Serviceman, but the latter's face bore its familiar guileless expression. "Well," continued Dick uncertain-

"it was the same with this bloke. When the Prince knelt down to try on the slipper, she pretty well fell down, too. There was no end of a row after that with the Prince refusing to continue with the part. So they either had to replace the Prince or the Ugly Sister. They couldn't find anyone else to play the Prince and, even if they could, no one would have have done the slipper fitting bit with that cheesey geyser. And so they had to replace the Ugly Sister. Which is now me."

Smithy inspected his assistant's woebegone visage.

"I think," he remarked encouraging-ly, "they made an excellent choice. To start off with, you've got the physical attributes."

"Come off it, Smithy," said Dick in-dignantly. "Anyway, I've told you all about Boxing Day, so you now show me the full circuit of this electronic dice of yours." "All right," grinned Smithy. "I've



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Fig. 6. Complete circuit of the device. The TIL302 is a seven-segment common anode display with a nominal character height of 0.3in. and the decimal point on the left. Some alternatives, such as the SLA7, have the same segment pinning and a single common anode at pin 14. The display plugs into a

got it all drawn up, so I'll bring it out."

He reached yet again into the cupboard under his bench and produced a large piece of paper, which he laid out on the bench surface. (Fig. 6.)

### COMPLETE CIRCUIT

"Here we are," he announced. "This is the complete circuit. At the left hand side there are the three multivibrators, giving the outputs from T to Z. They run at different frequencies because the capacitor in each has a different value. The outputs couple to a TIL302 seven-segment display. This has three common anodes, at pins 3, 9 and 14, and all the other pins shown are for the segments. If you trace out the circuit lines you'll see that the multivibrator outputs connect to the appropriate segments via  $470\Omega$ resistors, so that the segments con-cerned light up whenever the corresponding outputs go low in voltage. It two outputs go to one segment a diode is inserted in series with each so that one output can go low without in-terfering with the other. The three multivibrators and the diodes from D1 to D8 are the only logic components required for displaying the numbers from 2 to 6." "There seem," said Dick, "to be two

"There are," confirmed Smithy. "One of these is given by TR5 and ZD2, and it offers 5 volts stabilized for the 555's and a 7400 integrated circuit. This, incidentally, has four 2-input NAND gates. The second supply is for the seven-segment display. This is also 5 volts but it doesn't have to be stabilized, and the supply circuit's main function is to provide a simple means of inhibiting the display when the redundant output combinations from the multivibrators are given. If you check back on the list we made up, you'll see that these are VWZ and VXZ."

Dick looked at the column of com-

binations they had discussed earlier. "That's right," he said. "How do those combinations inhibit the dis-play?" "Well," said Smithy, "they're the only combinations with V and Z in

them, so all we have to do is to inhibit the display when V and Z are low. A 2-input NAND gate is an excellent device to use here but it must be remembered that the output of a NAND gate goes low when both its in-puts are high. When V and Z are low T and Y are high, so we connect T and Y to the NAND gate inputs at pins 1 and 2 of the 7400. The result is that, for all combinations except the two redundant ones, the output of the NAND gate, at pin 3, is high and a full 5.6 volts appears across zener diode ZD1. When the redundant combinations appear, the NAND gate output goes low, pulling down the voltage across ZD1 to less than a volt and thereby extinguishing the seven segment display," "Gosh," said Dick, "that's really

neat. What do the other NAND gates do?"

"They cause segments E, F and G to inhibited for the combination be TXY. Again we use the opposite multivibrator outputs, and the inhibiting action takes place when V, W and Z are high. We could use a single 3-input NAND gate here, but I've chosen the three remaining NAND gates in the 7400 instead. When V and W are high the output of the first NAND gate, at pin 6, goes low. The second NAND gate inverts this, passing a high to pin 13 of the third NAND gate. Output Z goes to pin 12, with the consequence that pin 11 goes low when V, W and Z are high. Pin 11 allows a current to flow into the base of TR4 via R17 and D12. This transistor then turns on and pulls the pins for segments E, F and G up close to the positive rail via diodes D9, D10 and D11. The final outcome is that E, F and G cannot be il-luminated and the display shows B and C only.'

"Hell's teeth," remarked Dick, supremely impressed by the sequence of events described by the Serviceman. "That really is something.

Are there any possible potential snags in the circuit, Smithy?" "I didn't encounter any myself," said Smithy. "In fact the whole circuit worked just like a text-book demonstration. There is, however, a very slight risk that the output of a 555 might not go sufficiently low to fully turn off the transistor connected to it. Should this occur, the cure is to add a 470  $\Omega$  resistor between the base and emitter of the transistor concerned. A 470  $\Omega$  resistor can also be added between the base and emitter of TR4 if the NAND gate output, at pin 11 of the 7400, doesn't go sufficiently high to allow this transistor to turn off. If the output at pin 3 of the 7400 doesn't go high enough to allow the full 5.6 volts to appear across ZD1, then one or more extra silicon diodes can be added in series. I had none of these difficulties with my own circuit and I'm only mentioning them now to cover all possible angles."

"Perhaps," suggested Dick, "it might be a good idea to run the multivibrators at a slow speed after the circuit has been assembled."

"That's an excellent idea," agreed Smithy. "If something like 4µF is temporarily across C1,  $6\mu$ F across C2 and  $8\mu$ F across C3, the circuit will run quite slowly with individual numbers and display inhibiting occurring in random fashion. If all the numbers from 1 to 6 appear properly then the circuit is all right and the temporary capacitors can be removed. These can

be electrolytic, incidentally." Dick leaned over, switched on the electronic dice again and turned the rotary switch. The number 6

appeared. "This is certainly a crafty gadget," he remarked. "How much current does it draw?" "It's rather high," replied Smithy.



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"It's about 45mA when the l.e.d.'s are lit up, dropping to about 30mA when they're inhibited. So you need a fairly large battery. Or, of course, you could use a small mains supply instead of the battery. There's one thing I've just remembered, too. The 555 comparator inputs at pins 2 and 6 are a little susceptible to ripple pick-up when the disabling switches are open, so it's best to keep the wiring here reasonably short and clear of mains fields. It helps in this respect to have the complete circuit in a metal case."

### CHRISTMAS CHEER

"Well," said Dick, brightening. "This little session has cheered me up no end. I'm beginning to almost look forward to Boxing Day, even."

But Smithy was once more in-vestigating the interior of his cupboard and as he rose again there came the musical clink of glass against bottle. He charged two glasses and handed one to his assistant.

### "A Merry Christmas, Dick."

"And a Merry Christmas to you too, Smithy.'

They both stood and held their glasses high.

"Let us now," stated Smithy, "wish a very Merry Christmas and a truly Happy New Year to all the readers who've put up with our antics over the last year.'

They drank deeply. "And let us end," concluded Dick, "as on so many previous Christmasses, by saying 'God Bless us, every one!' "



### **By F. T. JONES**

### A brief survey of the methods employed for identifying pins and lead-outs in electronic diagrams.

The identification of pins and lead-outs can be a little confusing for the newcomer to electronics. Fortunately, however, only a little time is needed to become familiar with the methods of presentation employed in technical magazines and in manufac-turers' literature.

### VALVE PINS

Valves hardly ever appear in constructional projects these days, except perhaps in amateur transmitter designs, but it is still desirable to be able to repair radio and television receivers incorporating these devices. Indeed, with the current rocketing prices of transistor radio batteries, a few people may be thinking quite seriously of blowing the dust off any old mains-driven valve radios they have and seeing

whether they can be brought back into working order again.

There are three main types of valve base, these being the octal, the B7G and the B9A types. The 8-pin octal valve base has the pins protruding from a bakelite moulding, the latter also having a circular centre piece with locating spigot. The pins are equally spaced and are numbered from 1 to 8 in a clockwise order, as in Fig. 1(a). With the B7G base, the pins protrude directly from the glass of the valve envelope. There are 7 pins, these being numbered 1 to 7 in a clockwise order, number location being provided by a gap between pins 1 and 7. See Fig. 1(b). The B9A base is slightly larger and uses 9 pins, but is otherwise similar to the B7G base. As can be seen in Fig. 1(c), there is a gap between pins 1 and 9.





In the diagrams of Fig. 1, all the valve pins are pointing towards the reader. The B9A base is of especial interest to the home-constructor because the popular Denco miniature dual-purpose coils have pins with the same spacing and numbering as those of a B9A valve. These coils can, in fact, be plugged into B9A valveholders.

### TRANSISTOR LEAD-OUTS

Transistor lead-outs are normally shown as parts of circuit or wiring diagrams, appearing in insets alongside the main section of the diagram. Unless otherwise stated, they are always drawn with the lead-outs pointing towards the reader. A typical example is shown in Fig. 2(a), in which "e" stands for emitter, "b" for base and "c" for collector. Fig. 2(b) now show the pins and lead-outs pointing towards the observer. The scene changes dramatically when we come to integrated circuit pinning diagrams. These diagrams have the pins pointing away from the reader, this fact being normally denoted by the legend "Top View" alongside. Fig. 3(a) shows an 8-pin dualin-line package. One end of the package has an identifying mark and a spot alongside pin 1. In practice, only one of these, the mark or the spot, may appear on the housing. The pin numbering then proceeds around the device in anti-clockwise manner (which would, of course, be clockwise if the pins were pointing at the observer).

An advantage given by showing integrated circuit pins in this manner is that trouble-shooting is eased. Test prods can be readily applied to the portions of





shows a power transistor in the familiar diamondshaped case. The emitter and base lead-outs are displaced to one side of the case centre to permit identification. The collector arrow points to the case. This is because the collector connection is provided by the metal case itself.

Occasionally, transistors have the centre lead offset, as in Fig. 2(c). The lead-out diagram then appears as in Fig. 2(d). The idea behind the offset is



Fig. 3(b) shows an integrated circuit in a circular can with locating lug and 8 wire lead-outs. Again this is a top view, with the lead-outs pointing away from the observer. As can be seen, the lead-out numbering also proceeds in an anti-clockwise direction. In this instance the easing of trouble-shooting procedures is not very evident, but the method of presentation



Fig. 3(a). Pin numbering for an 8-pin dual-inline integrated circuit (b). Lead-out numbering for an 8-lead i.c. housed in a circular can

that the transistor lead-outs then conform to a triangular pattern similar to that in Fig. 2(a), but this is a matter of academic interest only so far as the home-constructor is concerned.

### I.C. PINS

All the pin and lead-out diagrams discussed up to

shown is nevertheless that which has been adopted by the industry for integrated circuits in cans.

Returning to Fig. 3(a) to raise a final point, the abbreviation for dual-in-line is d.i.l., but an alternative abbreviation, d.i.p., may also be encountered. This is applied to dual-in-line integrated circuits having plastic housings.

# **WORKSHOP AIDS**

### Novel diagnostic aid

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By injecting the signal at various points in an amplifier circuit, the Usijet is an effective dynamic analyser for tracing breaks and component failure. The fundamental frequencies are 1 kHz and 500 kHz with an output voltage of 20V peak-to-peak. Maximum permissible voltage at the probe tip is 500V D.C. Powered by a self-contained 1.5V cell, the current consumption is about 25 mÅ.

current consumption is about 25 mA. The Usijet can be applied to fault finding in AF, IF and RF amplifier stages, radio in the LW, MW, SW, USW and FM wavebands and TV VHF and UHF channels up to 500 MHz.



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# MOMENTARY POWER FAILURE INDICATOR

### By J. Knapp

In items such as digital frequency counters a brief power supply failure (due to a momentary mains power cut or a faulty plug, etc.) can cause incorrect readings without being noticed. This is particularly the case if the failure is very short, say of the order of 0.01 second.

The circuit to be described detects these short power cuts, and indicates that they have taken place by turning on an l.e.d. until re-set by a front panel push-button. Operation is very simple and, as will be seen, the circuit can employ the odd one or two gates that are left over in a complicated t.t.l. circuit. Even if the i.c. to be used has to be purchased, its cost is very low.

### **CIRCUIT FUNCTIONING**

The basic circuit is shown in Fig. 1 and incorporates one gate of the quadruple AND gate type 7408. Since this is an AND gate, the output is low, at 0, when both its inputs are at 0, and is high, at 1, when both the inputs are at 1.

When the 5 volt supply is initially turned on a charging current flows to C1 via R2, LED1 and D1. However, before any appreciable voltage can be developed across this capacitor the gate output becomes 0 because of the low voltage at the input.No futher charging current flows to C1 and it is main-tained in the discharged condition by R1. Both the inputs and the output are stable at 0, and the l.e.d. is illuminated.

The circuit is set by pressing S1. This takes the gate inputs up to 1 and causes C1 to charge to the full 5 volts of the supply. The output also rises to 1 so that, when S1 is released, C1 is maintained in the charged condition via D1, which is now forward biased. The initial surge of charging current to C1 is borne by the switch, and so no excessive current is drawn from the gate output. Both inputs and output are at 1 and the l.e.d. is extinguished.

Should the power supply be cut for a short period, C1 discharges very quickly into R1 whereupon, when the power returns, both the inputs and the output are at 0 and the l.e.d. is illuminated, indicating that a power supply failure has taken place. The circuit can then be re-set by pressing S1 once more.

then be re-set by pressing S1 once more. In the diagram, LED1 is shown as a TIL209. Any other similar type of light-emitting diode can, of course, be used. This neat little circuit detects short power failures in t.t.l. equipment which would otherwise pass unnoticed.

### TIME OF DISCHARGE

For very short power cuts C1 may not discharge sufficiently, and the gate will return to the state where the inputs and output are at 1, with the l.e.d. extinguished, when the supply reappears. Hence the circuit will not detect breaks shorter than a particular time period which is dependent on the value of C1. It has been found by observation that the minimum time, in milliseconds, which the circuit can



Fig. 1. Basic circuit of the power failure indicator. This is set up by pressing S1, and it causes the l.e.d. to be illuminated after a short power failure detect is approximately equal to 1.25 times the value of C1 in microfarads. Thus, if the circuit is to disregard breaks of less than 1 millisecond C1 requires a value of 0.6 wF. The exact value will vary with different gates, but an approximate choice of minimum time period may be obtained by giving C1 the appropriate value. This component is shown in the diagram as an electrolytic capacitor, but for the lower values a plastic foil capacitor is used instead.

The circuit has been checked with values of C1 giving time periods from 10 microseconds to 1 second and has functioned correctly in every case. If C1 is to have a value larger than, say,  $10\mu$ F it would be preferable to add a  $10\Omega$   $\frac{1}{2}$  watt surge imiting resistor in series with S1 to prevent sparking at the switch. The resistor may be inserted between S1 and the positive rail.

### **OTHER GATES**

Instead of a 7408 gate, the circuit can use a quarter of a 7432 quadruple OR gate, as in Fig. 2. This is connected in the same manner as the 7408.

Another variation is shown in Fig. 3 where two gates of a 7400 quadruple NAND gate are connected in cascade. Each gate acts as an inverter, giving an output which is the same as the input. Two gates of a quadruple NOR gate type 7402 could be cascaded in a similar manner.

In Fig. 4, two inverters from the 7404 hex inverter are used and produce the same result. As may be











Fig. 2. The circult may also employ one gate of the 7432 l.c.





Fig. 4. Another version of the circuit incorporates two 7404 inverters

Fig. 5. The addition of a small power transistor allows the failure indicator to light a bulb instead of an l.e.d.



gathered, the basic circuit can employ a wide range of individual gates.

The output of the circuit does not have to be connected to an l.e.d., and it could be used to blank the display or carry out another function when it trips. Again, it could illuminate a bulb by way of a small power transistor, as illustrated in Fig. 5. The bulb can draw a current of the order of 200mA, and R4 limits the surge current which can flow through its filament in the cold state to 500mA.

# SPECIAL FEATURES RADIO ELECTRONICS

### SIMPLE REGENERATIVE S.W. RADIO

This little receiver employs an unusual regenerative f.e.t. detector circuit and covers 1.5 to 36 MHz by means of three plug-in coils. The output is at headphone level, and it may alternatively be applied to an a.f. amplifier. A particular attraction is the low current which is drawn from the 9 volt supply battery.

# **CMOS VOLTMETER**

The introduction of the CMOS linear operation amplifier type CA3130T allows the construction of very simple circuits which take advantage of its extremely high input resistance. This article describes an electronic voltmeter incorporating the CA3130T and offering ranges of 0-1 volt, 0-5 volts and 0-50 volts.



**ON SALE 2nd JANUARY** 

**DECEMBER 1976** 

# Radio By Topics Recorder

I have been doing quite a bit of design work recently for projects which are finally assembled on Veroboard. This is a fascinating process, particularly if d.i.l. integrated circuits are employed because connection layout is then governed by the i.c. pins themselves. If, for instance, pin 3 of an i.c. provides an input function, then the discrete component lead-outs and wiring associated with that input have to be soldered to the strip connecting to pin 3.

Everybody has their own ideas on making up designs on Veroboard, but my own approach may perhaps still be of interest.

### **INITIAL STEPS**

If the project incorporates a new and previously untried circuit, I first of all check it out in lash-up form. This is really a debugging exercise and it ensures that resistor and capacitor values are correct and that there are no little errors that have been overlooked. Believe me, there are many circuits conceived in the lofty fastnesses of theory which fail very dismally when tried out in the hard world of practice!

Wiring can be kept quite short in even the most hastily assembled of lash-ups, but it will still almost in-evitably be longer than occurs in the final Veroboard version of the circuit. In general, this is nearly always to the good as the longer wiring encourages any tendency in the circuit towards instability due to unwanted feedback from an amplifier output to its input. A fairly common trap here is given in circuits intended for d.c. or for switching operation and which have two or more transistors connected in cascade. A typical example occurs when a constant current transistor feeds the base or emitter of another transistor. You may fondly imagine that the base of the constant current transistor is firmly tied to its supply rail by the voltage reference diode or diodes, but this point cannot be banked on. If an apparently stable circuit starts to act unpredictably as your hand approaches any of the components or if, alternatively, it will only work properly when your hand is close to the components, then there's almost definitely r.f. instability due to a hidden amplifier chain and feedback path. The solution is normally quite

simple. To start with, should the circuit require a bypass capacitor across the supply rails ensure that this connects to the circuit with short leads and do not rely on any bypass capacitors inside, say, a bench power supply. If the instability continues with the supply bypass capacitor connected, find the amplifying chain and kill it by adding a bypass capacitor of around 0.01uF between one of the supply rails and any collector, emitter or base in the middle of the chain.

When the circuit is stable in the lash-up form it will almost certainly, provided certain rules are followed, be even more stable in its final Veroboard layout where the wiring will be shorter.

### CROCODILE CLIP LEADS

Crocodile clip leads can offer a considerable saving of time when trying out lash-up circuits. These leads consist of thin flexible insulated wires about 16in. long terminated at each end by a miniature crocodile clip having a flexible p.v.c. cover which allows only the extreme ends of the clip jaws to be visible. These leads are well worth the trouble of making up, and it is a helpful idea to use wire of a different colour in each lead. Obviously, crocodile clip leads cannot be employed for high impedance signal paths, but they can be used for quick low impedance connections.

When the lash-up circuit has proved to be satisfactory, the next process consists of working out a Veroboard layout. My procedure here is to use a piece of paper marked up with faint squares taken from a school exercise book. These books can be obtained in most stationers or from a Woolworth store and are about  $6\frac{1}{4}$  by 8in. If the exercise book staples are opened out, a good supply of squared sheets is available, each sheet measuring  $12\frac{1}{4}$ by 8in. The squares have a side of  $\frac{1}{4}$  in. or 5mm. depending on when the stock was last changed.

The procedure then is to regard the square corners as Veroboard holes and work out the project layout in pencil assuming that one is looking at the component side of the board. Breaks in the Veroboard strips are shown by crosses. If it is necessary to make a change in the layout then the first attempt is simply erased and a fresh start made. This is probably the most engrossing part of the procedure. The process will also show the size of Veroboard that is required. Incidentally, I prefer myself to use one of the standard size pieces of Veroboard with edges neatly cut by the manufacturer, but this is just a personal choice.

The only main rules to observe in the Veroboard layout concern high impedance input points. If an amplifier input and an in-phase output are at high impedance it is desirable not to connect them to adjacent Veroboard strips in case the capacitance between them (admittedly very small) allows positive feedback to occur. Again, a strip connecting to a sensitive high impedance amplifier input should be kept short and should be cut, after all the input connections have been made to it, rather than allow it to traverse the width of the board.

When the pencil layout has been completed, the components, connections and link wires are drawn more permanently in blue or black ink, and the crosses which indicate breaks in red ink.

The design is now complete and all is ready for making up the actual Veroboard assembly. The preliminary work will then show its value because, after its initial test in lash-up form, the circuit is almost certain to work first go. And the pencil design step will also ensure that the layout looks neat, clean and uncluttered.

Before concluding on this topic, there is another dodge which some may find helpful. If a typewriter is available, set it to single line spacing, insert a sheet of paper and type out a series of small letter 'o's' with a space between each. The result is a sheet of paper with rows of evenly spaced circles on it. This can then be used for working out a Veroboard layout instead of the squared pages of school exercise books.

### MEGGER - 1976 STYLE

Those of you who, like me, have fond Service memories of the Wee Megger and its larger brother will be more than interested in the accompanying photograph of this instrument in its latest form. For the benefit of readers who have not previously encountered the Megger, I should add that this is a unique resistance-measuring instrument capable of indicating extremely high resistance values. The energising voltage is obtained by turning a crank coupled to an internal generator.

The instrument illustrated is the Megger model SL15, which has been introduced in 1976. It continues the tradition of high range insulation resistance testers, robustly designed for the rigours of maintenance work and sensitive to the advanced development of insulating materials.

The a.c. generator in the SL15 can be hand driven or mains operated by way of an internal motor. Stepped test voltages from 500 to 5,000 volts d.c.



The latest instrument in the Evershed end Vignoles Megger range. This is the model SL15, and is capable of measuring resistance up to 250,000M  $\Omega$ 

are stabilized to 0.1 per cent by precision electro-mechanical engineering. The discharge of external circuit capacitance is achieved by manual switching on the voltage selection switch.

An amplifier-assisted Evershed cross-coils movement is incorporated, providing a sensitivity of 250,000M  $\Omega$ at 2,500 volts. Each instrument is assembled in a hard wood case fitted with a hinged lift-off cover for top panel protection, a carrying handle and levelling feet. Accessories supplied comprise a mains lead and three test leads.

The Megger model SL15 is manufactured by Evershed and Vignoles, Ltd., Archcliffe Road, Dover, Kent, CT17 9EN.

### COIL WINDER

Avo Limited, also of Archcliffe Road, Dover, Kent, do not only produce high grade test equipment, and they have been well-known for many years as manufacturers of coil winding machines. The coil winder shown in the photograph is the type CW63 Mk 3, and has been very recently added to the Avo range. This winder is the most versatile Avo machine, and it is particularly suitable for in-house coil winding by the electronics manufacturer who requires a moderate output of a wide diversity of coil designs.

All the machine controls for the coil winder are housed in the electronic controller fitted above the headstock. The newly designed foot control is interlocked with a "fail safe" latch-on system. This allows the machine to be left running on the set maximum speed, and enables the operator to control two or even more machines when the coils to be wound have a large number of turns.

Two important new features are



This highly versatile Avo coil winding machine, the type CW63 Mk3, is capable of winding coils in either direction and of providing an automatic stop at the end of a layer. Other features include a foot control system which enables several machines to be controlled by a single operator

reverse winding spindle rotation and "end-of-layer" stopping. The "Reverse Rotation" switch in the electronic controller allows coils having bidirectional winding to be easily produced. "End-of-layer" stopping is particularly useful when coils having single layer windings are being produced. It is also essential when hand interleaving between layers is necessary.

### OFFSHORE

### COMMUNICATIONS

Marconi Communications Systems Limited, a GEC-Marconi Electronics company, are undertaking an important study in communications for the European Space Agency Headquarters in Paris. The study is concerned with the use of the European Communications Satellite System (ECS) and the Maritime Orbital Test Satellite (MAROTS) in the 1980's to provide telecommunication facilities supporting installations engaged in offshore oil and gas exploitation.

The communications requirements of such installations will be met with the use of many small earth terminals. Since the major role of ECS will be in large earth terminal applications, like International Trunk Telephony and the exchange of television programmes, there is an obvious need to study the problems of sharing a single satellite between large and small earth terminal users. In the case of MAROTS there is the different problem of sharing the satellite's capacity between ships and exploration rigs.

Marconi Communications Systems have been involved since 1972 in a series of studies which have shown that there are no fundamental technical obstacles to using ECS and MAROTS type satellites to satisfy the communication requirements of oil companies engaged in the exploitation of the hydrocarbon resources in the European sea areas. The present study will result in a detailed definition of the space and earth sectors of such systems, compatible with the requirements of the trunk sector of ECS and the requirements of the mercantile shipping sector of MAROTS.

cantile shipping sector of MAROTS. In the case of ECS, Marconi Communication Systems are also identifying those aspects of the trunk services which might be modified to achieve integration. In addition, account will be taken of the development of alternative communications media such as tropospheric scatter (which, to date, has been entirely provided by Marconi for the oil fields in the North Sea) to ensure all-round compatibility. Further, means are being examined for achieving a smooth transition to an ECS-based system via the interim satellite OTS which is planned for a 1977 launch.

Things are certainly moving these days in the field of communications.

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