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FEBRUARY 1978 Volume 31 No. 6

Published Monthly (1st of Month) First Published 1947

Incorporating The Radio Amateur

Editorial and Advertising Offices 57 MAIDA VALE LONDON W9 1SN

Telephone 01-286 6141 Telegrams Databux, London

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Annual Subscription: £6.50 (U.S.A. and Canada \$12.00) including postage. Remittances should be made payable to "Data Publications Ltd". Overseas readers please pay by cheque or International Money Order.

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Correspondence should be addressed to the Editor, Advertising Manager, Subscription Manager or the Publishers as appropriate.

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Production .- Web Offset.

Published in Great Britain by the Proprietors and Publishers, Data Publications Ltd, 57 Maida Vale, London W9 1SN

The Radio & Electronics Constructor is printed by Swale Press Ltd.

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ca3090ag	mpx dec4 35			/255 complete fm stere
HA1196	mpx dec4 20	BF256 1ghz fet	0.34	tunerset. afc, agc, mute
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THE 'CASCODE' MEDIUM AND LONG WAVE PORTABLE

by Sir Douglas Hall, K.C.M.G. Part 1 (2 parts)

★ Three transistors give a five-stage performance

★ An unusual and ingenious design

As many readers know, there are areas where medium and long wave reception with a very simple receiver having a single tuned circuit, such as can be made around the ZN414 or two doubly reflexed bipolar transistors, is very difficult because of the presence of a powerful local transmitter. If this is in line with other required transmissions satisfactory use cannot be made of the ferrite aerial directional properties and, so far as t.r.f. designs are concerned, special circuits such as those employed in the author's Super Alpha receivers, described in earlier issues, have to be used to obtain the necessary station separation.

NEW CIRCUIT

The receiver to be described incorporates a new double reflex circuit with which r.f. amplification increases with wavelength. In many, but not of course all cases, the swamping station will be at the higher frequency end of the medium wave band and here, with less amplification offered, greater selectivity can be obtained. The author does not claim for this design the selectivity offered by his Super Alpha circuit, particularly the versions using variable inductance tuning, but the present receiver is less prone to swamping by a local station than is a single tuned circuit ZN414 receiver, especially if the interfering station is not in the lower frequency half of the medium wave band. The receiver, also, offers a high level of gain on the long wave band.

The cascode configuration will be familiar to many readers of this magazine and, with bipolar transistors, can be described as a common emitter amplifier feeding directly into the emitter of a common base amplifier. The appellation is not strictly accurate with the present design, but is appropriate enough: what happens is that a two stage r.f. amplifier, with both transistors in the common emitter configuration, is followed by a diode detector which feeds its audio output into a two-stage a.f. amplifier using the same two transistors as before. At a.f. the first transistor amplifies in the common base mode and the second functions as an emitter follower.

The first of the two transistors is biased so as to pass a very small current on medium waves and even less — only a few microamps — on long waves. Because the efficiency of a common emitter amplifier falls as frequency rises, and because this effect is more noticeable at very low collector currents, the result is a drop in amplification to a small figure at around 200 metres, although high efficiency is maintained at, say, 1,500 metres. The second transistor of the pair passes some 600uA and gives fair amplification at all wavelengths in the medium and long wave bands.

In Fig. 1 switch S1 is shown set to medium RADIO AND ELECTRONICS CONSTRUCTOR

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Fig. 1. The circuit of the "Cascode" medium and long wave portable

Inductors L1-L4 see text COMPONENTS L5 10mH r.f. choke type CH4 (Repanco or equivalent) T1 Output transformer type LT700 (Eagle) Resistors Semiconductors (All fixed values $\frac{1}{4}$ watt 5%) TR1 2N4058 R1 4.7k Ω **TR2 BC169C** R2 6.8kΩ **TR3 BC169C** R3 120kn D1-D5 1S44 R4 27kn D6 OA70, OA71, OA80 or OA81 R5 6.8k Ω R6 1.5k Ω Switch R7 3.9k Q S1 4-pole 3-way rotary, miniature (see text) R8 33 Ω Speaker VR1 2.2ko potentiometer, wire-wound (see LS1 30 or 40 speaker, 5in. (see text) text) VR2 10Mn pre-set potentiometer, skeleton Batterv (see text) 9-volt battery type PP9 VR3 1Mo pre-set potentiometer, miniature Miscellaneous skeleton, horizontal 3 knobs Capacitors 2 ferrite rods, 8in. by 3in. diameter C1 100µF electrolytic, 6V. Wkg. 4 Lektrokit clips, part no. LK2721 (see text) C2 0.1µF polyester **Battery connectors** C3 0.1µF polyester 18-way tagboard C4 1,000uF electrolytic, 6V. Wkg. 38 s.w.g. enamelled copper wire (for L1 and C5 1,000µF electrolytic, 10V. Wkg. L2) C6 0.047μ F polyester C7 100μ F electrolytic, 6V. Wkg. 24 s.w.g. enamelled copper wire (for L3 and L4) VC1 365pF variable, type 01 (Jackson) Materials for panel and case (see text) VC2 40pF trimmer, mica Nuts, bolts, wire etc.

waves. The medium wave coils, L3 and L4, are wound on a ferrite rod, with L3 being tuned by VC1. L4 is a small coupling coil giving correct matching to the base input of TR1. L1, the long wave tuned coil, is in parallel with L3, but has no significant effect on the performance of the latter. This method of wavechange switching is employed because if L1 were simply switched out of circuit on medium waves it could become resonant with its own self-capacitance at a frequency at the low wavelength end of the medium wave band, with undesirable absorption effects. As TR1 passes a small current its input impedance is not as low as is usual with a common emitter amplifier and a step-down ratio, from L3 to L4, of 6:1 gives satisfactory results. On long waves the step-down ratio is 7:1. With a conventional common emitter transistor stage the step-down ratio would be of the order of 12:1, with a consequent loss of voltage amplification. The amplified signal at TR1 collector feeds directly into the base of TR2, whose r.f. collector load is the r.f. choke, L5. The signal is also passed to the diode detector, D6, and the resultant a.f. signal is applied to the emitter of TR1, which now acts as a common base a.f. amplifier. The amplified a.f. signal is developed mainly across R4 and, at a relatively high impedance, couples to the base of TR2, this transistor functioning as an emitter follower at audio frequencies and having a similarly high input impedance. The emitter load for TR2 is R6 and the signal now passes directly to the base of TR3, a straightforward high gain common emitter output transistor. The primary of out-put transformer T1 is in the collector circuit of TR3 and its secondary is connected to a 3Ω or 4Ω speaker.

REACTION

Reaction is obtained by feeding back a small proportion of the r.f. signal at TR1 collector back to its base via VC2 and coils L3 and L4, phasing being in the correct sense for positive feedback. (For simplicity of circuit presentation, coils L4 and L2 are shown, in Fig. 1, with the opposite phasing to that which they have in practice). Reaction control is given by V.R1, which determines the current flowing through TR1. D.C. negative feedback, with a.f. bypassed by C4, is obtained from the collector of TR2 to the emitter of TR1 via D6. Further negative feedback of direct current, with a.f. again bypassed by C4, is given by returning R7 to a centre tap in the output transformer primary instead of direct to the lower positive line. These d.c. feedback loops help to maintain voltage stabilization in the circuit.

The three silicon diodes, D1, D2 and D3, keep

All the components are mounted on the rear of the front panel. There are two ferrite rod aerials, one for medium waves and one for long waves the direct voltage across VR1 to a constant figure of about 1.65 volts irrespective of the state of the battery. This gives constant reaction performance. VR3 is adjusted such that zero volume is just reached with VR1 at its zero setting. If VR3 offers too much resistance, zero volume will be given before VR1 is set at zero and, also, current through TR3 will rise unnecessarily. Should VR3 insert too low a resistance, zero volume with a powerful signal will be impossible to obtain.

A separate ferrite rod, on which are wound L1 and L2, is used on long waves. Otherwise, operation is the same as on medium waves with several small differences. The reaction feedback to TR2 base is now, of course, via the coupling winding L2. R3 is inserted in series with R4 to reduce the current through TR1, and pre-set potentiometer VR2 is brought into circuit. This potentiometer is adjusted for satisfactory volume control operation and it functions in an opposite way to VR3. If VR2 has too high a resistance, zero volume will be unobtainable with a powerful station. If it offers too low a resistance zero volume will be given before VR1 is set at zero and current through TR3 will rise.

D4 and D5 prevent a high surge of current in TR3 when the receiver is first switched on and the capacitors in the circuit charge up, and they limit the voltage across R6 to about 1.1 volts. Under working conditions, the voltage across this resistor is approximately 0.8 volts.

COMPONENTS

Some comments need to be made next concerning components. Switch S1 is a miniature 4-pole 3-way rotary component. Its body diameter is 1.1in, or less. VR1 is a small wire-wound potentiometer and that employed by the author was a type CLR1106/11S, obtainable from Electrovalue. This has a body diameter of 0.94in. Although specified as $2.2k\Omega$, any value from $2k\Omega$ to $3k\Omega$ will be satisfactory. VR2 has the high value, for a preset potentiometer, of $10M\Omega$, and this is also available from Electrovalue as type PR15. The speaker is specified as a 5in. component, working to the fact that most present-day nominal "5in." speakers have an actual diameter of $4\frac{1}{2}$ in. Also, it should not be more than $1\frac{7}{8}$ in. deep. If the speaker obtained should happen to exceed these dimensions it is merely necessary to modify the front panel and case dimensions accordingly.

Four ³/₈in. Lektrokit spring clips, part no. LK2721, are used for mounting the two ferrite aerial rods. These are available, in packets of 10, from Home Radio. Two tagstrips are employed in the construction, these being cut from an 18-way tagboard.





Fig. 2(a). The dimensions of the front panel and the layout of the components which are mounted directly onto it

(b). The positions taken up by the two ferrite rod aerials when, later, these are fitted into their clips. The letters "A" to "H" identify the leads from the aerial windings

(c). Side view showing how the spring clips are secured at the ends of the $1\frac{1}{2}$ in. 4BA bolts

(d). End view of the bolt and clip assembly





Looking down on the top of the receiver. Here, the medium wave ferrite aerial is nearer the reader

CONSTRUCTION

Construction starts by cutting out a piece of $\frac{1}{4}$ in. plywood to measure $10\frac{1}{2}$ by $4\frac{1}{2}$ in., as in Fig. 2(a). These dimensions assume that the speaker has an actual diameter of $4\frac{1}{2}$ in., as has just been mentioned. If the actual diameter of the speaker employed is 5 in. or a little less, the dimensions of the panel should be increased to 11 by 5 in. The other dimensions shown in Fig. 2(a) need not be altered, except that the $2\frac{1}{4}$ in. dimension to VC1 is increased to $2\frac{1}{2}$ in.

A speaker aperture is next cut out in the panel, after which 4 BA clear holes are drilled out for its four mounting bolts. Holes are also drilled for S1, VC1 and VR1. VC1 requires three 4BA clear holes for screws passing into tapped holes in its front plate, and their positions can be marked out with the aid of a paper template. This has a $\frac{3}{8}$ in. hole cut out in its centre and is held against the capacitor front plate, whereupon the positions of the holes are marked on it in pencil. The capacitor is mounted, later, with three short countersunk 4BA bolts, and spacing washers may be fitted over these between the panel and the capacitor front plate to give clearance for the protrusion around the spindle. It is important to ensure that the ends of the three bolts do not pass more than fractionally inside the front plate of the capacitor when they are finally tightened up as they may then damage the fixed or moving vanes. It is for this reason that short bolts are required.

Two tagstrips, one 10-way and one 6-way, are cut out from the 18-way tagboard. Two lengths of p.v.c. insulating tape are stuck to the plywood at the positions the tagstrips will take up, and the tagstrips are then screwed to the board by means of four small woodscrews passing through the holes in the centres of the appropriate tags. The positioning of the tagstrips and the woodscrews is shown in Fig. 2(a).

The speaker is secured with four countersunk 4BA bolts, one of which is $\frac{1}{2}$ in. long whilst the remainder are $1\frac{1}{2}$ in. long. The Lektrokit spring clips for the ferrite rods are secured to the ends of the longer bolts, as shown in Figs. 2(b), (c) and (d). A fourth $1\frac{1}{2}$ in. 4BA countersunk bolt is mounted on its own, $1\frac{1}{4}$ in. to the right of the lower right-hand speaker mounting bolt, and this is similarly fitted with a clip. It may be necessary to slightly enlarge the holes in the clips to allow the 4BA bolts to pass through. Fig. 2(b) illustrates the positions that the ferrite rods and coils will take up when they are fitted later. To avoid damage, the ferrite rods and coils are not mounted until all other components are in place.

The panel of Fig. 2 (a) will be vertical when the receiver is in use. The PP9 battery then rests on the clip at the end of the bottom right hand $1\frac{1}{2}$ in. 4BA bolt and on a woodscrew which is partly screwed into the panel. The correct position for this woodscrew can be found with the aid of the battery, and the woodscrew may then be fitted. S1, VC1 and VR1 can also be fitted at this stage.

NEXT MONTH

In next month's concluding article, details will be given of the aerial coil windings, the receiver wiring and the setting up procedure.

(To be concluded)

BOOK REVIEW OSCAR — Amateur Radio Satellites

192 pages 205 x 148mm. (8 x 5³/₄in.) **By Stratis Caramanolis** Distributed by the Radio Society of Great Britain Price £4.20 including postage

Already a best-seller in its original German edition, with over 7,000 copies sold, this first-ever book on Amateur radio satellites has now been translated into English and updated.

It fills a need which is becoming ever more apparent as more and more radio amateurs turn their attention to space communication. There are so many aspects of science not familiar to most radio amateurs involved in "Communication via OSCAR," but in this volume all the essential information for an appreciation of the techniques involved for a proper understanding of Orbital Satellites Carrying Amateur Radio is contained.

The first half of the book discusses the background to the subject, including orbital geometry, satellite anatomy, communications principles and telemetry. These chapters help greatly to give the reader a clear understanding of the principles involved in satellite communication. The book then goes on to describe the satellites of the OSCAR series and how they have been used for communication, education and experimentation. The use of published orbital predictions and the somewhat confusing subject of plotting orbits are both well explained.

For those who wish to obtain a comprehensive understanding of OSCAR techniques, this book is essential reading.



The CD4018AE presettable divide by "N" counter is one of the more interesting CMOS digital i.c.'s currently available to the home constructor. The device is capable of dividing by 2, 4, 6, 8 or 10 according to the external connections made to it, and it has five not-Q outputs. The full type number is CD4018A, with the suffix E added to indicate a plastic d.i.l. package.

PIN FUNCTIONS

The pin functions of the i.c. are illustrated in Fig. 1. The positive supply is connected to pin 16 and the negative supply to pin 8. The device is advanced one step by each positive-going pulse applied to the clock input.

If it is desired to divide by 2 the not-Q1 output is connected to the data input. Division by 4, 6, 8 or 10 is achieved by connecting the not-Q2, not-Q3, not-Q4 or not -Q5 output respectively to the data input, and the divided output can then be taken from the not-Q output which is so connected. A feature of which advantage is not normally taken is that, in all the dividing functions apart from that for divide by 2, the unused not-Q outputs follow a distinctive sequence which is repeated once for each division cycle. This pattern can be employed, with the aid of simple NAND or NOR gates, to set up a sequence generator.

to set up a sequence generator. In the article "CD4018 Truth Tables", which appeared in the June 1977 issue of this journal, the author presented tables showing all the not-Q output states for each clock count. These demonstrated that, after several initial counting steps, the outputs followed a fixed pattern of high or low states. The article "4-step CMOS Sequence Switch", in the July 1977 issue, gave an example of a 4-step switch consisting of a CD4018 and a CD4011 quad 2-input NAND gate. In this present article the author returns to the CD4018 and uses it, in its divide by 6 function, to provide a 6-step switch. The switch comprises a CD4018, a CD4011 and a CD4001 quad 2-input NOR gate, and its outputs drive six l.e.d.'s in sequence. The CD4018 has a relatively high frequency clock pulse input which can be interrupted by pressing a pushbutton, whereupon the circuit performs as an electronic dice having a fully random performance.

The accompanying Table 1 shows the states of the not-Q outputs of a CD4018 when it is con-



Fig. 1. Pin allocations for the CD4018, and the internal circuitry of the CD4011 and the CD4001 FEBRUARY 1978

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TABLE 1

Not-Q3 to Data input

	Not-Q outputs					
Step	1	2	3	4	5	
1	H	H	H	H	H	
2	L	H	H	H	H	
3	L	L	Н	H	Н	
4	L	L	L	Н	H	
5 6	H H	L H	L L	L L	H L	
7	H	H	Н	L	L	
8	L	Ĥ	Н	Н	L	
9	L	L	H	H	H	

nected as a divide by 6 device with its not-Q3 output connected to the data input, and is repeated from the June 1977 article. At switch-on (or after resetting) all the not-Q outputs are high. The first clock pulse takes the i.c. to Step 2 in the Table, with the not-Q1 output going low. The successive states of the not-Q outputs for subsequent clock pulses show the distribution of high and low states throughout the outputs, and it is found that these take up a steady pattern after Step 3. Step 9 is the same as Step 3, Step 10 will be the same as Step 4, and so on. As a result, Steps 3 to 8 inclusive will repeat for as long as the CD4018 circuit is switched on and positivegoing pulses are fed to its clock in³ put.

We can obtain six separate outputs from Steps 3 to 8 by finding high and low combinations which are peculiar to any single step and are not repeated in any other step in the division cycle. The process is not difficult and we find, at Step 3, that the not-Q3 and not-Q5 outputs are high at this step only. Similarly looking for unrepeated highs we find that it is only in Step 5 that not-Q1 and not-Q5 are high, and that it is only in Step 7 that not-Q1 and not-Q3 are high.

We do not readily find any further individual combinations of highs, so we next look for individual combinations of lows. It can at once be seen that it is only in Step 4 that not-Q1 and not-Q3 are low, that it is only in Step 6 that not-Q3 and not-Q5 are low, and that it is only in Step 8 that not-Q1 and not-Q5 are low. We now have the basic requirements for a sequence of six output steps, or "Output Numbers", and these are shown in 198 Table 2, where Output Number 1 corresponds with Step 3 of Table 1. The not-Q output states in Table 2 will proceed in the order shown, with another Output Number 1 following immediately after Output Number 6.

It is a very easy matter to extract the information from Output Numbers 1, 3 and 5, as all we have to do is to connect 2-input NAND gates to the not-Q outputs concerned. The output of a 2-input NAND gate goes low only when its two inputs are high. It is just as easy to extract the information from Output Numbers 2, 4 and 6, but this time we use 2-input NOR gates. The output of a 2-input NOR gate goes high only when its two inputs are low.

As already mentioned, the electronic dice incorporates a quad 2input NAND gate i.c. type CD4011 and a quad 2-input NOR gate type CD4001. To assist in following the circuit operation of the electronic dice, the internal connections in these two i.c.'s are also shown in Fig. 1.

ELECTRONIC DICE

The complete circuit of the dice is given in Fig. 2. IC1 is a 555 connected in a standard multivibrator circuit, and it feeds a train of positive-going pulses via S1 to the clock input of the CD4018, IC2. Pressing S1 stops the pulses passing into the clock input whereupon (assuming a perfect switch) the CD4018 stays in the last state it held at the instant of opening the switch.

All the "jam" inputs of the CD4018, as well as the preset and reset inputs, are taken to the negative rail. For the present application we do not require the not-Q2 and not-Q4 outputs, and no connections are made to their pins. The not-Q3 output is connected to the data input in order to give division by 6. The not-Q1, not-Q3 and not-Q5 outputs are taken to the CD4011 and the CD4001 i.c.'s and it happens that we can use the same input gate pins for each not-Q output from the CD4018. The fourth NAND and NOR gates in IC3 and IC4 are not needed, and their pins 12 and 13 are taken up to the positive rail. No connection is made to the gate outputs at pin 11. The NAND and NOR gate out-

The NAND and NOR gate outputs are not capable of driving l.e.d.'s directly, and so they are coupled to the transistor l.e.d. drivers, TR1 to TR6. The l.e.d.'s light up in the order indicated by their suffix numbers, and we shall next see how the CD4018 not-Q outputs are gated through to the l.e.d.'s at each successive Output Number, starting first with Output Numbers 1, 3 and 5.

On Output Number 1, Table 2 shows that not-Q3 and not-Q5 are high. These high outputs pass into pins 1 and 2 of IC3, whereupon pin

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3 goes low. This pin connects to the base of the emitter follower TR1, the emitter of which similarly goes low, causing current to flow through LED1. LED1 lights up. At Output Number 3, not-Q1 and not-Q5 are high, whereupon pins 5 and 6 of IC3 go high also. Pin 4 of IC3 goes low in consequence, and emitter follower TR3 then lights up LED3. Not-Q1 and not-Q3 are high at Output Number 5, pins 8 and 9 of IC3 are similarly high and pin 10 goes low. TR5 then causes LED5 to be illuminated.

Dealing next with Output Numbers 2, 4 and 6, Table 2 tells us that at Output Number 2 not-Q1 and not-Q3 are low. So also are pins 8 and 9 of IC4, whereupon pin 10 goes high. Base current flows via R6 into TR2, this transistor turns on

TABLE 2

Activating not-Q states

Output	
Number	Not-Q states
1	3 and 5H
2	1 and 3L
3	1 and 5H
4	3 and 5L
5	1 and 3H
6	1 and 5L

and LED2 lights up. At Output Number 4, not-Q3 and not-Q5 are low, these connecting to pins 1 and 2 of IC4. Pin 3 of IC4 goes high, TR4 turns on and LED4 becomes alight. Output Number 6 has not-Q1 and not-Q5 low, and these low outputs are applied to pins 5 and 6 of IC4. Pin 4 goes high and LED6 is illuminated.

Thus, the circuit causes LED1 to LED6 to light up successively in numerical order, with LED1 becoming illuminated again immediately after LED6 extinguishes. If the six l.e.d.'s are mounted on a panel with the numbers "1" to "6" alongside them, the circuit behaves as an electronic dice. IC1 is made to oscillate at a relatively high frequency and the dice number is that of the l.e.d. which remains steadily illuminated after S1 has been pressed. Since only one l.e.d. is alight at any time it is only necessary to provide one series resistor to limit l.e.d. current. This resistor is R7.

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Fig. 2. Full circuit diagram of the electronic dice. The light-emitting diodes turn on successively at high speed, and one of these will remain steadily illuminated when switch S1 is pressed

PRACTICAL POINTS

The circuit can be assembled in a small plastic case with the l.e.d.'s and the two switches on the front panel. Current consumption from the 6 volt battery is approximately 15mA both with S1 closed and open.

The frequency at which the 555 runs is approximately 150Hz, and this causes a very noticeable flicker in the l.e.d.'s when S1 is closed. If desired the 555 frequency can be increased by reducing C2 to 0.01μ F, and this value removes the flicker from the l.e.d.'s, which all light up at a reduced level before S1 is pressed. The author feels that the flickering with C2 at 0.047μ F, is, however, more impressive.

Circuit operation can be checked if the 555 is slowed down to about 1 cycle in 3 seconds by connecting a 20μ F electrolytic capacitor across C2. Obviously, the capacitor negative lead should connect to the negative supply rail.

negative supply rail. Following switch-on at this low speed, LED1, LED3 and LED5 will light up, after which LED1 will be lit on its own. This corresponds to Steps 1 and 2 in Table 1. At Step 3, LED1 will still be alight but the circuit will now have started the cycle of Table 2.

If S1 is pressed when the output of the 555 is low, the circuit will remain in the state it had when the switch was opened. Should S1 be pressed when the 555 output is high the circuit may pass quickly through one or more steps. This will be caused by the fact that the switch does not break the circuit cleanly and is giving an effect similar to that given by contact bounce. The circuit is almost certain to pass quickly through several steps if S1 is released when the 555 output is high, and this will be due to conventional contact bounce. Both effects are quite random and do not detract in any way from the overall random character of the output indications given. Similar effects can occur when the 555 is running at its proper speed but the 555 frequency will then be too high for them to be observed.

Since IC2, IC3 and IC4 are CMOS devices, their pins should be protected from high static voltages and they should be soldered into circuit with a soldering iron whose bit is reliably earthed. A good approach consists of using i.c. holders, the i.c.'s being fitted to these after all wiring has been completed.

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NEW PROFESSIONAL RECORDING TAPE

3M United Kingdom have introduced a new professional recording tape, specifically designed for the UK/European market which demands a quality tape with high output and low signal-toprint ratio.

NEWS

In addition to these features, the new tape — Scotch 256 — also enjoys improved wind characteristics, the results of a newly developed black back coating.

Scotch 256 has been evolved to provide a high dynamic range, low distortion, low signal-to-print ratio and high wind quality. The tape has a print-through level of -59dB, a bias noise level of -67dB and signal-to-noise value of 77dB (DIN measurements).

Scotch 256 comes in $\frac{1}{4}$ in., $\frac{1}{2}$ in., 1in. and 2in. widths and is suitable for open-hub type applications. It has been developed in the wellestablished 3M laboratories and manufacturing plant in Gorseinon, S. Wales.

3M claims that this tape sets new high, professional standards, and expects the Scotch 256 to achieve immediate acceptance for its low print through characteristic alone.



AND

Dave Skinner seen testing Scotch Type 256 professional recording tape at the 3M laboratories in Gorseinon, S. Wales. Test equipment includes a Bruel & Kjaer 3rd octave spectrometer type 2114 and Beat frequency oscillator type 1022 with a Bruel & Kjaer level recorder type 2305; a Telefunken M15 tape deck; a Hewlett-Packard distortion analyser type 333A and a Quantech wave analyser

IBA SPACE STUDY BEGINS

By tuning-in to an Italian space satellite, IBA engineers at Crawley Court, Winchester have begun the study of 12 GHz radio propagation which was delayed when the Thor-Delta launcher of a European communications satellite failed last September.

In readiness for the launch of the Orbital Test Satellite (OTS) of the European Space Agency, a special 12 GHz satellite receiving terminal was designed and built last summer at a cost of about £75,000. This station, with a 3-metre dish aerial, is located in front of the IBA's Engineering Centre at Crawley Court, Winchester, and is connected to measuring equipment in the engineering research laboratories.

The study programme, jointly planned with the Post Office and European broadcasters, is aimed at establishing a Eurovision satellite distribution network in the 1980s and, in the long term, opening the way for direct broadcasting to homes from satellites.

The 11-12 GHz Super High Frequency (SHF) propagation experiments include the careful monitoring and measuring of the strength and polarisation of the signals received from the beacon transmitter. Frequencies between 11.7 and 12.5 GHz will be used for space broadcasting in Europe, and signals on such frequencies are known to be more affected by rain, hail and water vapour than the lower frequencies currently used for space communications. It is therefore important to accumulate detailed knowledge of the range of 340 variations and anomalous effects likely to be encountered in operational systems.

When the first OTS launching failed it was known that it would be some months before a backup satellite could be in orbit, and it appeared that the study programme would inevitably be delayed.

However, an experimental Italian synchronous communications satellite 'SIRIO', designed for radio propagation and telecommunications experiments above 10 GHz, was successfully launched last August on behalf of the Italian National Council for Research (CNR), and this carries a beacon transmitter radiating continuously on a frequency of 11.596 GHz (less than 200 MHz from the intended beacon frequency of OTS).

By re-orientating the Crawley aerial dish, IBA engineers have found no difficulty in picking up, identifying and using the signals from SIRIO, which is located 22,300 miles above the Equator at a longitude of 15° West.

The Italians have co-operated and agreed to this unanticipated use of their satellite. The sensitivity of the IBA terminal will be increased shortly by means of a parametric amplifier in readiness for the reception of television signals through the OTS satellite, but initially this low-noise amplifier will be used to improve reception of the SIRIO beacon.

The Crawley Court satellite receiving terminal has been designed by IBA engineers and built to an IBA specification by a number of contractors including Marconi Space & Defence Ltd., Continental Microwave Ltd., Ferranti Ltd. and The Marconi Research Laboratories.

RADIO AND ELECTRONICS CONSTRUCTOR

COMMENT

BACKGROUND TO BBC WAVELENGTH CHANGES IN NOVEMBER 1978

As promised last month we set out below the wavelength changes which the BBC will be making in November, together with background information explaining the reasons for the changes.

The changes that will take place are:

Radio 1 moves to 1089kHz/275m and 1053kHz/285m;

Radio 2, at present on long wave moves to medium: 693kHz/433m and 909kHz/330m;

Radio 3 moves to 1215kHz/247m;

Radio 4, at present on several medium waves, moves to two long waves: 200kHz/1500m and in Central Scotland 227kHz/1322m.

The frequencies which are used for broadcasting in this country have to be carefully co-ordinated with those of other countries in Europe, and from time to time an international frequency plan is drawn up to ensure, as far as possible, that countries can develop their broadcasting service, and keep mutual interference between stations to a minimum.

On the long and medium wave bands there is only a limited number of channels, and to accommodate all the stations in the European area it is necessary for each channel to be used by several stations in different countries. This sharing of channels presents most problems during the hours of darkness, when stations can be received at much greater distances, because of reflections in the ionosphere, and as a result interference occurs between stations using the same channels. This has been an unfortunate feature of radio broadcasting for many years. During the last 20 years broad-

During the last 20 years broadcasting on vhf has been started in many countries, including the United Kingdom. Vhf has many advantages, one being that because the signals do not carry so far, stations in many parts of Europe can use the same channels without mutual interference.

However, the medium and long wave bands are still most widely used and the BBC must therefore endeavour to provide the best possible coverage on these bands.

During 1974 and 1975 an international conference was held to replan the medium and long wave broadcasting bands in Europe, Africa, Asia and Australasia. As a result a new plan was drawn up and this will come into effect on 23 November 1978.

At present in the European area there are some 1450 transmitters on the two bands, with a combined power of about 82 Megawatts. The new plan provides for 2700 transmitters in the same area, with a combined power of 214 Megawatts. As the number of channels has not increased appreciably, the overcrowding will increase and night time interference will become even worse than it is already. The deterioration on some channels will be greater than on others, and having examined the implications of the new plan it soon became clear to the BBC that it would not be satisfactory to con-tinue to transmit all of the programmes, on their existing fre-quencies. At the same time the BBC was anxious to make certain changes which would have been desirable even without a new frequency plan.

It was decided that Radio 4, as a main news and information channel, should be available complete throughout the United Kingdom and not only in England as at present, and that it was desirable to improve the mf coverage of Radio 1. Radio 2 to be transmitted on two good medium frequency channels, with coverage so far as possible of the whole United Kingdom.

The present Radio 3 frequency (647kHz/464m) will suffer increased interference at night time, and to continue with it would have meant that the night time service of Radio 3 would be severely limited, so a different frequency had to be found.

As a result of the changes, most listeners will have to accustom themselves to new places on the dial for the programmes they need, but the changes are considered to be essential if the best possible coverage is to be maintained.

NOTE: the vhf positions of all BBC Radio services on radio sets will remain the same.

'R.& E.C.' BINDERS

Binders to accommodate our new format will be available within the next three weeks. They will be of the previous high standard and the cost is only £1.60, including V.A.T., plus postage 30p. There are a few binders of the

There are a few binders of the old size available and orders for them should be placed without delay — see out advertisement pages.

pages. Plain binders are only available in the new format, price £1.50, plus postage 30p.



6th NATIONAL AMATEUR RADIO EXHIBITION

The Amateur Radio Retailers Association's Jubilee Year Exhibition held at the Granby Halls, Leicester, in October was as well attended as ever.

Amongst the 'new' features which caught one's eye was a well set out demonstration of microprocessor gear and its applications and several examples of Visual Display teleprinter systems. There was an increasing amount of gear for the higher frequencies and some microwave components to be found on some of the stalls. As usual the component stalls were popular, offering an opportunity to acquire those 'hard to get' bits and pieces. Altogether a very worthwhile show.



Blob-a-job

No. 8

COUNTDOWN!

by I. R. Sinclair

The fascination of t.t.l. operation combines with Blob Board ease of construction to make our last Blob-A-Job project the ultimate in the series.

This circuit will display, the number 9 on a readout when a push-button is pressed, and will then count down to zero when the button is released. At zero the count stops. A stop output can be taken from the circuit to start or stop other apparatus provided that the current and voltage limits of the t.t.l. integrated circuit providing the stop signal are not exceeded.

CIRCUIT OPERATION

In the circuit, shown in Fig. 1, IC1(a) is part of a quad 2-input positive NAND Schmitt gate, and it functions as an oscillator running at approximately 1Hz. Its output drives the countdown input of a 74192 reversible decade counter. The 74192 i.c. may seem expensive at around £1.25 (at the time of writing), but it saves the use of a number of other i.c.'s and also enables us to make up the circuit on a single ZB-4-IC Blob Board. In addition, the 74192 is a very versatile and useful counter i.c., being capable of counting up or down according to which terminal the clock pulses are connected.

There are four data inputs, labelled D, C, B and A, which enable the counter to be set to whichever digits are wanted. These inputs, each at 0 or 1 as required, are entered into the counter when the "load" input at pin 11 is taken to logic 0. The number 9 is represented in binary as 1001, so that we can have D at 1, C at 0, B at 0 and A at 1 for our data inputs. We can earth inputs B and C, leaving inputs A and D floating (or connected to the 5 volt positive supply rail), and the counter will be loaded when pin 11 is temporarily earthed by pressing the push-button S1. When clock pulses are applied to the countdown terminal, the countdown will start. The count will continue to zero, at which outputs QD, QC, QB and QA will all be 0, and the next clock pulse is gated internally to the "borrow" output at pin 13, used normally when a chain of counters is being operated. Pin 13 goes low and, in our circuit, operates a latch consisting of IC1(b) and IC1(c) which gates off the clock pulses and keeps the output of the counter at zero by taking the "clear" input at pin 14 high. This latch is reset by S1 at the same time as the loading operation is carried out for the next countdown sequence.

carried out for the next countdown sequence. The counter outputs at QD, QC, QB and QA are taken to the D, C, B and A inputs of the 7447 decoder, which then provides the correct outputs on seven lines to feed the display. Current limiting

COMPONENTS
own being and being and the barrier
Resistors (All 10%) R1 560Ω ¼ watt R2-R8 470Ω 1/8 or 1/10 watt (see text)
Capacitor C1 680µF electrolytic, 10V. Wkg.
Semiconductors IC1 74132 IC2 74192 IC3 7447
Display DY1 BDL747
Switch S1 miniature push button, press to close
Blob Board Blob Board type ZB-4-IC



Fig. 1. The circuit of the Countdown device. When S1 is pressed the display indicates a 9, then counts down to 0 when the switch is released. Circled number and letter references apply to Blob Board connection locations

resistors must be used between the decoder and the display to prevent excessive current from flowing in the display and decoder. The display suggested for this counter is the common anode BDL747 ("Jumbo") type, this being a large display that is visible across a room. The display brightness depends on the value of the limiting resistors; 470 Ω is suggested as a reasonable compromise between brightness and current consumption. The BDL 747 is available from Bi-Pak.

CONSTRUCTION

The circuit is constructed on a ZB-4-IC Blob Board, the suggested layout of the i.c.'s being shown in Fig. 2. Note that the BDL747, though it has nominally 18 pins, will (just) fit on a 16-pad Blob Board position, because several pins are omitted. See the pinout diagram in Fig. 3. The missing pins also help to define the pin numbering. The decimal point is not used in this application, so that no connection is made to its terminal pin. The

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i.c.'s should be mounted with their index marks towards the bottom of the board. Note that the 74192 and 7447 i.c.'s are 16-pin types whilst the 74132 is a 14-pin device. There are, in consequence, two Blob Board pads unused at the 74132 position, so that pin 1 of the i.c. is on pad 12G and pin 8 on pad 6D.

Start construction by tinning the pin-ends of the i.c.'s in the usual way. Then mount the i.c.'s on the board, checking carefully that each pin is on the correct pad. Start soldering in, always soldering pin 1 first, checking for correct position, then soldering the diagonally opposite pin (8 or 9) and then rechecking the positioning, before soldering on the rest of the pins.

Wiring can now commence, as illustrated in Fig. 7. The first step is to wire in the $470 \land$ resistors between the seven outputs of the 7447 and the seven cathodes of the display. The seven outputs of the 7447 are in a line on one side of the i.c., but appear in the order, starting at pin 9, of e, d, c, b, a,



Fig. 3. Pin functions of the display, viewed with the pins away from the reader. This is nominally an 18 pin device with pins 1, 8, 9, 10, 16 and 18 omitted. The positive signs indicate common anode pins





The assembled Countdown circuit forms a very neat assembly on the ZB-4-IC Blob Board

g, f. The cathode pins of the BDL 747 are not similarly arranged, so that the resistors will have to cross over each other. Some of the resistors can pass under the body of the display rather than take the longer route round it, and such a process is easier if the resistors used are 1/8 or 1/10 watt types with very small bodies so that a piece of sleeving can be passed over the resistor body and most of its lead-outs. Whatever method of routing the resistors is employed, sleeving must be passed over their lead-outs wherever there is a possibility of short-circuit to another lead-out, to a Blob Board pad or to the display terminal pins. The wiring is between corresponding letters, with output "a" on the 7447 connecting via a 4700 resistor to cathode "a" in the display, and so on.

SUPPLY CONNECTIONS

The supply connections may next be made. The 5 volt positive input is connected to strip M and the negative input to strip L. Link wires near the board corners then route these supplies around the outer strips of the board, as shown in Fig. 7. The BDL 747 has several common anode pins, and only one needs to be connected to the positive supply. This is pin 12 at pad 23D. There is no negative supply connection to the display, as the cathodes of the l.e.d.'s are coupled to the negative rail through the 7447







Fig. 5. The 7447 decoder has the pin functions shown here



Fig. 6. The pin functions of the 74192 up/down counter

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Fig. 7. Final steps in wiring of the Blob Beard. The positioning of resistors R2 to R8 can be carried out as described in the text

decoder. With the 7447, pin 8 (pad 22K) is taken to the negative rail and pin 16 (pad 29H) to the positive rail. With the 74192, pin 8 (pad 6K) is the negative supply connection and pin 16 (pad 13H) the positive connection. Pin 7 (pad 6G) of the 74132 connects to negative and pin 14 (pad 12D) to the positive rail. Use insulated wire for these connections and for all subsequent connections where there is any risk of short-circuits to other parts of the circuit.

The remaining signal connections can now be made. The A, B, C, D outputs of the 74192 on pins 3, 2, 6 and 7 of the 74192 are connected to the corresponding inputs of the 7447 at pins 7, 1, 2 and 6 respectively. This completes the decoding and display parts of the circuit. The oscillator section of the 74132 is next to be dealt with, with pin 3 (pad 10G) connected to pin 4 of the 74192 (pad 10K). This links the output of the oscillator to the countdown input of the counter. A 560 presistor is blobbed between pin 3 and pin 2 (pad 11G) of the 74132, and a 680μ F electrolytic capacitor between pin 2 and the negative rail with the capacitor positive lead connecting to pin 2.

There are now no further resistors or capacitors to mount. Pin 10 (pad 8D) of the 74132 is connected to pin 11 (pad 9D), and pin 8 (pad 6D) is

17" - 4"

connected to pin 12 (pad 10D), completing the latch inter-wiring. Pin 8 is connected to pin 1 (pad 12G) and this completes the wiring around the 74132. A few connections between the 74132 and the 74192 have next to be made. Pin 11 of the 74132 (pad 9D) is connected to pin 14 of the 74192 (pad 11H), and pin 9 of the 74132 (pad 7D) is connected to pin 11 of the 74192 (pad 8H) and also to one terminal of the push-button switch on pad 5H. The other terminal of the switch is soldered to pad 4H, which is then coupled to the adjacent negative rail track. Pin 13 of the 74192 (pad 11D) is next connected to pin 13 of the 74192 (pad 10H). Finally, pins 1 and 10 of the 74192 (pads 13K and 7H) are connected to the negative supply rail. If required, an output lead can be connected to pad 6D.

This completes the wiring on the board. It will be noted that one of the four gates in the 74132 is not used, and no connections are made to its pins. A number of other i.c. pins are left unconnected also; this is intentional as it is required that the pins concerned be left floating.

An output is available on the lead connected to pad 6D. The voltage on this lead is low before the countdown starts. It goes high during the countdown and then goes low again when the countdown is complete. + • + To blob board circuit IN4001 IN4001

Fig. 8. The circuit may be powered by a 6 volt accumulator if two forward connected silicon diodes are inserted in series to drop the voltage

TESTING

6V accumulator

The circuit draws a relatively high supply current of about 200mA and a mains power supply offering a regulated 5 volt output is the best source of power. A 5 volt stabilizing circuit was described in the first Blob-a-Job article in this series, this appearing in the June 1977 issue. A mains supply should not be used if the voltage can rise above 5.5 volts off load. A 6 volt accumulator may also be employed with two silicon rectifiers in series, as in Fig. 8, to drop the voltage to a safer level. The rectifiers also protect against accidental reversal of supply polarity, which can damage the integrated circuits. An accumulator which is on charge must not be employed. Dry batteries will not have a long life operating this display, although a fresh 4.5 volt battery could be used for a quick demonstration.

The title of this article "Countdown!", is symbolic since it is the last of the Blob-a-Job series, which is therefore similarly counted down. The projects which have been described in the series are useful and instructive, and they all demonstrate the ease and speed with which quite complex circuits can be readily assembled on Blob Boards.

RECENT PUBLICATIONS

RADIO DATA REFERENCE BOOK, Fourth Edition. By T. G. Giles, B.Sc., G4CDY and G. R. Jessop, C.Eng., M.I.E.R.E., G6JP. 200 pages, 225 x 140mm. $(9\frac{3}{4} \times 15\frac{1}{2}in.)$ Published by Radio Society of Great Britain. Price £3.00.

This is another R.S.G.B. publication, also appearing in a new and enlarged edition. Indeed, the text has been completely revised for the fourth edition and much new material added, including sections on transistors, heat sinks and modern filter design. The book has also been rearranged into nine subject areas for greater ease of reference. These areas are: units and symbols, basic calculations, resonant circuits and filters, circuit design, aerials and transmission lines, radio and television services, maps and meteorological data, materials and engineering data, and mathematical tables.

The purpose of the book is to provide as much reference data as is possible without taking up space with unnecessary details on basic theory. The book can be consulted for such diverse information as metric thread dimensions, great circle calculations, transistor y-parameters, RTTY standards, TV channel frequencies and, even, the Beaufort wind scale. This is a useful reference work for anyone who deals at design level with modern radio communication equipment.

MODERN ELECTRONICS MADE SIMPLE. By George H. Olsen, B.Sc., C.Eng., M.I.E.R.E., M.Inst.P. 318 pages, 215 x 125mm. ($8\frac{1}{2}$ x 5in.) Published by W. H. Allen & Co. Ltd. Price £2.95 (cloth), £1.75 (paper).

Appearing in the W. H. Allen "Made Simple" series, this book starts, very nearly from scratch, to take the reader into the basics of advanced present-day electronic devices and principles. All that the reader needs is a knowledge of elementary electricity.

The text is concise and, so far as is possible, mathematics are avoided. There is a considerable amount of useful information in the volume, certainly very much more than is encountered in some books which ostensibly set out to introduce beginners to the science of electronics.

Commencing with basic electron theory and the general functioning of electronic systems the book progresses rapidly, but not excessively so, to semiconductors, amplifiers, integrated circuits, power supplies, oscillators, radio transmission and reception, the oscilloscope, television and high fidelity reproduction. There then follows an introduction to digital circuits, a chapter on photoelectric devices and a final chapter describing simple projects which may be built.

As an example of the depth with which subjects are dealt, the chapter on power supplies includes diacs and triacs, and the chapter on digital circuits gives a description of the operation of a liquid crystal display. This is definitely a helpful book for the newcomer to electronics.

WITH Spare Detector

By Ron Ham

A receiver from the 1920's which incorporated two crystal detectors.

Occasionally an early set in good condition appears in a second -hand shop and through the vigilance of David Rudram of Worthing the British Thomson-Houston crystal receiver seen in the photographs was rescued for posterity.

DOUBLE CAT'S WHISKER

Two features of this set are the double cat's whisker and crystal unit mounted on the right of the ebonite front panel and the use of a variometer for tuning. The polished wooden cabinet with its



Overall view of the receiver, complete with headphones. The "Instructions For Using" are mounted inside the receiver case lid



The front panel of the BTH Radiola receiver. The tuning control, with scale, is to the left, and the dual crystal and cat's whisker unit is at the right. The top central control is a condenser selection switch and the lower central control switches in either of the two crystals

lid closed measures $11\frac{1}{4}$ in. by $5\frac{1}{4}$ in. by $8\frac{1}{2}$ in. high. After a good clean up both the BTH insignia, under the carrying handle, and the BBC approval label were found intact. The letters "BBC" are encircled by the usual words "Type Approved By Postmaster General", and another outer circle is inscribed "G.P.O. REG. No. 106".

As can be seen in the second photograph, the instruction card is mounted inside the lid. It is dated January 1925 and is followed by the words "Form 1996-A Fourth Edition" which, coupled with the extra circle surrounding the approvals label, made both the owner and the writer conclude that this was a special purpose set. However, a dig in the writer's early radio magazines revealed an advertisement in *Wireless Weekly* for October 22nd, 1924, which identified the set as "The Radiola Model A" and described it in the following manner.

"This set is provided with two crystals, and if one ceases to function, the other can be instantly switched into circuit. The normal range for telephony is 30 miles, but a greater range is possible under favourable conditions. The tuning is simple_and selective."

The price was quoted as £3. 10s. A pair of "B.T.H. 4000 ohm Head Telephones" could be obtained for £1. 5s. extra.

SELECTIVITY

Like all enthusiasts, David has tried the set out, and its selectivity between BBC Radios 1 and 4 in his area is surprisingly good. He found also that one crystal is slightly more sensitive than the other.

During the magazine research, another advertisement for BTH receivers was found in Wireless World dated December 24th, 1924, which showed an almost identical set, but with a larger cabinet and a valve built in. This was the "Radiola 1 (Valve Crystal) Receiver" and the advertisement stated: "This is the ideal set for Head Telephone reception up to 100 miles. Two crystals, with change-over switch, are provided."

The price, "with enclosed H.T. battery and B.5 valve", was £9. 15s. Again, BTH 4000 ohm headphones could be bought for £1. 5s. extra.

Should any readers have comments to make about these sets the writer would be pleased to hear from them.



By Frank A. Baldwin

Times = GMT

Frequencies = kHz

We commence this month by giving the Broadcast Bands a rest and delve into the 'goings-on' taking place at the CW ends of some of the Amateur Bands. No doubt CW (Morse Code - or more correctly Continuous Wave using the Morse Code) is not everybody's cup of tea but at least the results show just what may be heard using this mode of operation.

1.8 — 2.0 MHz (Top Band) DL10V, F8VJ, G13PDN, GM3XOQ, GW3 XJC, OL9CHZ and PAHIP. Noise levels on the band have been quite high of late and results obtained are far below those achieved last year.

7.0 — 7.1 MHz (Forty Metres) CO2SM, CO5PN, CO7ET, FM7AZ, HI8MOG, JA7UI/PZ, J28AY, LU9FEG, PY1EIZ, UD6DLL, UI8OU, UL7RAS, W4WE, 9K2DR. Undoubtedly the best time for listening on this band is the early mornings — and I mean early — around 0100 to 0400!

14.00 — 14.35 MHz (Twenty Metres)

AP2TN, CX4LO, EL2AZ, EP2IK, FY7YE, HC2TI, KL7HCC, KP4DDO, KV4AA, OA4AHO, OD5LX, UF6FDI, UJ8AC, UW8AT, VE7CML, VK4PB, VU2YK, ZL2AYJ, ZL3BK, ZS1PH, ZS5BO, 4J6AM. Which leaves this band still occupying the position of the best DX producer of them all.

As for the remaining Amateur Bands - I couldn't find the time to cover them!

CURRENT SCHEDULES

SOCIETY ISLANDS (FRENCH)

"Radio Tahiti" has a schedule in which programmes in French, Tahitian and English are radiated from 1600 to 1800 on 6135, 9750, 11825 and on 15170. Of these channels, the first two are 4kW and therefore unlikely to be heard here in the U.K. and the latter two are of 20kW. The English programme is from 1900 to 1915 (not Sundays) but the best chance of logging Tahiti is around 0530 to 0600 on **15170**. A Tahitian programme is presented from 0300 to 0600 and a French transmission is made from 0600 until sign-off at 0800.

LAOS

An External Service in various languages from Vientiane is on the air from 0400 to 0630 and from 1100 to 1430 on 7145. The English programmes are from 0600 to 0630 and from 1330 to 1400.

MONGOLIA

Ulan Bator has an External Service in which English programmes intended for South East Asia and the Far East are radiated from Monday to Saturday from 1220 to 1250 on 9575 and 11860 and from 1715 to 1745 on 8890.

KENYA

"The Voice of Kenya", Nairobi, operates a Domestic Service in which programmes in Swahili are broadcast from 0630 to 1330 on 7240 and on 4885 from 0300 to 0630 (Sundays 0330 to 0630); from 1330 to 2010 (Saturdays 1330 to 2110). This is the National Service.

The General Service from Nairobi has an all-English programme content and may be heard from 0900 to 1100 Monday to Friday and from 0620 to 1330 on Saturday and Sunday on 7125. From 0300 to 0620 (0330 to 0620 on Sunday) and from 1300 to 2010 (1300 to 2110 on Saturday) on 4804.

Vernacular Service from Nairobi is from 1500 to 1900 on 4950 and from 0900 to 1045 on 7150. This service is not broadcast on Sundays or public holidays.

Programmes from the Mombasa station (vernaculars) are on 4915 from 0330 to 0500 and from 1400 to 2005 and on 7140 from 0800 to 1100.

• NIGERIA

The "Voice of Nigeria" Lagos, presents programmes in English as follows — from 0555 to 0835 on 7275 and on 15120 to West and East Africa, the Middle East and Europe; from 1800 to 1930 on 7275, 11770 and on 15120 to West, Central and Southern Africa, Europe and the Middle East.

Programmes in English may also be heard from 1530 to 1700 on 7275 and on 11770 when being directed to West, Central, East and Southern Africa and the Middle East.

• CHINA

Broadcasts to Taiwan in the External Service of Radio Peking in Standard Chinese, Amoy and Hakka are scheduled as follows — from 1439 to 1900 on 4770; 2000 to 2314 and from 1430 to 1900 on 5125; from 1226 to 1900 and from 2000 to 0014 on 6790; from 0830 to 1900 and from 2000 to 0610 (Sunday to 0705) on 9170; from 0830 to 1438 and from 2000 to 0610 (Sunday to 0705) on 11100;

from 0830 to 1429 and from 2315 to 0610 (Sunday to 0705) on **15710**; 0830 to 1225 and from 0015 to 0610 (Sunday to 0705) on **15880**.

The Domestic 1st Programme in Standard Chinese is now as follows — from 1303 to 1735 and from 2000 to 2300 on **3220**; from 2000 to 2300 on **3450**; from 1133 to 1735 and from 2000 to 2200 on **4905**; from 0848 to 1735 and from 2000 to 2400 on **5320**; from 1018 to 1735 and from 2000 to 2330 on **7935**; from 2000 to 1735 on **9080**; from 1100 to 1735 and from 2303 to 0100 on **10245**; from 2303 to 1300 on **11330**; from 0733 to 1130 and from 2203 to 0430 on **12420**; from 0103 to 0845 on **15550** and from 2333 to 1015 on **15590**.

The Domestic 2nd Programme in Standard Chinese is announced as from 1403 to 1700 and from 2100 to 2240 on **3290**; from 1118 to 1700 and from 2100 to 0030 on **4250**; from 1203 to 1700 and from 2100 to 2400 on **4850**; from 1033 to 1700 and from 2100 to 2400 on **5163**; from 1203 to 1700 and from 2100 to 2001 on **6345**; from 0033 to 1115 on **7190**; from 0800 to 1700 and from 2100 to 0220 on **7770**; from 0700 to 1030 **9670**; from 0700 to 1200 on **9745**; from 2243 to 1400 on **10260**; from 0003 to 1200 on **11040**; from 0133 to 1000 on **12200** and from 0001 to 1100 on **15030**.

AROUND THE DIAL

TANZANIA

Dares Salaam on 5050 at 1931, local songs and music in typical local style in the Commercial Service (in Swahili). The schedule is from 1300 to 2015 whilst the National Service is radiated on this channel from 0300 to 0500. The power is 10kw.

MOZAMBIQUE

Radio Mozambique, Beira, on **4895** at 0353, interval signal of drums and orchestra repeated. identification in English, Portuguese and vernaculars repeated ("This is Radio Mozambique, Beira").

• EGYPT

Cairo on **9805** at 1831, OM in Italian to Europe, the schedule of this programme being from 1830 to 1930.

Cairo on **9850** at 1835, OM in Arabic in the Domestic Service, the Schedule of which is from 1800 to 2355 on this channel.

• INDIA

AIR Delhi on **9912** at 1840, a programme of Arabic music in a broadcast to the Arab world, scheduled from 1730 to 1930.

Hyderabad on **4800** at 1533, YL with a newscast in English. The schedule is from 1200 to 1830, all programmes in Arabic except English news bulletins at 1232 and 1530. The power is 10kW.

Kurseong on 3355 at 1543, YL with the news in English. The schedule is from 1130 to 1800 (East Regional Service in vernaculars except for the English newscast at 1530). The power is 20kW.

• SINGAPORE

Radio Singapore on a measured **5052** at 1540, OM with a ballad in English. The schedule is from 2230 to 1630 (Sunday to 1700) and the power is 20kW.

• CZECHOSLOVAKIA

Prague on 9605 at 1810, YL with a talk in the English programme directed to Africa, scheduled from 1730 to 1830.

• NETHERLANDS

Hilversum on **9895** at 1818, YL in Dutch to the Middle East, Africa and Europe, identification, National Anthem and off at 1820. The schedule of this programme is from 1700 to 1820.

• ECUADOR

Radio Popular, Cuenca, on **4800** at 0417, YL with songs in Spanish. This one is audible after Radio Lara signs off at 0400. The schedule of Radio Popular is around the clock, the power is 2kW and it sometimes identifies as "Amiga Popular de Cuenca".

Radio Nacional Progreso, Loja, on **5060** at 0406, local-style dance music, songs in Spanish. The schedule is from 1030 to 0415 (sign-off is variable) and the power is 5kW.

Radio Rio Amazonas, Macuma, on **4870** at 0347, OM with identification in Spanish, National Anthem and off — which is not in conformity with the schedule, this being from 1130 to 0300. However, since it has also been reported closing as early as 0140 the sign-off time is not surprising. The power is 10kW and the programme language is mainly local vernaculars.

• COLOMBIA

Ecos del Atrato, Quibdo, on 5020 at 0357, YL with songs in Spanish, OM with announcements, National Anthem and off at 0401. The schedule is from 1100 to 0400 and the power is 1kW.

NOW HEAR THIS

La Voz de Galapagos, Isla San Cristobal, on 4810 at 0357, identification in Spanish, announcements and off after full National Anthem at 0401 — but who touched the record and temporarily slowed it down?

HOBBIES EXHIBITION — EXMOUTH, 13th to 16th MARCH 1978

The Exeter Amateur Radio Society will be exhibiting at the above exhibition to be held in the Pavilion, Exmouth, South Devon.

On their stand there will be HF TX/RX, VHF/UHF and Amateur TV stations, plus a fully equipped SWL station. In addition to the foregoing there will be an Electronics Section. The Society's monthly meetings are held in the Community Centre, St. David's Hill, Exeter at 1930 hours on the second Monday in the month.

w americanradiohistory.com

2 METRE CONVERTER

By A. P. Roberts

This crystal controlled unit converts the 2 metre amateur band to a range of 28 to 30MHz, thereby permitting 2 metre reception on a standard short wave receiver.

144MHz

TO 28MHz

There seems to be a continuing increase in interest in the v.h.f. and u.h.f. amateur bands, and in particular in the 2 metre band. For anyone who already has a good short wave receiver the obvious way to get started on v.h.f. amateur band reception is to use a converter ahead of the existing receiver. Provided the receiver has good sensitivity, such an arrangement can produce excellent results for a relatively small monetary outlay.

The converter which forms the subject of this article is crystal controlled, and uses four transistors including two dual gate MOSFET's and a Jugfet. It is of the type where the s.w. receiver is used as a tuneable i.f. strip, and in this case the i.f. is 28 to 30MHz. The prototype is used in conjunction with a Trio QR-666 receiver, and good reception of stations up to about 25 miles away is possible with only a telescopic aerial. Of course, by using a more sophisticated aerial, and possibly also adding a high gain low noise aerial amplifier, a far greater range could be attained.

POWER

On

It should perhaps be pointed out that although the circuit is fairly simple, this is not a project which is really suitable for anyone of limited experience. Alignment, for instance, is more difficult than is the process of aligning, say, a medium wave superhet receiver. Ideally, both a multimeter and a v.h.f. signal generator should be available, although it is possible to manage without either of these.

OPERATING PRINCIPLE

A block diagram of the converter is shown in Fig. 1. the heterodyne principle is used to convert aerial signals in the frequency range of 144 to 146 MHz



(the limits of the 2 metre band) to a range of 28 to 30MHz so that they fall within the coverage of a short wave receiver.

The aerial input signal is amplified by an r.f. stage before being fed into the mixer stage. Both these stages use dual gate MOSFET's, which provide a low noise level. The crystal oscillator operates at a frequency of 58MHz, and its output is doubled in frequency by a Jugfet multiplier stage prior to being fed to the second input of the mixer.

If, for example, an aerial signal of 144MHz is received, this will be heterodyned with the 116MHz signal from the multiplier stage to produce a difference frequency of 28MHz (144 minus 116 equals 28). The two input signals, plus the 260MHz sum frequency, will also appear at the output of the mixer, but these will lie well outside the tuning response of the short wave receiver and can be ignored. Obviously, a 145MHz aerial signal will produce a mixer output at 29MHz and a 146MHz signal an output of 30MHz. Thus, by coupling the converter output to a receiver having a coverage of 28 to 30MHz it is possible to tune over the entire 2 metre band. Furthermore, the receiver tuning dial calibration can be used to determine the reception frequency since it is merely necessary to add 116MHz to the indicated dial frequency.

THE CIRCUIT

The complete circuit of the 2 metre converter appears in Fig. 2. The aerial signal is coupled to a tap in L1 which, together with TC1, forms the input tuned circuit, resonant at 145MHz. This connects direct to the gate 1 of the r.f. amplifier transistor, TR1. The gate 2 is biased a few volts positive by the potential divider consisting of R1 and R2, and this bias produces maximum gain from the device. The drain load for TR1 is choke L2. R3 is the source bias resistor, and this is bypassed by C3. C1, C2 and R4 provide decoupling for the r.f. stage.

The output from TR1 is coupled via C4 to a tap in coil L3, which connects to the gate 1 of the mixer transistor, TR2. L3 and TC2 form a second tuned circuit resonant at 145MHz. Both the aerial and mixer tuned circuits have pre-set tuning. Variable tuning would serve no purpose as the bandwidth of both tuned circuits is greater than 2MHz.

TR2 is a conventional dual gate MOSFET mixer, with its output being developed across the tuned circuit consisting of L4 and C5. Again, pre-set tuning is employed and the iron-dust core of L4 is adjusted so that the circuit is resonant at the centre output frequency of 29MHz. The output signal is taken from a tap in L4 and is coupled to the output socket, SK2, by way of the d.c. blocking capacitor, C6. The tap in L4 is closer to the drain end of the coil than the earthy end, it being found that this provides optimum signal coupling into the short wave receiver used with the converter.

TR4 is the crystal oscillator transistor and it runs at the crystal third overtone of 58MHz. The tuned circuit given by L6 and TC4 ensures that the oscillator operates at the correct frequency.

The multiplier stage incorporates TR3 in the common gate mode, with TC3, L5 and C10 being resonant at 116MHz. C11 couples the output of the oscillator to the source of TR3. The second harmonic of the oscillator is amplified by TR3 and the resultant 116 MHz signal is fed to the mixer by way of C9.

S1 is the on-off switch and is the only control

which is fitted to the converter. Power is obtained from a 9 volt battery type PP6, which has quite a long operating life since the current consumption of the converter is only about 10mA. This is much less than the current required by most converters, and is achieved by the use of a relatively high frequency oscillator, which requires only one multiplier stage instead of the more usual two or three.

COMPONENTS

Most of the components employed in the design are standard readily available items, but inevitably a few parts are of a more specialised nature. The 58MHz third overtone crystal can be obtained from Doram Electronics and possibly from other firms who specialise in crystals. The crystal lead-outs were soldered directly to the printed circuit board in the prototype but readers who prefer to do so may alternatively use a crystal holder, soldering this to the board and then plugging in the crystal. The holder should be an HC-25/u type. The four ceramic trimmers are Doram Type A components. Other trimmers having a similar capacitance swing would probably be satisfactory, but it would almost certainly be necessary to alter the printed circuit board to accommodate them physically.

The MEM616 transistors specified for TR1 and TR2 are available from Ambit International, whilst the BF254 used for TR4 is listed by Electrovalue. L4 is wound on a 10mm. diameter coil former type 450, available from Maplin Electronic Supplies, and is fitted with an 8mm. iron-dust core type 8, also supplied by Maplin. L2 is wound on a



352

ange of 28 to overage of a

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e converter coupled to a orms the inz. This conplifier tranolts positive R1 and R2. in from the ke L2. R3 is assed by C3. he r.f. stage. C4 to a tap of the mixer econd tuned e aerial and ng. Variable andwidth of 1Hz.

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istor and it 88MHz. The res that the lency.

TR3 in the d C10 being output of the second har-TR3 and the nixer by way which is fitted to the converter. Power is obtained from a 9 volt battery type PP6, which has quite a long operating life since the current consumption of the converter is only about 10mA. This is much less than the current required by most converters, and is achieved by the use of a relatively high frequency oscillator, which requires only one multiplier stage instead of the more usual two or three.

COMPONENTS

Most of the components employed in the design are standard readily available items, but inevitably a few parts are of a more specialised nature. The 58MHz third overtone crystal can be obtained from Doram Electronics and possibly from other firms who specialise in crystals. The crystal lead-outs were soldered directly to the printed circuit board in the prototype but readers who prefer to do so may alternatively use a crystal holder, soldering this to the board and then plugging in the crystal. The holder should be an HC-25/u type. The four ceramic trimmers are Doram Type A components. Other trimmers having a similar capacitance swing would probably be satisfactory, but it would almost certainly be necessary to alter the printed circuit board to accommodate them physically.

The MEM616 transistors specified for TR1 and TR2 are available from Ambit International, whilst the BF254 used for TR4 is listed by Electrovalue. L4 is wound on a 10mm. diameter coil former type 450, available from Maplin Electronic Supplies, and is fitted with an 8mm. iron-dust core type 8, also supplied by Maplin. L2 is wound on a

CONFONENTS
Resistors
(All $\frac{1}{4}$ watt 5%)
R1 10k Ω
R2 8.2kΩ
R3 470 12 R4 11 0
$R5 22k \Omega$
R6 120k Ω
R7 1k Ω
R8 150k Q
a second second second
Capacitors
$C1 0.015 \mu F$ or $0.022 \mu F$ ceram
$C_2 0.0022\mu F$ ceramic
C4 10pF ceramic
C5 47pF polystyrene (see text
C6 47pF polystyrene
$C7 0.004/\mu$ F ceramic C9 0.1 μ F type C280 (Mullard
C9 10pF polystyrene or ceram
C10 39pF polystyrene or cera
C11 8.2pF ceramic
C12 0.015 μ F or 0.022 μ F ceran
TCI 4-20pF trimmer, ceramic



Fig. 2. Circuit diagram of the converter. TR1 is the aerial signal amplifier an TR3 doubles the crystal frequency and mixing is carried of

only control

FEBRUARY 1978

COMPONENTS

 Resistors

 (All ¼ watt 5%)

 R1 10k Ω

 R2 8.2k Ω

 R3 470 Ω

 R4 1k Ω

 R5 2.2k Ω

 R6 120k Ω

 R7 1k Ω

 R8 150k Ω

C1 0.015 μ F or 0.022 μ F ceramic C2 0.0022 μ F ceramic C3 0.0047 μ F ceramic C4 10pF ceramic C5 47pF polystyrene (see text) C6 47pF polystyrene C7 0.0047 μ F ceramic C8 0.1 μ F type C280 (Mullard) C9 10pF polystyrene or ceramic C10 39pF polystyrene or ceramic C11 8.2pF ceramic C12 0.015 μ F or 0.022 μ F ceramic TC1 4-20pF trimmer, ceramic TC2 4-20pF trimmer, ceramic TC3 3-10pF trimmer, ceramic TC4 10-60pF trimmer, ceramic

Inductors L1-L6 see text Transistors TR1 MEM616 TR2 MEM616 TR3 2N3819 TR4 BF254

Crystal X1 58MHz third overtone, HC-25/u Switch

S1 s.p.s.t. toggle

Sockets

SK1 coaxial socket SK2 coaxial socket

Miscellaneous Instrument case type BV3 9 volt battery type PP6 (Ever Ready) Battery connector Materials for coils (see text) Printed circuit board HC-25/u crystal holder (see text) Nuts, bolts, wire etc.



m of the converter. TR1 is the aerial signal amplifier and TR4 the crystal oscillator. doubles the crystal frequency and mixing is carried out by TR2

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7mm. former fitted with an iron-dust core. A suitable former can be cut from a former type 351/8BA, this being fitted with an iron-dust core type 6. These two items can, again, be obtained from Maplin Electronic Supplies.

The converter is housed in a case type BV3, this being supplied by Bi-Pak Semiconductors.

PRINTED BOARD

Virtually all the circuitry is assembled on a printed board which is reproduced, full-size, in Fig. 3. For greatest accuracy and least risk of strain on the component the two holes for the crystal pins should be marked out accurately with the aid of the crystal itself. The hole positioning may differ, and the copper pattern may need to be slightly amended, if a crystal holder is used. The other holes will cater for the components specified without difficulty. The three board mounting holes, and the two mounting holes for L4 former, are drilled out 6BA clear.

All the coils are home constructed. L1 and L3 are identical, and they each consist of exactly 4 turns of 0.9mm. diameter (or 20 s.w.g.) enamelled copper wire. They are self-supporting and are wound around a temporary fin. diameter former, such as the shank of a fin. twist drill. With each coil the enamel insulation is removed at a point $1\frac{1}{2}$ turns from one end (the earthy end) and the exposed copper is tinned with solder. This is the tapping point on the coil. The coil turns are spaced evenly so that each coil is 0.5in. long, and the lead-out sections which pass through the board are very short. The lead-out sections also, of course, have the enamel scraped away and are then tinned.

L5 is virtually identical with L1 and L3, the only difference being that it does not have a tapping point. L6 is very similar to L5 but has exactly 5 instead of 4 turns.

L2 is wound on a $\frac{1}{2}$ in. length of 7mm. diameter plastic tubing. As was mentioned earlier, this can be cut from a 7mm. former. The winding consists of 20 turns of 0.28mm. diameter (or 32 s.w.g.) enamelled copper wire which is tightly scramble-



The printed circuit board fits comfortably into the ready-made metal case, leaving adequate room for the battery



Fig. 4. Detail illustrating the manner in which L4 is wound

wound around the middle of the former. The two coil ends are twisted together close to the winding so that there is no possibility of the coil springing apart. The iron-dust core referred to earlier is fitted centrally in the former. It does not require any later adjustment as L2 is merely an r.f. choke.

L4 is wound on the Maplin former type 450 using 0.9mm. (or 20 s.w.g.) enamelled copper wire. The coil is illustrated in Fig. 4. The two end cheeks are made from small circular pieces of plastic or s.r.b.p. with a diameter of around 20mm. and a 10mm. diameter hole in the centre. The author used a couple of pieces of plastic which were removed from an old case by means of a $\frac{3}{4}$ in. chassis punch. Initially, one of the end cheeks is glued in position towards the bottom of the coil former. A 6BA solder tag is temporarily bolted in place at each mounting hole of the former by means of a short 6BA bolt and nut. The tags take up the positions shown in Fig. 3.

The winding has 6 turns and these are initially slightly spaced out. The ends are soldered to the tags. The insulation is scraped off the wire $1\frac{1}{2}$ turns down from the top of the coil and the exposed copper is tinned with solder. An insulated lead about 35mm. long is soldered to this point, which is the output tapping. Then the second cheek is glued in position, being used to compress the winding so that there are no significant gaps between the turns. Finally, C5 is soldered in position between the two tags.

The printed board has been designed such that, when L4 is bolted in position, the connections between the two ends of the coil and the copper pattern are made via its mounting bolts and nuts. The temporary bolts and nuts used when winding the coil are removed, and the coil is then secured in position on the board with the mounting bolt heads on the copper side. As was stated when circuit operation was described, the tap in L4 is nearer the end of the coil which connects to the drain of TR2. In Fig. 3 the mounting bolt which connects to the drain is the one nearer TR2.

CASE

The converter is housed in the metal case type BV3 and the general layout, which is very simple, can be seen in the photographs. The board is secured to the bottom of the case with three 6BA bolts and nuts, short metal spacing washers being fitted between the case bottom and board underside to prevent short-circuits to the case surface.



Fig. 3. Almost all the components are assembled on a printed circuit board. This diagram shows the copper and component sides of the board, and is reproduced full size for tracing

The imput and no put coaxiel sockets are mounted on the rear panel



The chassis connection to the board is made via one of these spacers. The board is oriented such that the crystal is to the front near the on-off switch. SK1 and SK2 are coaxial sockets mounted symmetrically on the rear panel. SK1 is positioned near TC1 and SK2 near C7. The connection to the outer contact of each is via the chassis and its mounting bolts and nuts. The battery may be held in place when the case lid is screwed on by a piece of foam plastic fitted between its upper surface and the lid underside. Alternatively, a simple clamp can be devised.

A metal case is necessary to ensure that all the circuitry is screened, thereby minimising breakthrough in the 28 to 30MHz range.

ADJUSTMENT

Any normal type of v.h.f. aerial can be connected to SK1, and for local reception a simple telescopic or dipole aerial is quite suitable. A 2 metre dipole has an overall length of $38\frac{1}{2}$ in., and the elements are each 19in. long. It is preferable for the aerial to be mounted fairly high up, and it is connected to the converter via a length of 75α coaxial feeder. The feeder is terminated at an ordinary TV aerial type of coaxial plug, which is fitted into SK1.

The converter is connected to the receiver by a short length of 75α coaxial cable, and the outer braiding of this is connected to the chassis of both the converter and the receiver. This must be a screened cable in order to minimise i.f. breakthrough, and it should be as short as possible to reduce losses and detuning effects due to its self-capacitance.

Initial setting up involves the measurement of battery current. A multimeter set to a high current range is inserted in series with the positive supply lead. The converter is then switched on. If the meter reading indicates that it is safe to do so the converter is switched off, the multimeter set to a current range which allows clear readings around 10mA, and the converter is then turned on again. A reading of approximately 10mA should now be given.

Adjusting TC4 should result in some variation of the reading, with there being a range of settings where there is a reduction in the current consumption. This is where the oscillator circuit is operating, and TC4 should be set towards the centre of this range.

If a suitable signal generator is available this can be used to feed a signal in the 144 to 146MHz range into the aerial circuit of the converter. The resultant heterodyned signal is then tuned in on the receiver after which L4 core, TC1 and finally TC2 are adjusted to peak the signal. TC3 can also be set to a peak level, but this will not be its final setting.

It will be found that the test signal can be received at two settings of the receiver dial, these being

spaced equally on either side of 29MHz. One signal is produced by the desired combination of the 116MHz oscillator signal and the aerial signal, whilst the other is the combination of the third harmonic of the 58MHz oscillator and the aerial signal. The 174MHz harmonic produces spurious responses with aerial signals of 146MHz to 144MHz as the receiver is tuned from 28MHz to 30MHz. There is obviously no way of reducing these spurious responses by improving the front end selectivity, since they are generated by the same range of frequencies which offer the required responses.

However, the responses can be minimised by adjusting TC3 to null the crystal third harmonic. The receiver is tuned to a spurious response from the test signal after which TC3 is adjusted to the null point. This point is very well defined and is just slightly off tune from the point where TC3 peaks the spurious response. Provided the adjustment is carried out correctly, the spurious response should be some 40dB or more down on the main one.

It should be found that the gain of the converter can be improved by adjusting TC4 towards the point where oscillation ceases. Be careful not to adjust TC4 too close to this point though, since this could cause the oscillator to be unreliable and it may not start when the unit is switched on. If the setting of TC4 is altered, then TC3 will need some slight readjustment.

Should a suitable signal generator and a multimeter not be available then the initial adjustments become largely a matter of trial and error, with the trimmers being tried at various settings and the band being searched for a transmission to act as the test signal. TC4 should be adjusted for almost maximum capacitance, whereupon the oscillator should then operate. The core of L4 can be peaked for maximum noise output from the receiver at first. When a suitable transmission has been located, the unit is adjusted in the manner just described.

If a comparatively long cable is used between the converter and the receiver it may be found that the core of L4 has to be almost fully unscrewed in order to peak sensitivity. This condition can be corrected by reducing the value of C5 to 39pF.

Editor's Note. Since this article was prepared for the printer it has been found that difficulty may be experienced in obtaining the HC-25/u crystal specified. An alternative is available, in HC-18/u, from P. M. Electronic Services, 7A Arrow Park Road, Upton, Wirral, Merseyside L49 0UB. The HC-18/u crystal has lead-out wires which may be soldered directly to the printed board.

RADIO AND ELECTRONICS CONSTRUCTOR

CONSTRUCTOR'S CROSSWORD

Compiled by J. R. Davies



CLUES ACROSS

- 1. Rubicon sham reveals frequency division. (11) 7. Burnt-out component defeated. (7)
- 8. Connector once known as a puff. (4)
- 9. Employ in humorous effect. (3)
- 10. Applies to ancillary extra. (3-2)

- Applies to alternary extra. (0-2)
 Enlist with the short wave enthusiasts. (6)
 Too many of these can form hum loops. (6)
 Light diffractive device. (5)
 Prefix, a muddled one, indicating newness. (3)
- 20. Ordered set of binary digits. (4)
 21. It revolves, yet is the same both ways. (7)
 22. We are all subject to this pressure. (11)

CLUES DOWN

- 1. This battery actually accumulates electricity. (7)
- 2. Little 'orror of a discharge device. (7)
- 3. Chassis edge tied up with strings? (5)
- 4. Booklet descriptive of organ key-board tuning? (6)
- 5. Natural logs of voltage ratios. (6)
- 6. A pair in connection. (6)
- 12. Con-artist in the crystal microphone. (7)
- Pertaining to figures. (7)
 Visual static display of north-seeking polarity. (6).
- 16. Double-cycle effect given by two stages in cascade. (6)
- 17. Pants which can cause sudden surges. (6)
- 18. Musical frequency. (5)

HIGH RATIO VARICAP DIODES

By W. Poel

Part 1 (2 parts)

Varicap diodes with high maximum to minimum capacitance ratios can control tuned circuits over a wide frequency range, including the full medium wave band. Our contributor discusses present performance and, in next month's concluding article, will describe an a.m. tuner incorporating a single i.c. and having full varicap tuning.

It is still a little known fact that relatively low cost varicap diodes for tuning the lower frequency bands have been on the market now for some two years. This is surprising bearing in mind the large number of advantages accruing from the use of such devices, as opposed to the more conventional mechanical counterparts. One or two constructional features employing the diodes have been published, but really nothing too elaborate.

tional features employing the diodes have been published, but really nothing too elaborate. Varicap diodes present a high capacitance at low reverse voltages and low capacitance at high reverse voltages. Historically, the early high capacitance ratio diodes — those having a ratio of high to low capacitance in excess of 15:1 — were costly to manufacture, highly individual in characteristics and certainly very expensive. £7 per diode was about average. However, Motorola's ion implantation technique as applied to the MVAM capacitance diode series has revolutionised production, producing high volumes of closely matched diodes at a price which is now competitive with any of the mechanical means of achieving three-gang tuning of radio frequency circuits.

VARICAP PRINCIPLE

The basis of operation is quite easy to grasp. The varicap diode junction is reverse biased so that no current flows, and there is a depletion layer at the junction between mobile charges. Increasing the reverse voltage increases the thickness of the depletion layer. Since capacitance can be demonstrated to increase by bringing two conductors closer together then it is simple to extend the idea to include the diode conducting areas on either side of the depletion layer. The diode capacitance increases when the depletion layer reduces in width, and it becomes smaller when the depletion layer increases in width. Here is our basis for the tuning of radio frequency circuits by means of a d.c. potential. Fig. 1 demonstrates a typical design for a v.h.f. tunerhead, an application where varicap diode tuning has been known for some five or six years. A tuning voltage ranging from 2 to 16 volts controls the tuning of the signal tuned circuits as well as that of the oscillator tuned circuit.

A much smaller capacitance change is needed to tune the v.h.f. band than is necessary for tuning the medium wave band, and technology has been well able to provide suitable varicap diodes for some time. The spread of the f.m. Band II is only 88 to 108 MHz, or 20 MHz. The ratio of the band (20MHz) to the highest frequency (108MHz) is only 0.18.

On medium waves the band extends from 525kHz to 1,605kHz. This gives a spread of 1,080kHz and a ratio of 0.67. The range of the necessary tuning capacitor swing is consequently far greater.

Typical U.K. standards for the medium wave band are 366pF tuning capacitors combined with 160 μ H oscillator coils, and an intermediate frequency of 470kHz. The oscillator range is consequently set to be 525 plus 470kHz to 1,605 plus 470kHz, which equals 995 to 2,075kHz.

From

$$C = \frac{(2 \wedge f)^2 I}{(2 \wedge f)^2 I}$$

we find that at 995kHz the capacitance total needs to be 160pF and at 2,075kHz it needs to be 36pF.

At signal frequencies an inductance of around 330μ H is employed, leading to a tuning capacitance at 525kHz of about 300pF and at 1,605kHz one of 30pF. Thus with the 366pF gang, and taking into account residual capacitance of about 12 to 20pF for an average design, the medium wave band is covered with a substantial margin of overlap. Obviously it is possible to tune the band with a smaller capacitance swing, but

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Fig. 1. A comprehensive v.h.f. tunerhead for Band II f.m. reception. TR1 and TR2 are r.f. amplifiers, with TR3 as mixer. The oscillator is TR5, with TR4 functioning as a buffer amplifier. All resonant circuits are tuned by varicap diodes

then stray capacitance can become the limiting factor, restricting the coverage of the higher frequency end of the band. Since the oscillator capacitance swing is so much less than the signal frequency capacitance swing, a series padding capacitor of about 270pF can be used to reduce the oscillator tuning range so that it tracks with the signal frequency range. Trimmers are obligatory in the circuits to "take up the slack" and ensure accurate tracking. The basic signal frequency and oscillator tuned circuits are shown in Fig. 2.

The same approach and formula can be used to determine L and C values anywhere in the r.f. spectrum, and the widespread availability of scientific calculators should make life a lot easier for the would-be tuned circuit designer.

DESIGNING WITH VARICAPS

The wide range varicaps used for tuning the medium wave band are available in two basic forms, both with a similar capacitance range, but the MVAM115 uses a 15 volt maximum tuning bias as opposed to the 25 volts of the MVAM125. The curve of capacitance versus voltage is shown in FEBRUARY 1978







Fig. 3. Curves illustrating capacitance change against bias voltage for the MVAM115 and MVAM125 varactor diodes

Fig. 3. There is a residual capacitance of 20pF at the maximum bias, which is rather greater than with most mechanical systems. But since the mechanical gang capacitor may need to be connected via relatively long wires to the tuned inductors, a considerable saving on circuit strays can be made by designing layout so that the varicaps are close to the inductors.

The long leads can then be on the d.c. control bias lines instead. Fig. 4 illustrates a comparative example.

It is the convenience aspect of designing with varicaps that really makes the difference for the amateur constructor. R.F. and oscillator circuits no longer need to be painstakingly set out alongside the tuning gang — they can be placed where they are most conveniently located in the layout, and fed from d.c. bias that is independent of the layout considerations that afflict r.f. circuits.

The electrical considerations of the circuit are broadly the same as for the previously worked example. Just remember the higher residual capacitance, and use a value of oscillator coil that will permit latitude of adjustment.





SHORTCOMINGS OF VARICAPS

As with all good things, there are some shortcomings. However, none are so drastic as to outweigh the advantages. The primary problem is the behaviour of the diodes at low bias voltages of 2 volts or less. In an oscillator tuned circuit the chances are that the oscillator voltage may exceed the bias voltage and thus become rectified by the diode action of the varicap. See Fig. 5. When this happens the oscillator may stop altogether, or at least become distorted and noisy.





(b). A relatively high signal amplitude, as may appear in an oscillator circuit, can be dealt with satisfactorily by the varactor diode when the tuning bias voltage is high

(c). Too low a bias voltage can result in the diode becoming conductive (as a normal silicon diode) over part of each cycle. The result is heavy distortion of the waveform

If this rectification takes place due to high levels of signal in the signal frequency amplification chain then cross-modulation may result, causing the receiver in which the diode is fitted to become unusable under certain circumstances. However, provided the minimum bias voltage is kept to about 2 to 3 volts these problems will not cause serious troubles.

Contrary to popular belief, varicaps do not suffer excessive thermal drift. In fact, the thermal stability of the varicap circuit is quite satisfactory — provided obvious aspects of design, like keeping the oscillator away from sources of heat such as power regulator stages — are taken into consideration.

RADIO AND ELECTRONICS CONSTRUCTOR

An important point to watch for is tuning voltage stability. Up to frequencies of about 4MHz a very small ripple on the tuning voltage will be unnoticed, but circuit requirements become more stringent at the higher frequencies. The span of 15 volts given with the MVAM115 could for instance tune a range of 16MHz in the 14 to 30MHz band. With a channel spacing of some 4 to 6kHz this represents about 3,200 discrete channels. Such conditions call for absolutely stable tuning voltages and a high degree of resolution in the tuning potentiometer system. A ten or twenty turn potentiometer is obligatory and, even then, a separate fine tuning control is a useful idea. This is shown in Fig. 6.



Fig. 6. When tuning over a large number of channels it is helpful to have a fine tune potentiometer in series with the main tuning control. The component values shown here are representative

At high frequency tuning ranges it is perhaps wiser to employ the 25 volt bias versions of the varicaps, since any ripple on the tuning voltage has proportionately less effect in terms of capacitance variation. Remember, too, that large values of decoupling capacitance may not be employed after the tuning potentiometer since these will slug the tuning rate, and the consequent slow change in tuning voltages will make the unit impossible to tune with any degree of accuracy. In Fig. 7 there is





no limit on the value of C1 but the maximum value for C2 is 0.1μ F. But then, the high impedance on the tuning voltage line also makes it susceptible to the pick-up of hum and noise, much as with a high impedance audio line. In other words there is a slight dilemma, which can be resolved by using an emitter follower to lower the impedance, as in Fig. FEBRUARY 1978



Fig. 8. An emitter follower after the tuning control permits the use of larger bypass capacitors on the tuning bias line

8. This will increase the current consumption of the circuit slightly but the lowered impedance of the tuning voltage line nevertheless makes it a worth-while addition. The thermal characteristics of the emitter follower lead to drift in the tuning unless care is taken to isolate the device as far away from heat as is possible.

And so, the arrangement we finally arrive at as being the ideal for tuning varicap diodes is that shown in Fig. 9.



Circuit for low frequencies

Fig. 9. A very good arrangement for controlling varicap diodes, showing alternative circuits at the diodes themselves for high and low frequencies. The 100µH choke cuts out h.f. noise

Readers familiar with the various techniques employed in optimizing r.f. circuitry will probably have thought up one or two other ideas, such as decoupling the feed resistor with 100pF to help cut out v.h.f. "spurii". But the number of such minor details is endless, and so the theory must stop here for the time being. Next month we shall move on to the application and consider a practical unit incorporating high capacitance ratio varicap diodes.

(To be concluded)

THE "DUETTE" STEREO AMPLIFIER Part 2 (conclusion)

By R. A. Penfold

In this concluding article details are given of the pre-amplifier printed circuit board, the tone control wiring and the input selector switch connections. Also discussed is the use of the amplifier with a high output gram cartridge.

In Part 1, published last month, the circuit functioning of the stereo amplifier was described, followed by constructional details for the case, the power amplifier printed board and the power supply board. We now proceed to the printed



The pre-amplifier board. This has two identical channels, each incorporating an f.e.t. amplifier

board on which the pre-amplifier circuitry is assembled.

PRE-AMPLIFIER BOARD

The pre-amplifier board is illustrated full size in Fig. 8, which shows both the copper pattern and the component layout. This printed board assembly is constructed in the usual way. It is mounted in the same manner as the previous two boards, using $\frac{1}{2}$ in. metal spacers on the mounting bolts. However, it cannot be finally fitted in position until it has been connected to the rest of the amplifier. It is oriented such that its two mounting holes are furthest away from the power amplifier.

A twin stereo screened cable connects the preamplifier panel to S1, and the braiding of its leads is earthed at the board only. Although the channels on the pre-amplifier board are identical it will be found helpful to assume that the components with the suffix "(a)" are in the left hand channel. The wires passing to C13-R15 and to C13(a)-R15(a) are not screened.

CONTROL WIRING

The wiring of the tone, balance and volume controls is illustrated in Fig. 9. This diagram shows one channel only, but both channels are of course the same. The chassis connections are made to a 6BA solder tag which is mounted on the base of the chassis between, and just to the rear of VR1 and VR4. A 6BA clear hole has to be drilled for the 6BA bolt and nut which secure this tag.

bolt and nut which secure this tag. Construction of this part of the unit will be found easier if the front sections of the dual gang potentiometers are wired up first. Try to keep the wiring reasonably short and direct. The tags of the potentiometers and the ends of component leadout wires should be well tinned with solder prior to making a connection. To maintain the channel routing, the front sections of the dual gang potentiometers can be made the right hand channel and the rear sections the left hand channel. Reference to the circuit diagrams of Figs. 1 and 2, which were published last month, will assist when wiring up the components around the balance control.





Fig. 9. Wiring up the volume, balance and tone controls. These are shown for one channel only FEBRUARY 1978

Another view of the completed stereo amplifier. The legends on the front panel are taken from Panel-Signs. Set No. 4



INPUT SELECTOR

The input wiring is illustrated in Fig. 10. The resistors making up the four attenuators are wired up directly on the tags of SK4 and SK5, and the lead-out wires of these resistors must be cut fairly short so that this wiring is reasonably rigid and there is no significant pick-up of mains hum. Although not shown in Fig. 10, the connections to S1 are made by way of screened wire. The attenuators at SK5 are connected via twin stereo screened cable, the attenuators at SK4 are connected via a second stereo screened cable, and SK3 is connected via a third stereo screened cable. The braiding of each cable is earthed at the appropriate input socket.

GRAM INPUT

The gram input has quite a high sensitivity, but some crystal and ceramic cartridges provide a nominal output level of only about 50 to 100mV r.m.s., and so these have to be catered for. If, however, the amplifier is to be used with a medium or high output cartridge it would be advisable to add an attenuator at the gram input, as otherwise there is the likelihood that the signal will be clipped at the output of the pre-amplifier, with the reproduced signal quality being extremely poor in consequence.

An attenuator for each gram channel could easily be wired up on SK3, in exactly the same manner that the existing input attenuators are wired up on



Fig. 10. The input selector wiring. Before connecting to S1 confirm the three outer tags which correspond to each centre switch arm tag; their relative positioning may differ with some switches

As a final detail a couple of bands of insulating tape can be used to hold together some of the many connecting cables, so that a neater interior finish is obtained. These bands of tape can be seen in the photograph of the interior which appeared in last month's issue.

The completed amplifier requires no adjustment whatsoever and so, once the unit has been completed and thoroughly checked for errors, it is ready for a practical test. SK4 and SK5. Suitable resistor values would be $820k_{\Omega}$ for the series input resistors, and $270k_{\Omega}$ for the following shunt resistors. This would leave the input impedance at its previous level of about $1M_{\Omega}$ but would reduce the input sensitivity to approximately 200mV r.m.s. for 2 watts output. About 1.6 volts r.m.s. would then be needed in order to seriously overload the pre-amplifier, and it is unlikely that any cartridge would supply such a signal. (Concluded)

(Concluded)

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Before undertaking any constructional project described in a back issue, it must be borne in mind that components readily available at the time of publication may no longer be so.

Chassis, Earth and Ground

EVGU

By D. F. Thomas

If you make a connection to the metalwork of a piece of equipment, does that connection go to "chassis," "earth" or "ground"?"

IS FOR N

An aspect of electronics which can confuse the beginner is the apparently indiscriminate usage of the terms "chassis," "earth" and "ground." Sometimes they appear to refer to the same thing and sometimes they seem to refer to quite different things.

In one text you may find that all the connections to the metalwork of a piece of equipment are described as being "connected to chassis," and in another you may find that they are "connected to earth." Then in a third text you may encounter the statement that an equipment "chassis is not earthed"!

There is, in addition, the mysterious "ground" which quite often crops up in technical literature. Things are described as being "connected to ground" or as being "grounded."

"EARTH" AND "GROUND"

We can clear up the terms "earth" and "ground" without delay, since they both mean exactly the same thing. "Earth" is the term mainly employed in Britain whilst "ground" is almost invariably used in the U.S.A. Nowadays, the word "ground" is being used more and more in the U.K., partly because we have employed a lot of American designed devices, such as integrated circuits, for which the technical references include the word "ground." But, to reiterate, there is no difference in meaning between the two terms. If this article were to appear in an American magazine all the remaining section of it would use the word "ground" instead of "earth," simply because American readers are more used to the term.

Dealing next with symbols, Fig. 1(a) shows the circuit symbol for "earth" and Fig. 1(b) that for "chassis." Often, and particularly in service manuals, the chassis symbol consists of a broad horizontal line, as in Fig. 1(c).

We now come to "chassis" and "earth" and here we do encounter conflicts of meaning. In general, virtually all electrical and electronic systems have a metal framework which provides a common reference or connection point for all the stages and sections of the system. We frequently refer to this as the "earth" of the system, even when it is not actually connected to earth. A common example is encountered in car electrics, where the metal frame of a car is referred to as "earth," despite the fact that the framework is well and truly insulated from the actual earth by four rubber tyres.

In early domestic radio receivers in the U.K. the same thing applied. Many of these sets were built like battleships with heavy metal chassis, and the chassis provided the "earth" of the receiver. A connection to the chassis was a connection to "earth." In practice the chassis was frequently connected by a wire to the actual earth itself by way of, say, a length of copper tubing driven into the soil.



www.omorioonrodichistory.com



Fig. 2(a). An example of a valve receiver a.c. mains supply incorporating a mains transformer

(b). A pewer supply without a transformer. This early circuit could operate from d.c. as well as a.c. mains supplies, and there is a direct connection between the receiver chassis and one side of the mains.

MAINS-DRIVEN RADIOS

The first a.c. mains-driven radios had mains transformers with separate h.t. and heater secondaries, as shown in Fig. 2(a), and again the receiver chassis could be directly connected to earth. Then along came "transformerless" receivers in which, to reduce costs, the mains transformer was omitted and the chassis was connected directly to one side of the mains, as in Fig. 2(b). The chassis was housed in a wooden or plastic cabinet and was completely enclosed to prevent its being touched and thereby imparting a shock. Usually, the mains supply lead had two wires only, so that it was just as likely that the receiver chassis could connect to the live side of the mains as to the neutral side. This practice continued with television receivers, with the result that nearly all domestic mainsdriven receivers in the U.K. had chassis which could connect to either the live or the neutral side of the mains. These receivers were largely responsible for the replacement of the term "earth" "chassis" for the common connection point.

Now, there is nothing wrong with referring to the chassis of a motor car as "earth," because it is merely insulated from earth and is not connected to anything else. But it is quite another thing to refer to a receiver chassis as "earth" when that chassis can connect to a voltage which is actually lethal with respect to earth! This is when the use of the word "chassis" started in earnest, and was applied to connections to the chassis of receivers of this type.

Those old mains valve radios are now almost a thing of the past, although there are still very many "live chassis" TV sets around. And so the term "earth" is creeping back again, at least so far as connections to a radio receiver chassis are concerned. Indeed, some of the smaller items of equipment

don't have a metal chassis at all, consisting as they do of a printed board fitted in a plastic case. If these are battery operated, the "chassis," or "earth," may simply be the negative supply rail or the positive supply rail.

And that, in general, sums up the situation. If a piece of equipment has a chassis or metalwork which is actually connected to earth, connections to it can be described as connections to "earth" or connections to "chassis." A similar situation arises when the chassis is not connected to earth but is also not connected to anything else. But when the equipment chassis connects to one side of the mains the use of the term "chassis" instead of "earth" is almost obligatory, because the chassis is in this instance quite dangerously removed from earth potential.





Did you know that a straight piece of wire has inductance? And that this inductance increases as the wire gets thinner?

These are just two of the many little facts concerning inductance that one bumps into when working in radio and electronics. In general, we tend to accept inductance without question but it is an electrical quantity that can play quite a few tricks on us if we don't take appropriate precautions. When it starts to reveal what are usually hidden effects, inductance can be much more mischievous than the other two foundation stones of electronics — capacitance and resistance.

INDUCTANCE

The electrical properties of inductance are just the opposite to those of capacitance. After settling down following the initial connection, an inductance acts, at d.c., as a length of wire (actually, the wire with which it is wound) whilst a capacitor acts as an open-circuit. At a.c. the reactance (very roughly "a.c. resistance") of an inductance increases as frequency increases whilst the reactance of a capacitance decreases with increase in frequency.

A capacitor tends to oppose a change of voltage across its plates, and an inductor, or coil, tends to oppose a change in the current flowing through it. When a sinusoidal alternating voltage is applied to a capacitor the resultant current leads the voltage by 90 degrees (one quarter of a cycle), but in an inductor the current lags by 90 degrees on the voltage. This is fairly easy to visualise: if you connect a battery via a series resistor to a capacitor the initial current is high and then reduces as the voltage across the capacitor for an inductor and the initial current flow is low, rising as the voltage across the inductor increases.

It is not difficult to make a capacitor which is very close to being an "ideal capacitor", i.e. a capacitor without resistance or in-

ductance and having infinite resistance between its plates. Making an inductor which is close to an "ideal inductor" is much harder. To start off with, the inductor has to be wound with wire, which has an inevitable resistance, and there are bound to be stray capacitances between turns. Additional factors accrue if the inductor has a ferrite or iron-dust core, or has a laminated core as has a mains transformer. The permeability of all these cores varies according to the current which flows in the inductor and therefore changes the inductance. This effect is, fortunately, much too small to be of any significance with coils having ferrite or iron-dust cores which are used in the r.f. and i.f. stages of radio and television receivers, because the currents flowing in such coils are very low. On the other hand it is of considerable importance when high currents flow in the inductor, as occur with ferrite cored tape recorder erase heads and ferrite cored TV line output transformers. Changes in permeability with the laminated cores of mains transformers and the like can be very high, and these have to be taken into account in the design of the transformer.

WIRE INDUCTANCE

And now let us turn to the fact that a straight piece of wire has inductance. At low frequencies this inductance is normally too small to raise any problems but it can become quite a serious matter at v.h.f. frequencies above 50MHz or so. Band III television tuners covering Channels 6 to 13 (175 to 220MHz) use coils in the aerial, oscillator and mixer tuned circuits. If one of these is tuned to Channel 12, inserting half an inch of wire in series can easily make it tune down to the lower frequency Channel 11! Band III coils usually have only three or four turns of wire in them and, with a standard turret tuner, these have to connect to two tags on the turret. It may happen that Channels 6 to 9 can be covered by coils having four turns of wire. Since there has to be a discrete

number of turns on each coil so that it can connect to its tags, the reducing inductance as the Channel numbers go up can be provided by increasing the spacing between turns and by using thicker gauges of wire for the coils. If the coils for Channel 10 cannot be wound with four turns, the windings switch for this Channel to three relatively closely spaced turns of thin wire. The large drop in inductance given by going from four to three turns is partly compensated for by the extra inductance of the thin wire. Turret tuners are now, of course, largely obsolescent since current manufacture is for the u.h.f. Bands IV and V, which extend from 470 to 855MHz. At these frequencies all attempts at using coils in the aerial, oscillator and mixer tuned circuits are abandoned. The u.h.f. tuned circuits consist of straight Lecher lines and variable capacitors or varicap diodes.

Whilst the inductance of a straight piece of wire is, in general, too low to be very troublesome at frequencies below some 50MHz or so it is still necessary to follow common-sense rules so far as the wiring up of tuned circuits is concerned. It is always very desirable to have short connecting wires between the capacitor and inductor of any r.f. tuned circuit, and the need for such short leads increases with frequency. A long connecting wire may not alter the resonant frequency to any appreciable extent, but it will certainly introduce losses and lower the Q of the tuned circuit. There has to be wire in the inductor and it is preferable that there be as little extra wire in the tuned circuit as is reasonably possible

Another little item to add to the list which qualifies the behaviour of inductors is "skin effect". When a radio frequency current is caused to flow through a wire it sets up magnetic fields in the wire which in turn induce secondary currents which oppose the primary current. The result is that the r.f. current has to flow mainly on the outer surface of the wire, with the result that

RADIO AND ELECTRONICS CONSTRUCTOR

the r.f. resistance of the wire is quite a lot higher than its d.c. resistance. This skin effect becomes worse as frequency increases, which is one reason why short wave tuned coils are wound with quite thick wire having a large surface area. In the early days of domestic radio Litzendraht multi-stranded wire was introduced to reduce skin effect. This wire, commonly known as Litz wire. has a number of strands of thin enamelled wire woven together in such a manner that each strand comes out to the surface in turn, with the result that all the strands carry the same amount of r.f. current. The theory is that it is then possible to force more r.f. current through the Litz wire than can be passed by a solid wire of the same overall diameter. The Litz wire also reduces discrepancies in currentcarrying capabilities along the length of a coil. The theory works very well in practice over a rather limited range of frequencies, and coils wound with Litz wire have a higher Q than coils wound with single wires of comparable diameter. Nowadays, much of the Litz wire employed in electronics is the cheaper "bunched Litz", which does not have the careful weaving pattern of the original Litz formulation but which nevertheless functions very nearly as well.

LIMITED FREQUENCIES

The only snag with Litz wire is that it is only really effective at frequencies from around 200kHz to 3MHz. You will therefore find it mainly in i.f. transformers resonant at around 460kHz and in medium and long wave aerial and oscillator tuned circuits.

The individual strands of Litz wire are insulated from each other except at the ends where they are all soldered together at the coil connection points. If you ever have to resolder a broken Litz wire, you don't need to despair at the thought of cleaning the enamel off all the individual strands before you can solder to them. This was necessary in the old days when the wire

Partiel . .

enamel was oil-based (similar to that employed on most of the enamelled wires sold on the homeconstructor market). With presentday Litz wires the strands are covered with "solder-through" enamel, whereupon you merely apply a reasonably hot soldering iron and resin cored solder to the Litz wire ending and all the enamel on the wire strands dissipates into the solder and flux, allowing a satisfactory joint to be made to the copper underneath.

I have only just covered the surface of inductance in these notes, but you will see that it presents quite a complex subject in its own right. Fortunately, the homeconstructor can either buy his inductors ready-made or can successfully wind basic coils himself. But it is always a good thing to have an idea of what goes on, even in such simple assemblies as these.

T.T.L. STICKIES

One problem in trouble-shooting or breadboarding t.t.l. circuits is that, unless you're fully conversant with the pin allocations of the i.c.'s, you have to keep referring back to the data books to find which pin does this and which pin does that. A very neat idea to overcome this problem is illustrated in the accompanying photograph. Affixed to the top of the i.c. is an adhesive printed label which indicates at a glance all the pin functions of the i.c. concerned.

These labels are "STICKIES", and are made by Concept Electronics. They have the same size as the i.c. to which they are applied and are available to show pin-outs for the 61 most popular 14 and 16 pin t.t.l. packages. They can be used for constructing and debugging prototypes, fault-finding on production circuits, and even for designing printed circuit layouts. They are also a valuable teaching aid.

Available in sets of 450, "STICKIES" are packed in reusable plastic folders complete with comprehensive instructions. The instructions also give a list of logical equivalents which can be used to extend the range to cover 86 i.c.'s. Further details can be obtained from Concept Electronics, 8 Bayham Road, Sevenoaks, Kent.

JUMPING THE GUN

One of the problems of producing a constructional journal is that long articles describing the more ambitious projects have usually to be spread out over two issues. This is of course unavoidable and we ease things by giving as much information as possible on the components that are required in the first part of the article. Such an approach enables the constructor to have all the relevant information on the parts in a single issue.

One of our regular contributors has raised another point concerning articles which appear in two parts. Where setting-up instructions are given it is obvious that these will normally appear in the second part since, by then, all the constructional and wiring details will have been given. There is the possibility that someone, working to the circuit diagram and what constructional details are given in the first part, may build the project and then be unable to set it up correctly. This doesn't seem to be the cause of much trouble in practice, but it might be worth mentioning that it is always best to wait until setting-up instructions appear and are followed before making the assumption that a project can be considered complete.

WATER EARTH

Since 240 volts a.c. can cause a lot of unhappiness if it turns up in the wrong places, I'm rather a fanatic so far as checking the earthing of domestic mains appliances is concerned. Recently, a member of the Recorder family acquired a second-hand electric kettle at a local jumble sale and I decided to test it for safety before anyone put it to use. The 3-core lead and the connector plugging into the side of the kettle seemed to be

H nu to tell, at a glance, the pin hunctions of a t.t.f. integraned circuit. Concept Electronics have introduced stick-on printed labels which can be applied to the GI most popular togic i.o.'s By telling adventage of equivalents the range can be extended to cover all packages

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perfectly satisfactory and the kettle looked, indeed, as though it had not seen a great deal of use. Out came the multimeter, to reveal a satisfactory low resistance reading between live and neutral leads and virtually infinity between either of these leads and the earth lead.

I next checked between the earth lead and the metal shell of the kettle. An open-circuit! Complimenting myself on having discovered a possible shock hazard I examined, the side of the kettle to see if any connection had come adrift, to find that there simply was no connection to the metal of the kettle.

And then the penny dropped. A check between the earth lead and the metal cover of the heating element inside the kettle revealed zero ohms. The kettle was manufactured such that the metal shell was floating but was earthed, when the kettle was filled, by way of the water in the kettle itself.

I suppose this is a reasonable approach. But what is the situation, I wonder, if we should happen to use the kettle to boil distilled water?



AUTOMATED HAND TOOL FOR ACCURATE COAX CABLE STRIPPING

A new hand operated coaxial cable stripper has been introduced by AB Engineering Company under the model number COAX-1. Simple hand pressure ensures fast and accurate stripping of television, telecommunication and other coaxial cables up to 7.5 mm diameter. The tool incorporates four apertures offset to a common cutting blade which when automatically adjusted by means of two screws provides the necessary variation in the depth of cut to:

Aperture one — strips the outer insulation; Aperture two — cuts through the screen and strips the insulation leaving a 7 mm or 12 mm length; Aperture three — strips the dialectric; Aperture four — cuts through the cable.

In use the spring loaded cutting blade is opened by pressure on the handle, the cable inserted and the tool is then spun round and the spring loaded blade pressure is sufficient to complete the cutting and stripping operations.

For economy in use the cutting blade may be adjusted when it becomes blunt. Each blade is guaranteed for a minimum of 150 operations and



spare blades are available in packs of ten.

The AB Engineering Coax-1 is ideal for service engineers, small volume workshop levels and production rectification work. Further details may be obtained from AB Engineering Company, Apem Works, St. Albans Road, Watford, Herts.

THE SINCLAIR ENTERPRISE CALCULATOR



What sort of calculator does the British public want? Dual-purpose for desk top or hand-held use; a readable display in any light; long battery life; a full memory, percentage key and, to Sinclair's surprise, a square root facility.

Those were the answers Sinclair received from a nationwide research programme into the non-specialist calculator user market, which the company values at £20 million plus.

The result? A combination of all these requirements — the new Sinclair Enterprise — an all-purpose calculator now available at £9.95 plus VAT. The Enterprise is slimly built for portability; has a big, clear, bright display; well spaced keys and a battery life averaging one year from a standard, readily obtainable, transistor radio battery.

The Enterprise measures 5ins. x 2ins., is ‡in. deep (max) and weighs 40z. Power is provided by a simple to fit PP3 type battery, with mains power as an option. Features a 'king size' eight digit red display with wide-angle vision under all light conditions.

With a durable satin silver finish, special rubber non-slip feet for desk-top use, the Enterprise has the standard Sinclair 12 month 'repair or replace' guarantee. It is presented in a unique flip-up display box with a detailed instruction book and its own carrying case.

The Sinclair calculator range now comprises the Enterprise, the Cambridge Memory, Universal, Scientific and Programmable handheld units, the desk-top Oxford Universal and Scientific, and the prestige Sovereigns, available in solid gold, gold and silver plate, chrome and matt black.



N-STAGE SEQUENCER

Smithy describes a fully electronic sequencer which simply causes a succession of relays to energise, one after the other, with a predstermined time interval between the energising of one relay and the energising of the one which follows.

"My Auntie Eff," said Dick chat-tily, "had rather an unpleasant accident with her Christmas dinner." "Did she?" queried Smithy. "What happened?"

"Well, she decided to have a duck this time instead of the usual turkey, and one of her cats, a real mangy old thing, ate a great chunk

out of it just after she'd cooked it." "Dear me," remarked Smithy mildly. "That must have raised a problem for her." "It did," grinned Dick. "It left

her with a duck-filled tatty puss!'

Smithy winced. "I should have known better," he sighed morosely. "I was hoping you'd be giving up those terrible gags of yours for 1978." The Serviceman looked around

him. This was the first morning of work after the New Year holiday and all signs of the frantic rush before Christmas and the fairly busy subsequent period leading up to New Year's Eve had now com-pletely disappeared. The "For Repair" rack was empty, awaiting the first servicing jobs of 1978, and the Workshop was pervaded with an unstrained and unwonted at-

mosphere of serenity. "It seems peculiar," mused Dick, "to think that we are already in 1978. All we do is go trundling through time at a breakneck speed,

and the years fall away behind us like leaves in Autumn." "Blimey, you're getting a bit poetic, aren't you?" remarked Smithy, cocking an eyebrow at his assistant. "So far as I'm concerned take time as it comes I if a is just a I take time as it comes. Life is just a series of sequences following each other."

"Sequences," - repeated Dick slowly. "It's funny you should refer to sequences. Over the holiday I **FEBRUARY 1978**

heard about a guy who's assembled a whacking great model train layout. When he wants to run it he puts all the trains and signals and points and things through a set of programmed sequences. The idea of the whole thing has stuck in the back of my mind ever since I was told about it."

Smithy's interest was aroused. "How," he asked, "does he programme the sequences ?" "Apparently he's made up a tape

and plays this through a tape recorder. When something on the model train installation is due to happen there's a short a.f. tone from the tape and this goes through filters and activates the appropriate relay controlling the action re-quired."

"That's one way of running a se-quence of operations," said Smithy thoughtfully. "But there's an alter-native idea which comes to my mind straightaway. This could consist of a fully electronic sequencer which simply causes a succession of relays to energise one after the other, with a predetermined time interval between the energising of one relay and the energising of the one which follows it."

"How many different steps could you incorporate in a sequencer like that ?"

"As many as you liked," replied Smithy. "There would have to be a starting stage and this would then be followed by a series of identical sequencer stages, each one triggering off the next after a pre-set time delay. At the end would be a, finishing stage which, when operated, would turn the whole lot off again!"

"Stap me," said Dick, impressed, "this sequencer scheme you're talking about seems to be very versatile."

"It is," confirmed Smithy. "Apart from the fact that it can have as many stages as you like the timing period for each stage in the sequence can be pre-set to any length from about 2 seconds to longer than 2 minutes. The completed sequencer could control steps. in a model train layout or in virtually any other electrical system in which operations are carried out in

a predetermined order." "You said just now," stated Dick, "that each stage in the sequencer would use an identical circuit. What would that circuit be like, Smithy?"

The Serviceman pulled his notepad towards him.

"Come over here," he called out as he started to sketch a circuit on the pad, "and I'll show you."

Eagerly, Dick carried his stool over to Smithy's bench and perched himself alongside the Serviceman. With a flourish, that worthy drew in the last line of his circuit diagram. and then placed his pen on his bench. (Fig.1.)

"There you are," he remarked. "Now, this sequencer stage is very simple and it works entirely from basic first principles. It is preceded by a stage having precisely the same circuit and whose output is coupled to R2 in my diagram. As you can see, the stage employs a 555 timer. The output from the previous stage is at a high voltage before it is ac-tivated and this causes current to flow into the base of TR1 via R2. In consequence, TR1 turns hard on and ensures that C1 is fully discharged. R3 is merely a current limiting resistor which prevents excessive collector current in the transistor when, at a much later stage in the proceedings, the transistor



Fig. 1. One of the intermediate stages in Smithy's N-Stage Sequencer. Combined with suitable starting and finishing stages, there can be any number of these intermediate stages, each following a previous one. The timing period of the stage is controlled by the values of R1 and C1.

causes C1 to be discharged from the charged condition. The 555 comparator inputs, at pins 2 and 6, are very close to the potential of the negative rail, and under this condition the 555 output, at pin 3, is high. This means that relay RL1 does not energise.'

"Will there be a similar relay coil in the previous stage ?" "There will," stated Smithy.

"Now, at a pre-ordained moment in the overall sequencer cycle, the preceding stage will turn on, and the output at pin 3 of its 555 will go low, energising its relay. When the preceding stage output goes low, the voltage at the left hand end of R2 in my diagram falls nearly to the negative rail and TR1 turns off. C1 is now able to charge via R1. When the voltage across C1 reaches twothirds of that on the positive rail the 555 suddenly changes state and its output, at pin 3, goes low. C1 con-tinues to charge after this, but it has no further effect on circuit operation. Relay RL1 now energises and its contacts switch on the appropriate controlled circuit in the sequence. At the same time, the low voltage output at pin 3 passes to the next stage, which has a similar resistor to R2 at its input."

ROBUST OPERATION

"Blow me," said Dick, "you couldn't have things much easier than that. I assume that the time taken for the relay to energise after the preceding stage has operated is controlled by R1 and C1."

"That's right," confirmed Smithy. "And it will be approximately equal to the time constant of these two components. The time constant of R1 and C1 is the time needed for the voltage across the capacitor to reach 63 per cent of the supply voltage after TR1 turns off. This is very nearly the same as the two-thirds of supply voltage 372

which is needed to trigger the 555, and so the calculations needed to find the values of R1 and C1 for a specific time delay are quite easy to carry out. Another useful aspect of the circuit is that it is very robust. The only sensitive points which are likely to respond to unwanted voltage spikes or pulses are the i.c. comparator inputs at pins 2 and 6, but in this circuit they are held at a low impedance to the negative rail by C1. In practice, this capacitor

will have a value of at least $10\mu F$." "I see what you mean," remark-ed Dick, as he gazed at the circuit. "Well, that shows one of the stages in your sequencer. How about adding another stage and also the starting and finishing stages?"

All right," said Smithy obliging-"The starting and finishing stages need a little more thought to work out than did the stage I've just drawn, so I'll have to check out some ideas here."

Smithy bent over his note-pad and proceeded to draw out several experimental circuits, mentally checking their operation as he did so. After some minutes he gave a grunt of satisfaction.

"Ah yes," he said cheerfully, "I've got my ideas sorted out now. Hang on a bit and I'll draw the full circuit.

He tore off the top sheet of his note-pad, then proceeded to sketch a new circuit. This was much larger than the previous one and it was quite some time before Smithy had it completed. Dick watched in fascination as the circuit took shape. (Fig.2.)

N-STAGE SEQUENCER

"Now how about this ?" Smithy put his pen down again and rubbed his hands together briskly. "One complete sequencer which can offer you all the steps you require. I think I'll call it my 'N-Stage Se-

quencer'!"

"It certainly looks impressive," commented Dick. "How do you get it started ?"

"First of all," said Smithy, "you switch on the power at S2 with S1 in the 'Stop' position. All the C1 capacitors throughout the sequencer are discharged, whereupon all the 555 pin 3 outputs are high and all the relays are de-energised. The high 555 outputs also mean that all the transistors from TR1B to TR1N are turned hard on and all the capacitors from C1B to C1N are maintained discharged."

"What about TR1A?"

"That's turned hard on, too, and it keeps C1A discharged. However, TR1A doesn't get its base current from a preceding 555; instead it gets it via the coil of relay RL2. The coil resistance of RL2 will be much lower than the value of R2A, which means that TRIA will get nearly as much base current as the other transistors. At the same time the current in the coil of RL2 will be too low for that relay to energise." "There's a relay contact set in the

negative supply to the relay coil. What relay does that belong to ?"

"It belongs to the last relay in the sequencer, which is relay RL1N," explained Smithy. "And the circuit shows the contact sets in the de-energised position." "Right! Let's get this sequencer started, Smithy!"

"Okay," replied Smithy equably. "We kick off by moving S1 from the 'Stop' to the 'Start' position. This completes a circuit, via the deenergised contacts of RL1N, from the negative supply rail to the coil of RL2, and this relay energises. Its contacts close, to start off the first step of the sequencer. Since the lower side of the relay coil is now connected to the negative rail there is no base bias current for TR1A, and this transistor turns off. C1A commences to charge and, when it has reached two-thirds of supply potential, IC1A changes over and relay RL1A energises. Its contacts actuate the second step of the sequence. TR1B is now turned off and capacitor C1B commences to charge. And so the sequence proceeds, with each relay in the system becoming energised in turn.'

"What happens when the sequence advances to TR1N and IC1N? That i.c. circuit looks a little different to the others."

"That's because it's in the finishing stage," stated Smithy. "When the sequence reaches C1N this begins to charge just like the previous capacitors did, and IC1N changes state when the capacitor voltage reaches two-thirds of the supply voltage. IC1N output at pin 3 then goes low and relay RL1N energises by way of D3. As a result the contacts of RL1N, over at the left hand end of the diagram,

RADIO AND ELECTRONICS CONSTRUCTOR



the Sequencer. The intermediate stages all have the circuit of Fig. Resistors R2 and R3 are $\frac{1}{2}$ watt 10% of Fig. 2. The complete circuit

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GAREX

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change to the energised state, and complete a circuit, via S1 in the 'Start' position, from the negative rail to the lower side of RL1N coil. So RL1N is now held energised by its own contacts and not by IC1N.

"If RL1N contacts change to the energised position," said Dick, frowning, "there'll be no energising circuit for the coil of relay RL2.

"There won't be," agreed nithy. "As soon as RL1N Smithy. energises, its contacts cause RL2 to release. A base bias current now flows into TR1A via R2A and the relay coil, and TR1A turns hard on, rapidly discharging C1A. When the voltage across C1A falls to one-third of the supply voltage TC1A changes state and its output goes high, caus-ing RL1A to release and TR1B to be turned on. TR1B quickly dis-charges C1B. This process runs rapidly through the sequencer with each relay releasing in turn." "Blimey," breathed Dick. "It's

like a row of dominoes falling down! What about the last relay, RL1N?"

"That's the only one which remains energised. In its turn, the output of IC1N will go high but this merely causes D3 to be reverse bias-ed. RL1N remains energised via its own contact set, as I explained just now. So RL1N has caused the sequence to be completed with all the other relays de-energised. To start the sequence again, S1 is first put to 'Stop'. This breaks the circuit to the coil of RL1N, which releases, whereupon its contacts at the left hand end of the circuit change over to the de-energised state. The sequence will now start all over again if S1 is returned to the 'Start' position. Incidentally, you can shut off the sequence at any point in its cycle if you wish to do so by putting S1 to 'Stop'. This causes all energised relays to release.'

RELAY LOGIC

"Hell's teeth," said Dick appreciatively, "this is no end of a cir-cuit, Smithy! Hey, I've just thought of something!" "What's that?"

"All the relays stay energised after they've been turned on,"

stated Dick. "That means that all the controlled sequence steps will stay on after they've been turned on in the sequence. Shouldn't each one be turned off after the appropriate step?" "Ah," said Smithy. "This is

where we apply a little relay logic. Now, in virtually all the controlled systems I can think of, each step can be controlled by applying a supply voltage to the circuit concerned. So what we do is connect the relay contacts in an arrangement like this.

Again Smithy picked up his pen

and drew out a circuit. (Fig. 3.). "What happens here," he went on as he completed his diagram, "is that, at the start, the contacts of RL2 close and cause the supply voltage to be passed to controlled circuit A. When RL1A energises its contacts break the supply to circuit A and pass it to circuit B. The sequence proceeds in this manner un-til we reach the relay for the stage immediately before the last one. The contacts of this relay merely pass the supply current direct to the controlled circuit, which is circuit N. When relay RL1N energises it causes relay RL2 to release, and the supply voltage is cut off by RL2 contacts. You should note that it is the release of RL2 which finally ends the sequence. Since RL2 contacts open first, the contacts of subsequent relays cannot pass momentary supplies of current to the controlled circuits before they, too, release. This assumes, of course, that relay RL2 is the same type as the other relays or, at least, has the

"What do you do," asked Dick, "if you want to have a controlled circuit turned on after another cir-cuit has started and before it ends?"

"You use relay logic again," said Smithy. "Look, I'll give you an example.

He drew another circuit on his pad. (Fig. 4.).

"Here's the idea," he continued. "In this circuit contacts C close at some point during the sequence and cause the supply to be passed to controlled circuit C via contacts F. After a period, contacts D operate



Fig. 3. Relay contact logic allows each controlled circuit to be switched on and off in turn. All the contacts are shown in the deenergised state.

and pass the supply voltage to controlled circuit D. This connection breaks when contacts E operate, and some time later contacts F operate and break the supply to circuit C. So we have managed to insert circuit D into the middle of circuit C. Got it?"

cuit C. Got it?" "Blimey, yes. Incidentally, what sort of relays should be used in the sequence?"

"Whatever, within reason, you can lay your hands on. There is for instance a very nice and suitable relay with a 410Ω coil which is available from Maplin Electronic Supplies and this has a changeover contact set which will switch a.c. mains voltages up to 45 amps, or low voltages at d.c. up to 5 amps. Whatever relays are used it is preferable that they have reasonably high resistance coils, say 300Ω or more, as this eases supply current demands. Also, if a relay coil draws too much current, the output at pin 3 of the associated 555 i.c. may not go sufficiently below 0.6 volt to turn off the following transistor. Theoretically, this can happen when the current drawn through the relay coil is in excess of around 30mA or so, although in practice the 555's I have handled seem to be able to draw down much higher currents than that without the output voltage being above 0.6 volt. If it should happen that any 555 output does not go sufficiently low to turn off the following transistor, it is merely necessary to add another 10k or resistor between the transistor base and the negative rail or, in really bad cases, to insert a silicon diode in series with the base circuit as well. But, as I say, it's unlikely that you'll need to do either of these things in practice." (Fig. 5(a) and (b).).

POWER SUPPLY

"You mentioned power supply demands just now," said Dick. "What sort of power supply would you recommend?"

"Pretty well anything that will supply the current required at some 10 to 12 volts," replied Smithy. "Each 555 will draw something like 8mA all the time and you have to add to that current drawn by the relay coils as they become energised. The relay current continually increases as the sequence proceeds. reaching its maximum just as RL1N energises, but this should still be well within the capabilities of a simple unregulated mains supply using a bridge rectifier. The mains transformer secondary can have a voltage of around 8 to 9 volts and, if it is rated at a current well in excess of the maximum consumed by the sequencer, regulation should be quite adequate. The power supply voltage must not exceed 16 volts, incidentally, which is the maximum rated voltage for the 555." (Fig. 6.)



Fig. 4. A further example of relay logic, in which circuit D is turned on and off during the period when circuit C is on. The contacts of the following relay, RL1G, will then control circuit E.





In series with R2.



Fig. 6. A simple mains supply such as that shown here is quite adequate to power the sequencer. As there is now a mains on-off switch, S2 of Fig. 2 can be dispensed with



Fig. 7(a). In practice C1 has to be an electrolytic component, whereupon R1 must be partly variable. The component values shown here allow the circuit to be set up for 5 seconds (b). A timing circuit suitable for 50 seconds

(c). These components can be adjusted for a timing period of 2 minutes

Smithy looked expectantly at his assistant. "Fair enough?" "Yes, sure," said Dick. "No,

there's just one other thing. What about the values for R1 and C1 in each stage?"

"Blimey, I'd forgotten all about them! Well, as I said earlier on, the period for each stage will be approximately equal to the time constant of the resistor and the capacitor. The time constant in seconds is equal to the resistance in megohms multiplied by the capacitance in microfarads. It will be preferable to keep the resistance in the range of some $20k\Omega$ to $500k\Omega$, which means that the capacitors will all have to be electrolytic types with their well known wide tolerance on value. In turn, this means that the resistance will have to be partly variable so that it can be set up accurately to the re-quired period." "What value of R1 and C1 would

you want for a period of 5 seconds?" "A good combination here," said Smithy thoughtfully, "would be 50μ F and $100k\Omega$. In practice the $100k\Omega$ could be given by a $100k\Omega$ pre-set pot in series with a 47ko fixed resistor. This would give you

more than sufficient range to be able to set up a 5 second period: Also, the capacitor could in practice be 47μ F." (Fig. 7(a).) "And 50 seconds?"

"Just increase the capacitance to 470 μ F." (Fig. 7(b).) "All right, then, what about a period of 2 minutes?"

"That's 120 seconds," stated Smithy. "1,000 μ F and 120k Ω would be a satisfactory combination here. You could still use a $100 \text{ k} \Omega$ pre-set pot, and the series fixed resistance this time would be $100k\Omega$, too. It would be necessary to have a number of trial runs before you had the pot adjusted for the correct time period and a good way of doing this would be to set S1 to 'Stop' and temporarily short-circuit the base of the appropriate dis-charge transistor to the negative rail. This procedure allows any stage to be set up on its own. The process will be a little timeconsuming, but then that's life." (Fig.7(c).)

TIME AGAIN

"As you say," said Dick philosophically, "that's life. Just

like your sequencer we go through a series of episodes all the time. Like the steps we've gone through this morning in your explanation of the way the sequencer works.'

"Haven't you any more questions on, it?"

"Nope," said Dick in a satisfied tone. "I have none at all. I am now fully clued-up on this sequencer design of yours."

He glanced around at the empty Workshop.

"To change the subject," he remarked chattily, 'I can't remember whether I told you that my Auntie Eff had rather an unpleasant accident with her Christmas dinner."

Smithy drew breath to deliver the irate retort that Dick had indeed told him

(But, just for the hell of it, let us turn our own sequencer switch to 'Stop' and then back to 'Start'

again) "Did she?" queried Smithy. "What happened?"

this time instead of the usual turkey, and one of her cats, a real mangy old thing

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Notes on the answers. A bleeder resistor is connected across a smoothing capacitor in a high voltage power supply, usually to discharge it when the supply is switched off. Nepers are akin to decibels as units, but are based on Naperian, or natural, logarithms. The piezoelectric slices in a crystal microphone are referred to as "benders" or "twisters" according to the type of stress they undergo when the microphone diaphragm moves.

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ELECTRONICS DATA

30

FOR THE BEGINNER

FULL-WAVE RECTIFIER RATINGS

In (a) the 100 volt secondary of a mains transformer connects to a half-wave rectifier and a reservoir capacitor. As we saw in "Electronics Data 29" the peak voltage across the secondary is 140 volts and the peak inverse voltage applied to the rectifier is 280 volts, or 2.8 times the applied r.m.s. voltage.

A full-wave rectifier circuit is shown in (b), where a centre-tapped 100-0-100 volt secondary is connected to two rectifiers. The rectifiers conduct on alternate half-cycles. On half-cycles when the upper end of the secondary is positive the upper rectifier is in the same circuit as (a), as is the lower rectifier on half-cycles when the lower end of the secondary is positive. In consequence, the p.i.v. applied to each rectifier is 280 volts, or 2.8 times the r.m.s. voltage across each half of the secondary.

The full-wave bridge rectifier of (c) has four rectifiers. D2 and D4 conduct when the upper end of the secondary is positive (trace the conducting path — from positive to negative — through D2, the load and D4 back to the lower end of the secondary). D3 and D1 conduct when the lower end of the secondary is positive. With little or no load current the voltage across the reservoir capacitor is virtually 140 volts.

In (d) the peak voltage of 140 volts appears across the secondary and, since D2 and D4 are conducting, this 140 volts is applied, with inverse polarity, across D3 and D1. A similar inverse voltage appears across D2 and D4 on alternate peaks. Thus, in a bridge rectifier circuit the peak inverse voltage, the applied to the rectifiers is 1.4 times the applied r.m.s. voltage.



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